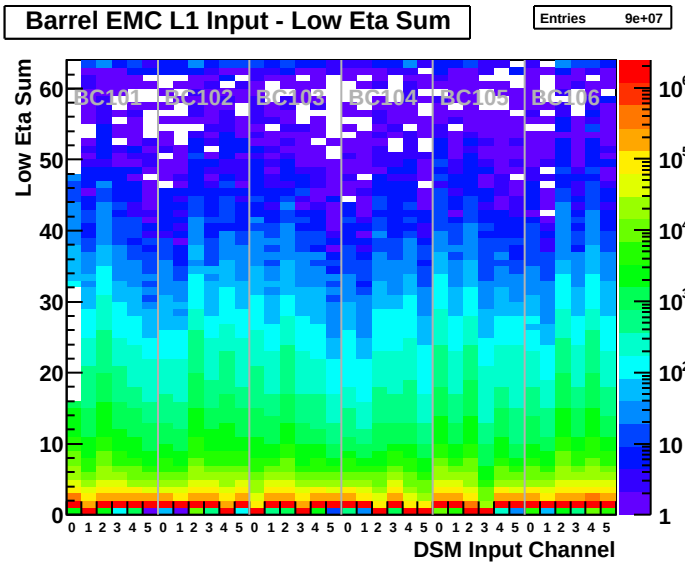
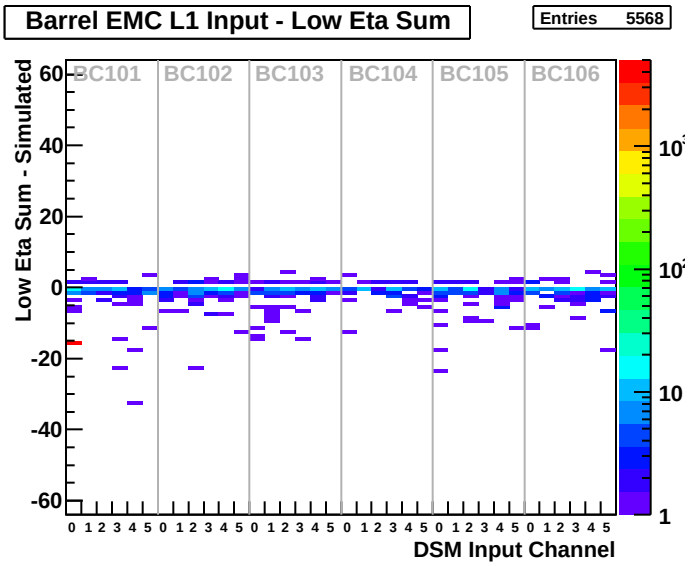
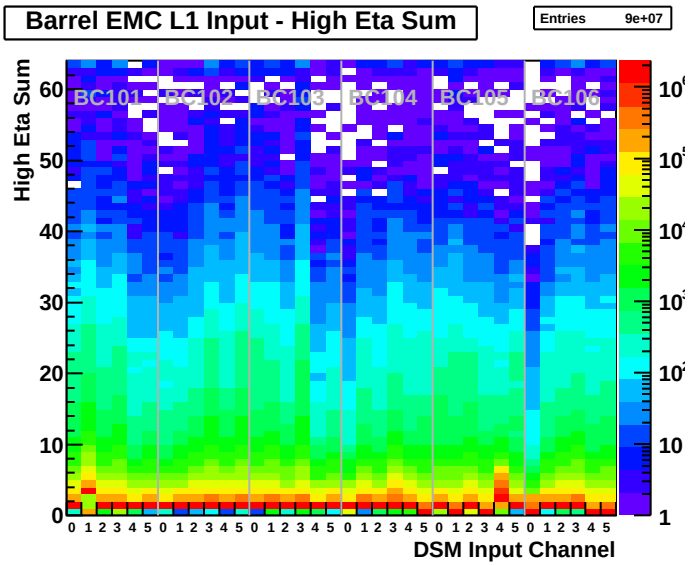
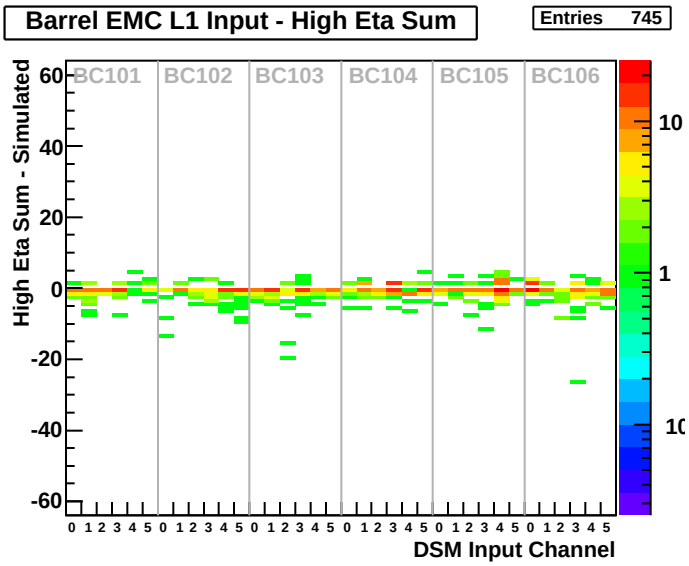
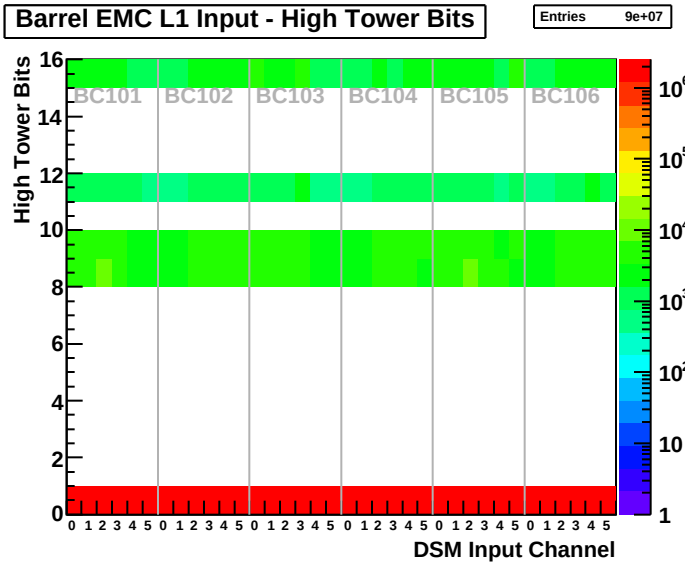
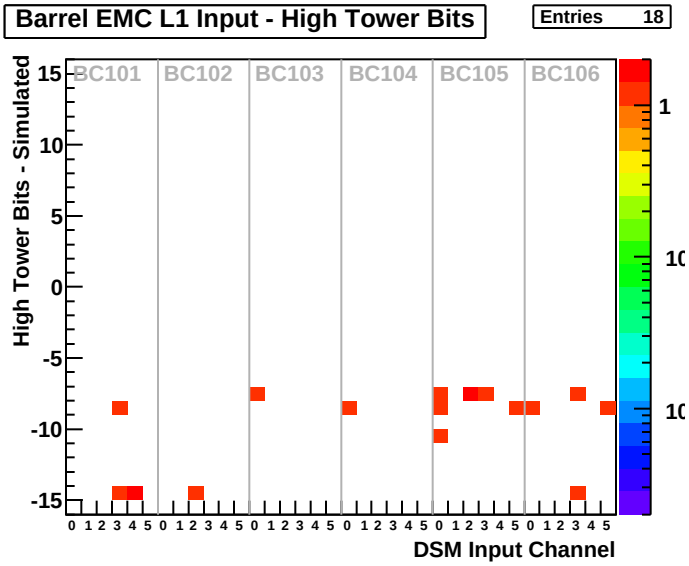

Barrel EMC L1 Input - Low Eta Sum
Entries 9e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

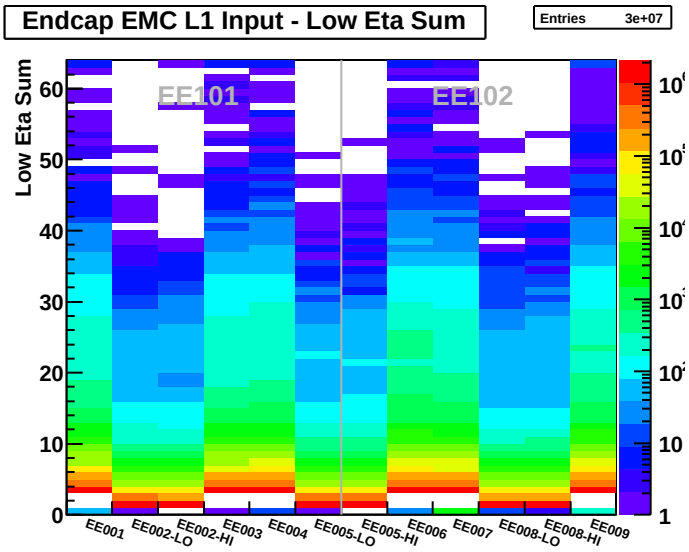

Barrel EMC L1 Input - Low Eta Sum
Entries 5568
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

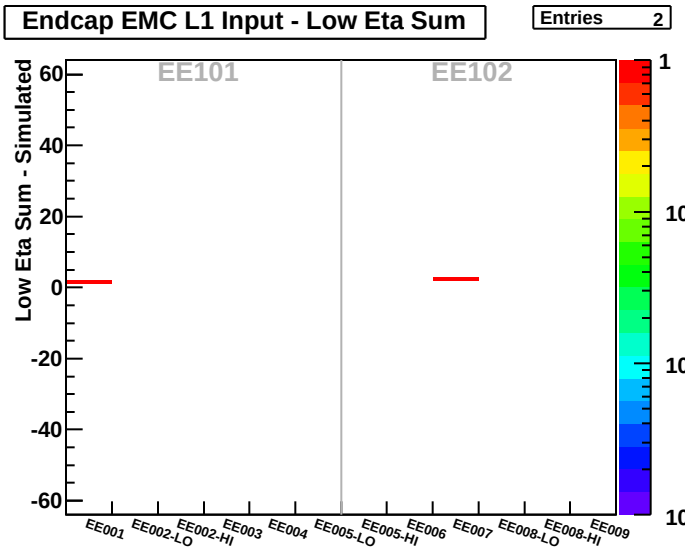

Barrel EMC L1 Input - High Eta Sum
Entries 9e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

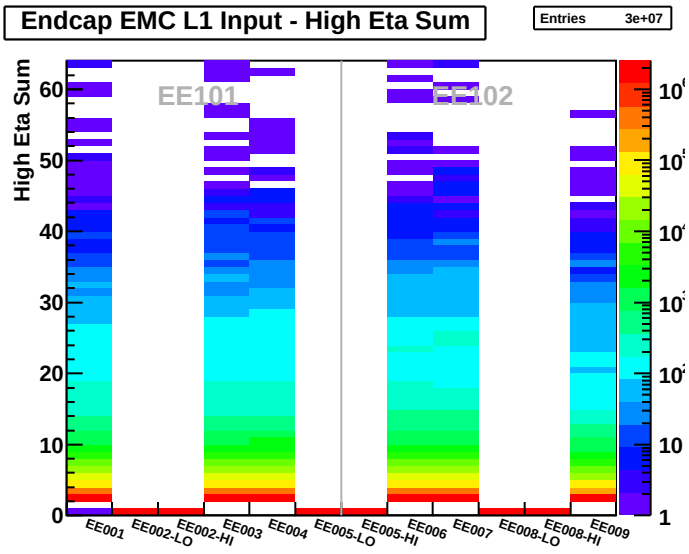

Barrel EMC L1 Input - High Eta Sum
Entries 745
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

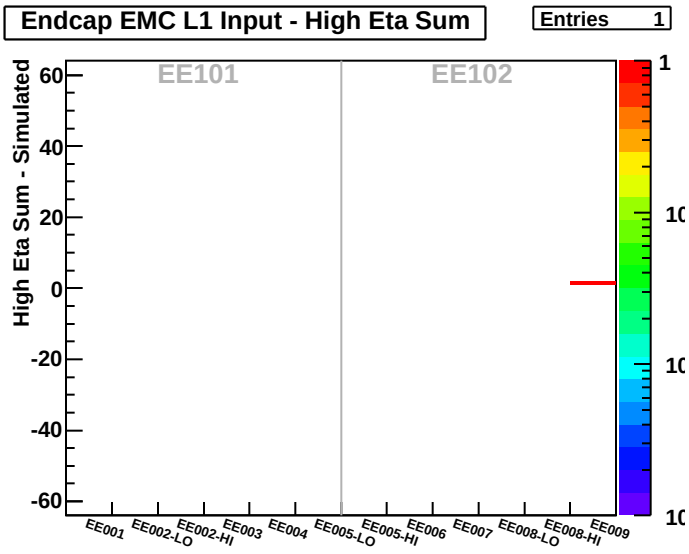

Barrel EMC L1 Input - High Tower Bits
Entries 9e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

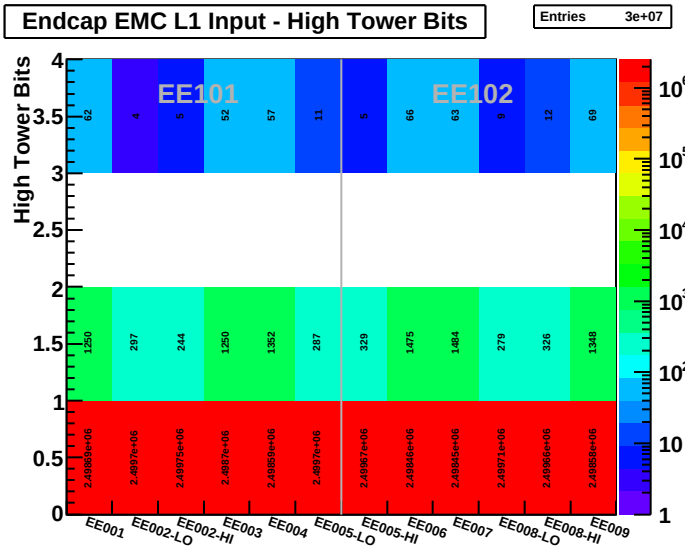

Barrel EMC L1 Input - High Tower Bits
Entries 18
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

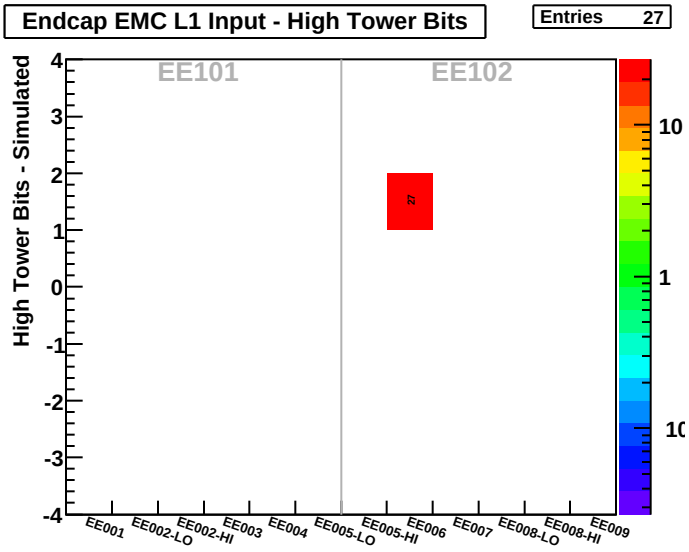

Endcap EMC L1 Input - Low Eta Sum
Entries 3e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

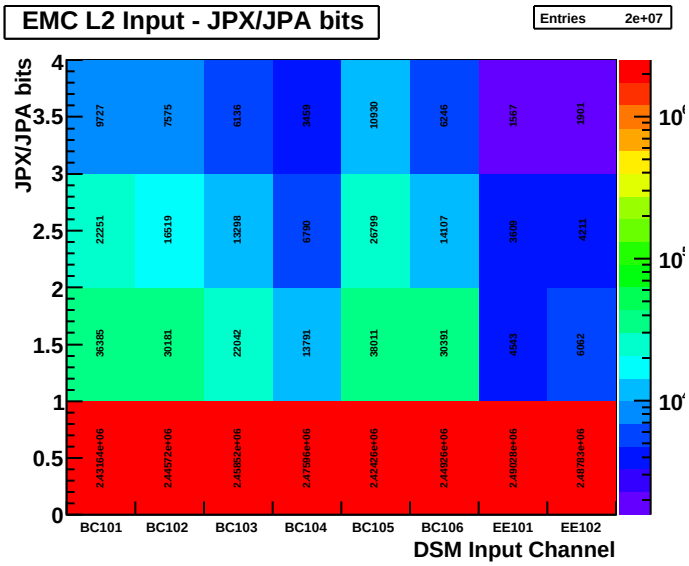

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

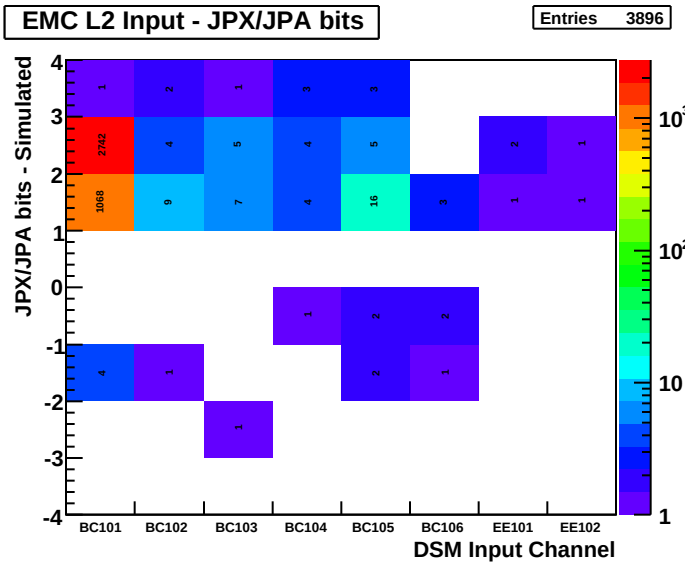

Endcap EMC L1 Input - High Eta Sum
Entries 3e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

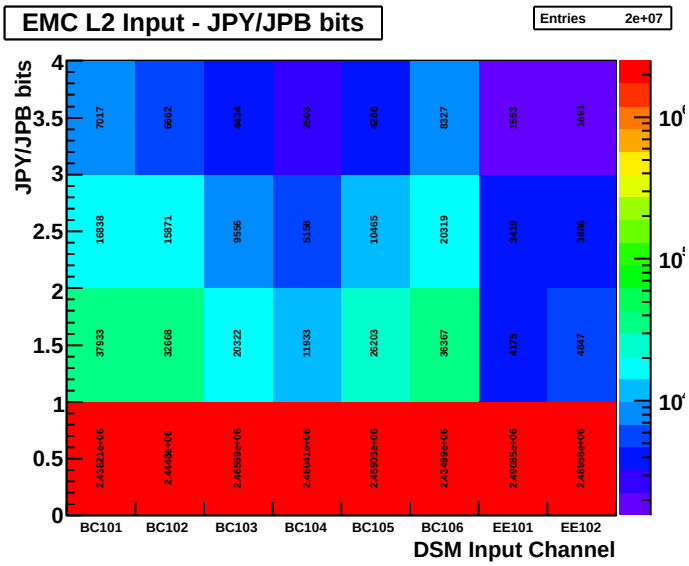

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

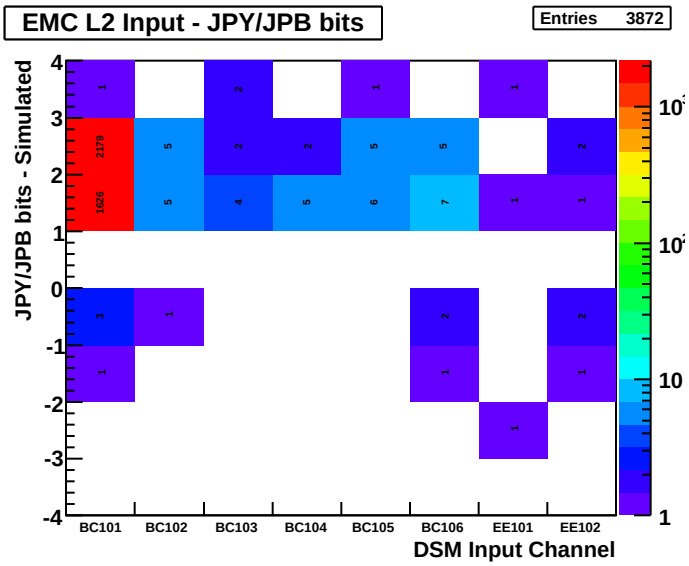

Endcap EMC L1 Input - High Tower Bits
Entries 3e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

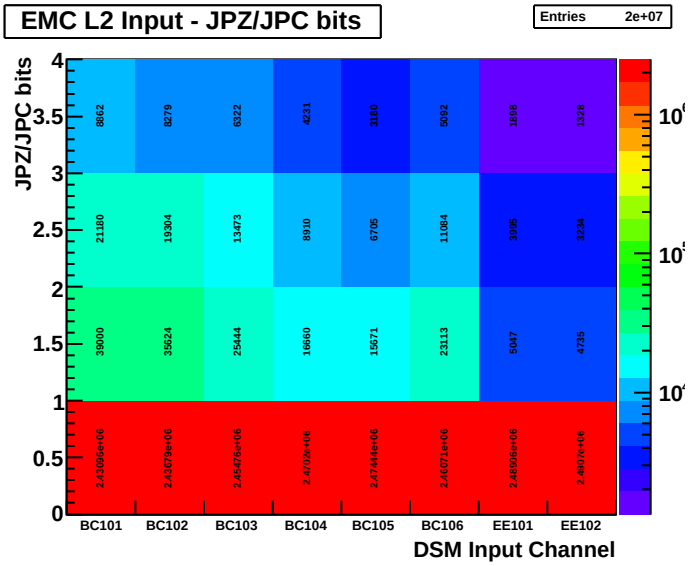

Endcap EMC L1 Input - High Tower Bits
Entries 27
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

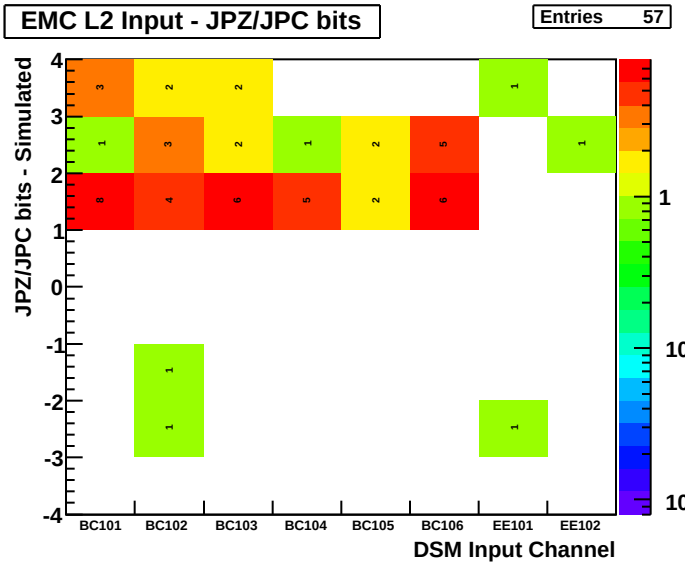

EMC L2 Input - JPX/JPA bits
Entries 2e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

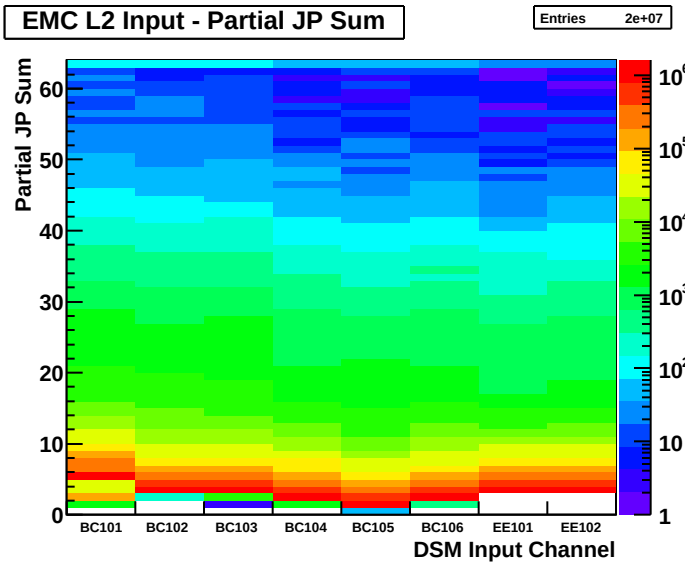

EMC L2 Input - JPX/JPA bits
Entries 3896
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

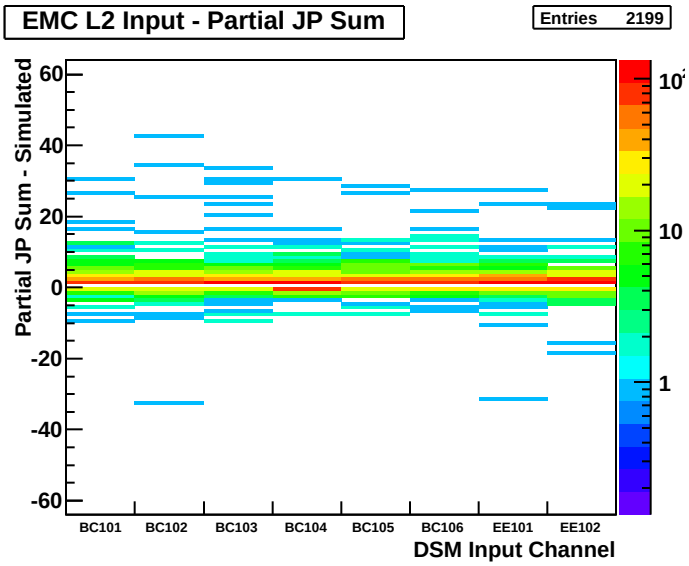

EMC L2 Input - JPY/JPB bits
Entries 2e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

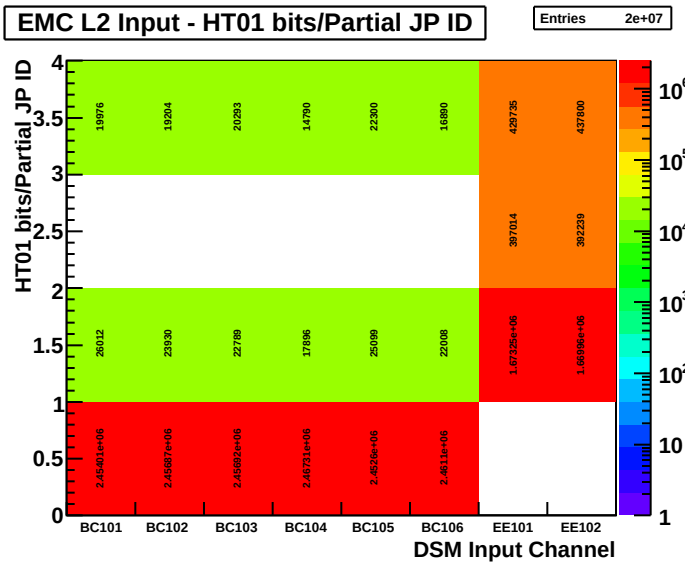

EMC L2 Input - JPY/JPB bits
Entries 3872
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

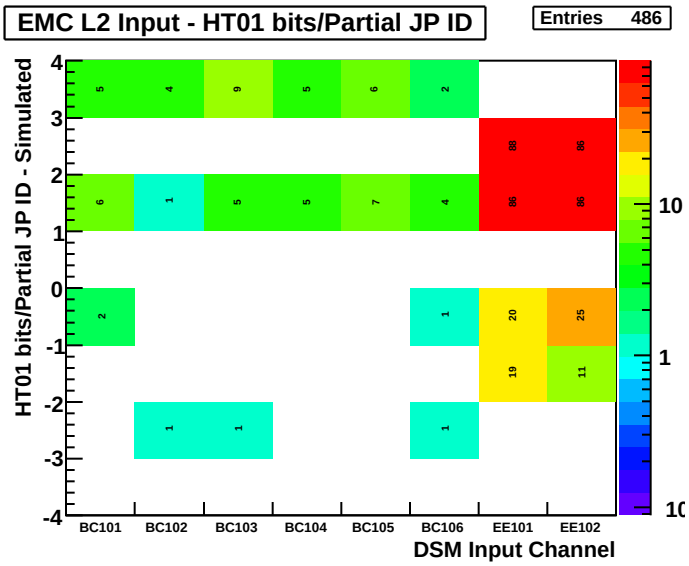

EMC L2 Input - JPZ/JPC bits
Entries 2e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

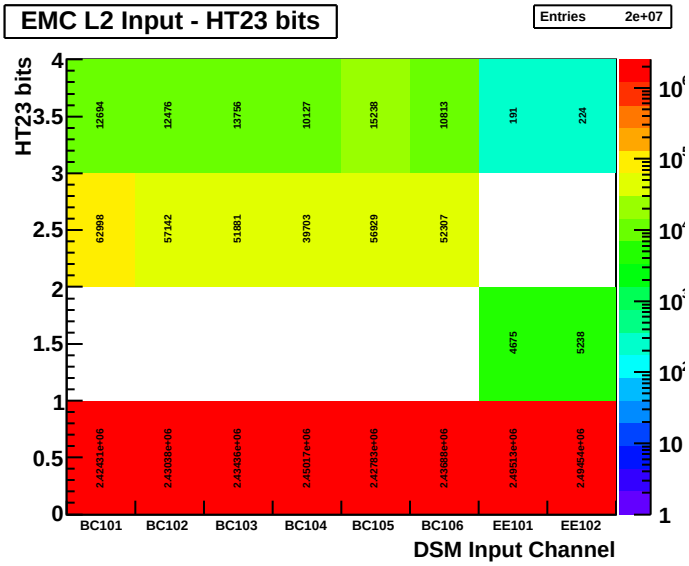

EMC L2 Input - JPZ/JPC bits
Entries 57
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

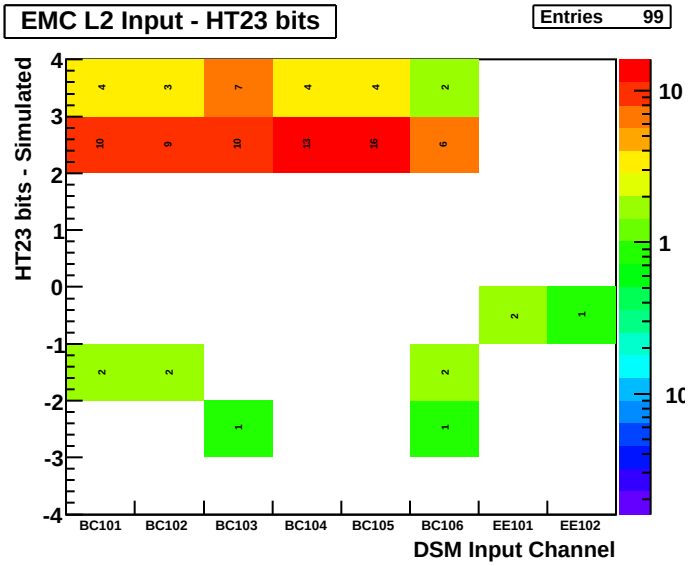

EMC L2 Input - Partial JP Sum
Entries 2e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

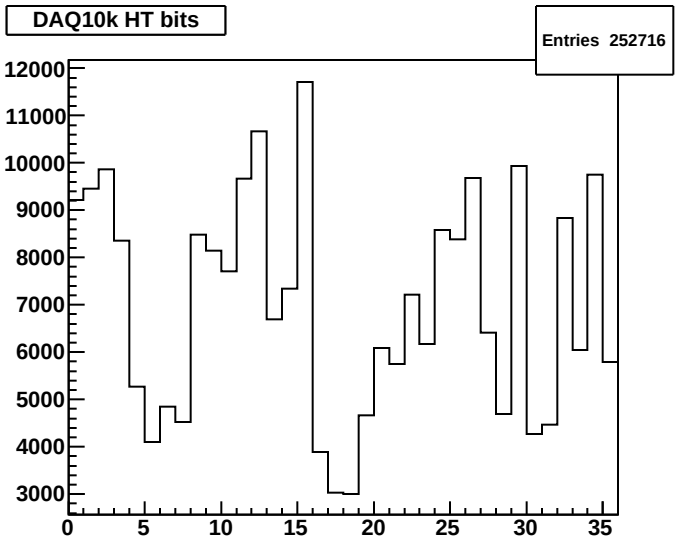

EMC L2 Input - Partial JP Sum
Entries 2199
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

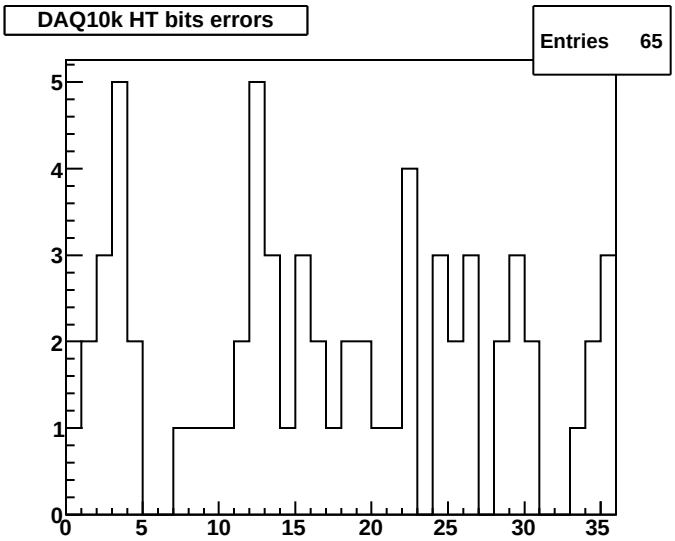

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

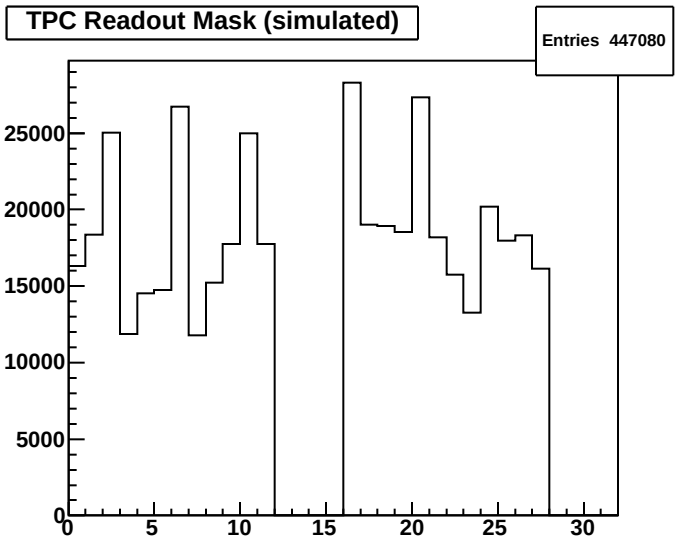

EMC L2 Input - HT01 bits/Partial JP ID
Entries 486
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

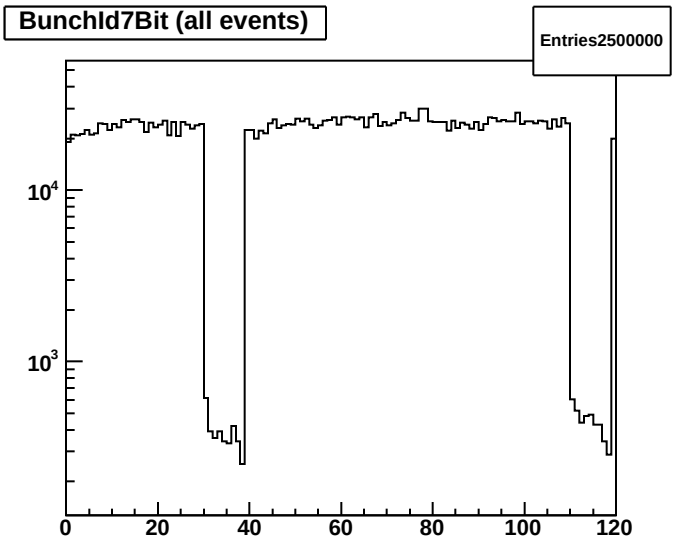

EMC L2 Input - HT23 bits
Entries 2e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

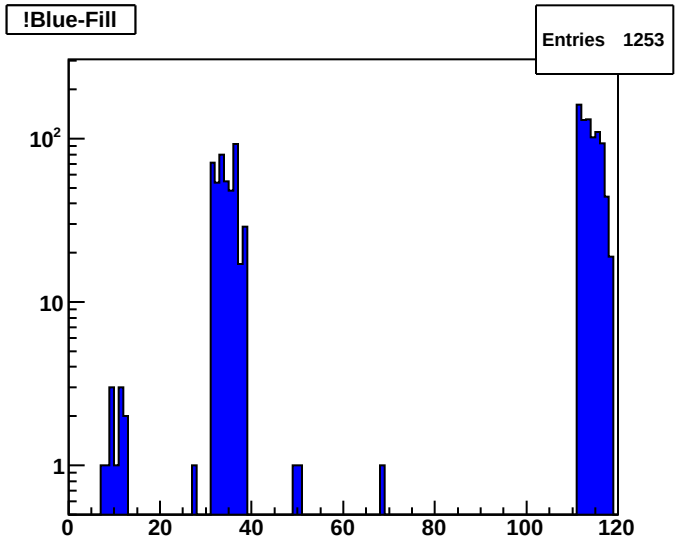

EMC L2 Input - HT23 bits
Entries 99
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

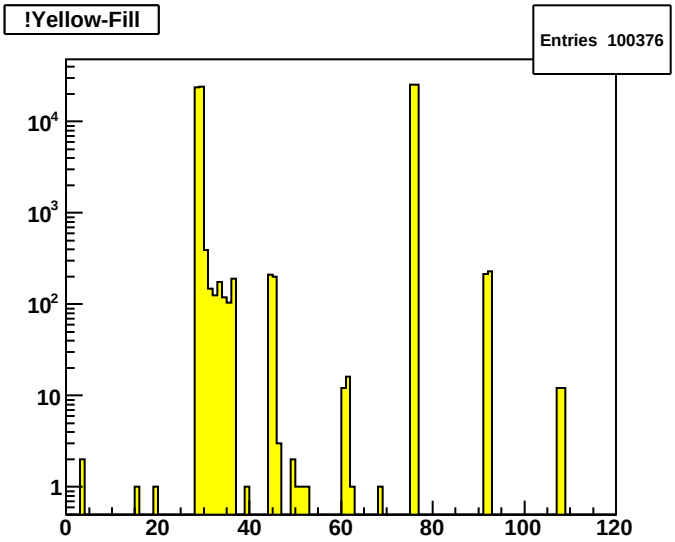

DAQ10k HT bits
Entries 252716

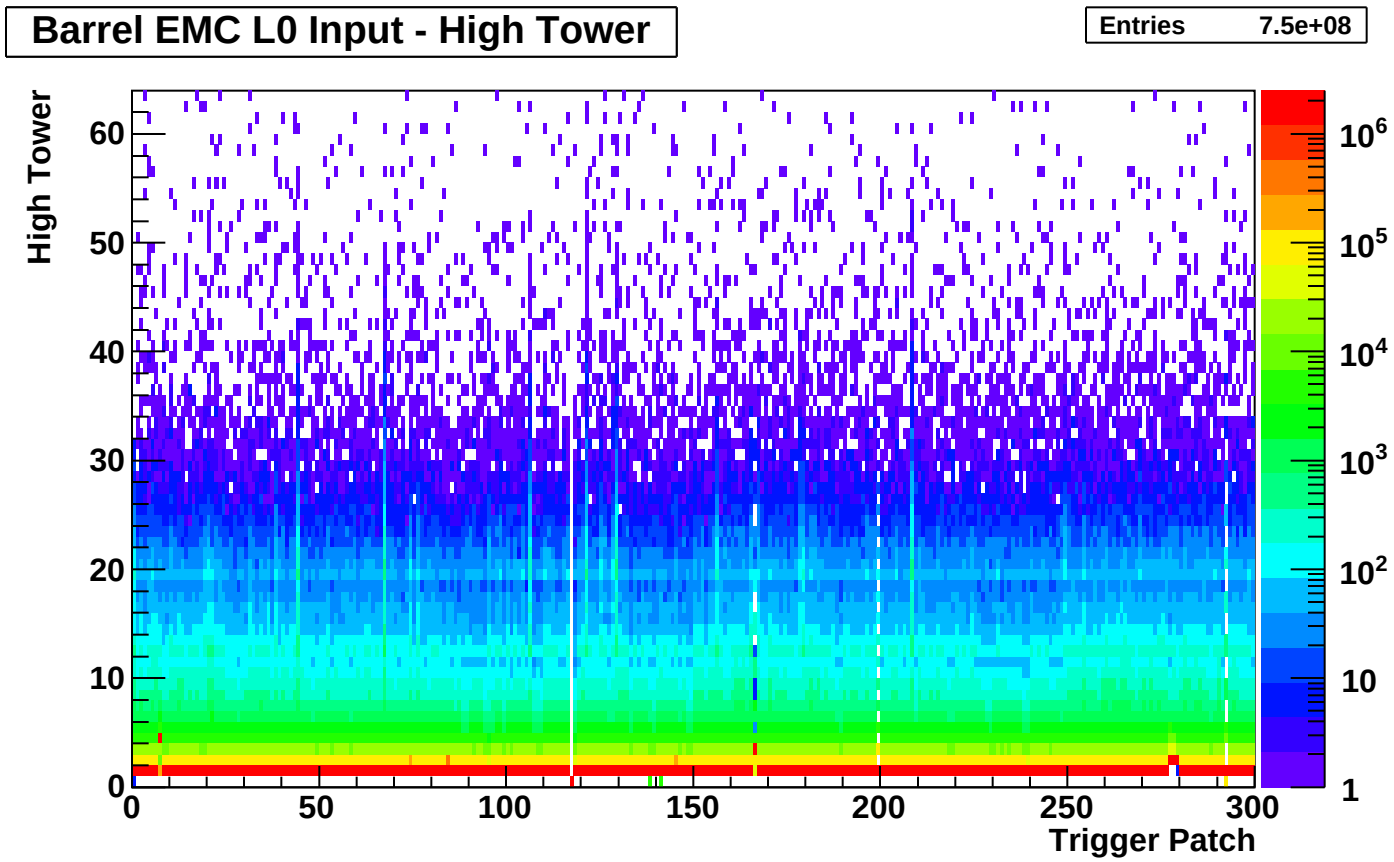

DAQ10k HT bits errors
Entries 65

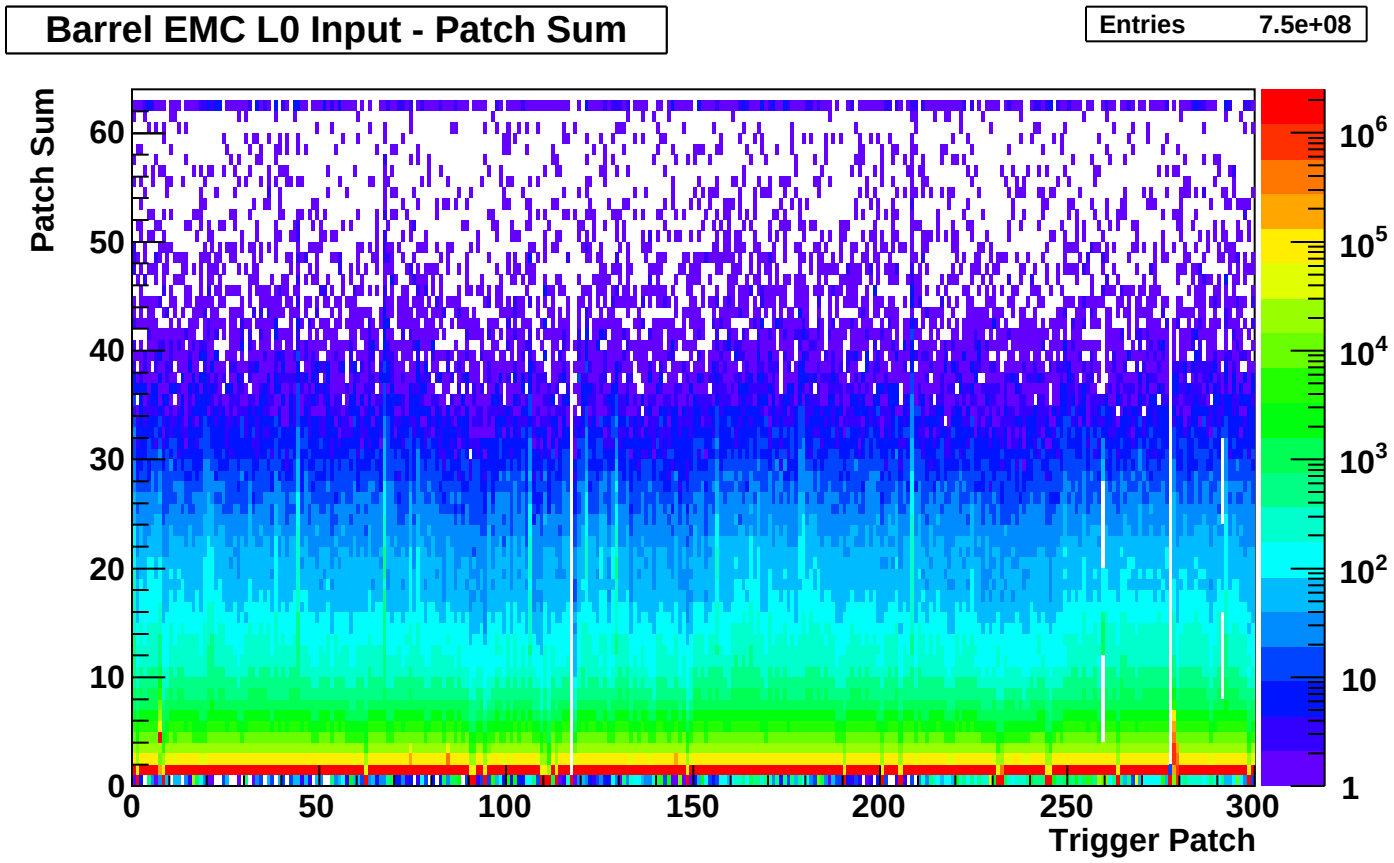

TPC Readout Mask (simulated)
Entries 447080

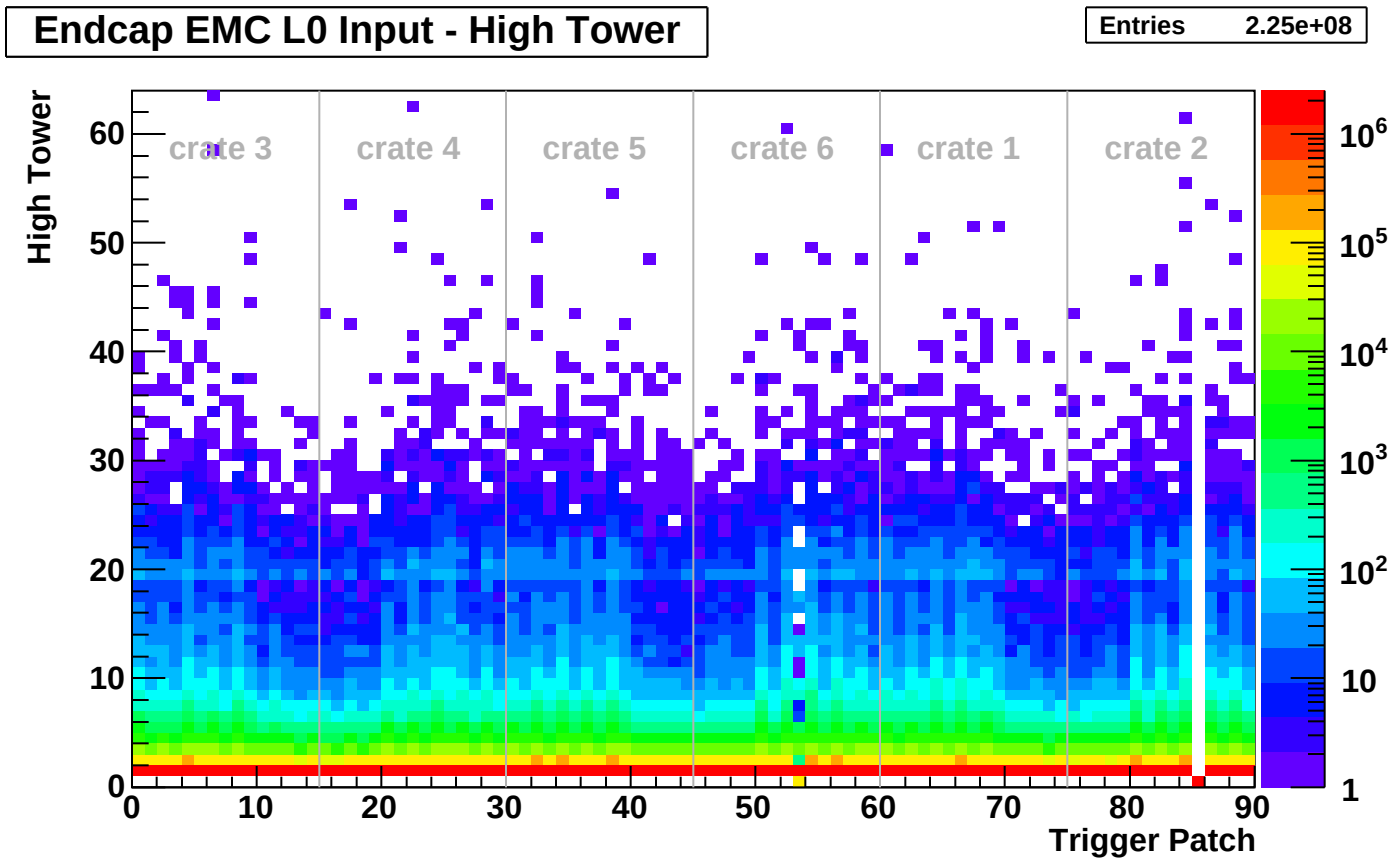

BunchId7Bit (all events)
Entries2500000

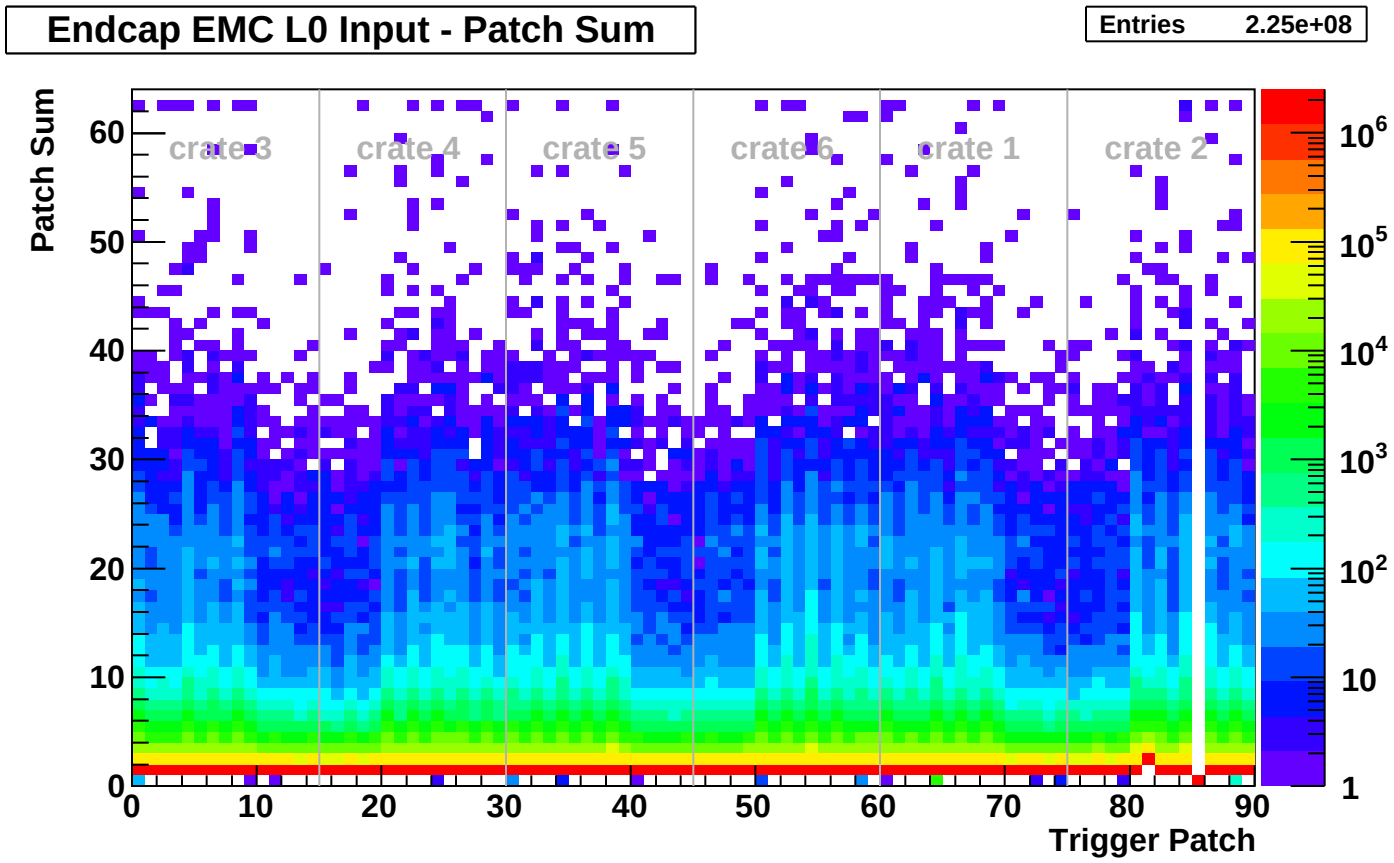

!Blue-Fill
Entries 1253

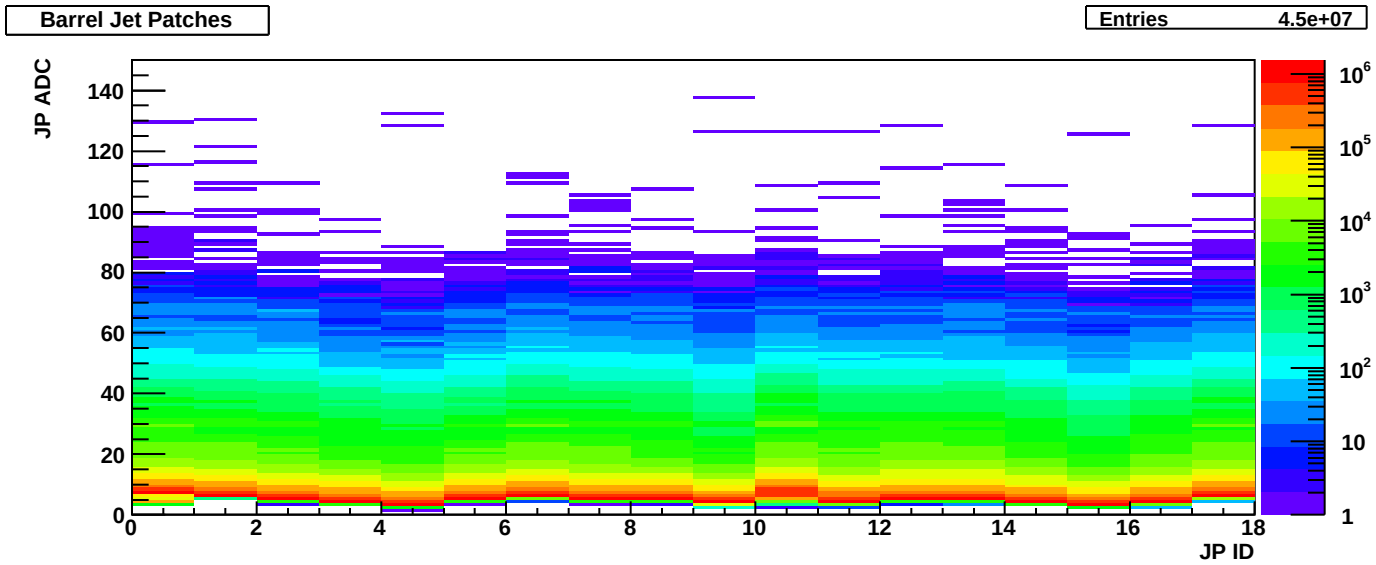

!Yellow-Fill
Entries 100376

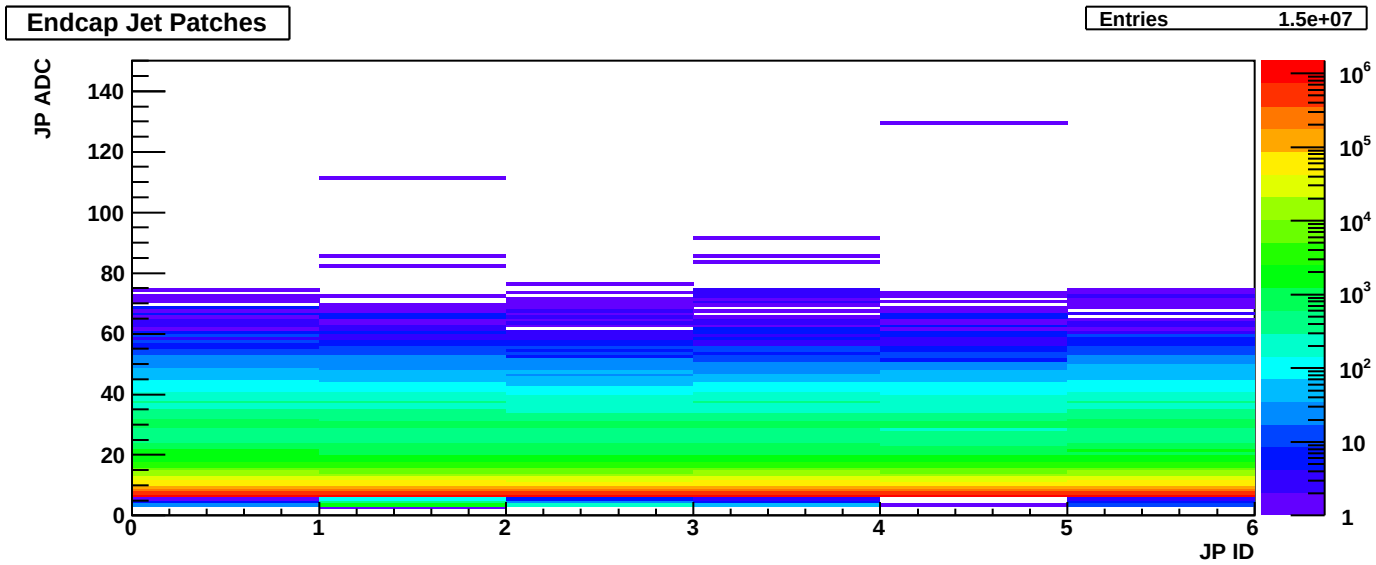

Barrel EMC L0 Input - High Tower
Entries 7.5e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

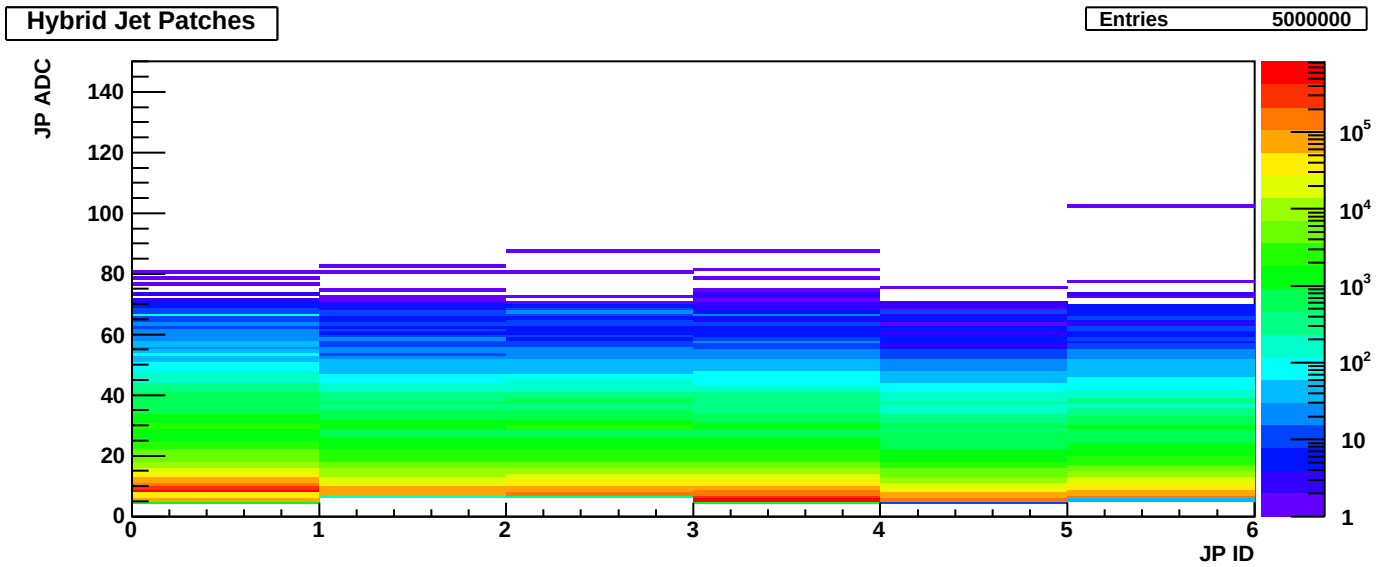

Barrel EMC L0 Input - Patch Sum
Entries 7.5e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

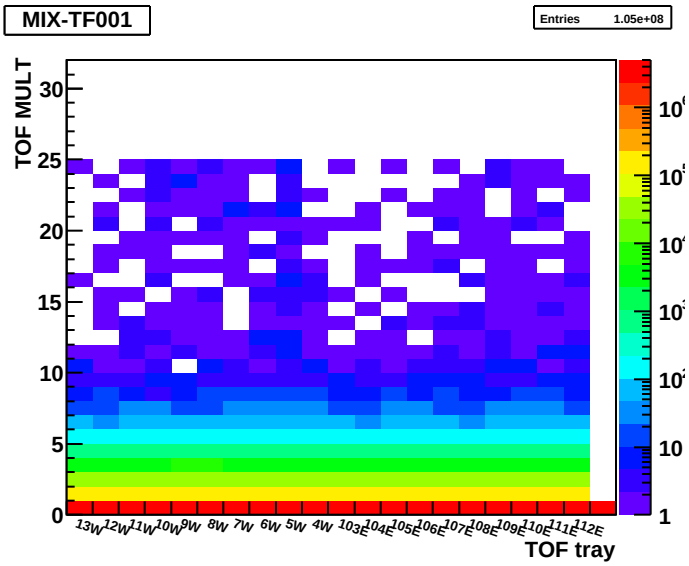

Endcap EMC L0 Input - High Tower
Entries 2.25e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

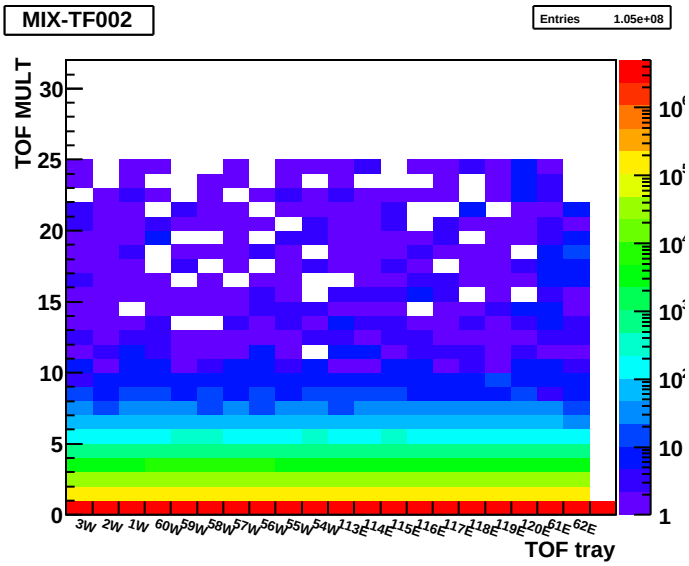

Endcap EMC L0 Input - Patch Sum
Entries 2.25e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

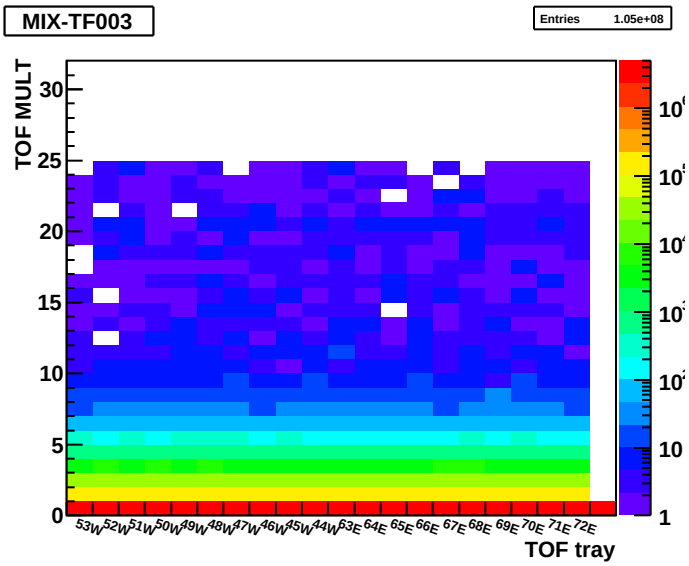

Barrel Jet Patches
Entries 4.5e+07
JP ADC
JP ID

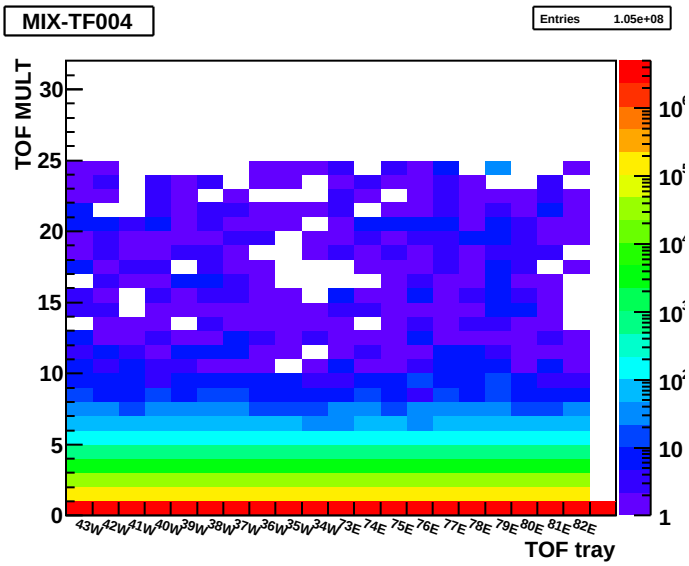

Endcap Jet Patches
Entries 1.5e+07
JP ADC
JP ID

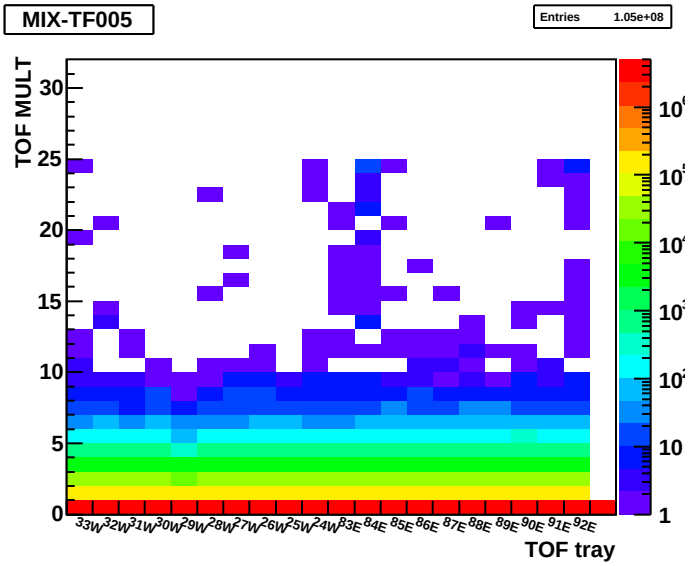

Hybrid Jet Patches
Entries 5000000
JP ADC
JP ID

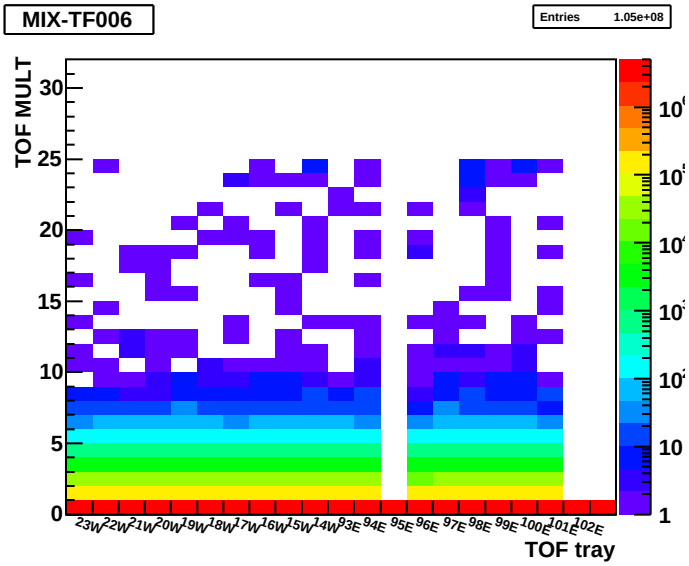

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

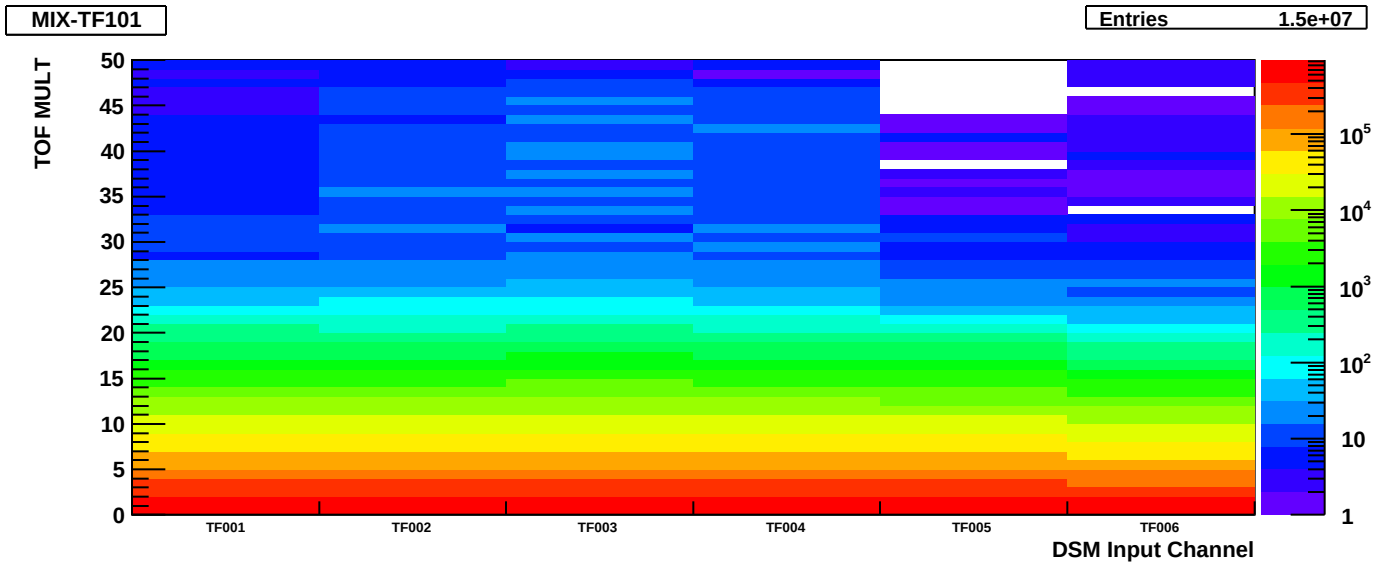

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

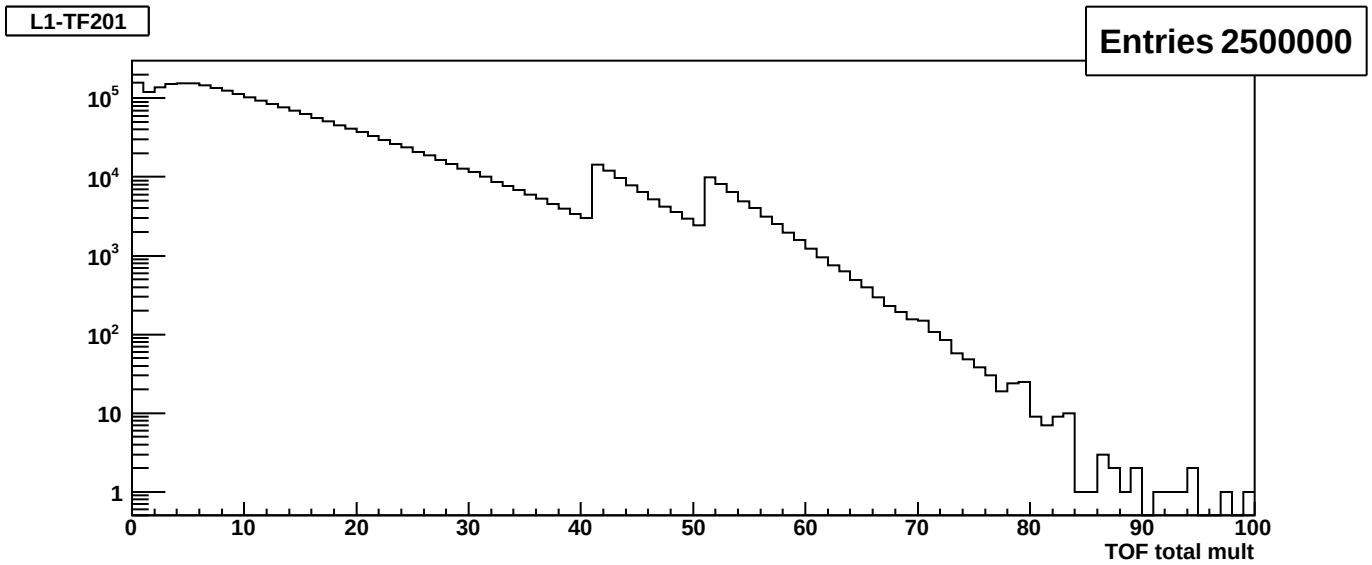

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

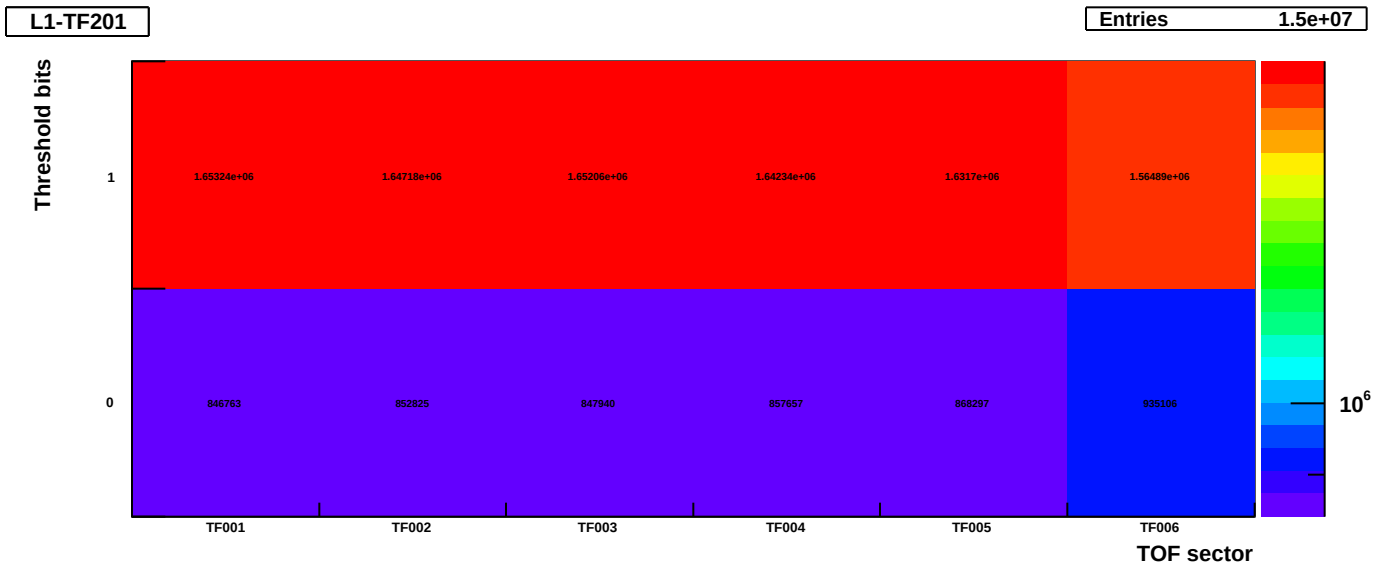

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

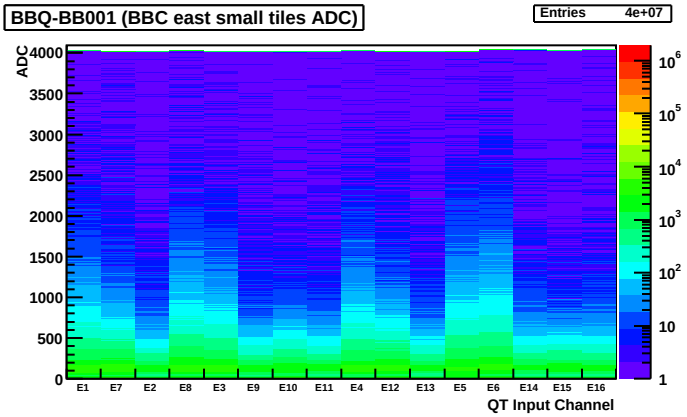

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

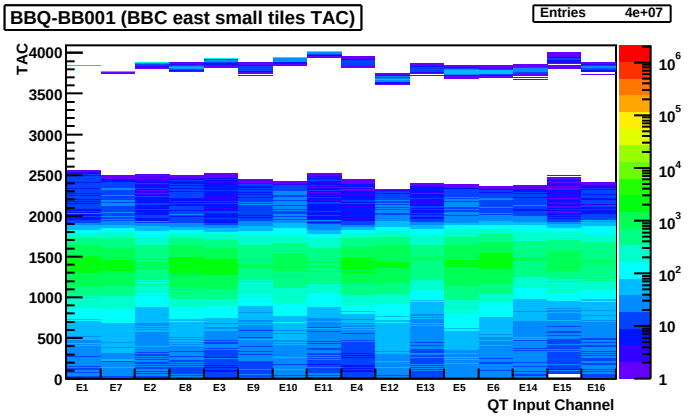

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

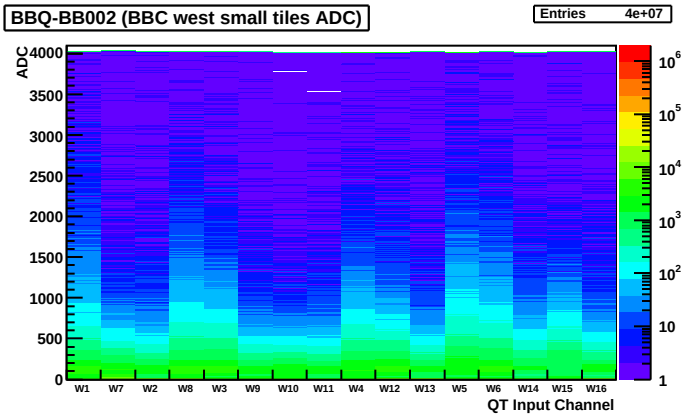

MIX-TF101
Entries 1.5e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

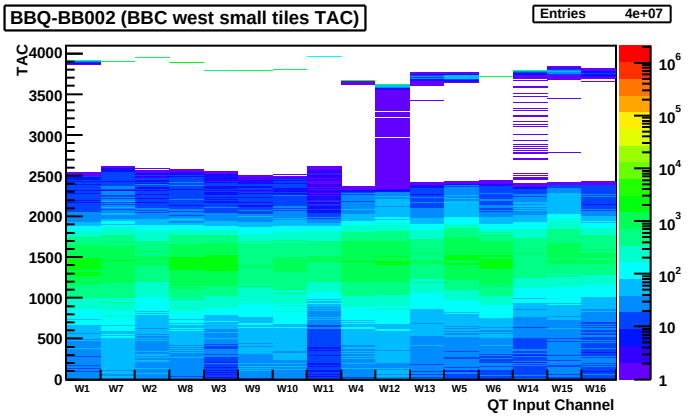

L1-TF201
Entries 2500000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

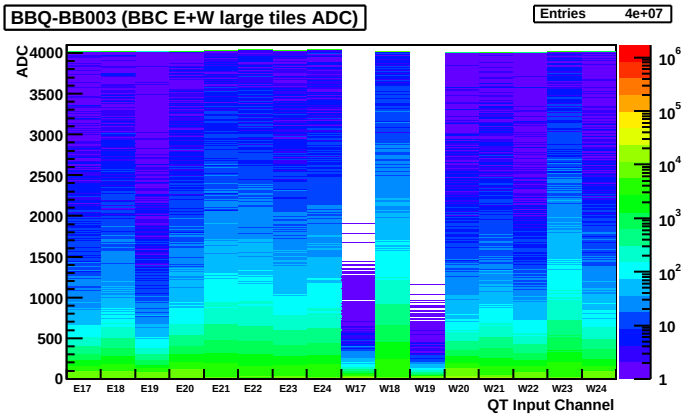

L1-TF201
Entries 1.5e+07
Threshold bits
1
0
1.65324e+06
1.64718e+06
1.65206e+06
1.64234e+06
1.6317e+06
1.56489e+06
846763
852825
847940
857657
868297
935106
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

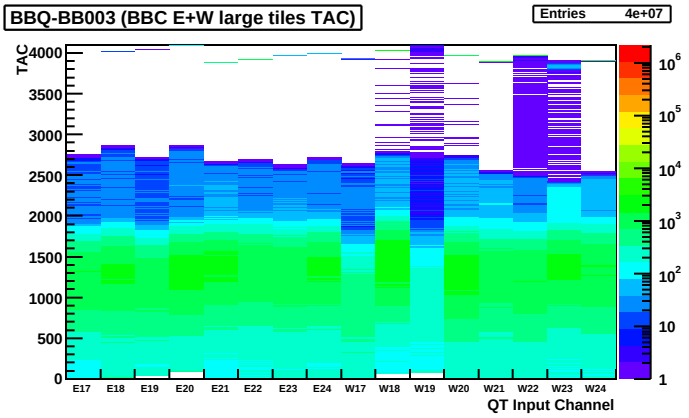

BBQ-BB001 (BBC east small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

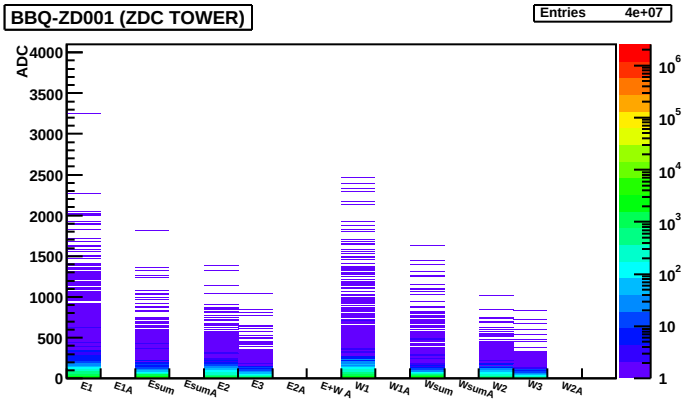

BBQ-BB001 (BBC east small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

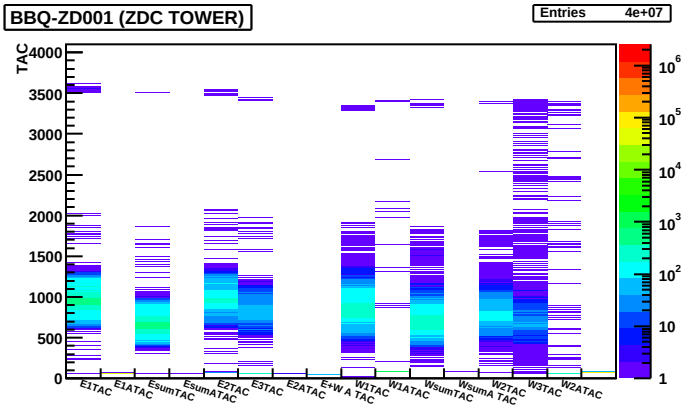

BBQ-BB002 (BBC west small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

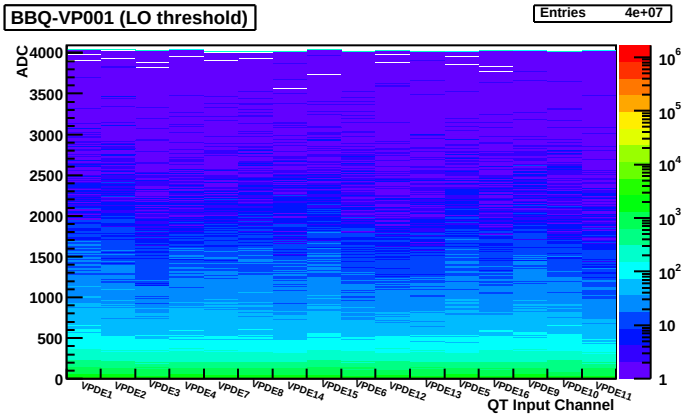

BBQ-BB002 (BBC west small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

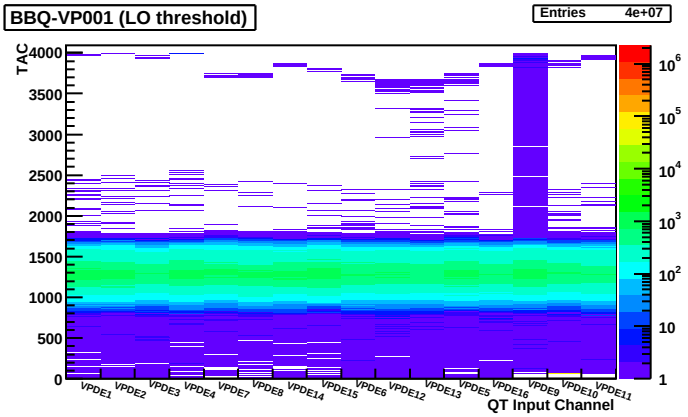

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4e+07
ADC
QT Input Channel

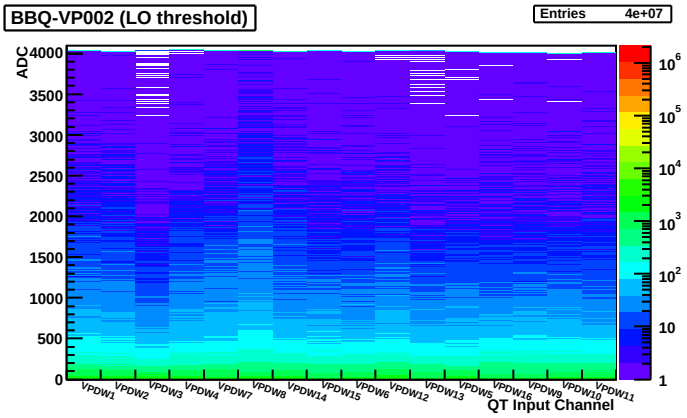

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4e+07
TAC
QT Input Channel

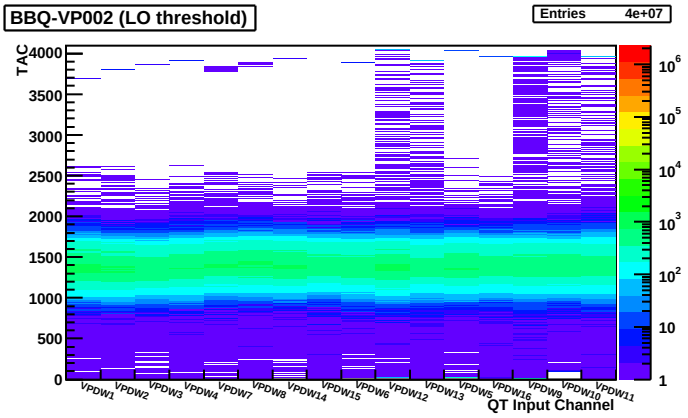

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
ADC

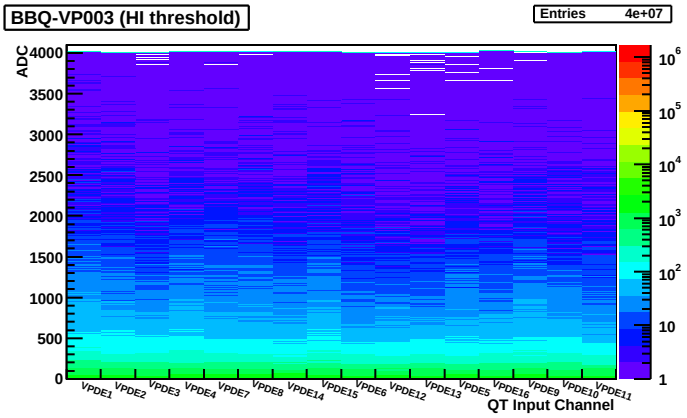

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
TAC

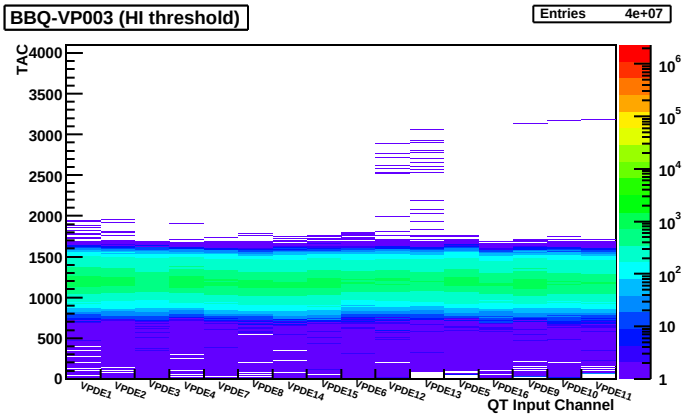

BBQ-VP001 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

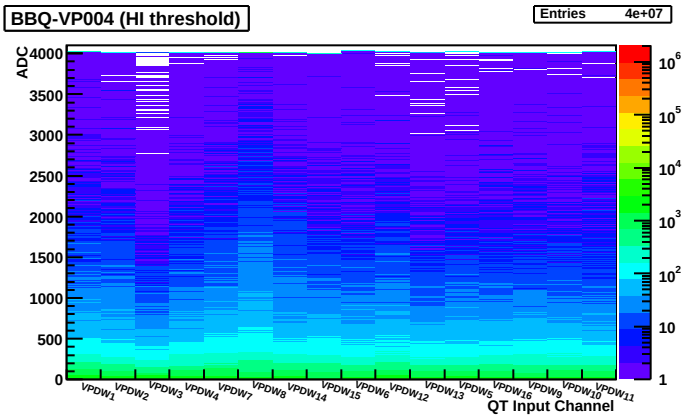

BBQ-VP001 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

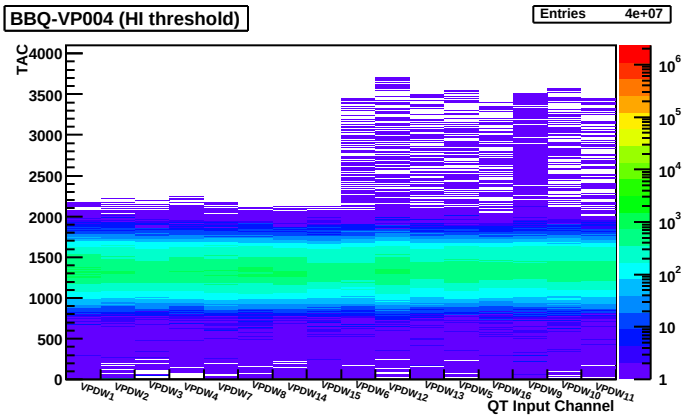

BBQ-VP002 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

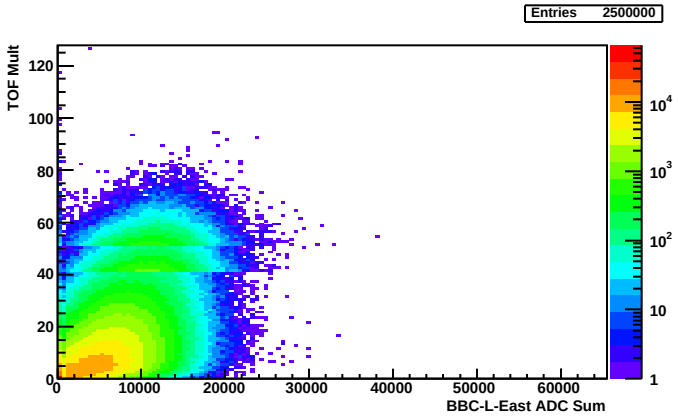

BBQ-VP002 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

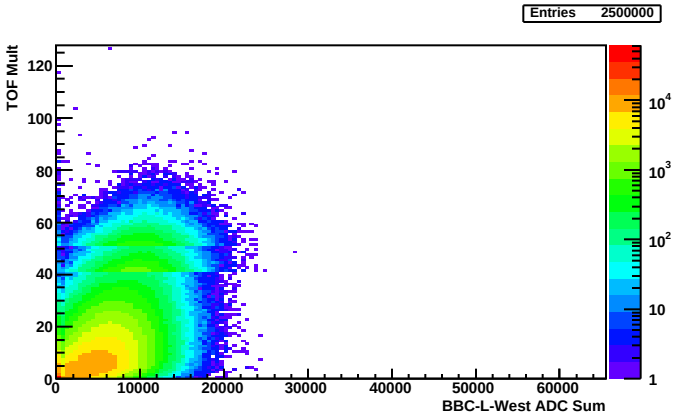

BBQ-VP003 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

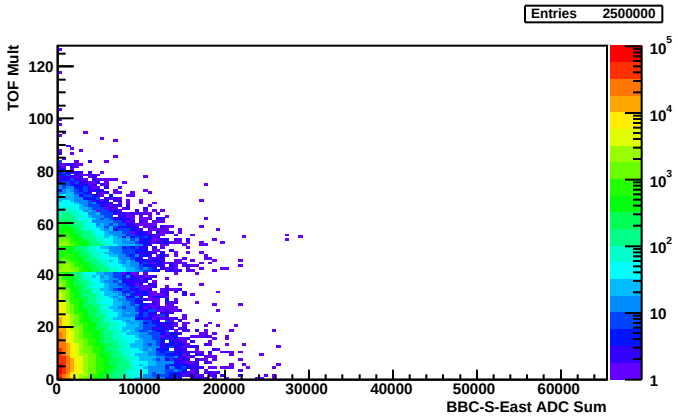

BBQ-VP003 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

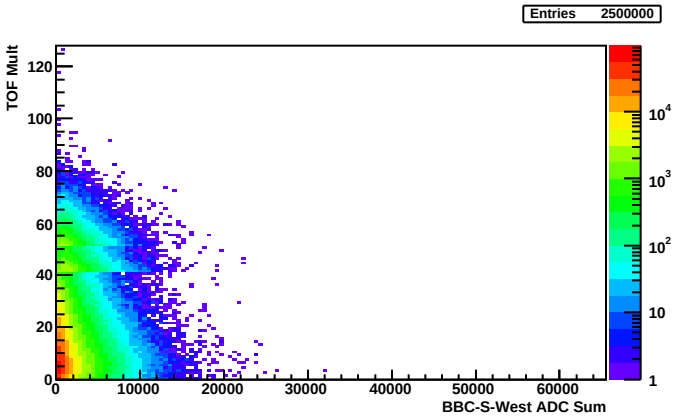

BBQ-VP004 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

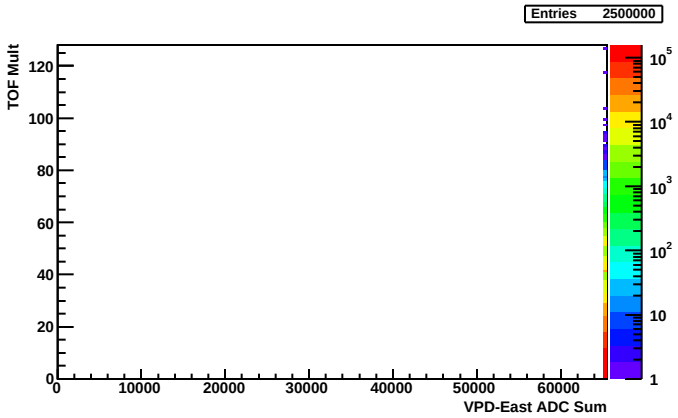

BBQ-VP004 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

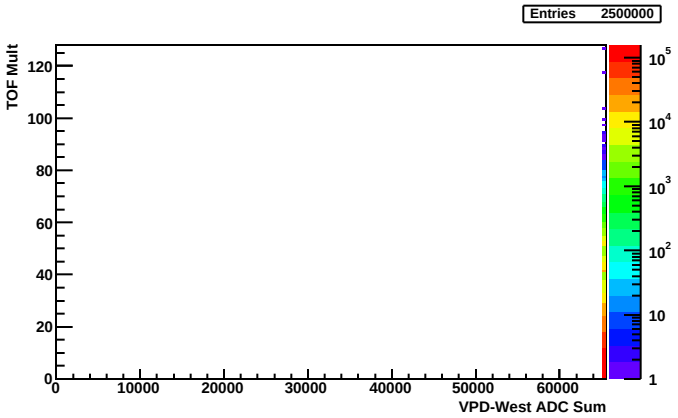

Entries 2500000
TOF Mult
BBC-L-East ADC Sum

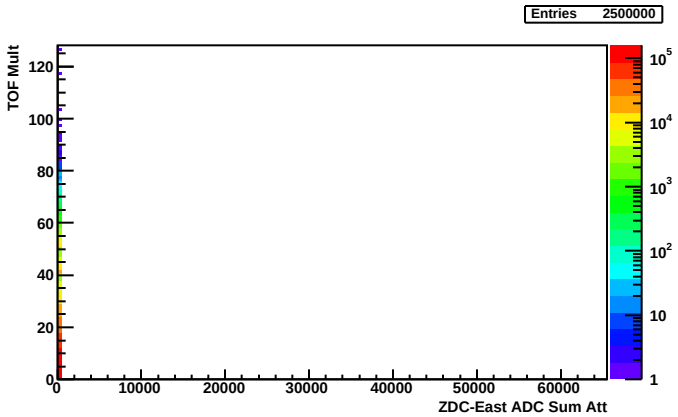

Entries 2500000
TOF Mult
BBC-L-West ADC Sum

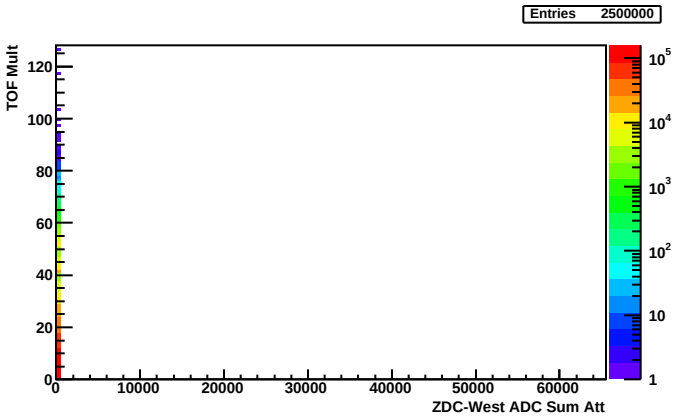

Entries 2500000
TOF Mult
BBC-S-East ADC Sum

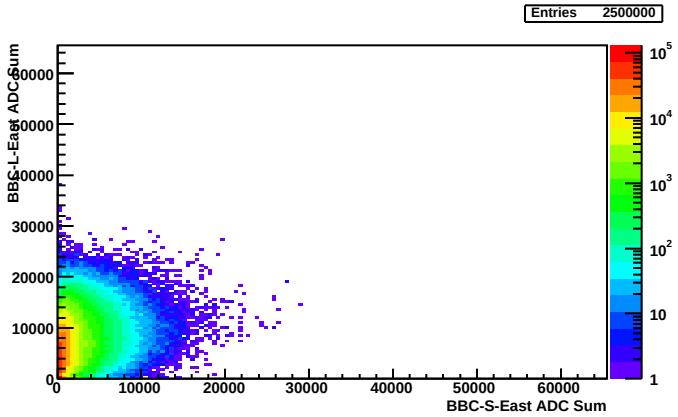

Entries 2500000
TOF Mult
BBC-S-West ADC Sum

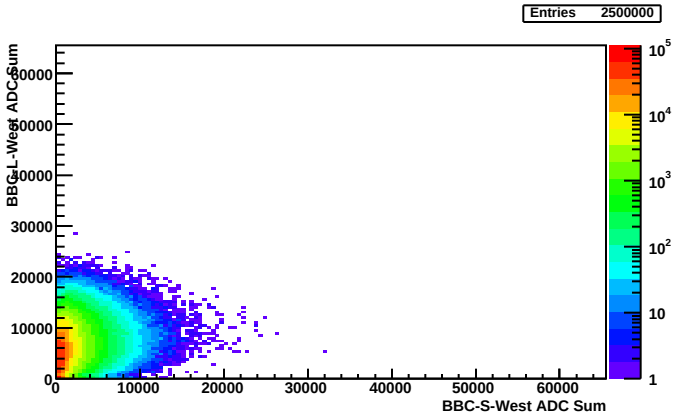

Entries 2500000
TOF Mult
VPD-East ADC Sum

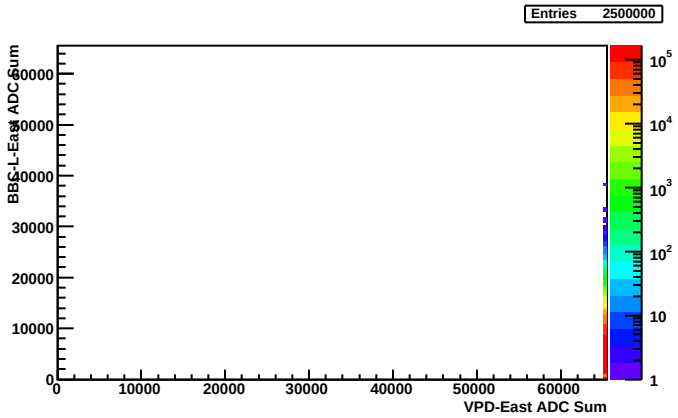

Entries 2500000
TOF Mult
VPD-West ADC Sum

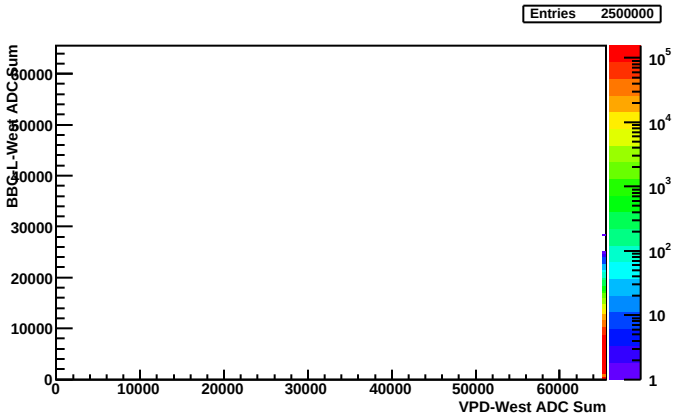

Entries 2500000
TOF Mult
ZDC-East ADC Sum Att

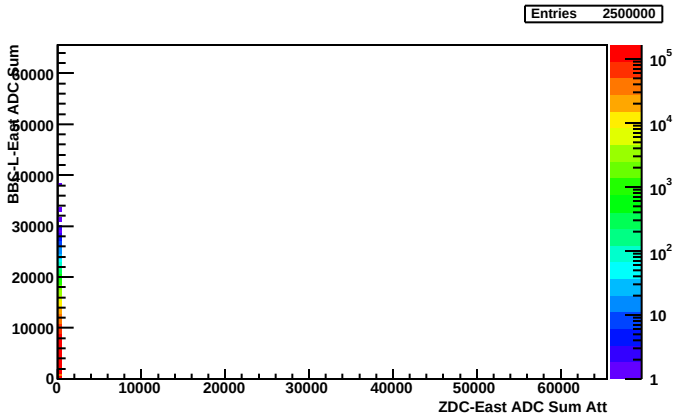

Entries 2500000
TOF Mult
ZDC-West ADC Sum Att

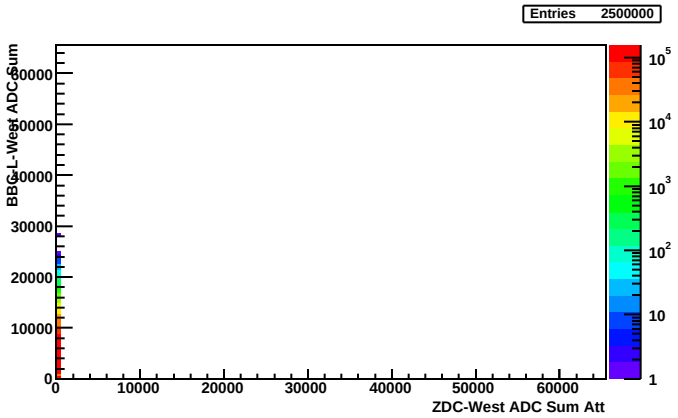

Entries 2500000
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2500000
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2500000
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2500000
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 2500000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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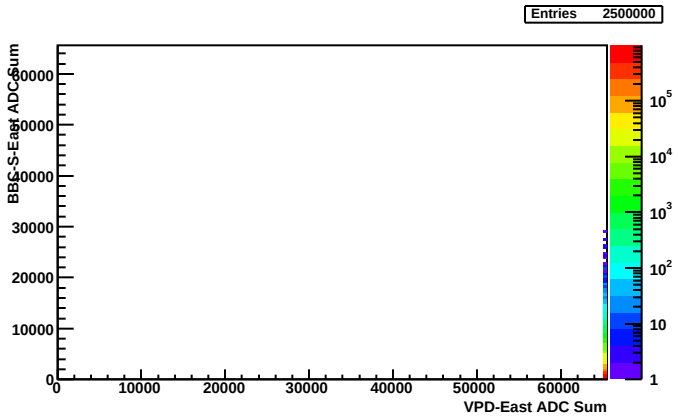
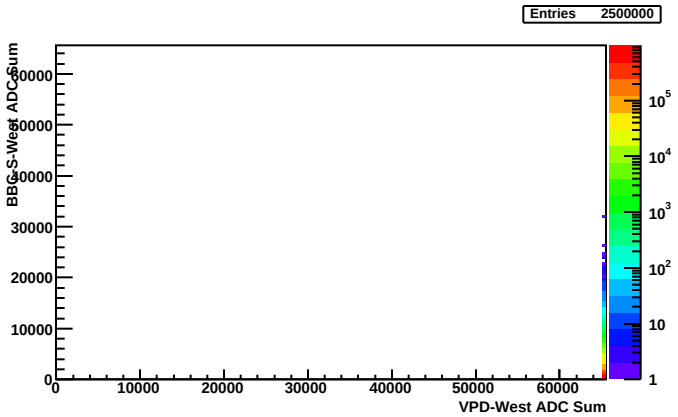
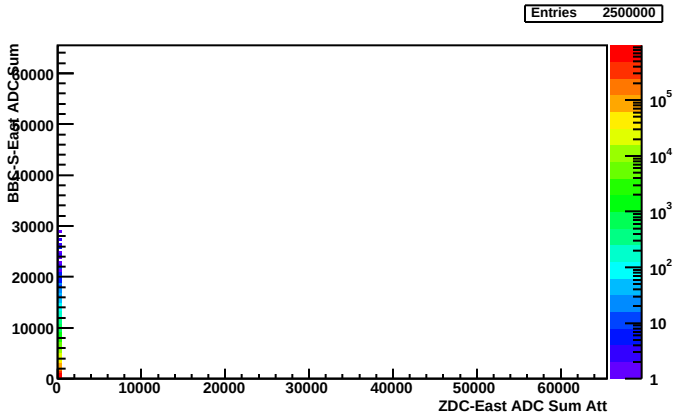
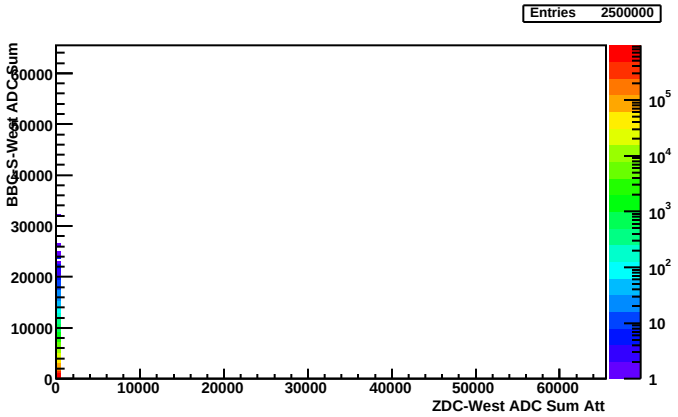
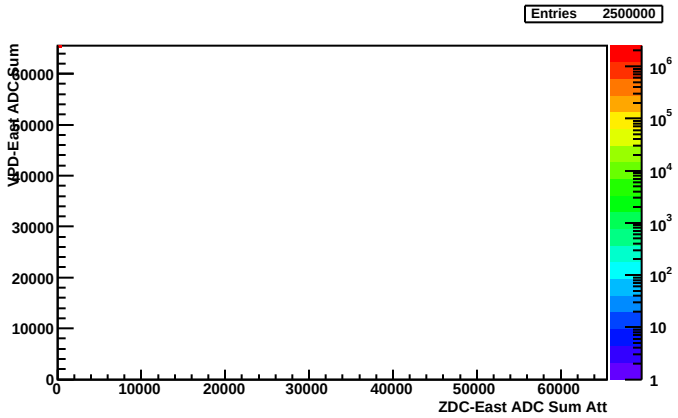
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

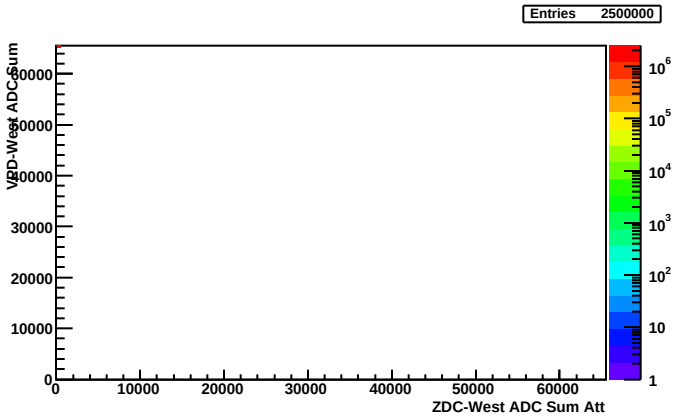

Entries 2500000
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2500000
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2500000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2500000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2500000
VPD-East ADC Sum
ZDC-East ADC Sum Att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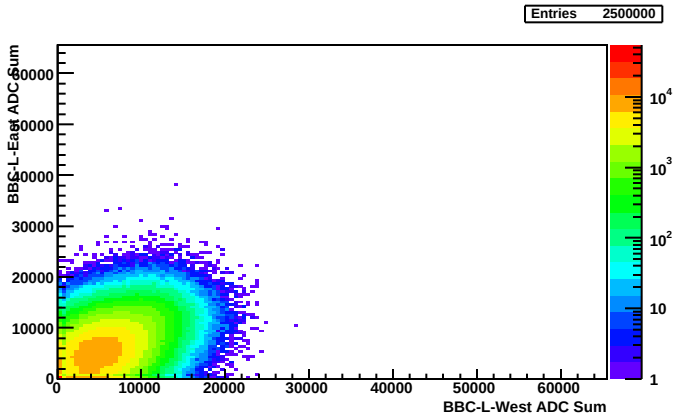
VPD-West ADC Sum
ZDC-West ADC Sum Att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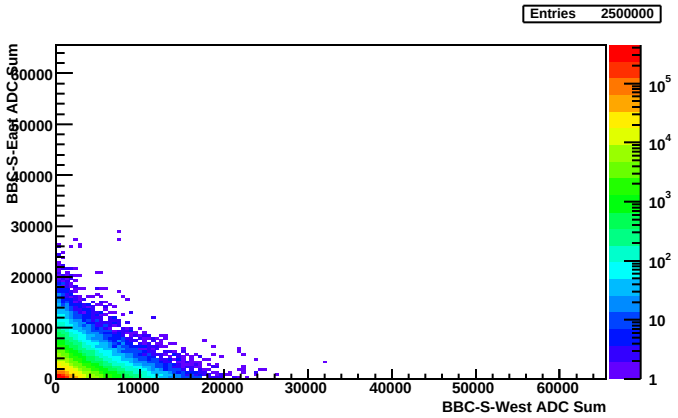
BBC-L-East ADC Sum
BBC-L-West ADC Sum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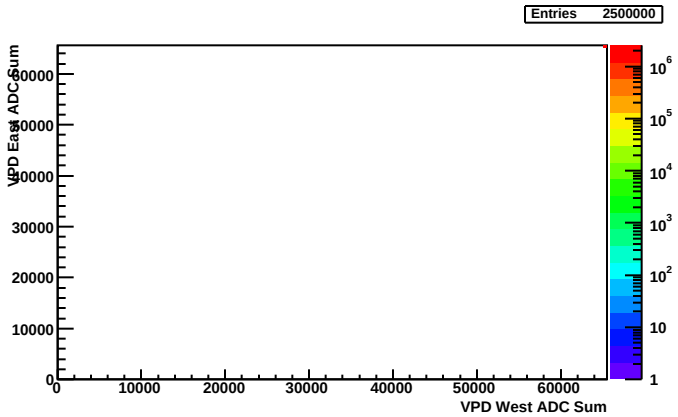
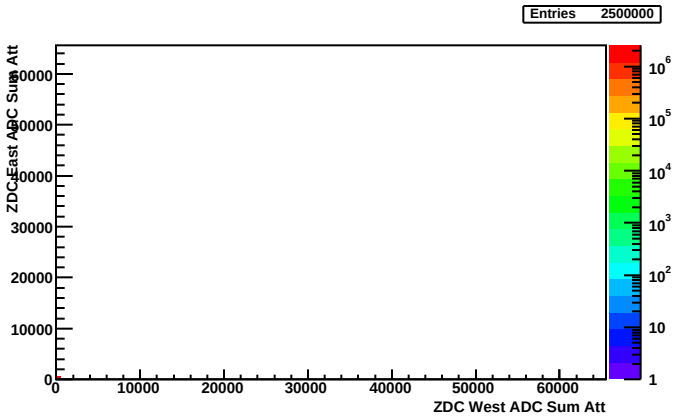
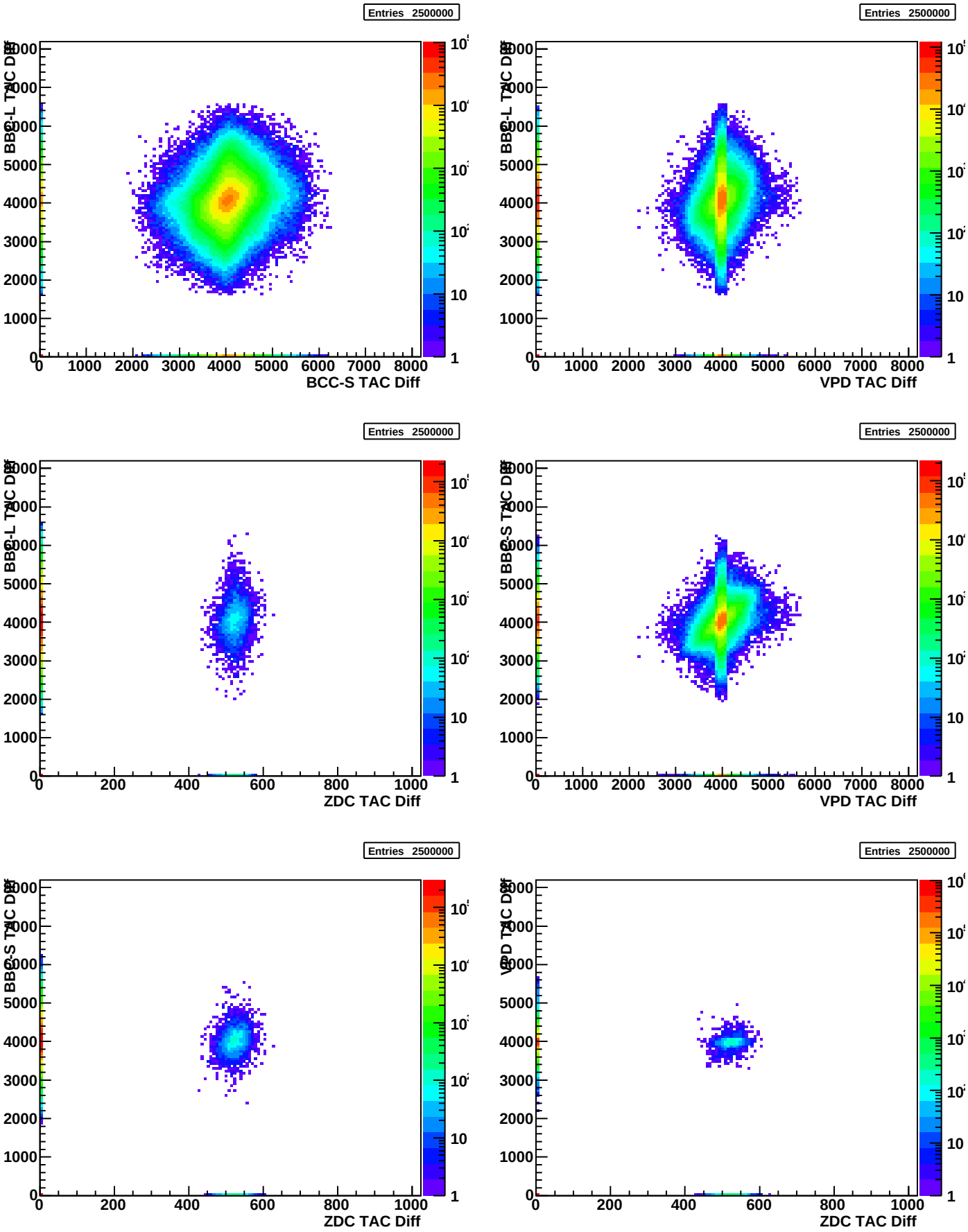
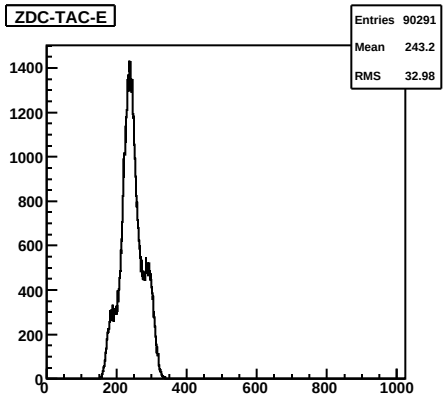
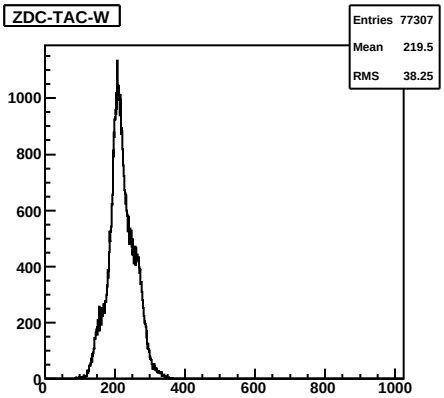
BBC-S-East ADC Sum
BBC-S-West ADC Sum

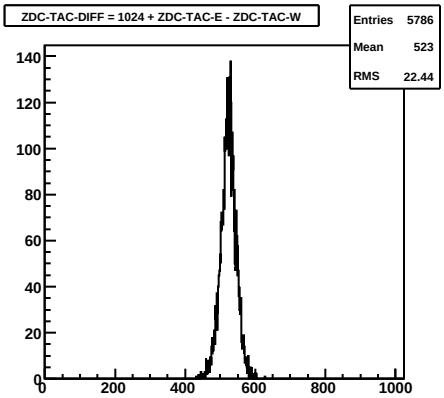

Entries 2500000
VPD East ADC Sum
VPD West ADC Sum

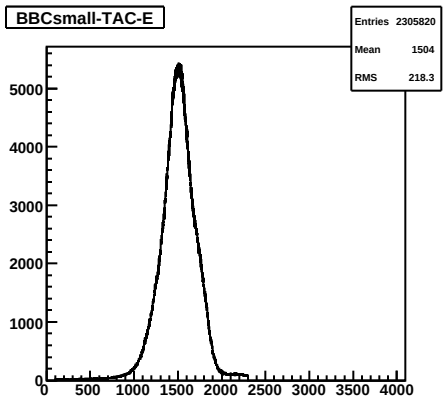

Entries 2500000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

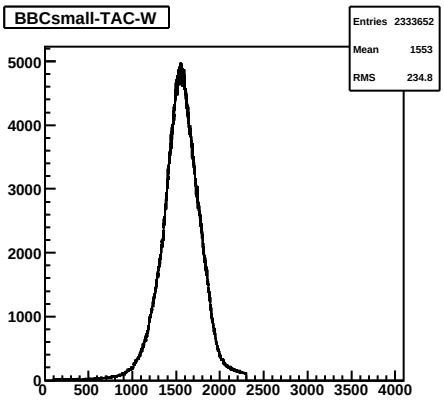

Entries 2500000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2500000
BBC-L TAC Diff
VPD TAC Diff
Entries 2500000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2500000
BBC-S TAC Diff
VPD TAC Diff
Entries 2500000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2500000
VPD TAC Diff
ZDC TAC Diff

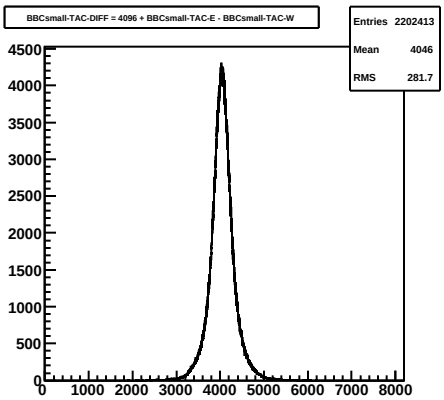

ZDC-TAC-E
Entries 90291
Mean 243.2
RMS 32.98

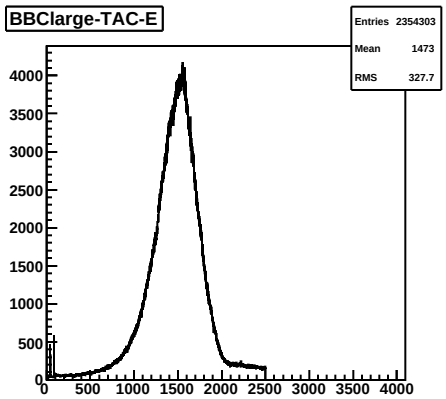

ZDC-TAC-W
Entries 77307
Mean 219.5
RMS 38.25

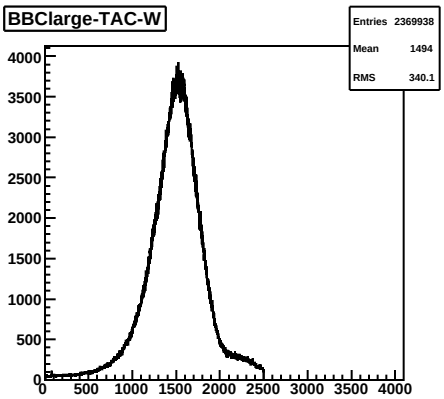

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5786
Mean 523
RMS 22.44

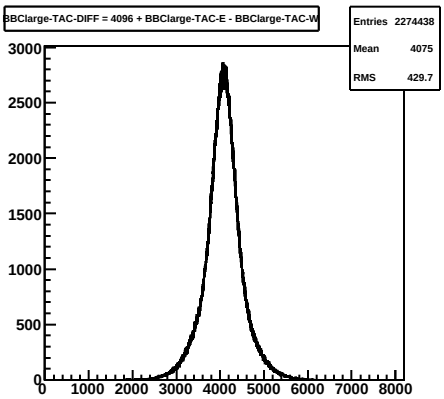

BBCsmall-TAC-E
Entries 2305820
Mean 1504
RMS 218.3

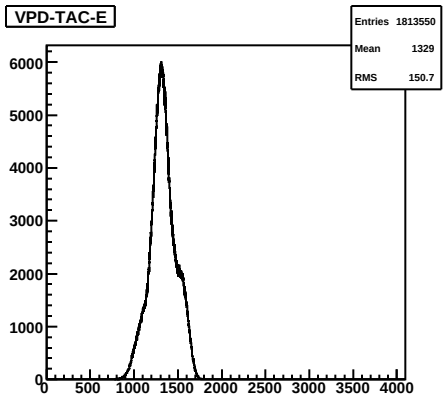

BBCsmall-TAC-W
Entries 2333652
Mean 1553
RMS 234.8

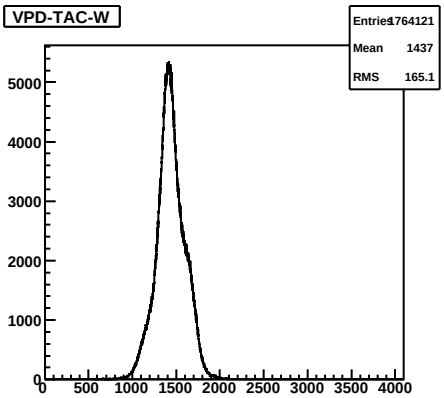

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2202413
Mean 4046
RMS 281.7

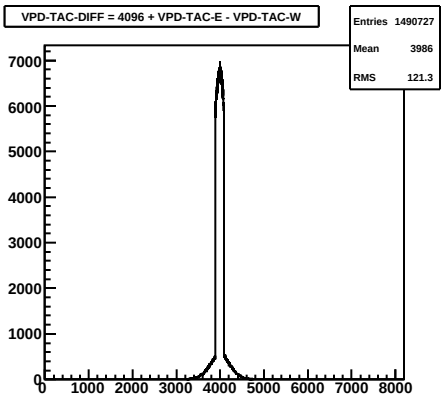

BBClarge-TAC-E
Entries 2354303
Mean 1473
RMS 327.7

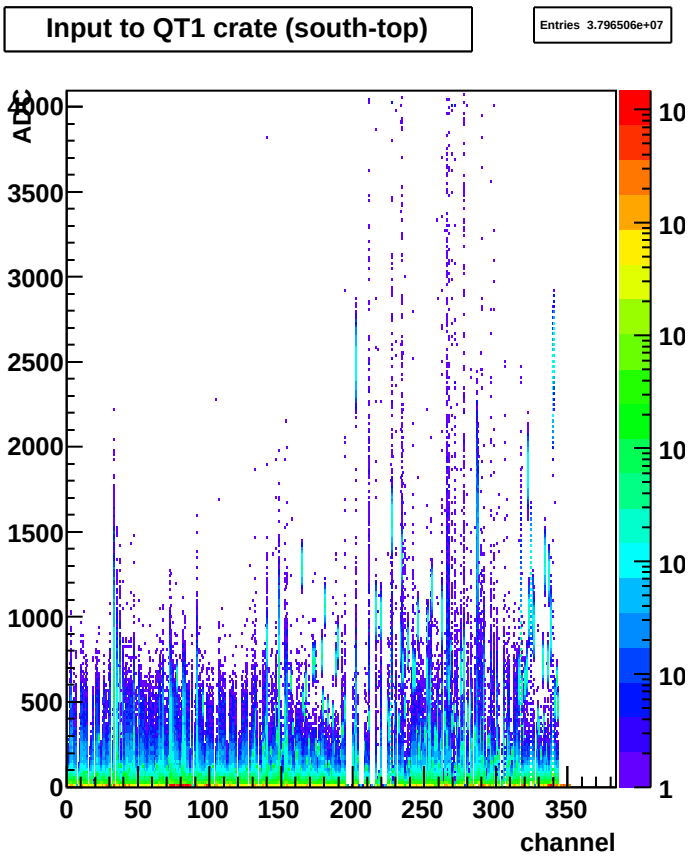

BBClarge-TAC-W
Entries 2369938
Mean 1494
RMS 340.1

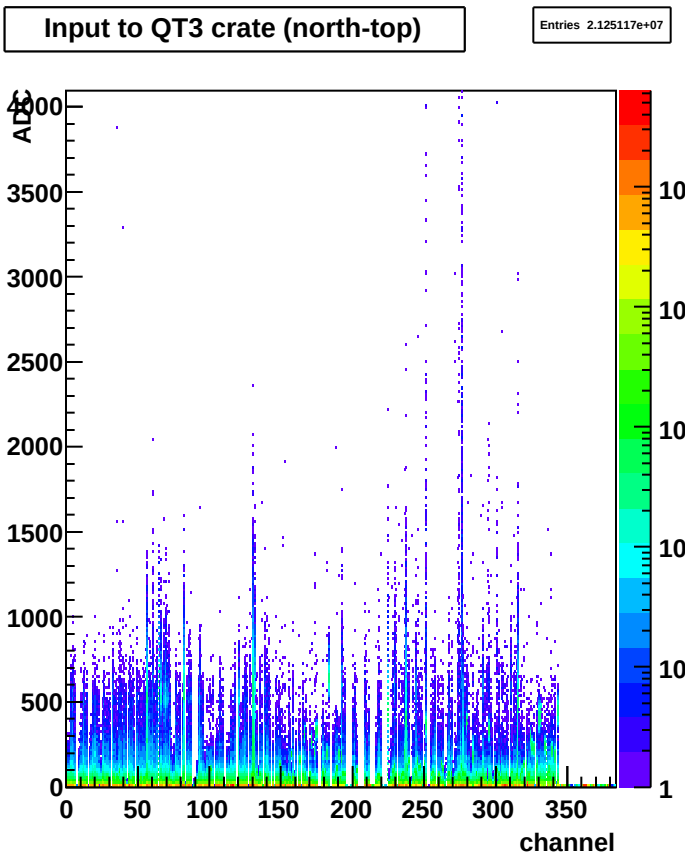

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2274438
Mean 4075
RMS 429.7

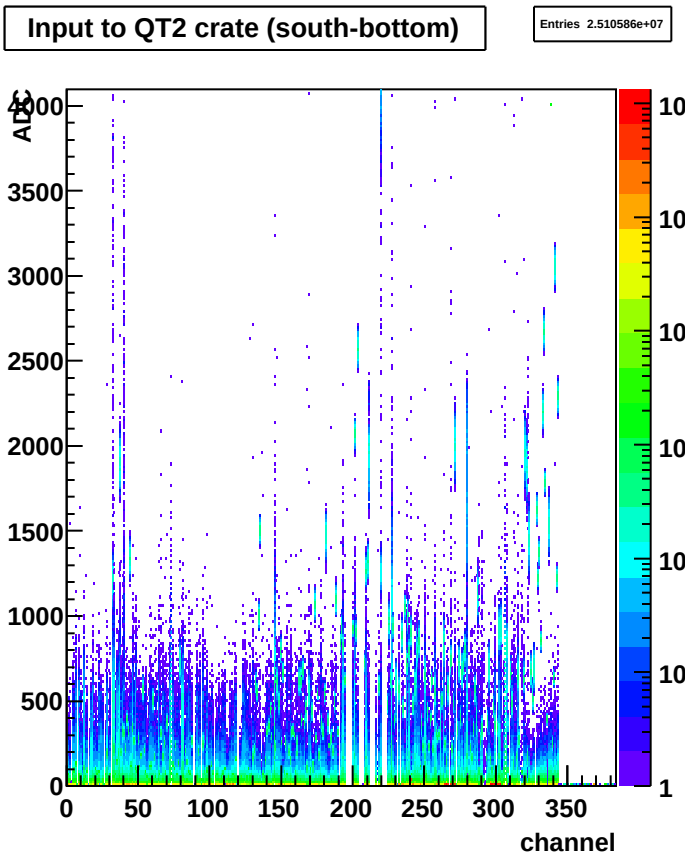

VPD-TAC-E
Entries 1813550
Mean 1329
RMS 150.7

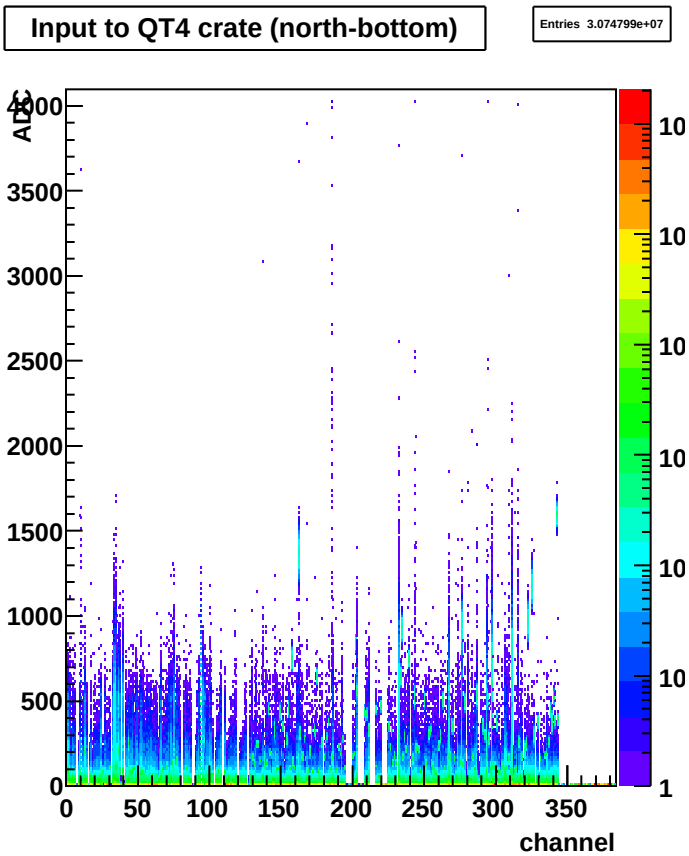

VPD-TAC-W
Entries 1764121
Mean 1437
RMS 165.1

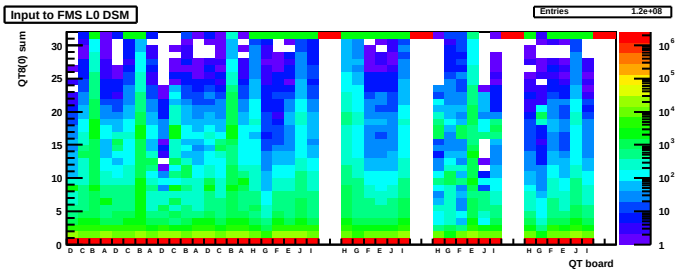

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1490727
Mean 3986
RMS 121.3

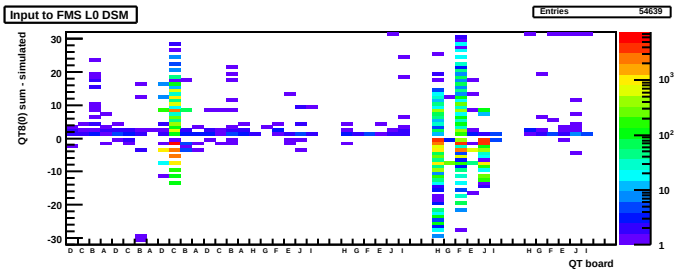

Input to QT1 crate (south-top)
Entries 3.796506e+07
ADC
channel

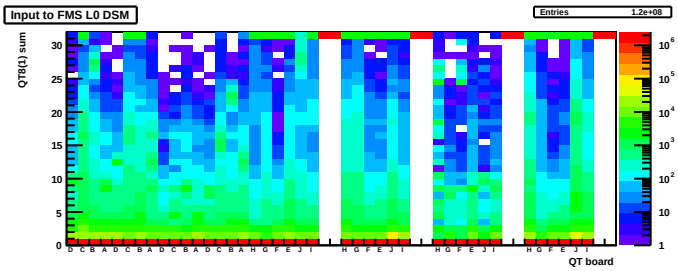

Input to QT3 crate (north-top)
Entries 2.125117e+07
ADC
channel

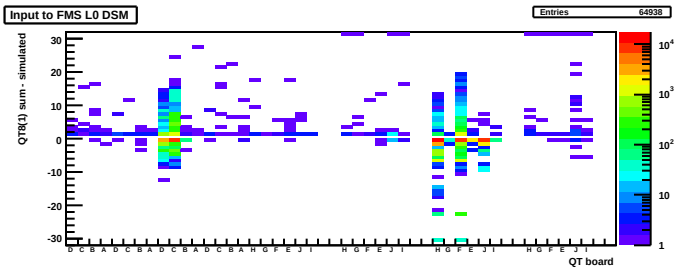

Input to QT2 crate (south-bottom)
Entries 2.510586e+07
ADC
channel

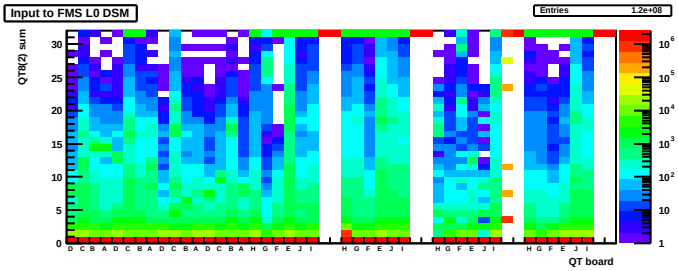

Input to QT4 crate (north-bottom)
Entries 3.074799e+07
ADC
channel

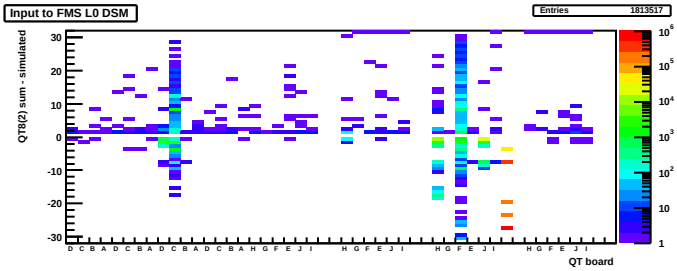

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

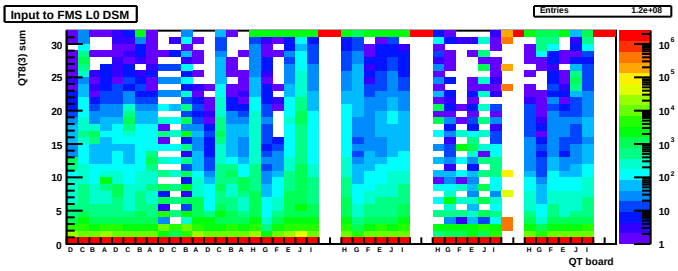

Input to FMS L0 DSM
Entries 54639
QT8(0) sum - simulated
QT board

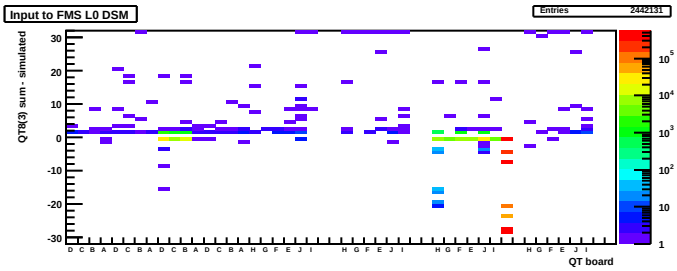

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

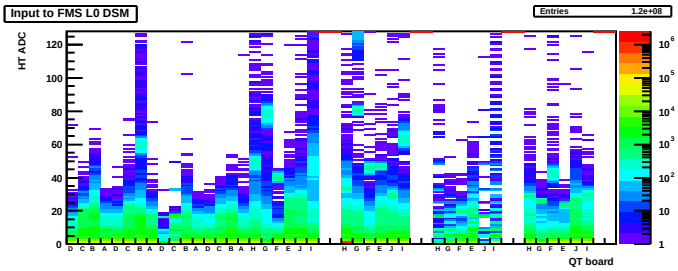

Input to FMS L0 DSM
Entries 64938
QT8(1) sum - simulated
QT board

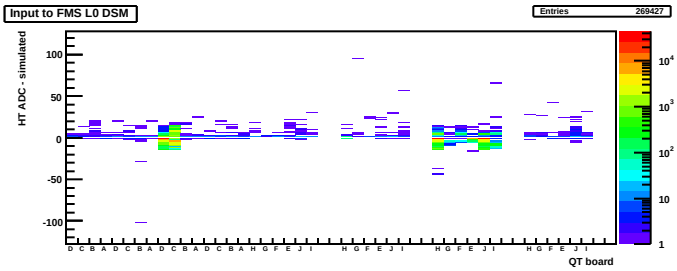

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

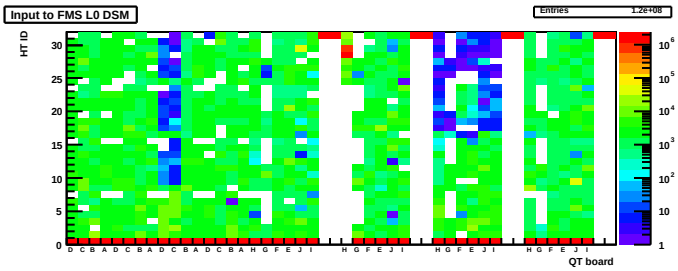

Input to FMS L0 DSM
Entries 1813517
QT8(2) sum - simulated
QT board

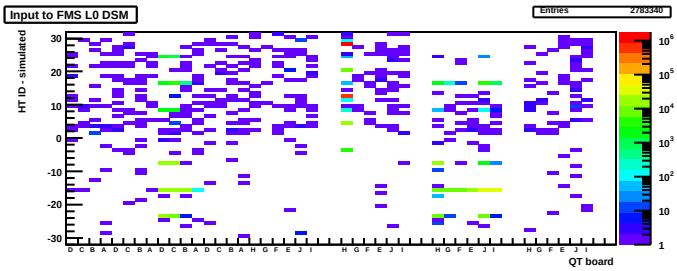

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

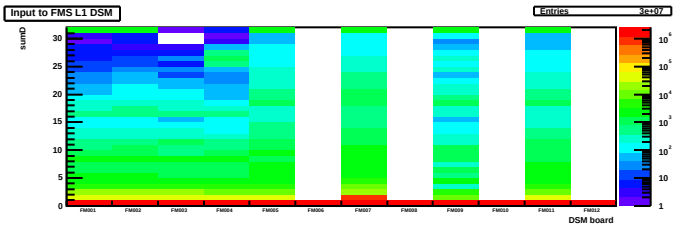

Input to FMS L0 DSM
Entries 2442131
QT8(3) sum - simulated
QT board

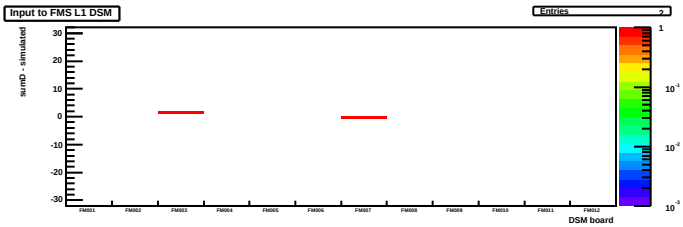

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

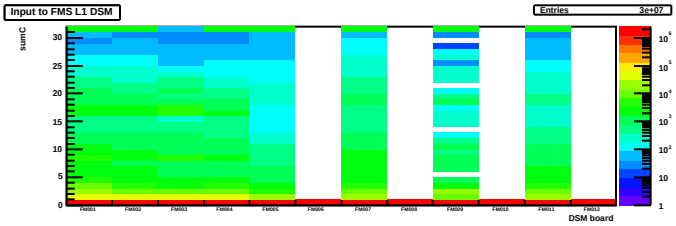

Input to FMS L0 DSM
Entries 269427
HT ADC - simulated
QT board

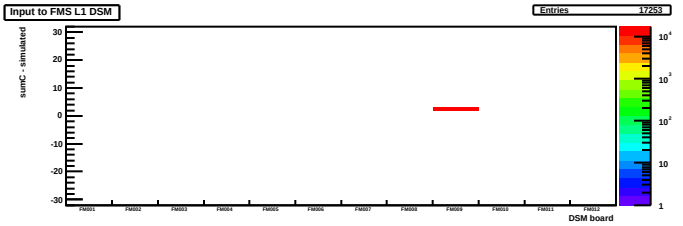

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

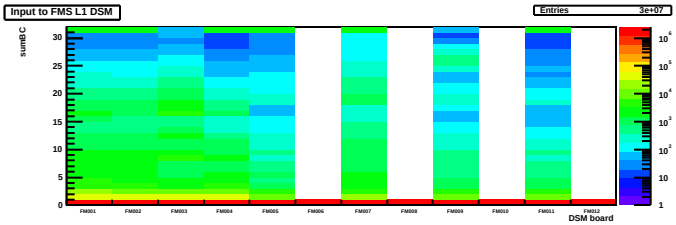

Input to FMS L0 DSM
Entries 2783340
HT ID - simulated
QT board

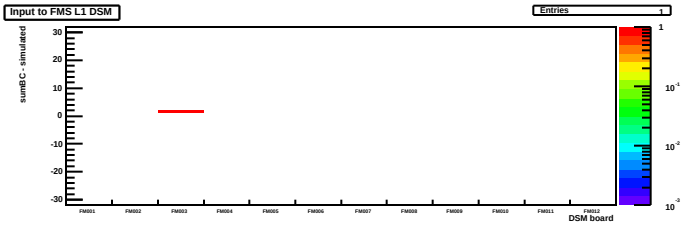

Input to FMS L1 DSM
Entries 3e+07
sumD
DSM board

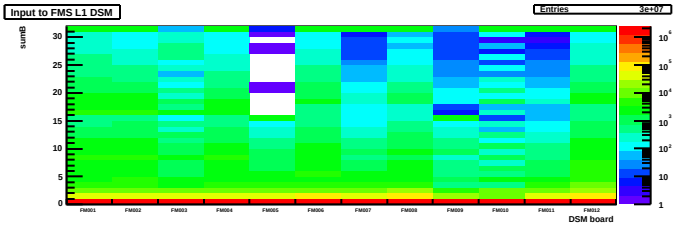

Input to FMS L1 DSM
Entries 2
sumD - simulated
DSM board

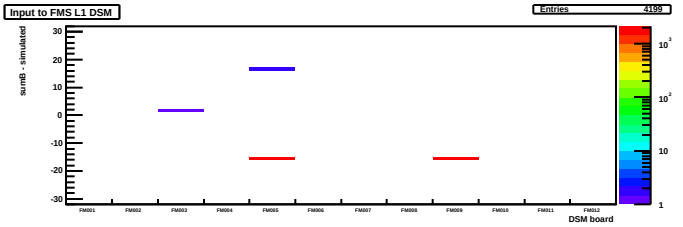

Input to FMS L1 DSM
Entries 3e+07
sumC
DSM board

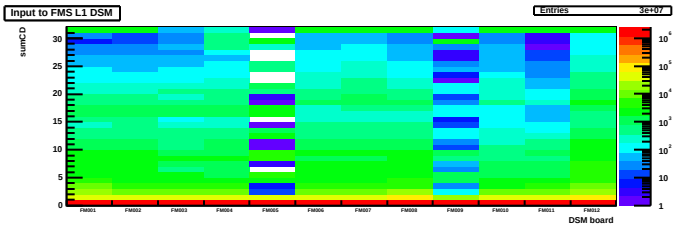

Input to FMS L1 DSM
Entries 17253
sumC - simulated
DSM board

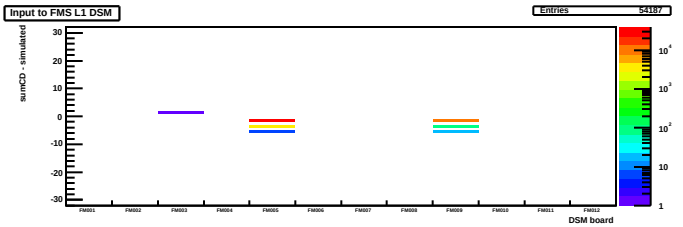

Input to FMS L1 DSM
Entries 3e+07
sumBC
DSM board

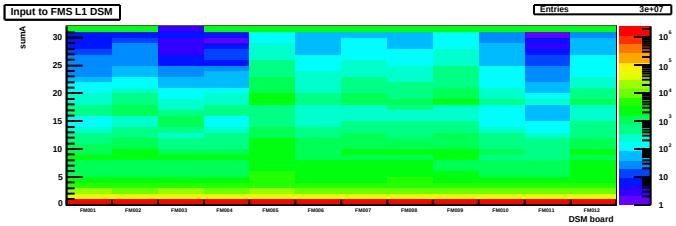

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

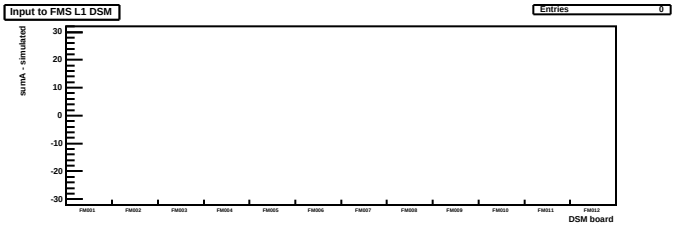

Input to FMS L1 DSM
Entries 3e+07
sumB
DSM board

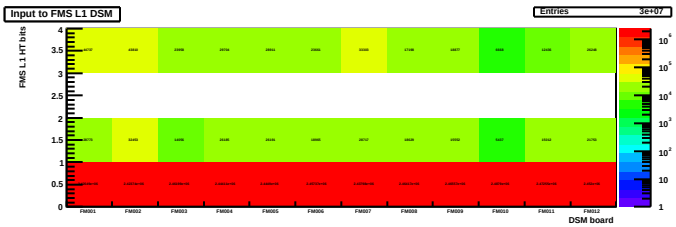

Input to FMS L1 DSM
Entries 4199
sumB - simulated
DSM board

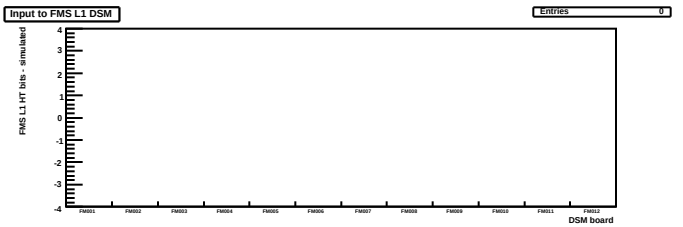

Input to FMS L1 DSM
Entries 3e+07
sumCD
DSM board

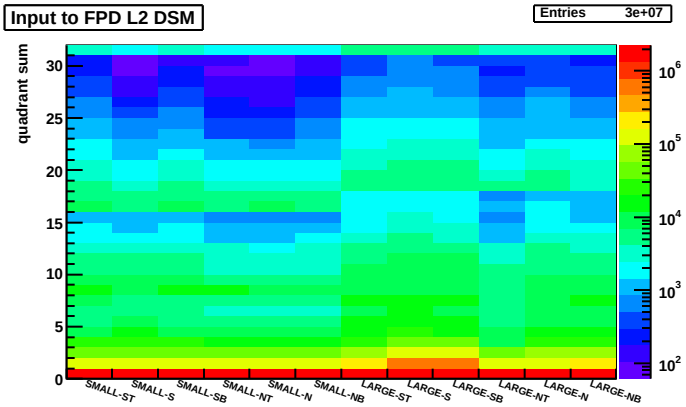

Input to FMS L1 DSM
Entries 54187
sumCD - simulated
DSM board

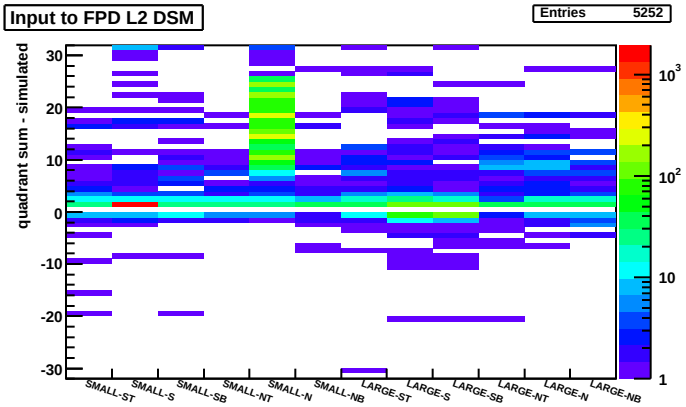

Input to FMS L1 DSM
Entries 3e+07
sumA
DSM board

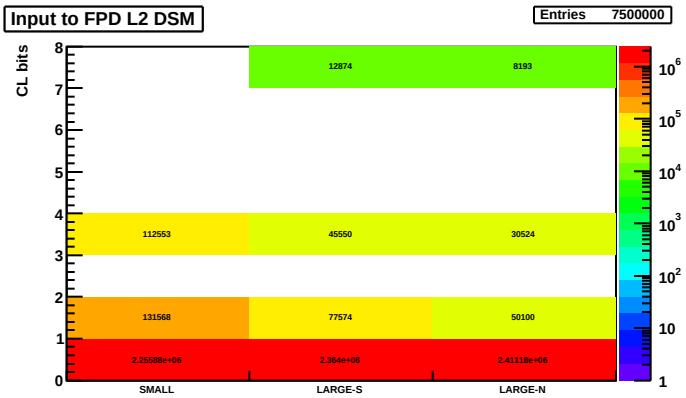

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

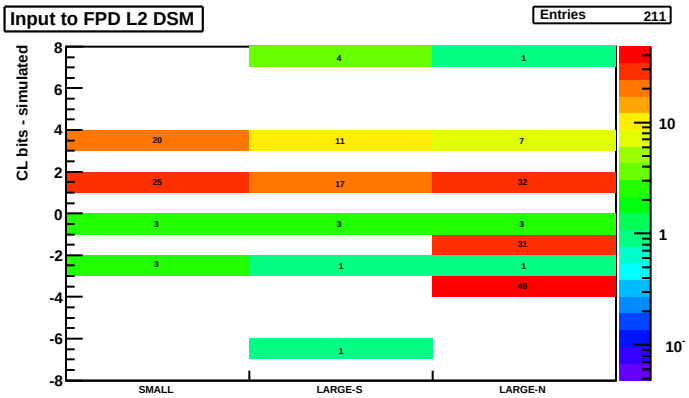

Input to FMS L1 DSM
Entries 3e+07
FMS L1 HT bits
DSM board

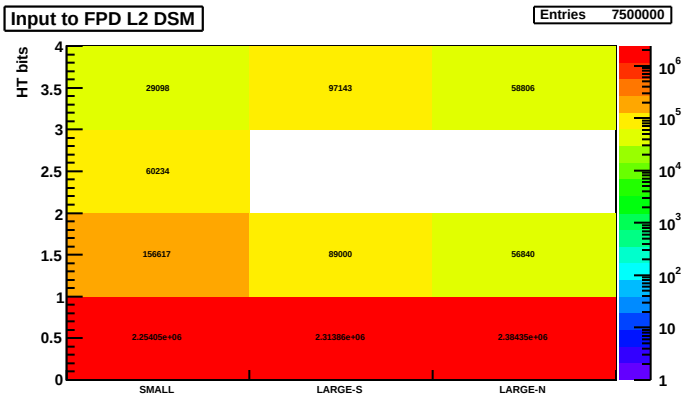

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

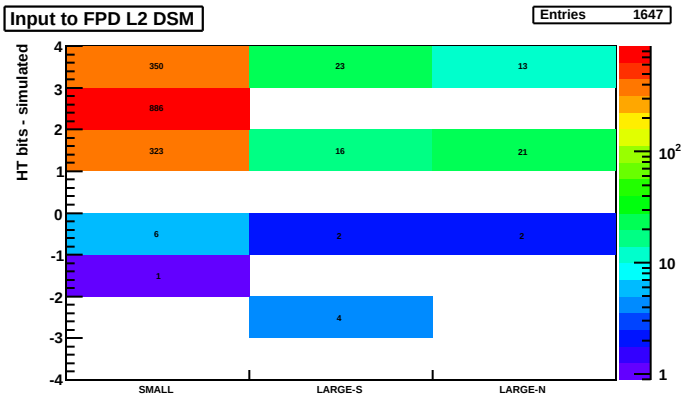

Input to FPD L2 DSM
Entries 3e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

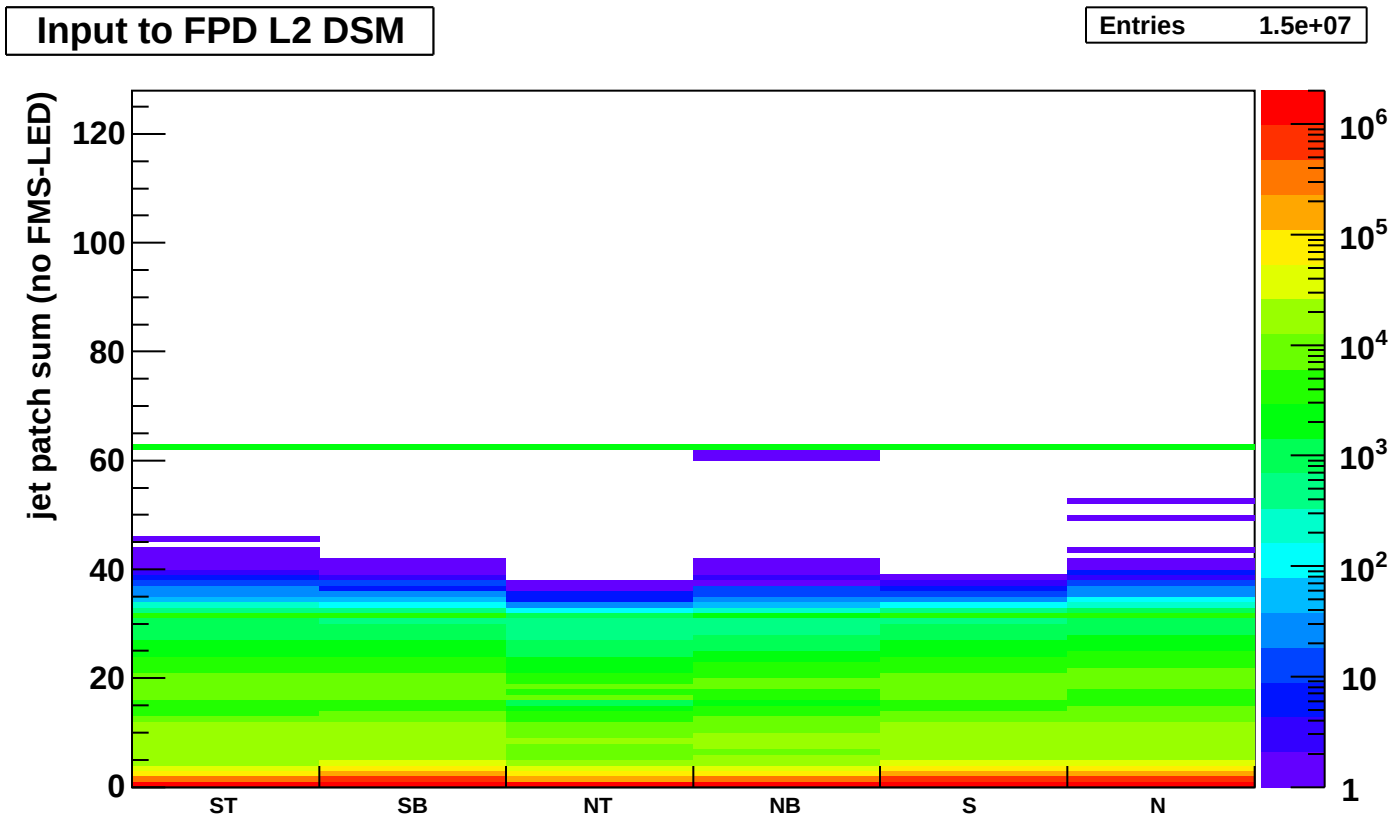

Input to FPD L2 DSM
Entries 5252
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

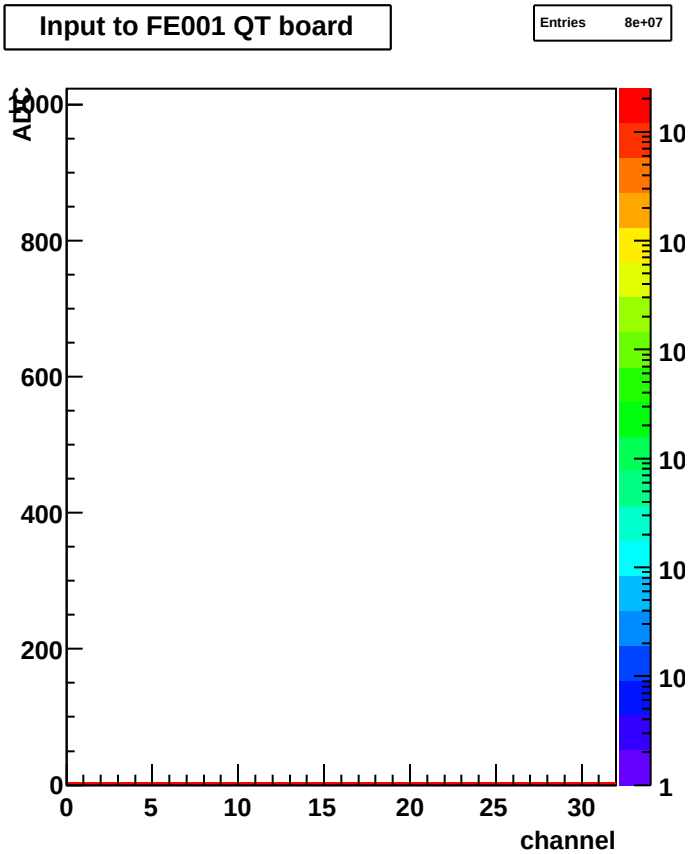

Input to FPD L2 DSM
Entries 7500000
CL bits
12874
8193
112553
45550
30524
131568
77574
50100
2.25588e+06
2.364e+06
2.41118e+06
SMALL
LARGE-S
LARGE-N

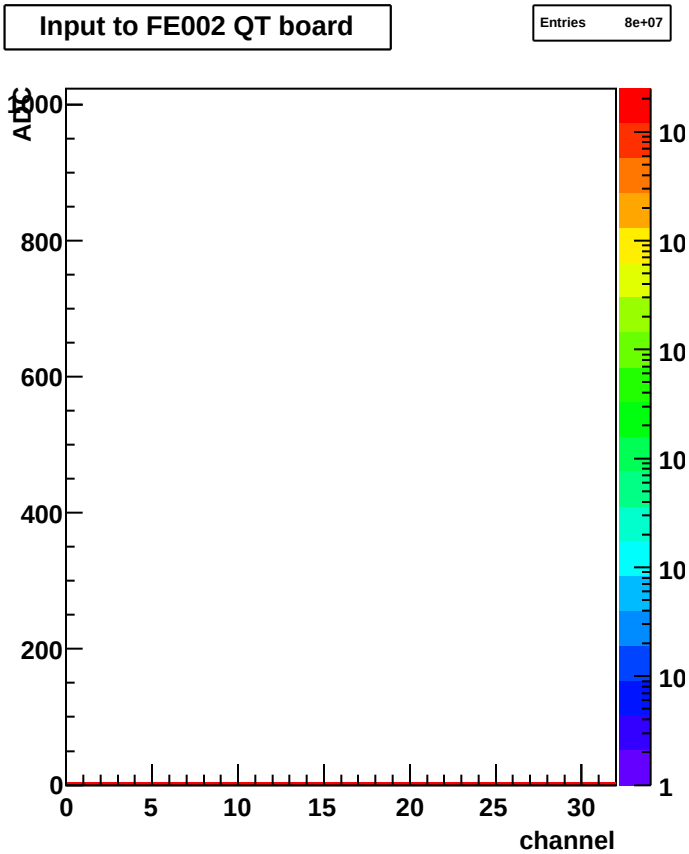

Input to FPD L2 DSM
Entries 211
CL bits - simulated
4
1
20
11
7
25
17
32
3
3
3
31
3
1
1
48
1
SMALL
LARGE-S
LARGE-N

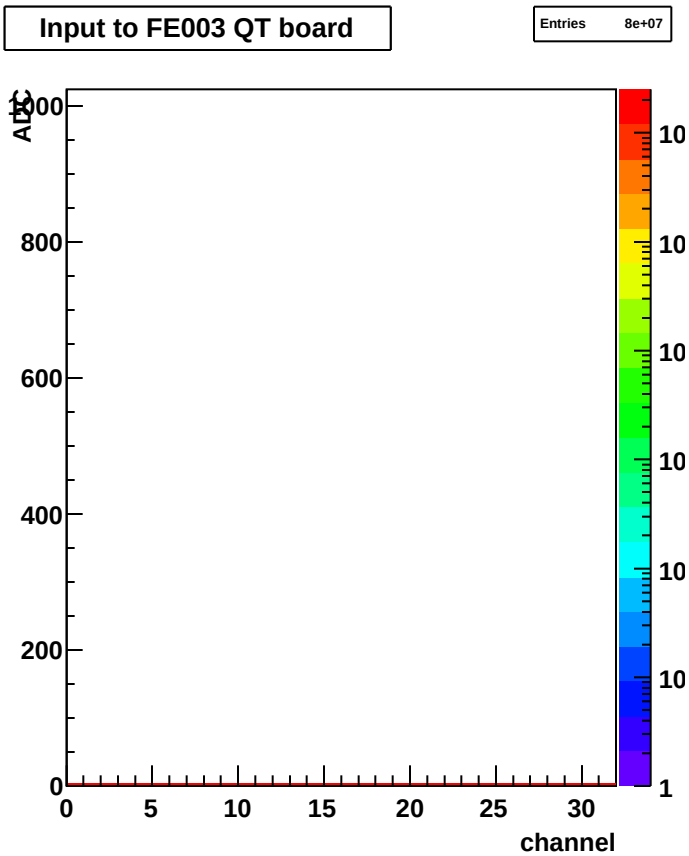

Input to FPD L2 DSM
Entries 7500000
HT bits
29098
97143
58806
60234
156617
89000
56840
2.25405e+06
2.31386e+06
2.38435e+06
SMALL
LARGE-S
LARGE-N

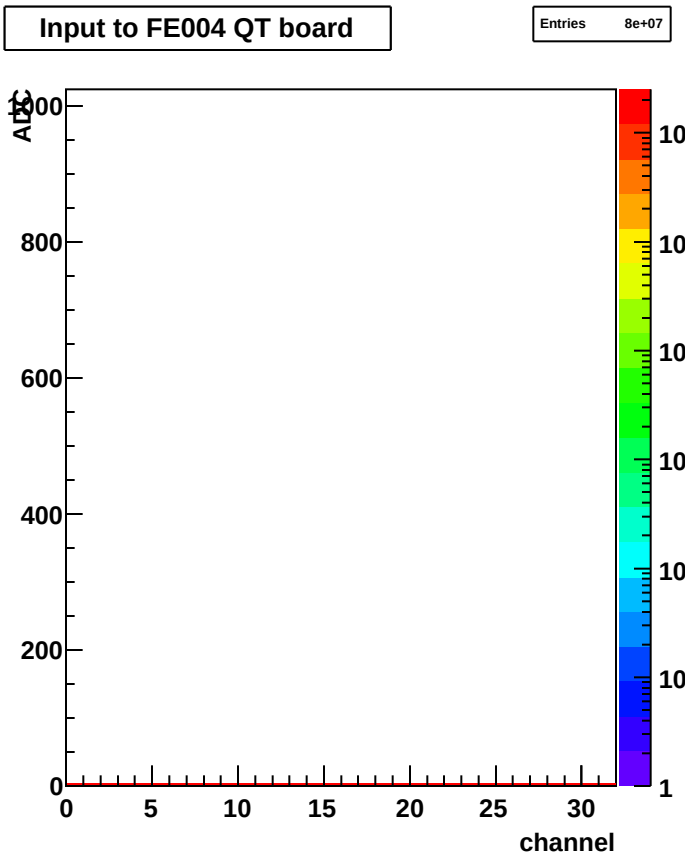

Input to FPD L2 DSM
Entries 1647
HT bits - simulated
350
23
13
886
323
16
21
6
2
2
1
4
SMALL
LARGE-S
LARGE-N

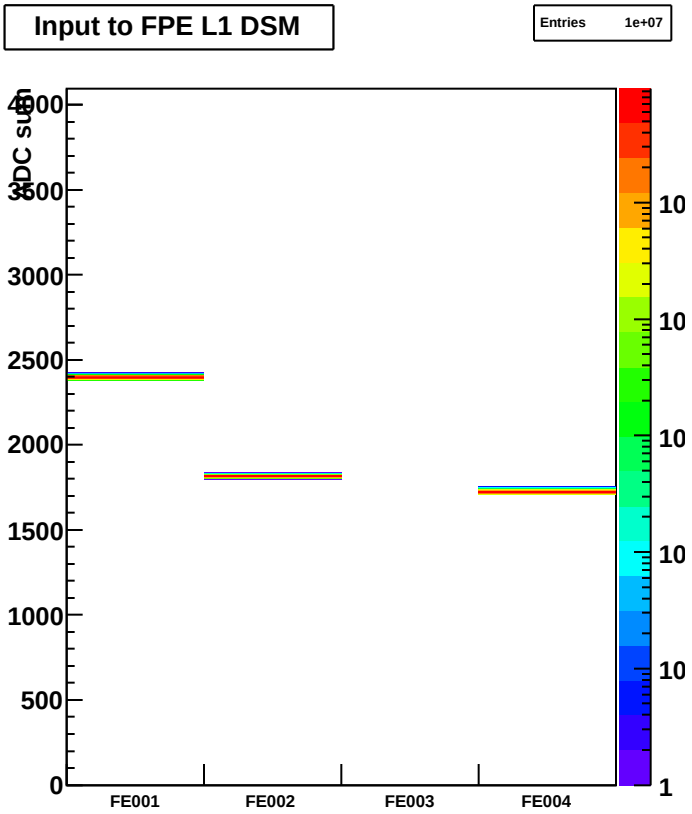

Input to FPD L2 DSM
Entries 1.5e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

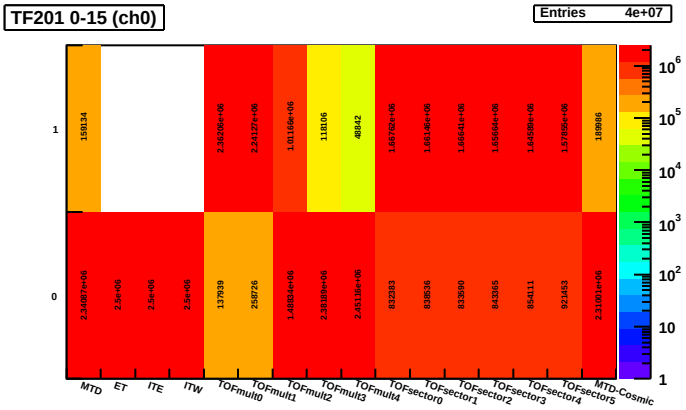

Input to FE001 QT board
Entries 8e+07
ADC
channel

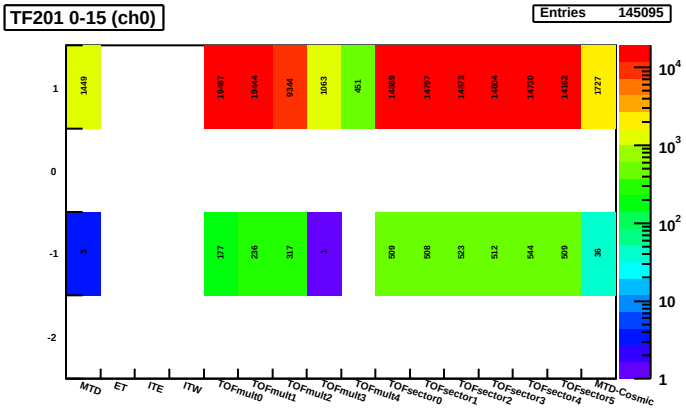

Input to FE002 QT board
Entries 8e+07
ADC
channel

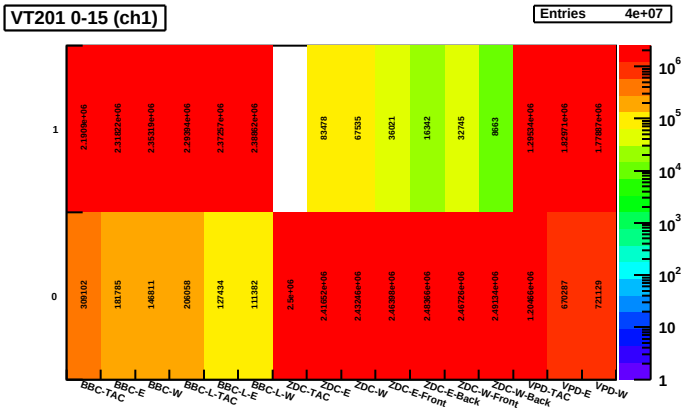

Input to FE003 QT board
Entries 8e+07
ADC
channel

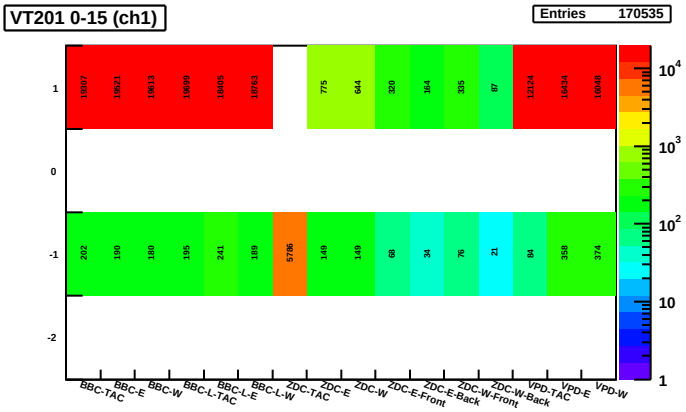

Input to FE004 QT board
Entries 8e+07
ADC
channel

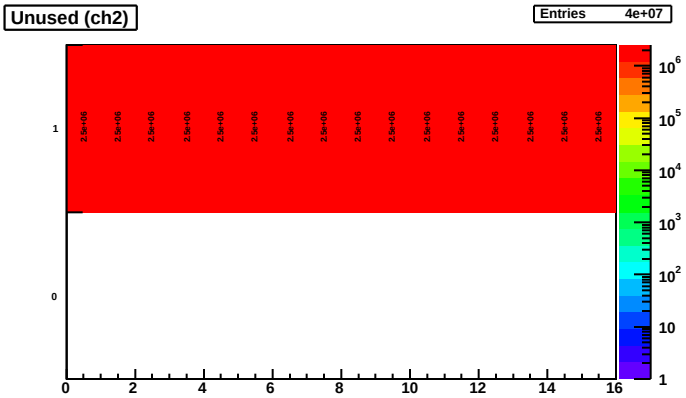

Input to FPE L1 DSM
Entries 1e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

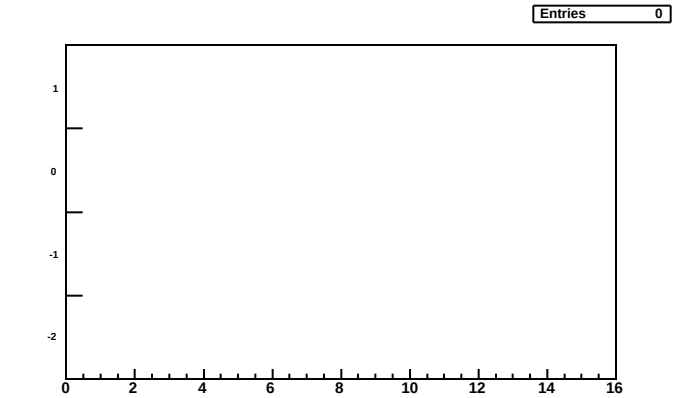

TF201 0-15 (ch0)
Entries 4e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

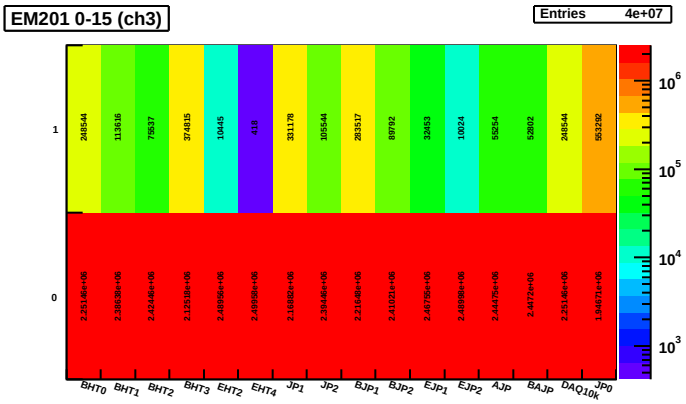

TF201 0-15 (ch0)
Entries 145095
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 170535
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 4e+07


Entries 0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Entries 4e+07
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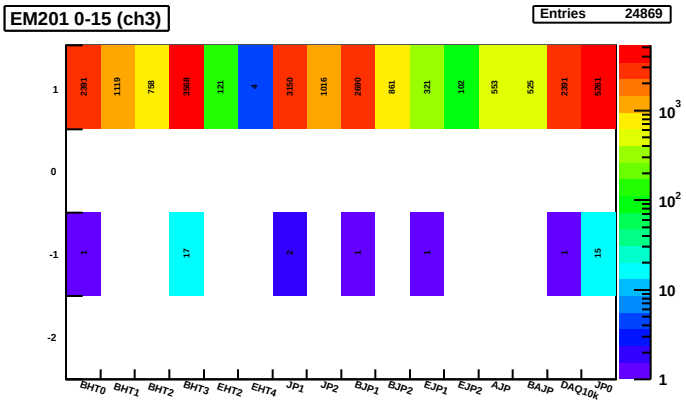
EM201 0-15 (ch3)
Entries 24869
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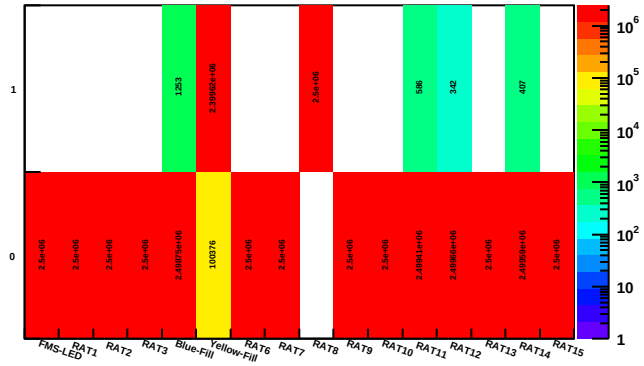
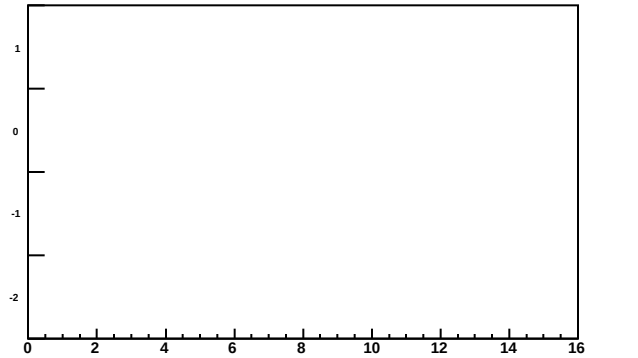
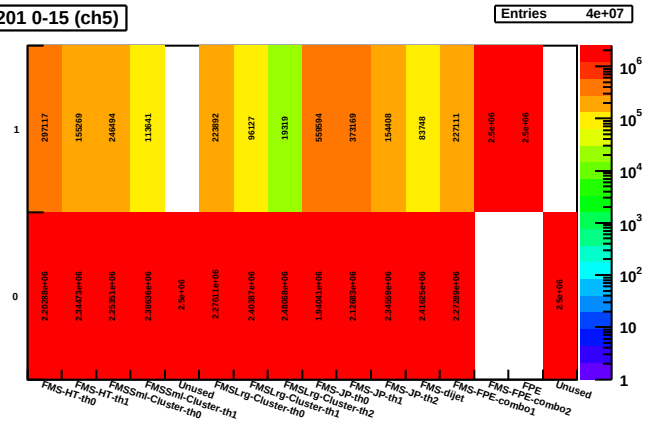
RAT board (ch4)





FP201 0-15 (ch5)



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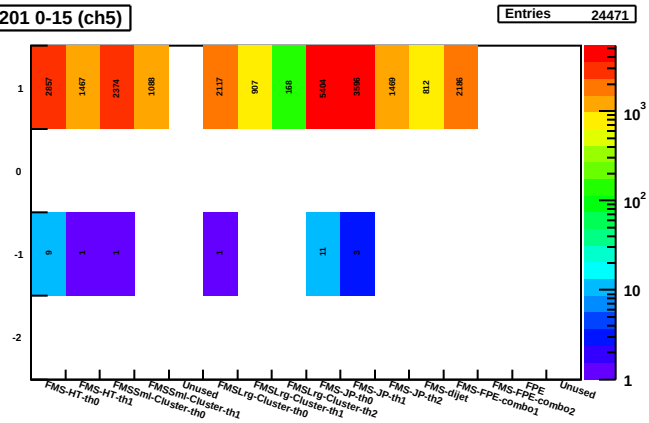
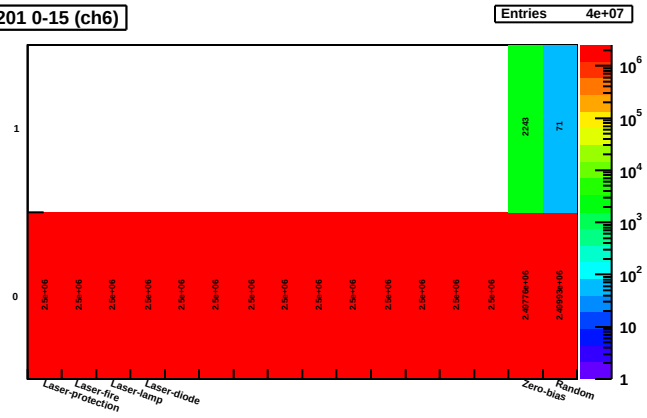
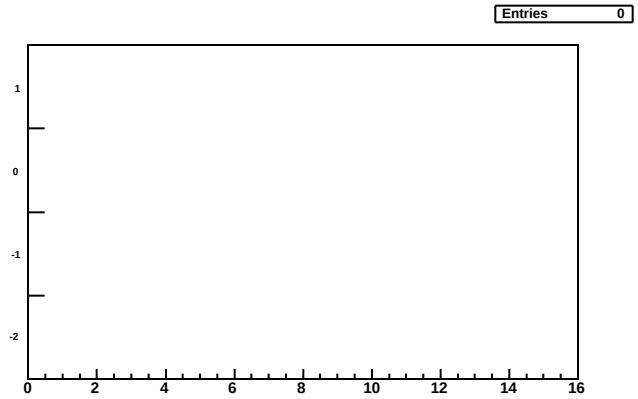
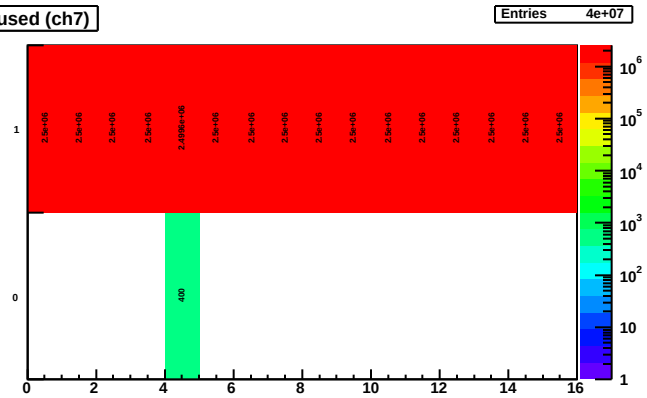
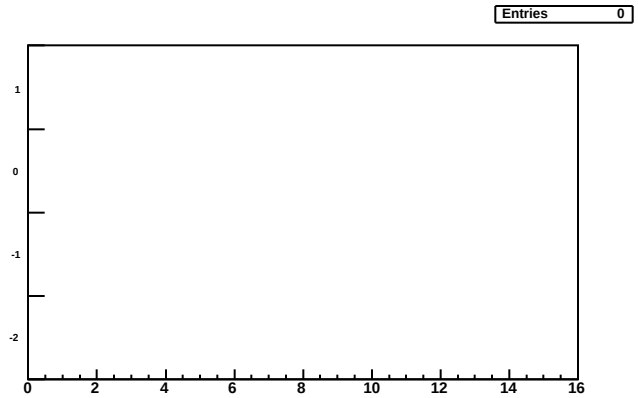


ST201 0-15 (ch6)





Unused (ch7)