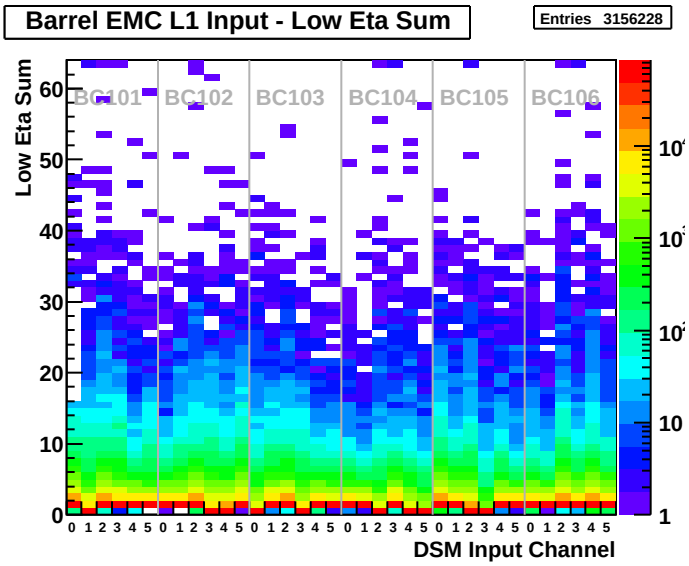
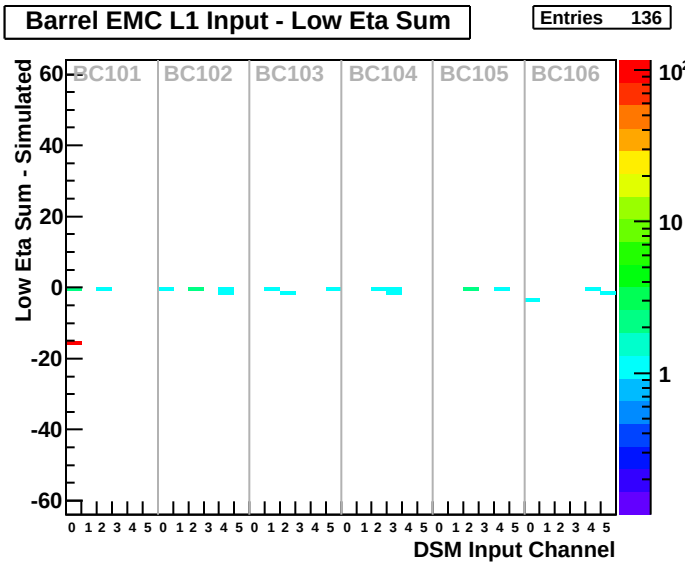
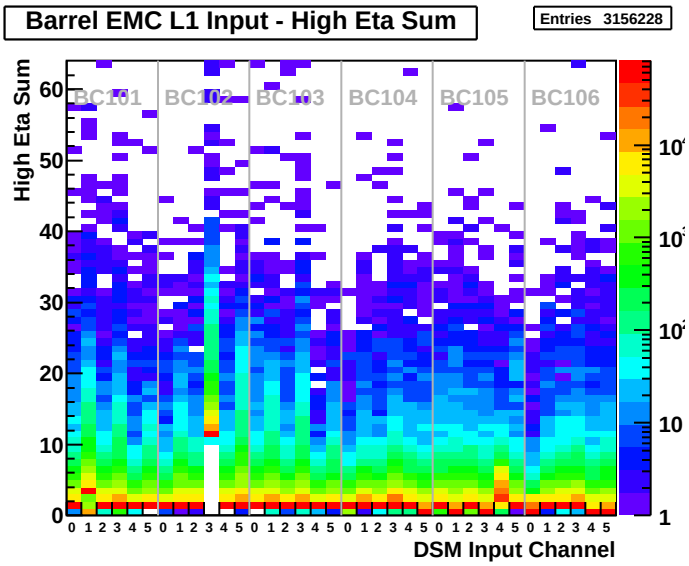
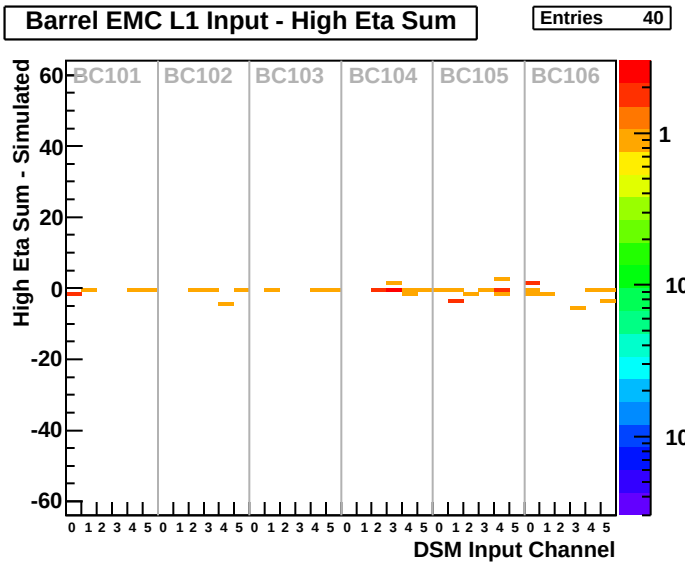
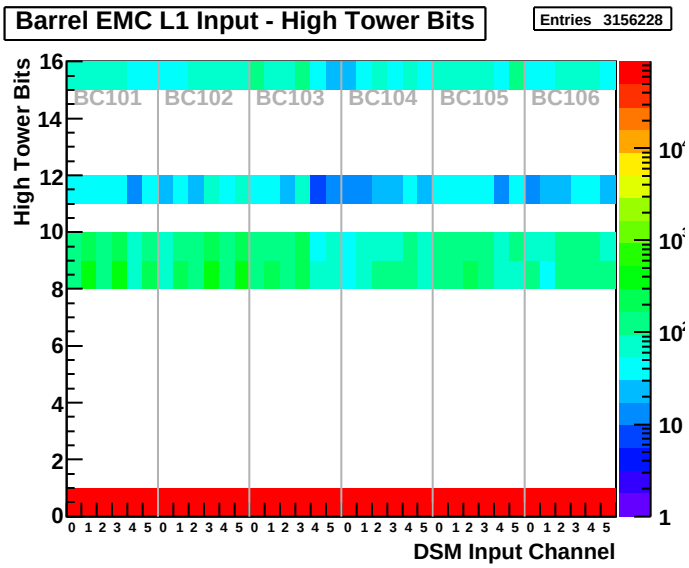
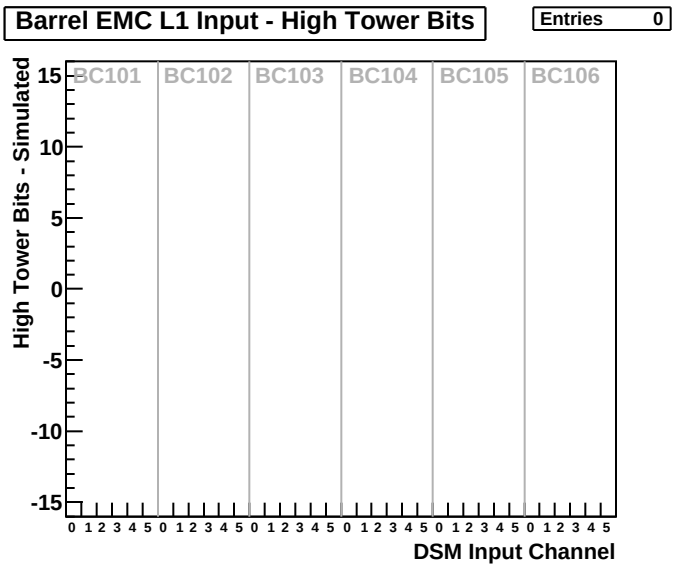

Barrel EMC L1 Input - Low Eta Sum
Entries 3156228
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

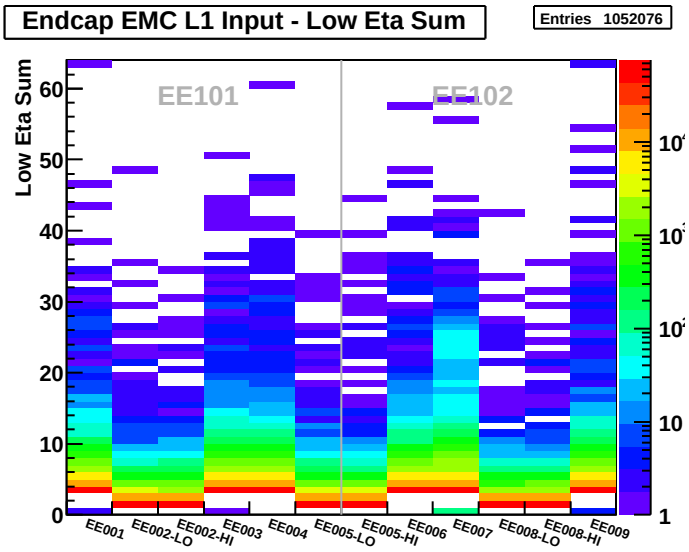

Barrel EMC L1 Input - Low Eta Sum
Entries 136
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

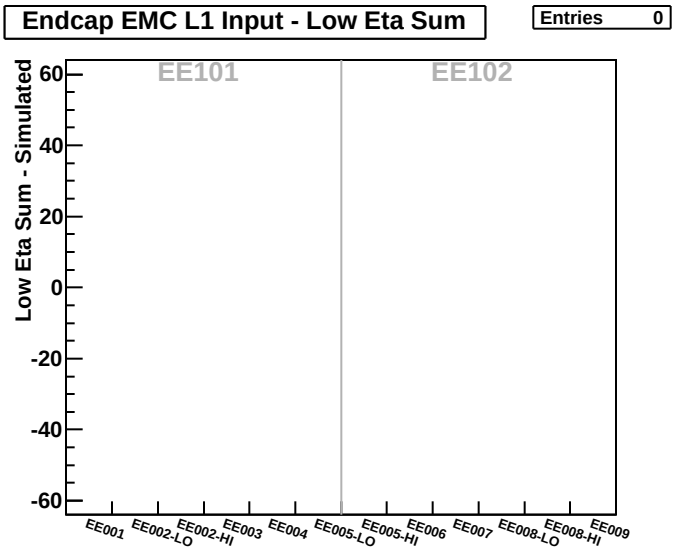

Barrel EMC L1 Input - High Eta Sum
Entries 3156228
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

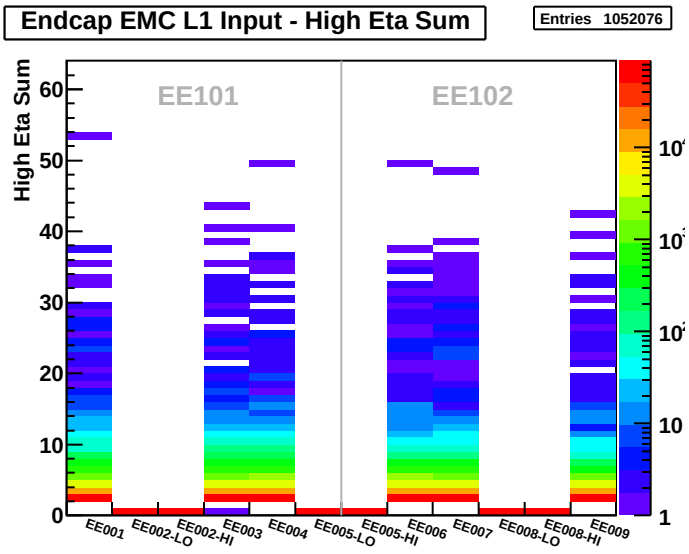

Barrel EMC L1 Input - High Eta Sum
Entries 40
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

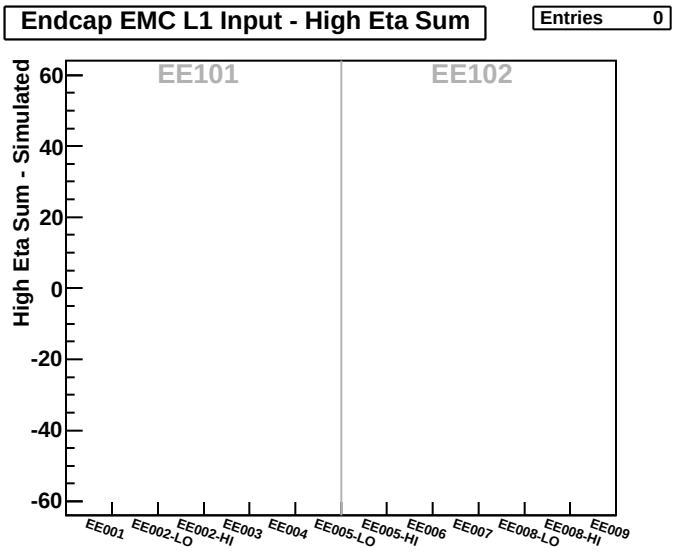

Barrel EMC L1 Input - High Tower Bits
Entries 3156228
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

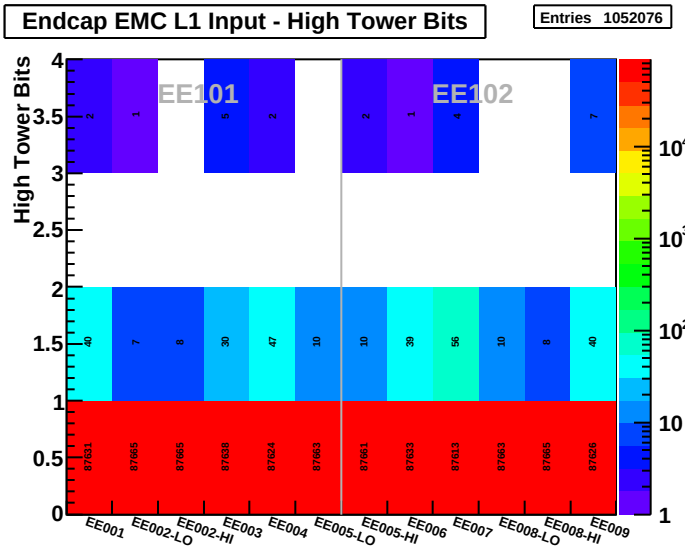

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

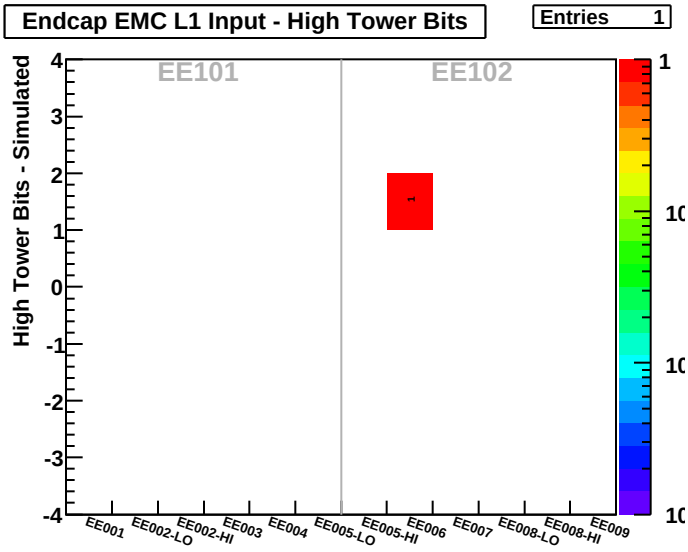

Endcap EMC L1 Input - Low Eta Sum
Entries 1052076
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

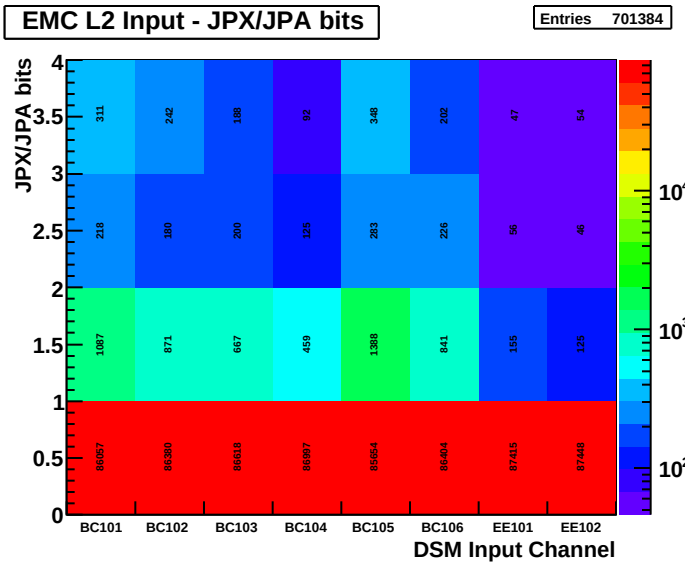

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

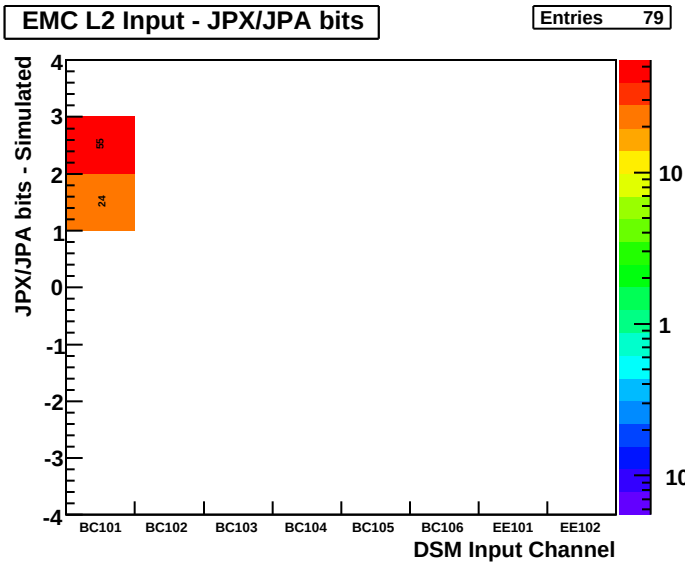

Endcap EMC L1 Input - High Eta Sum
Entries 1052076
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

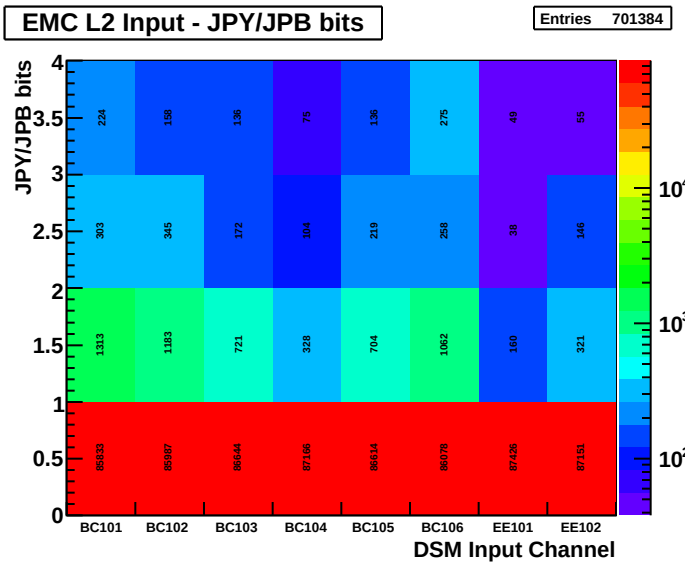

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

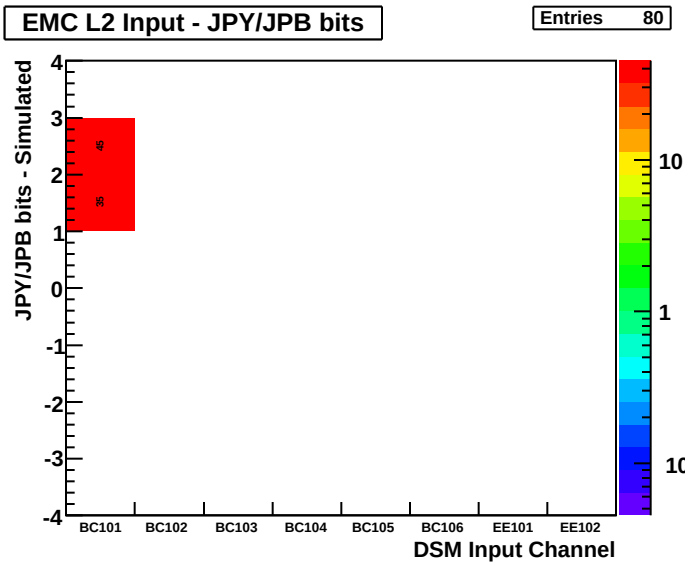

Endcap EMC L1 Input - High Tower Bits
Entries 1052076
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

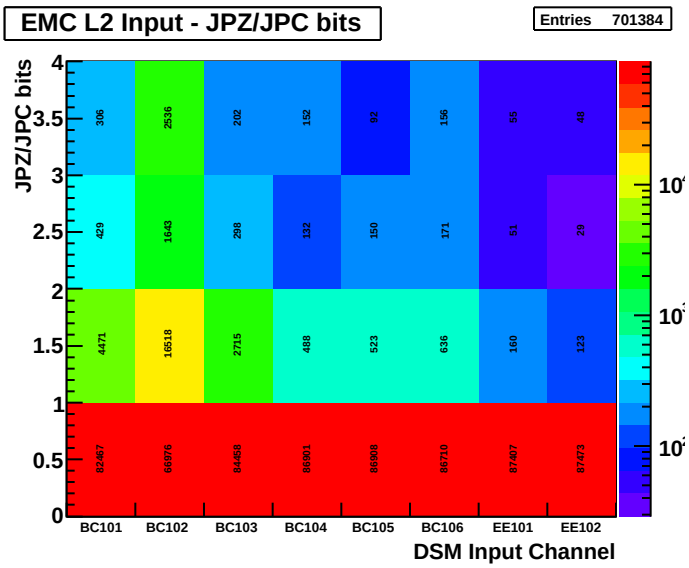

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

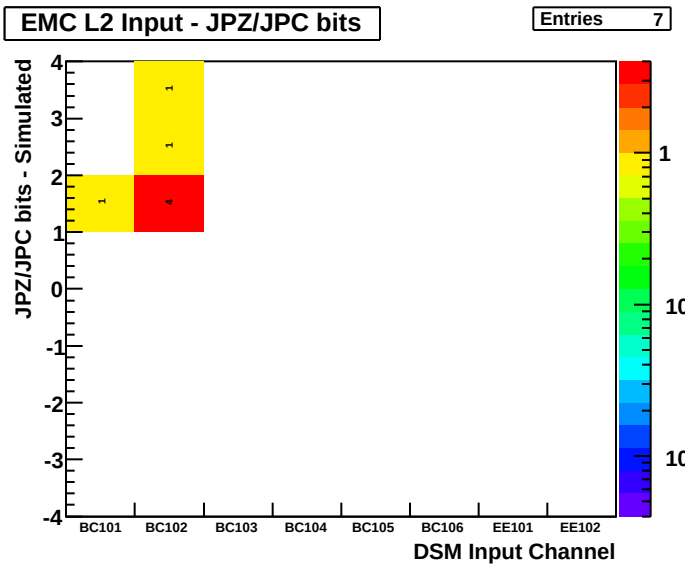

EMC L2 Input - JPX/JPA bits
Entries 701384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
311 242 188 92 348 202 47 54
218 180 200 125 283 226 56 46
1067 871 667 459 1388 841 155 125
86057 86380 86618 86997 85654 86404 87415 87448

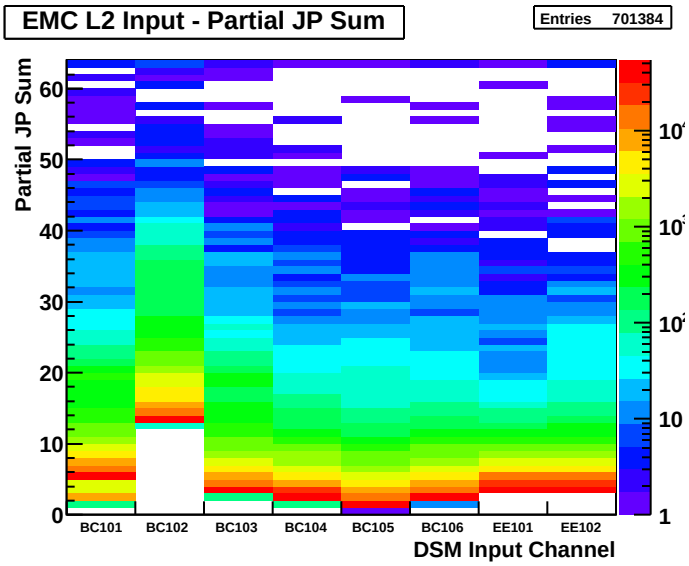

EMC L2 Input - JPX/JPA bits
Entries 79
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
55
24

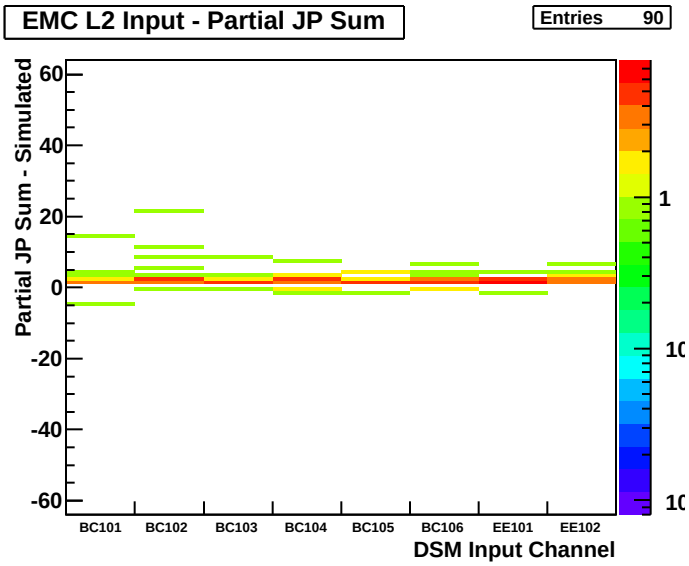

EMC L2 Input - JPY/JPB bits
Entries 701384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
224 158 136 75 136 275 49 55
303 345 172 104 219 258 38 146
1313 1183 721 328 704 1062 160 321
85833 85987 86644 87166 86614 86078 87426 87151

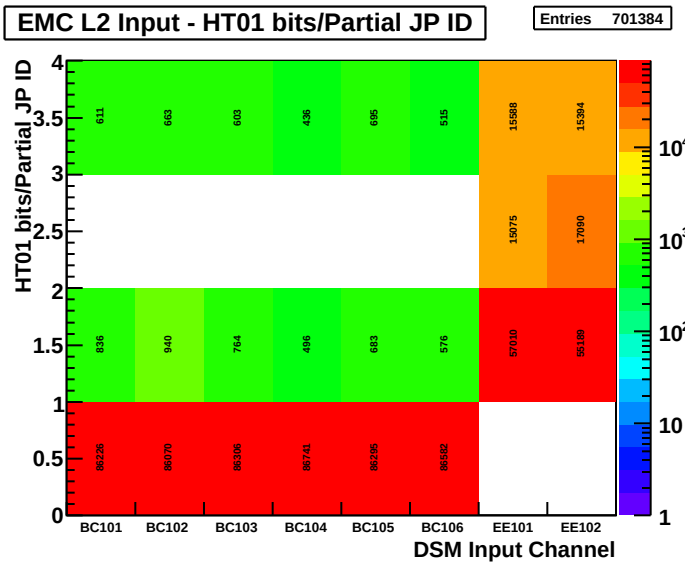

EMC L2 Input - JPY/JPB bits
Entries 80
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
45
35

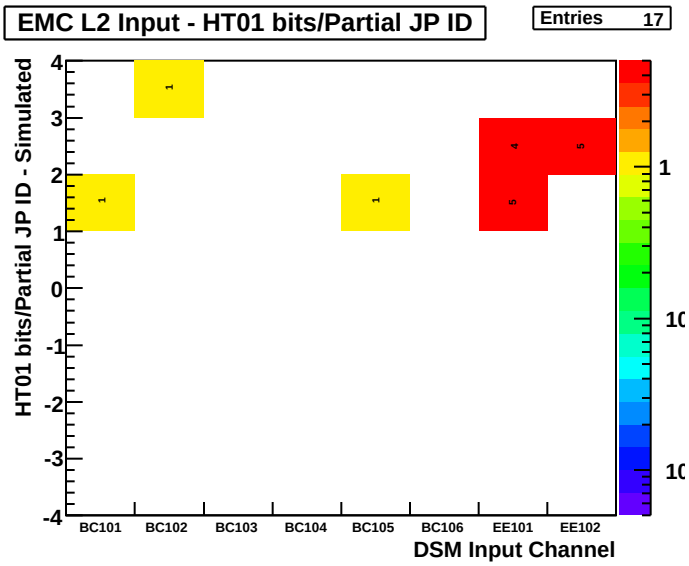

EMC L2 Input - JPZ/JPC bits
Entries 701384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
306 2536 202 152 92 156 55 48
429 1643 298 132 150 171 51 29
4471 16518 2715 488 523 636 160 123
82467 66976 84458 86901 86908 86710 87407 87473

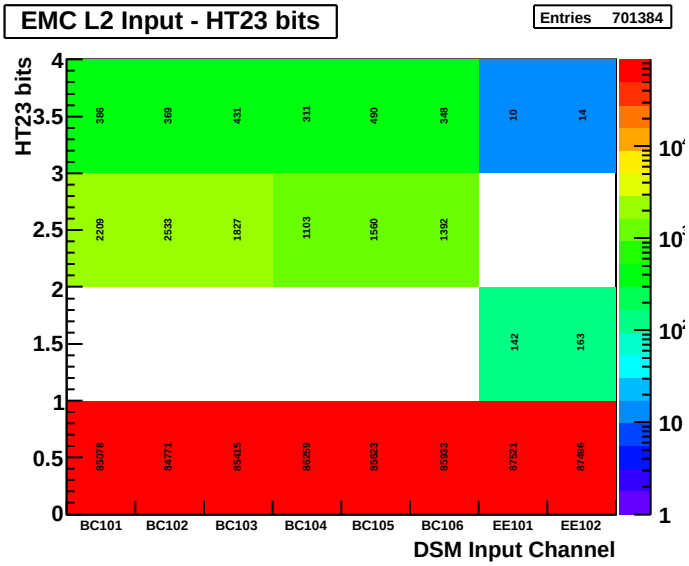

EMC L2 Input - JPZ/JPC bits
Entries 7
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1
1
4

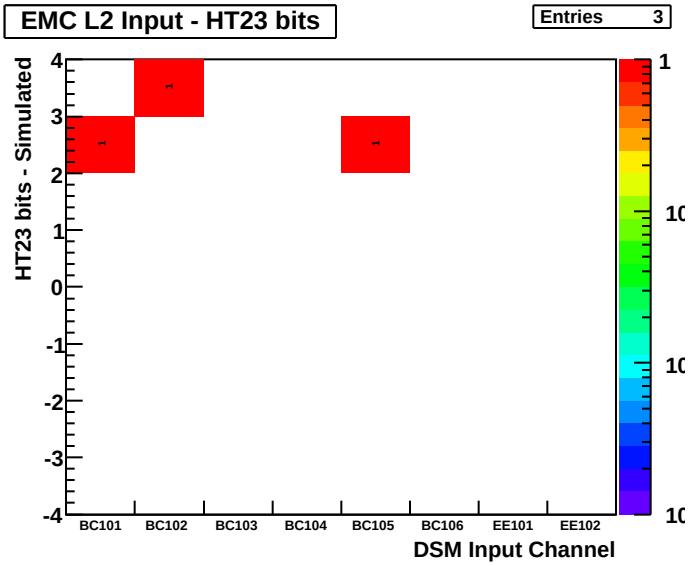

EMC L2 Input - Partial JP Sum
Entries 701384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

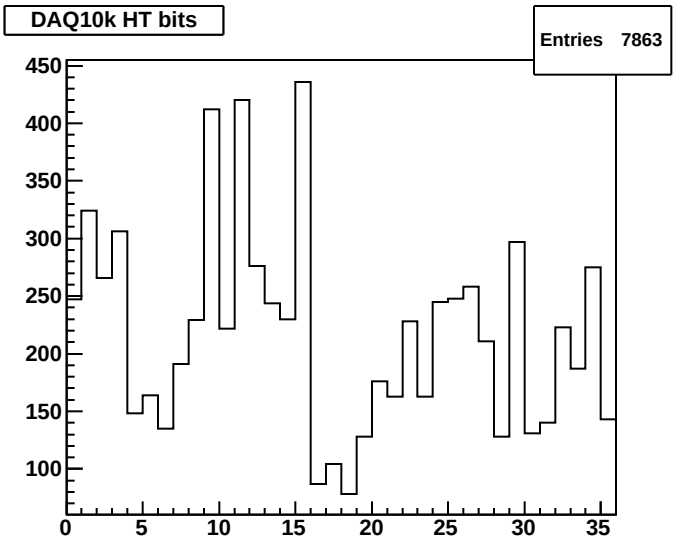

EMC L2 Input - Partial JP Sum
Entries 90
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

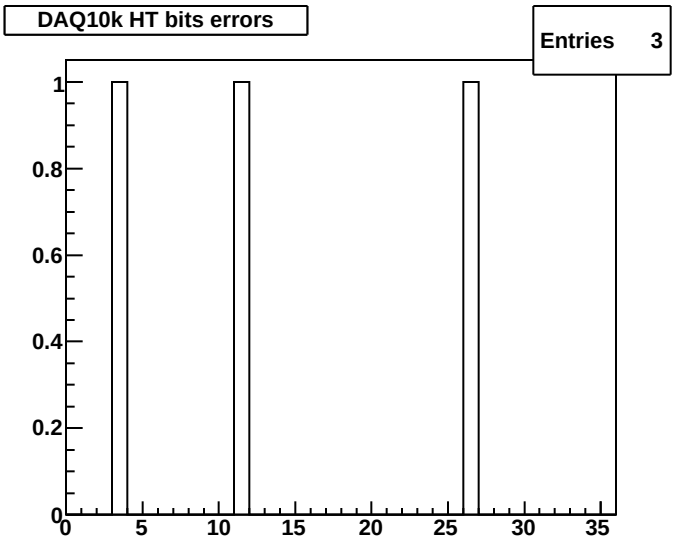

EMC L2 Input - HT01 bits/Partial JP ID
Entries 701384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

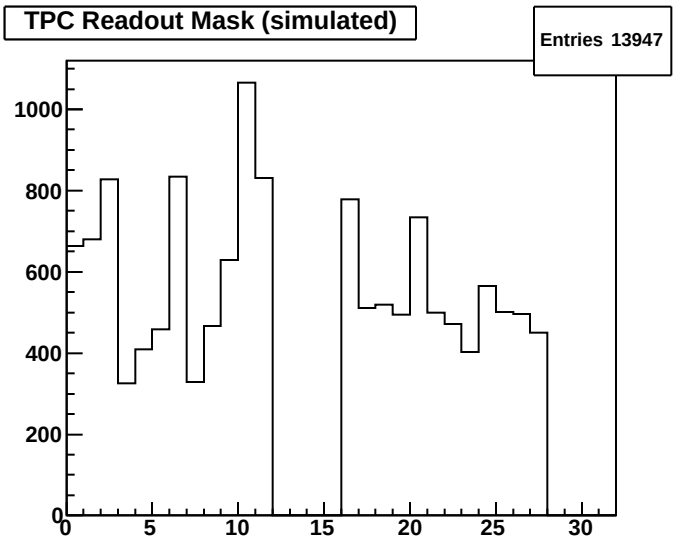

EMC L2 Input - HT01 bits/Partial JP ID
Entries 17
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

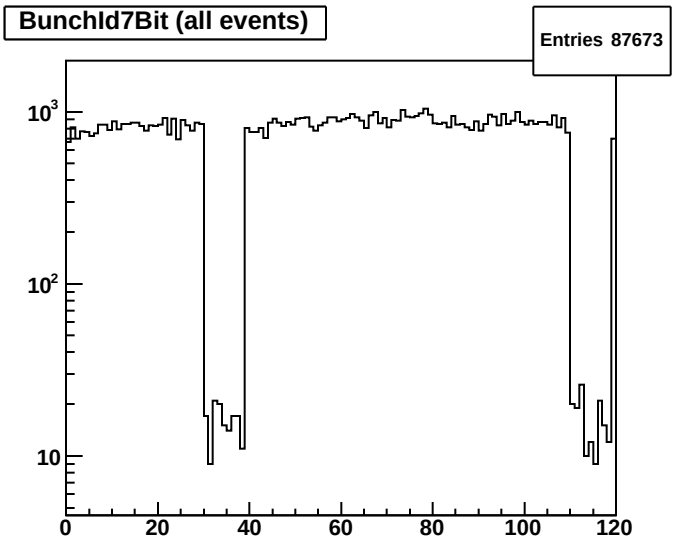

EMC L2 Input - HT23 bits
Entries 701384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

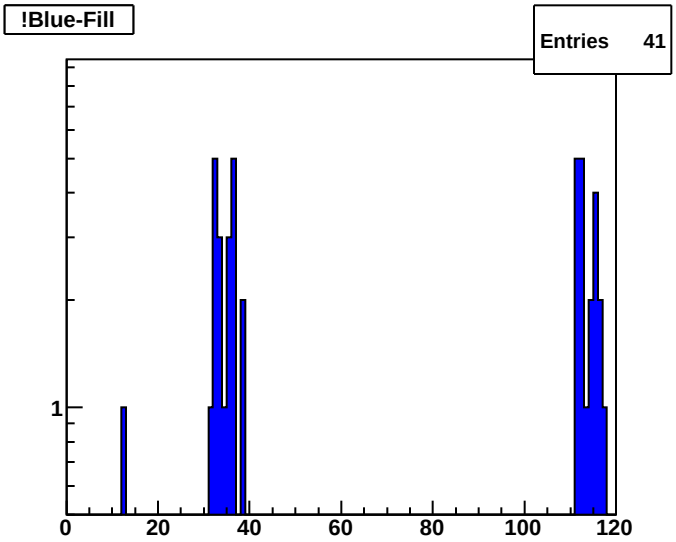

EMC L2 Input - HT23 bits
Entries 3
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

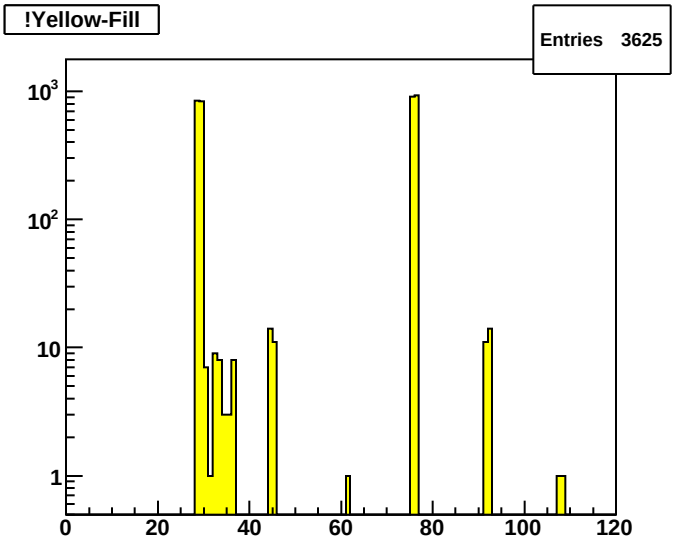

DAQ10k HT bits
Entries 7863

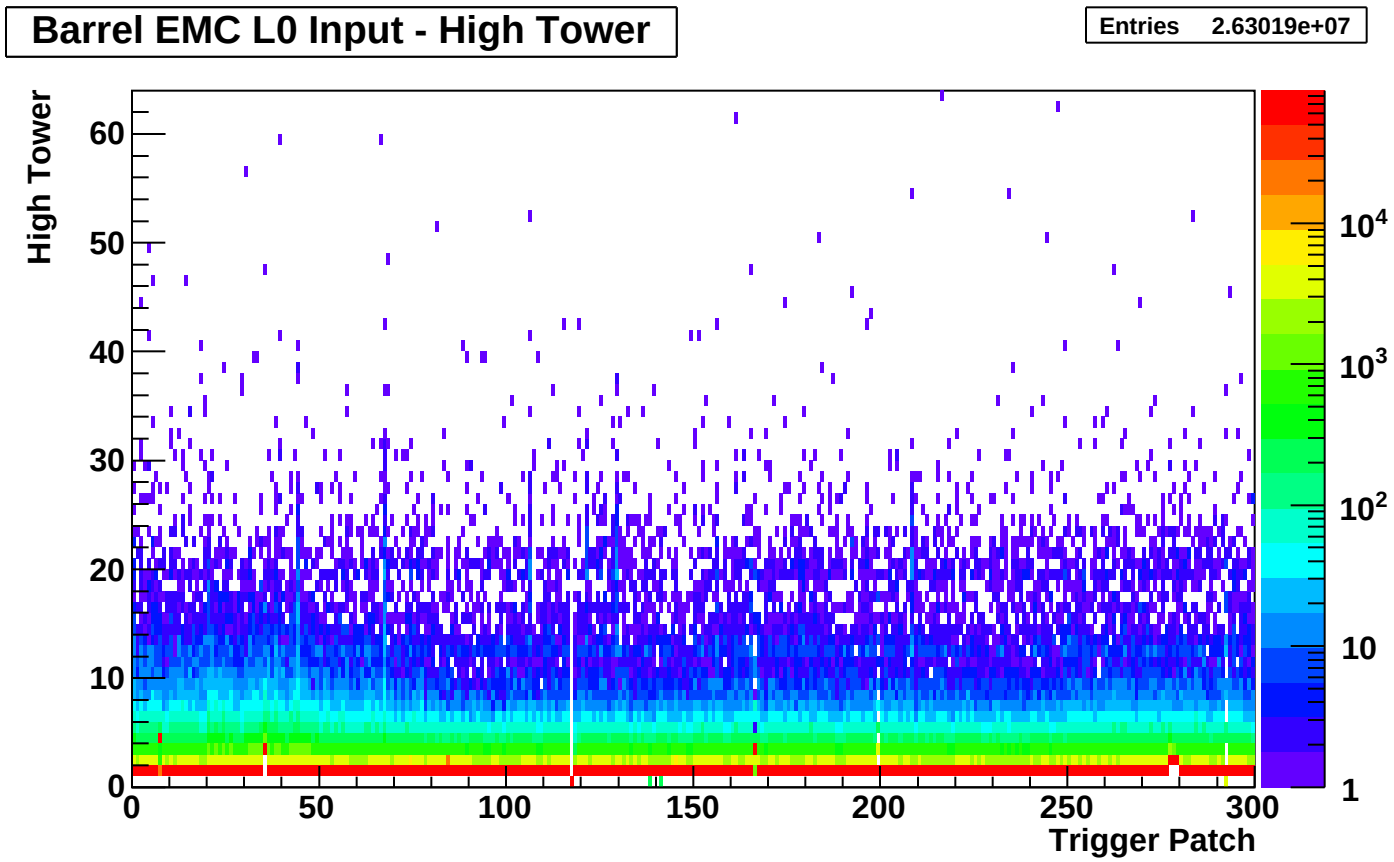

DAQ10k HT bits errors
Entries 3

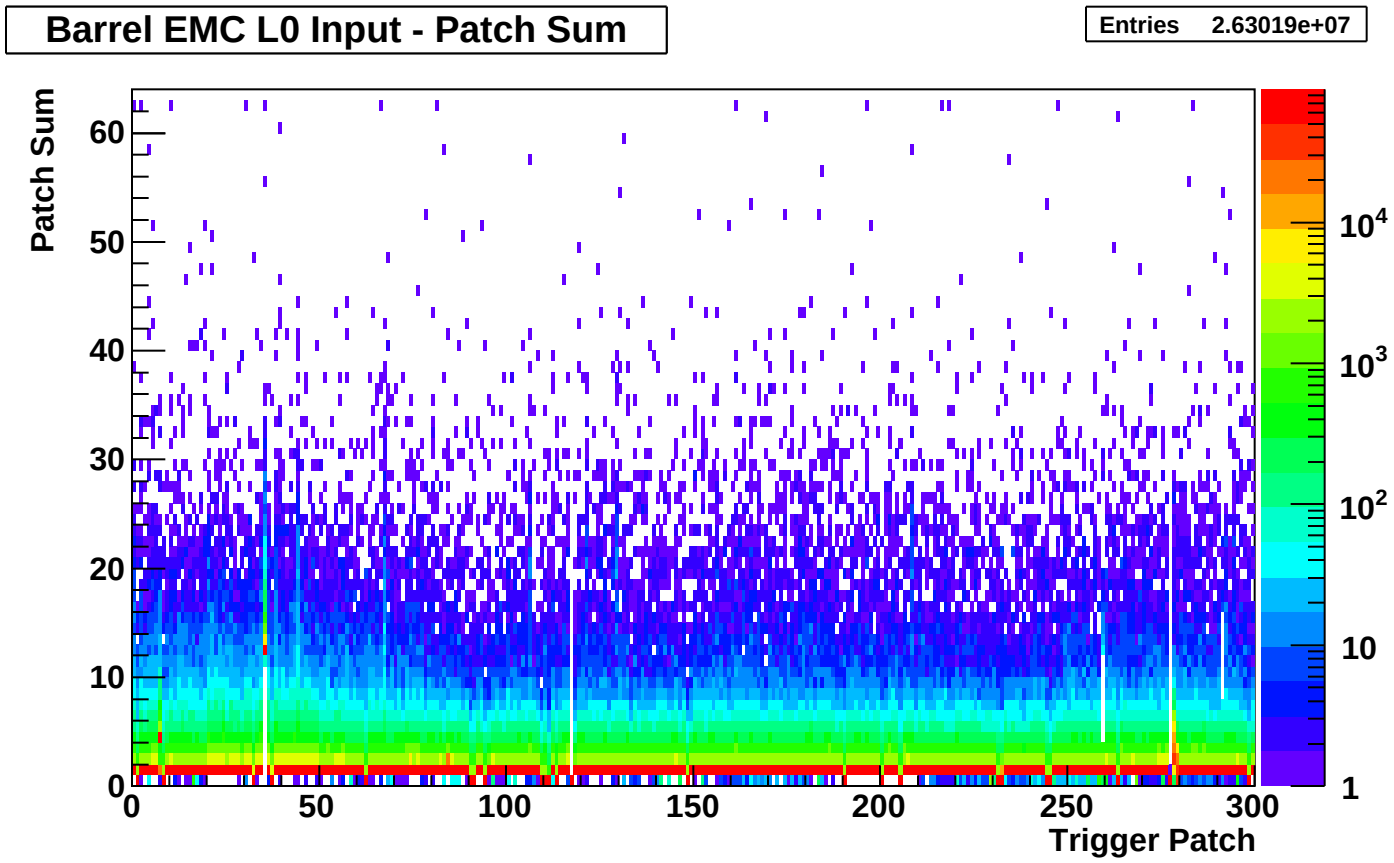

TPC Readout Mask (simulated)
Entries 13947

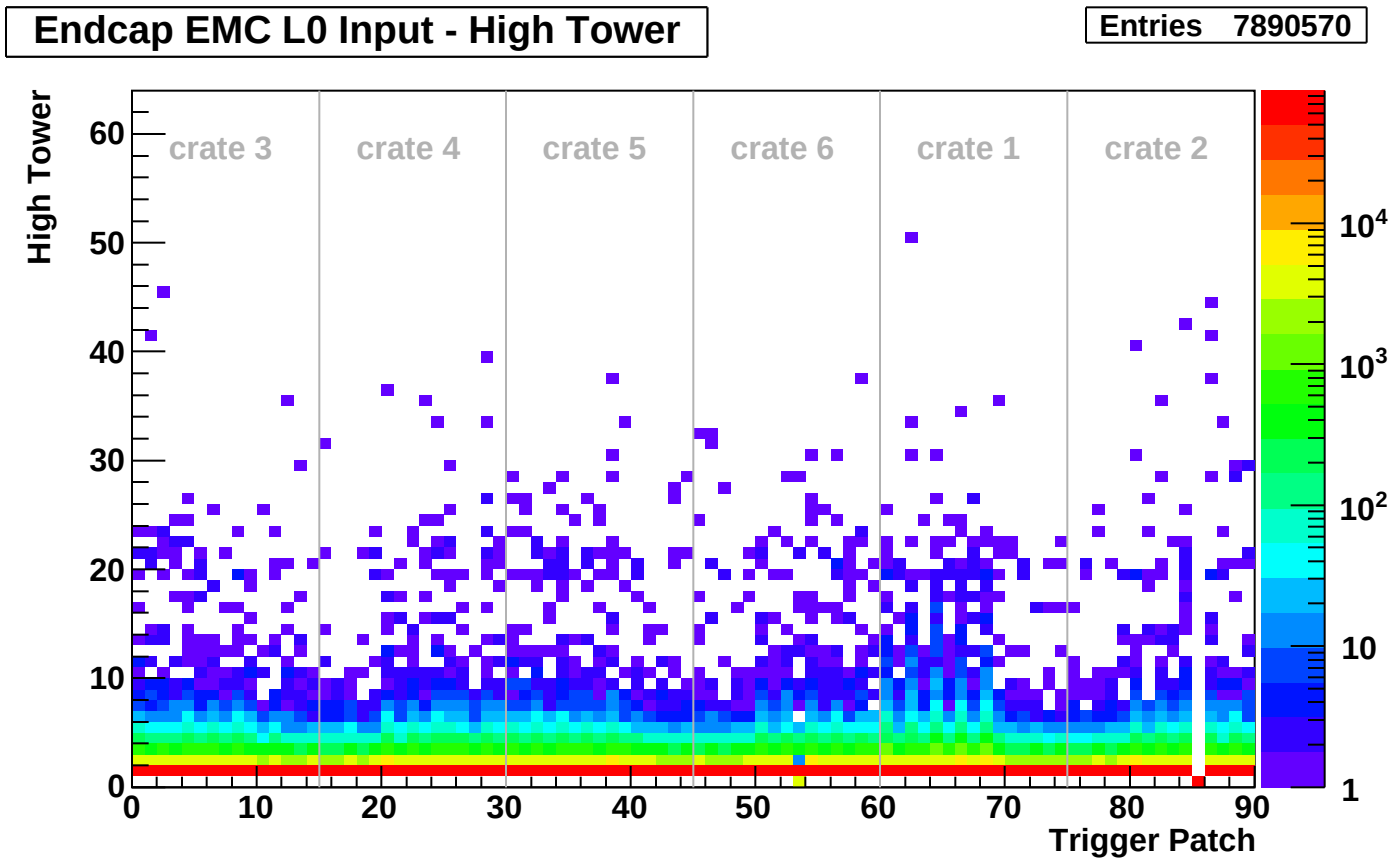

BunchId7Bit (all events)
Entries 87673

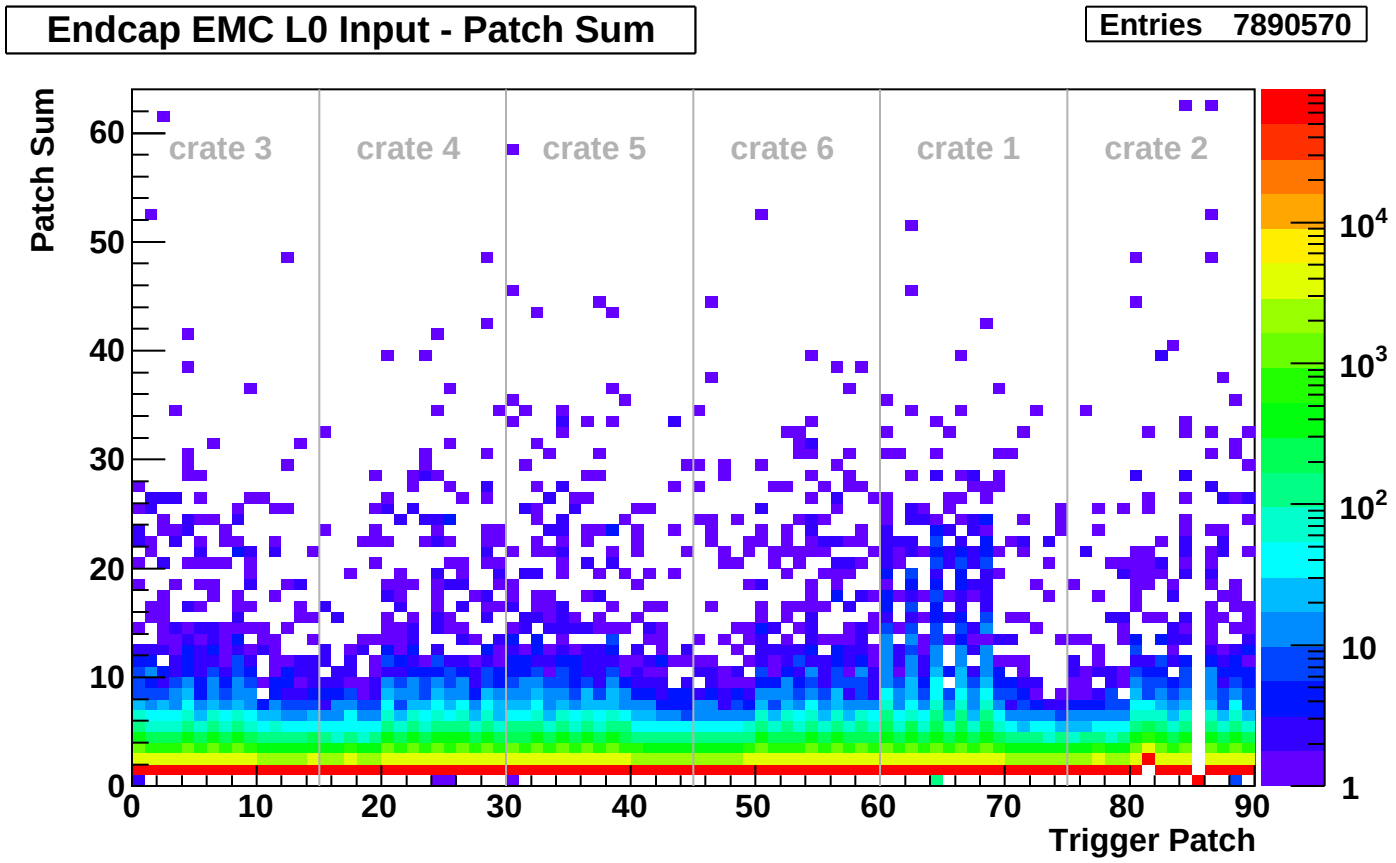

!Blue-Fill
Entries 41

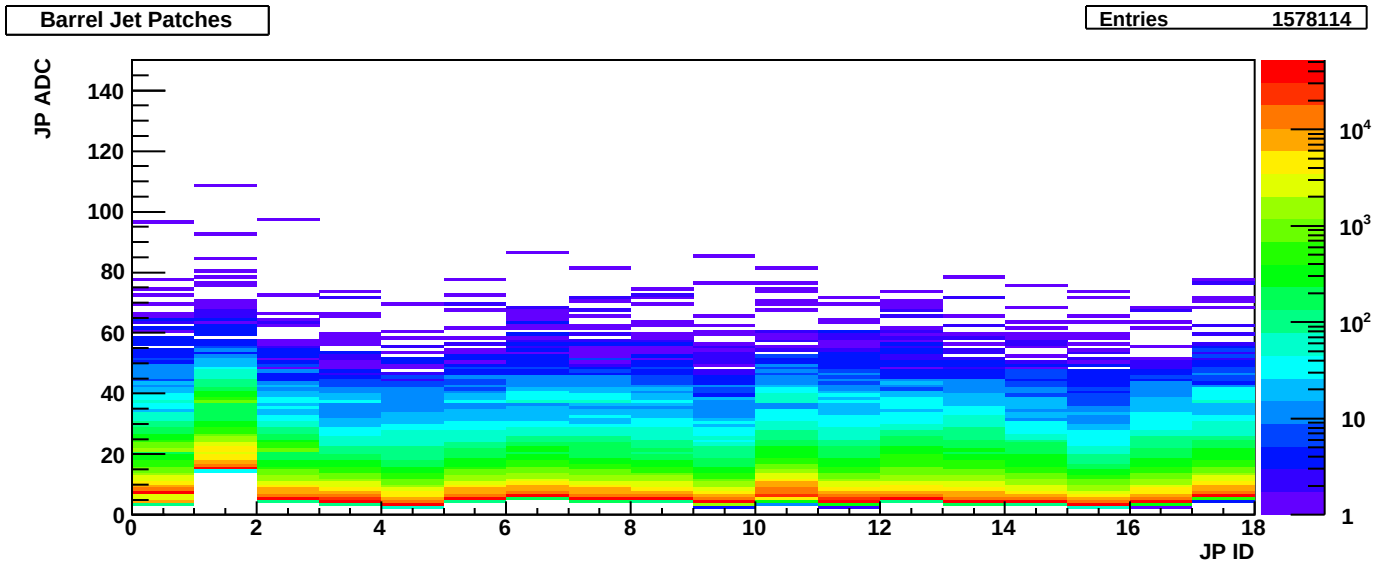

!Yellow-Fill
Entries 3625

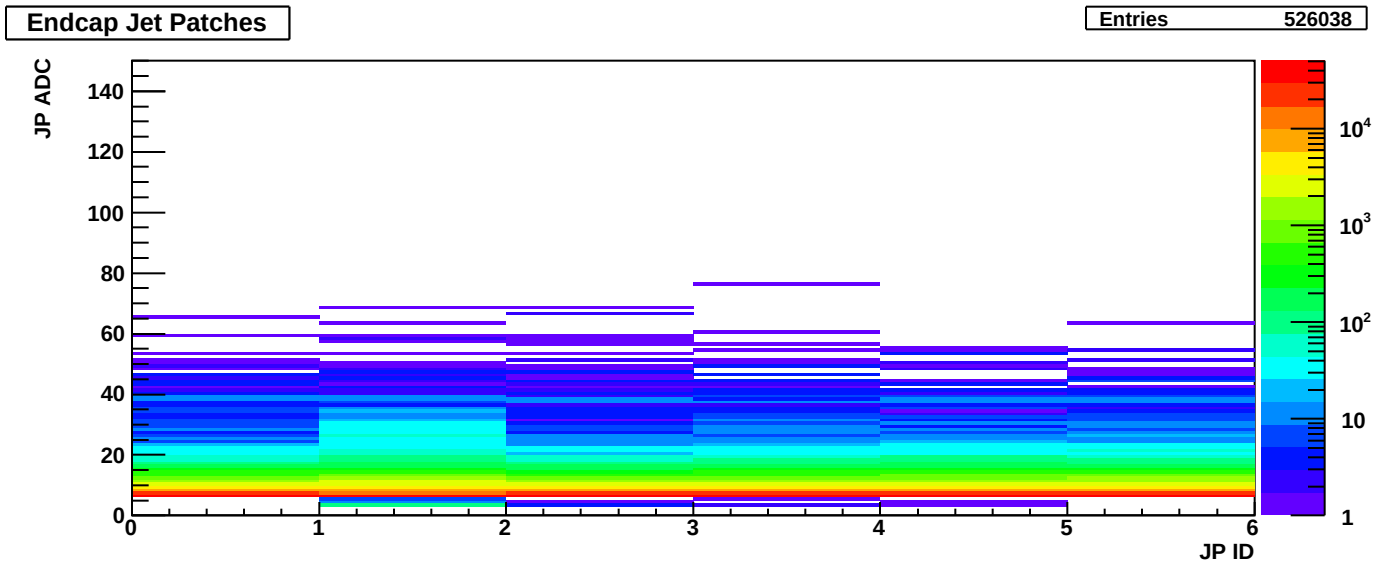

Barrel EMC L0 Input - High Tower
Entries 2.63019e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

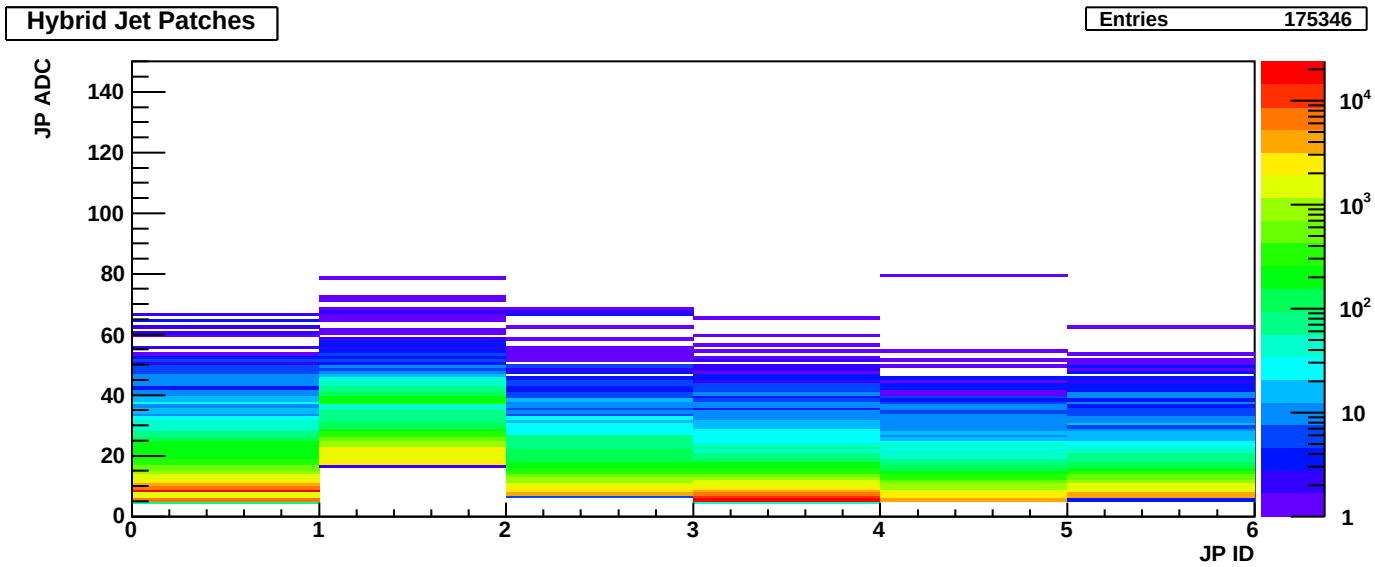

Barrel EMC L0 Input - Patch Sum
Entries 2.63019e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

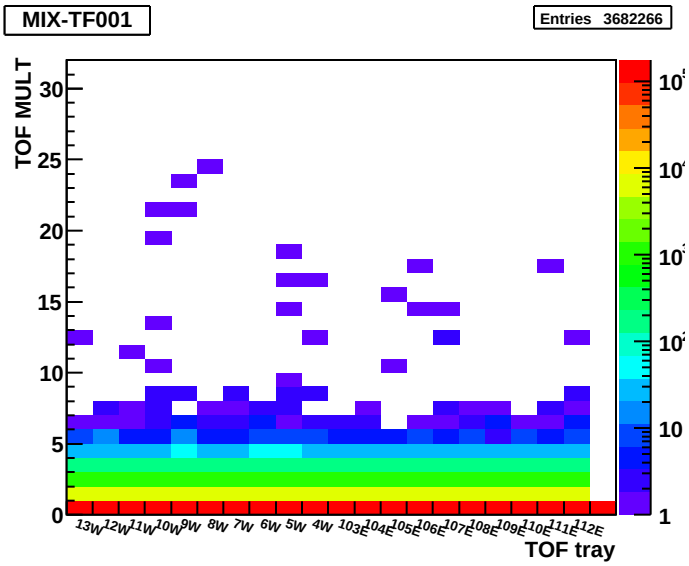

Endcap EMC L0 Input - High Tower
Entries 7890570
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

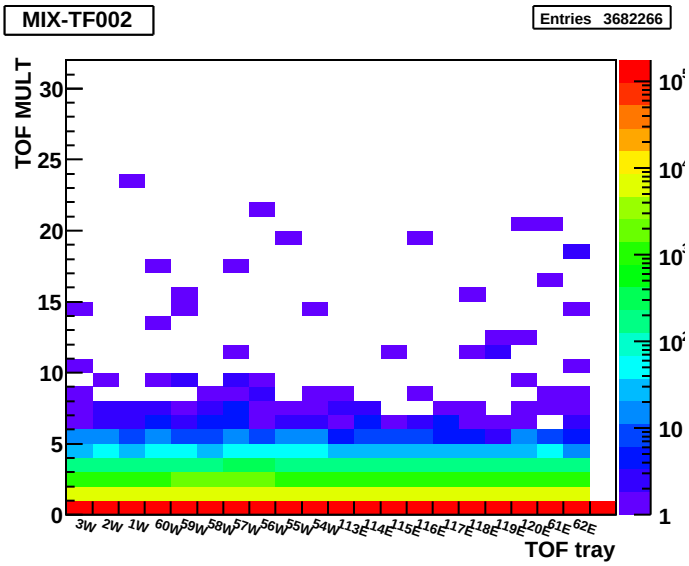

Endcap EMC L0 Input - Patch Sum
Entries 7890570
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

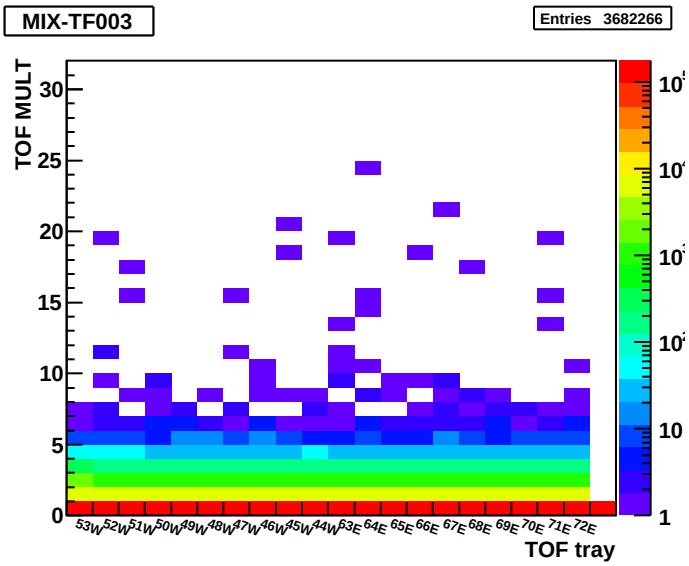

Barrel Jet Patches
Entries 1578114
JP ADC
JP ID

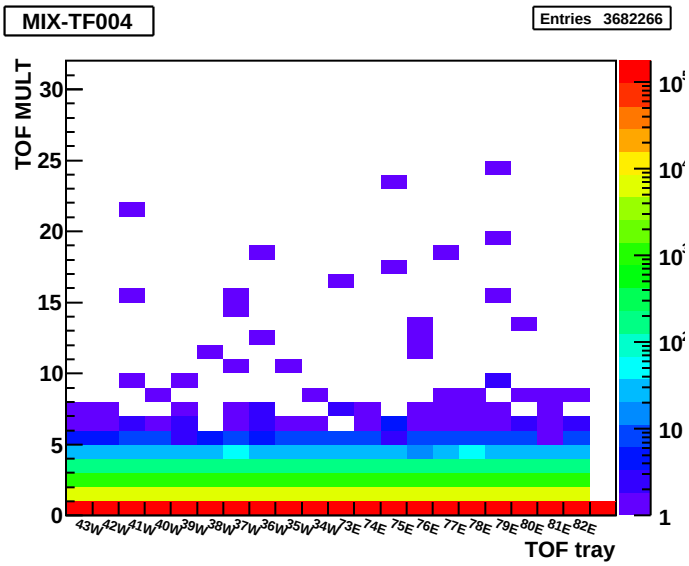

Endcap Jet Patches
Entries 526038
JP ADC
JP ID

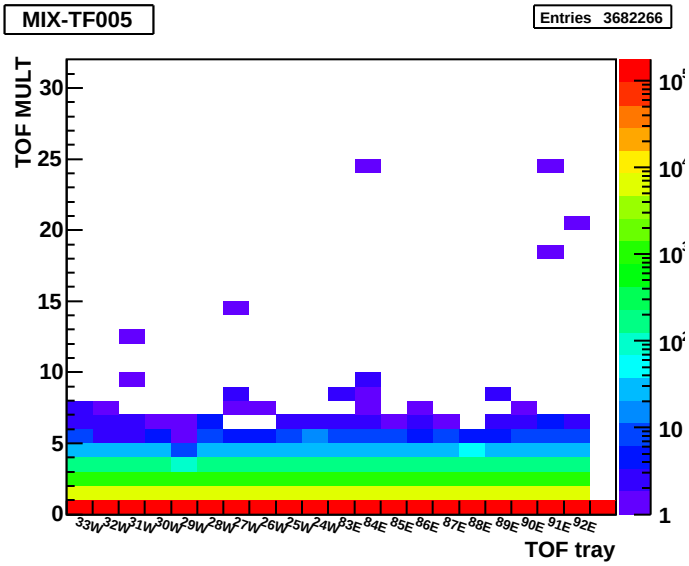

Hybrid Jet Patches
Entries 175346
JP ADC
JP ID

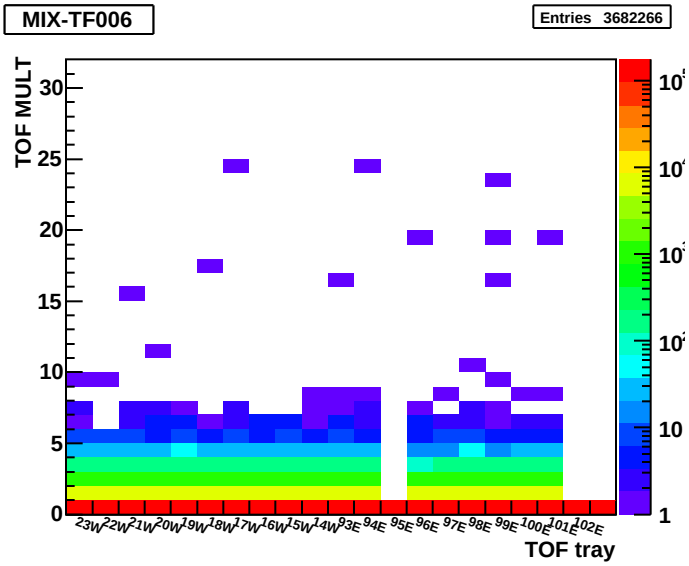

MIX-TF001
Entries 3682266
TOF MULT
TOF tray

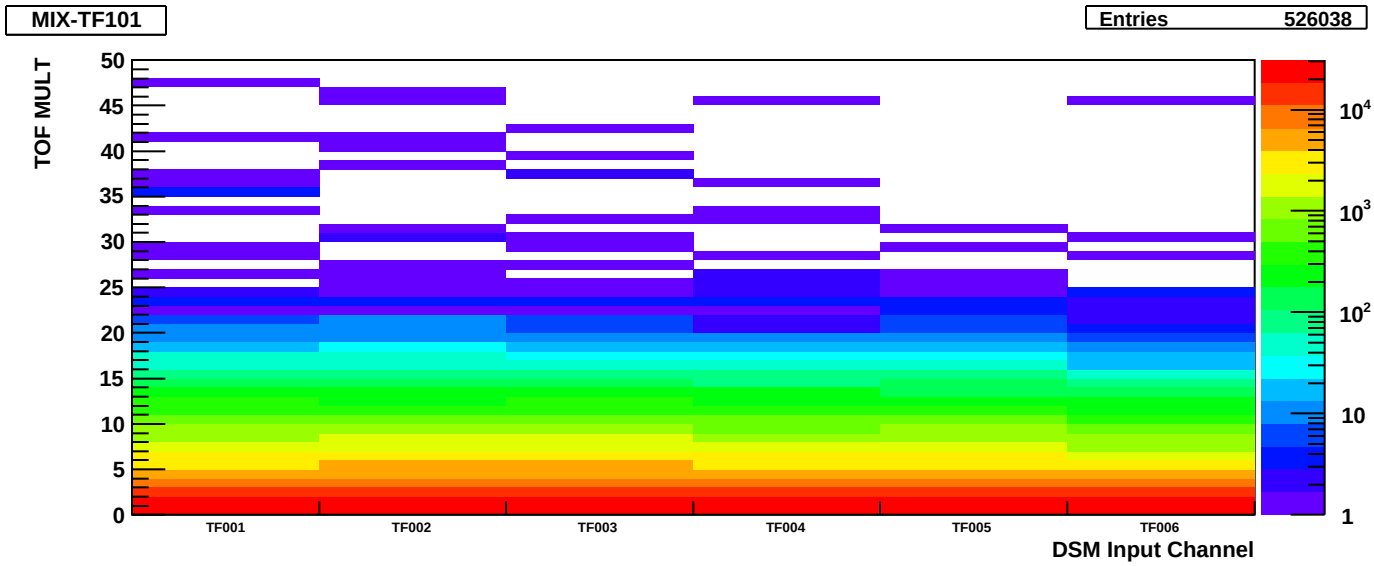

MIX-TF002
Entries 3682266
TOF MULT
TOF tray

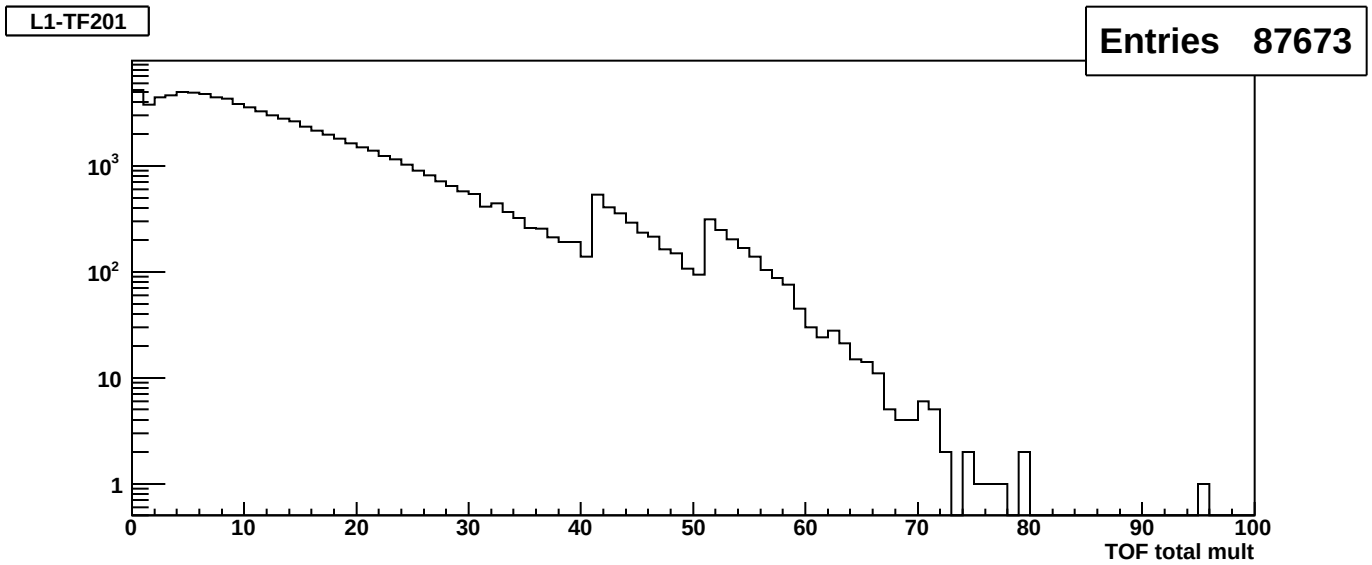

MIX-TF003
Entries 3682266
TOF MULT
TOF tray

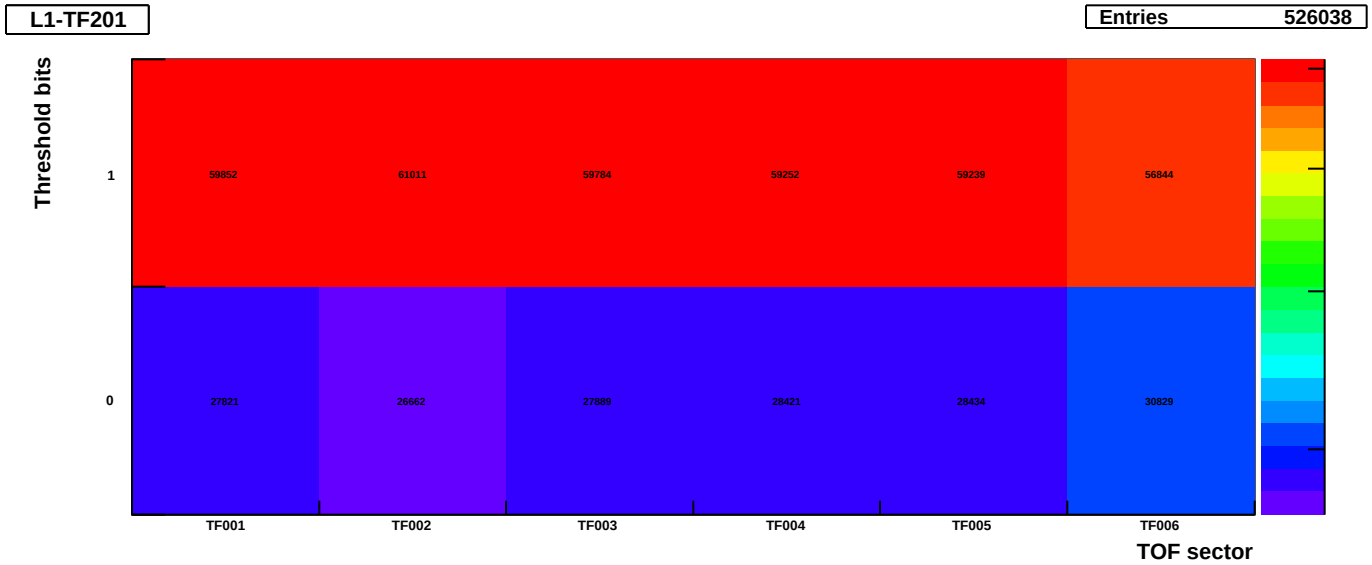

MIX-TF004
Entries 3682266
TOF MULT
TOF tray

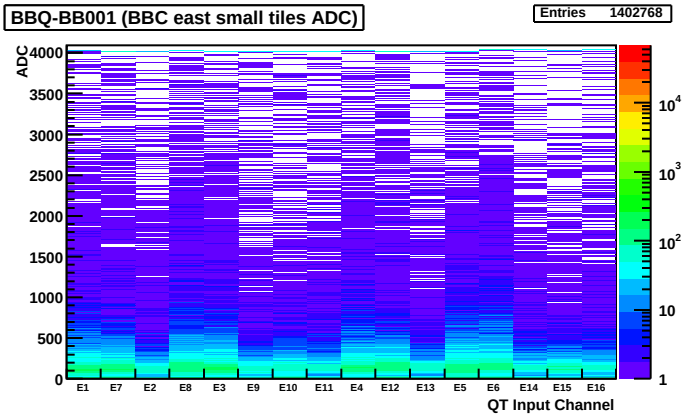

MIX-TF005
Entries 3682266
TOF MULT
TOF tray

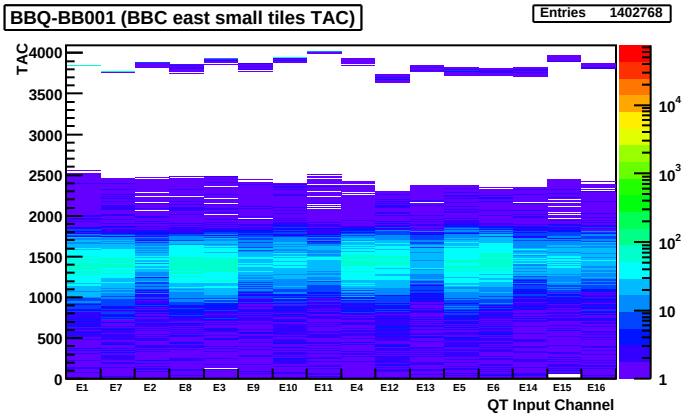

MIX-TF006
Entries 3682266
TOF MULT
TOF tray

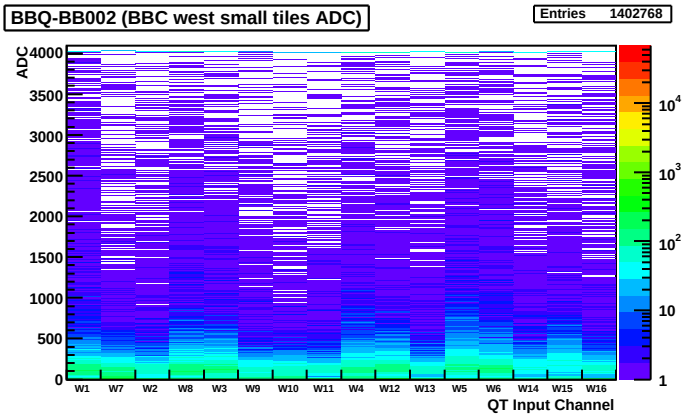

MIX-TF101
Entries 526038
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

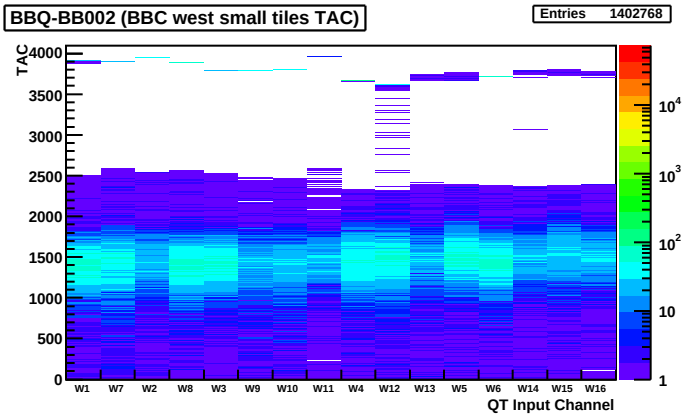

L1-TF201
Entries 87673
TOF total mult

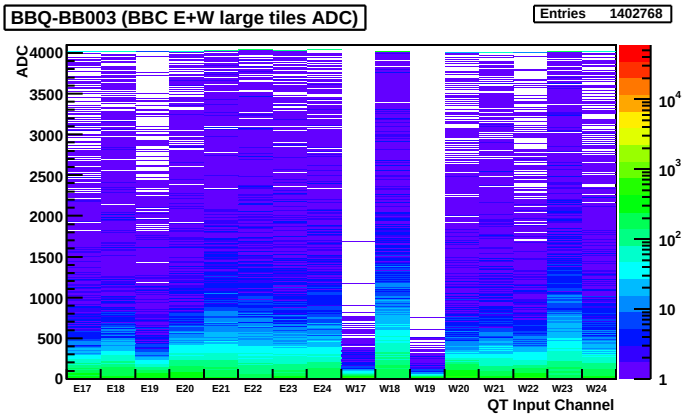

L1-TF201
Entries 526038
Threshold bits
59852
61011
59784
59252
59239
56844
27821
26662
27889
28421
28434
30829
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

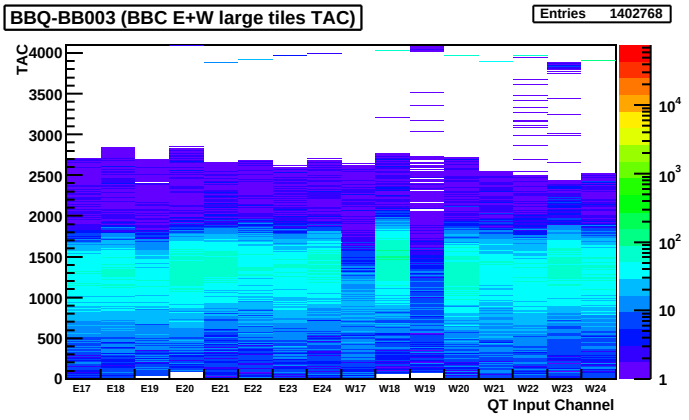

BBQ-BB001 (BBC east small tiles ADC)
Entries 1402768
ADC
QT Input Channel

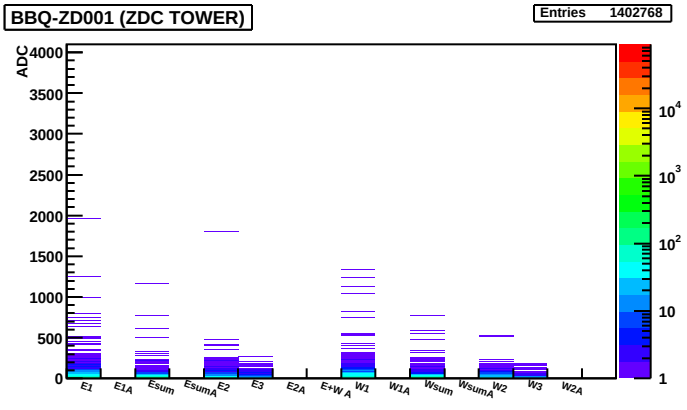

BBQ-BB001 (BBC east small tiles TAC)
Entries 1402768
TAC
QT Input Channel

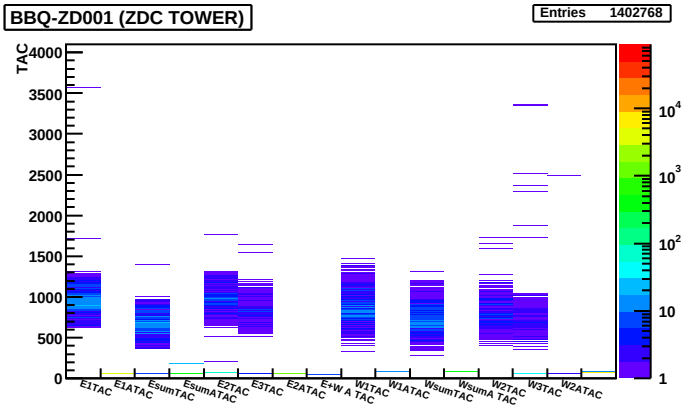

BBQ-BB002 (BBC west small tiles ADC)
Entries 1402768
ADC
QT Input Channel

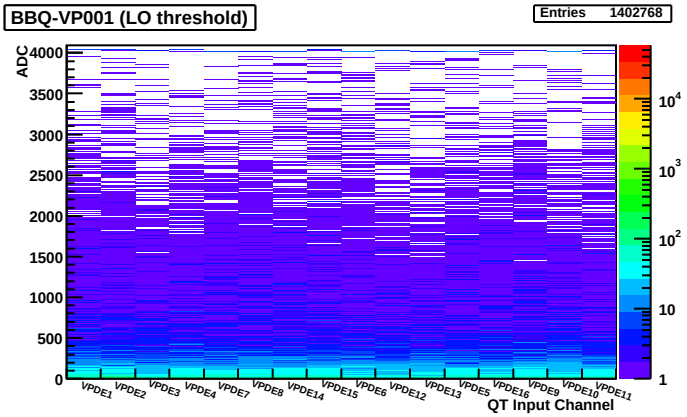

BBQ-BB002 (BBC west small tiles TAC)
Entries 1402768
TAC
QT Input Channel

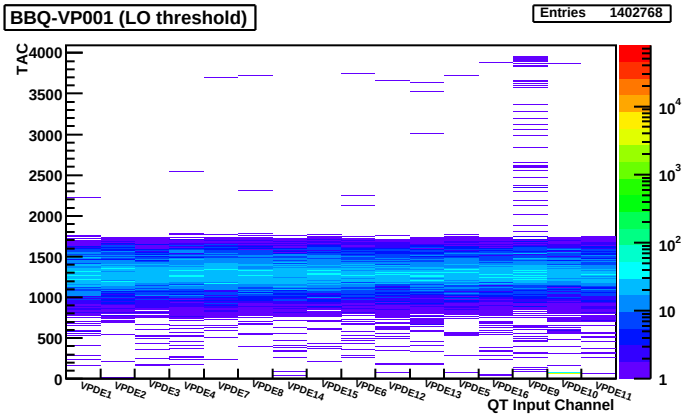

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1402768
ADC
QT Input Channel

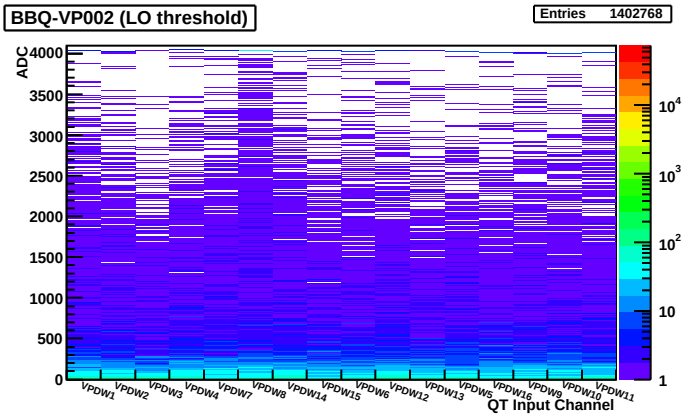

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1402768
TAC
QT Input Channel

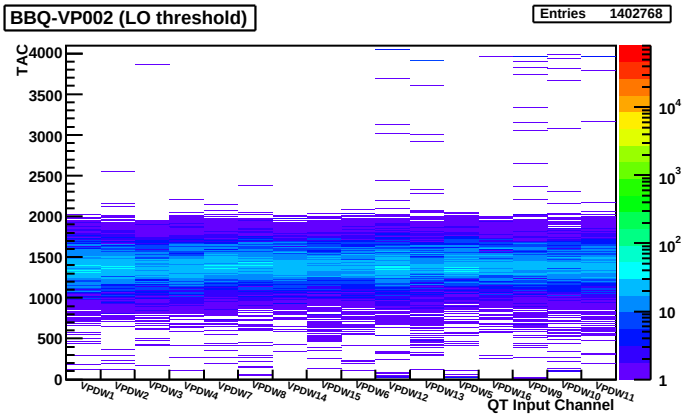

BBQ-ZD001 (ZDC TOWER)
Entries 1402768
ADC

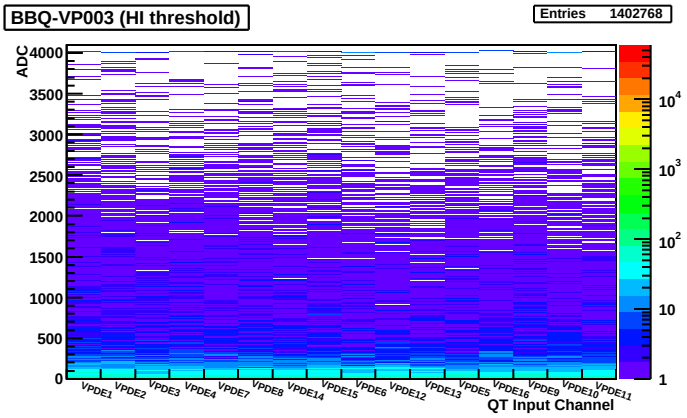

BBQ-ZD001 (ZDC TOWER)
Entries 1402768
TAC

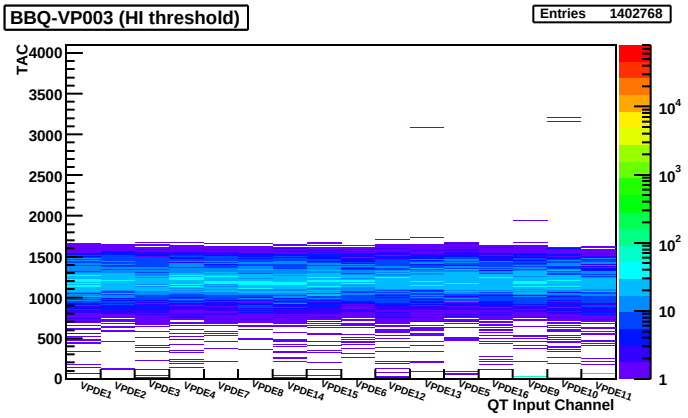

BBQ-VP001 (LO threshold)
Entries 1402768
ADC
QT Input Channel

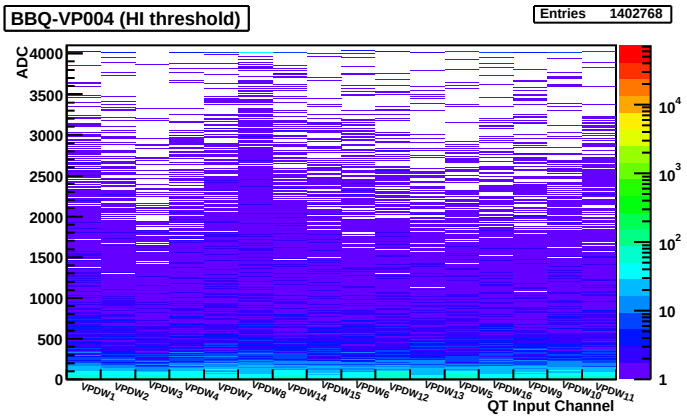

BBQ-VP001 (LO threshold)
Entries 1402768
TAC
QT Input Channel

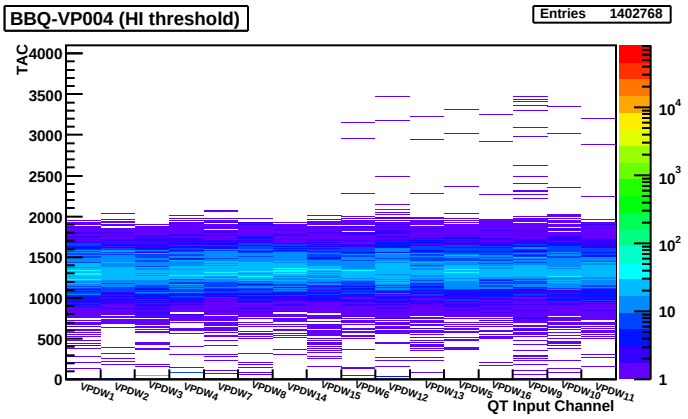

BBQ-VP002 (LO threshold)
Entries 1402768
ADC
QT Input Channel

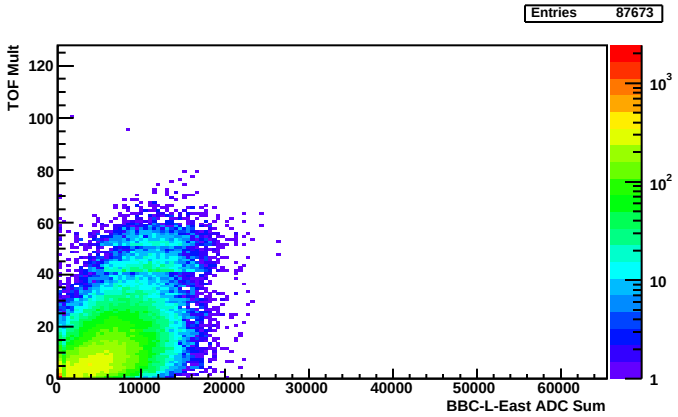

BBQ-VP002 (LO threshold)
Entries 1402768
TAC
QT Input Channel

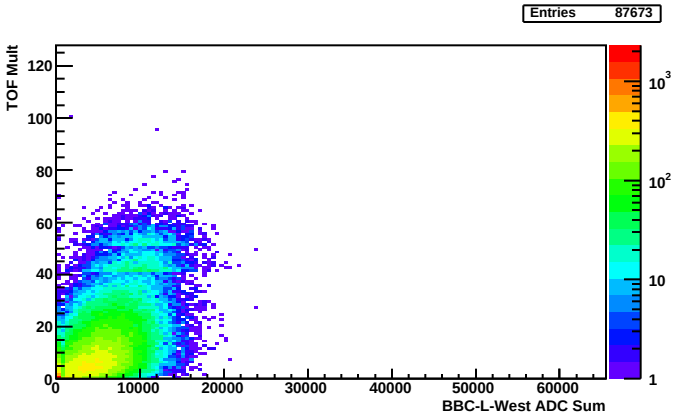

BBQ-VP003 (HI threshold)
Entries 1402768
ADC
QT Input Channel

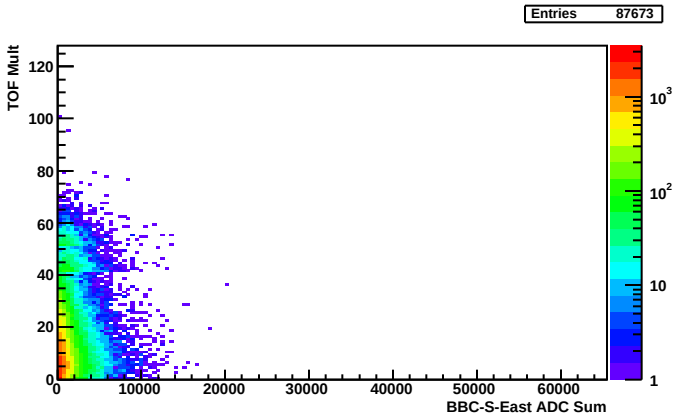

BBQ-VP003 (HI threshold)
Entries 1402768
TAC
QT Input Channel

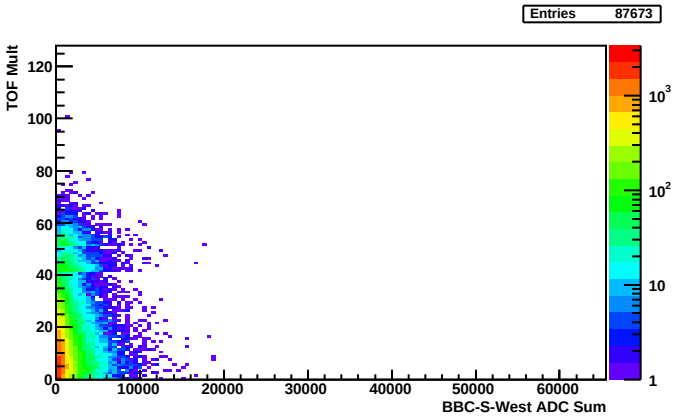

BBQ-VP004 (HI threshold)
Entries 1402768
ADC
QT Input Channel

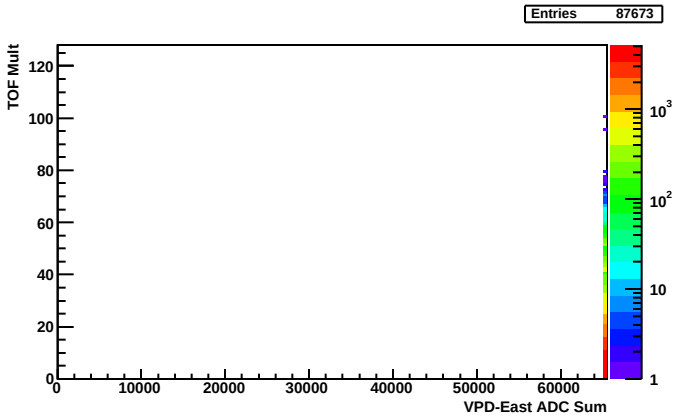

BBQ-VP004 (HI threshold)
Entries 1402768
TAC
QT Input Channel

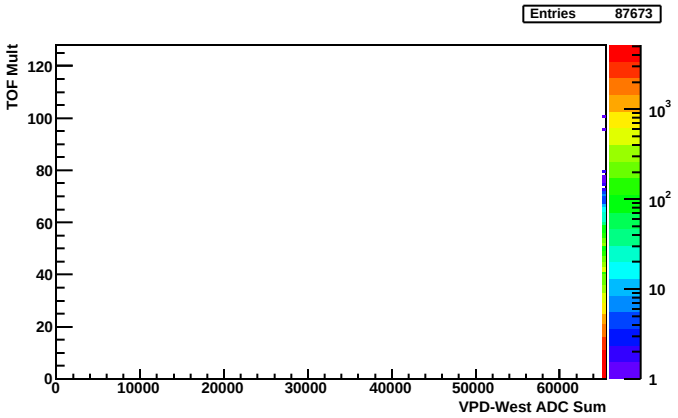

Entries 87673
TOF Mult
BBC-L-East ADC Sum

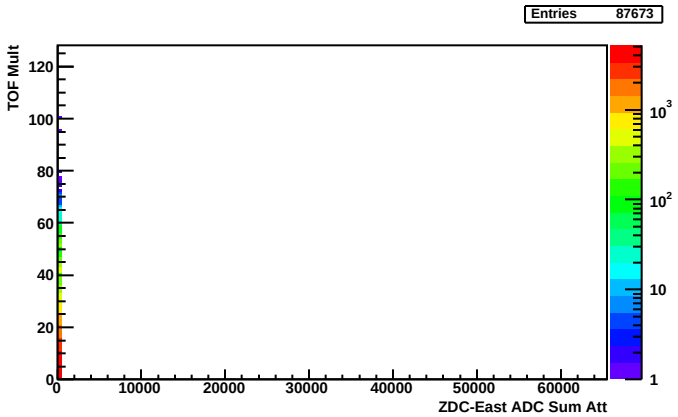

Entries 87673
TOF Mult
BBC-L-West ADC Sum

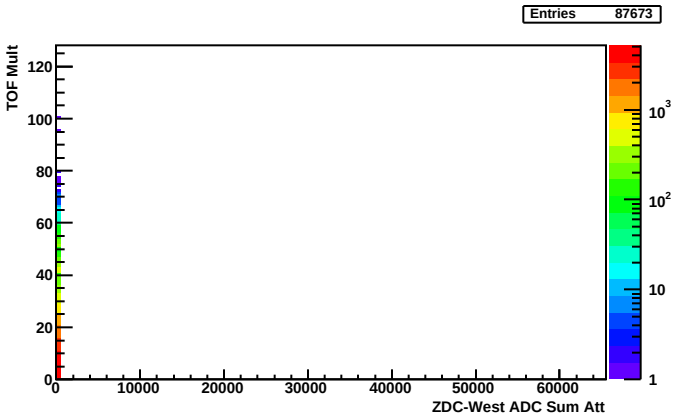

Entries 87673
TOF Mult
BBC-S-East ADC Sum

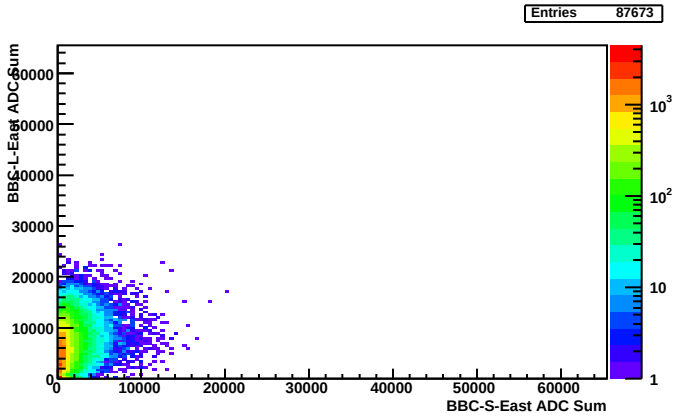

Entries 87673
TOF Mult
BBC-S-West ADC Sum

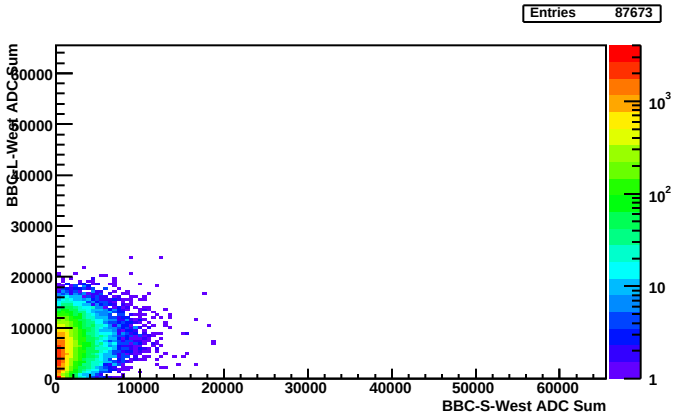

Entries 87673
TOF Mult
VPD-East ADC Sum

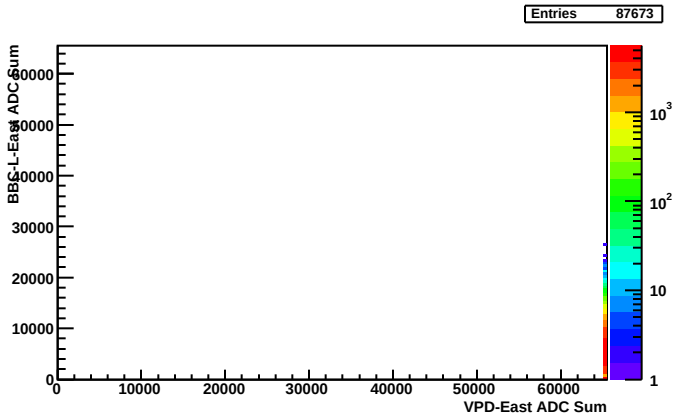

Entries 87673
TOF Mult
VPD-West ADC Sum

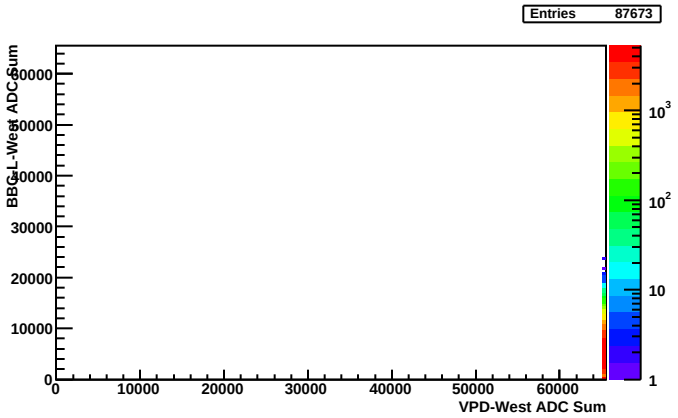

Entries 87673
TOF Mult
ZDC-East ADC Sum Att

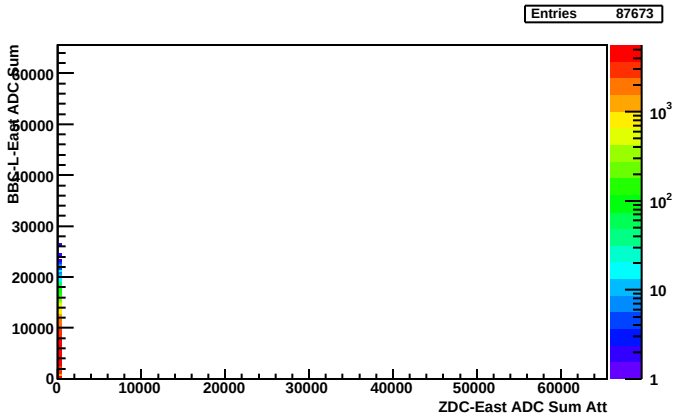

Entries 87673
TOF Mult
ZDC-West ADC Sum Att

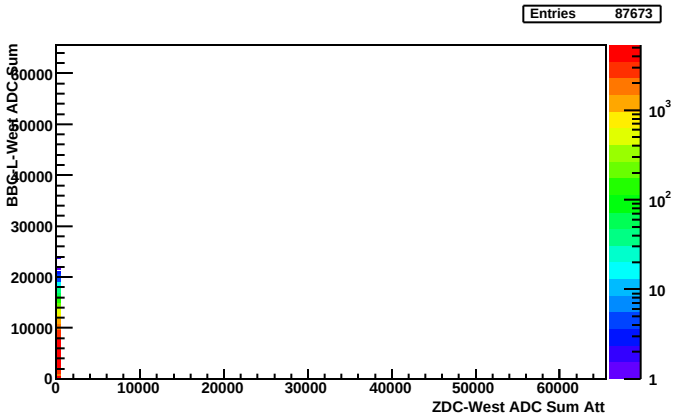

Entries 87673
BBC-L-East ADC Sum
BBC-S-East ADC Sum

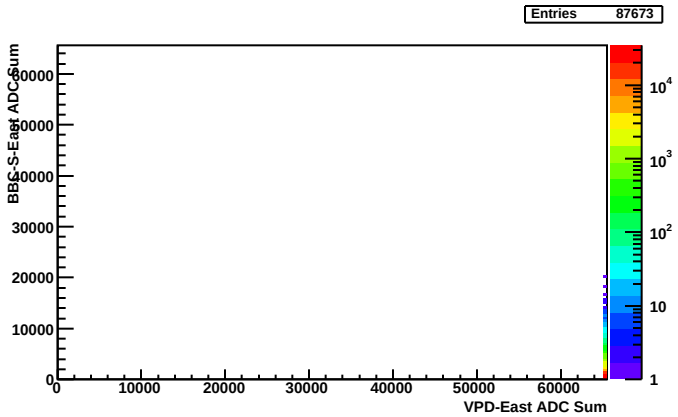

Entries 87673
BBC-L-West ADC Sum
BBC-S-West ADC Sum

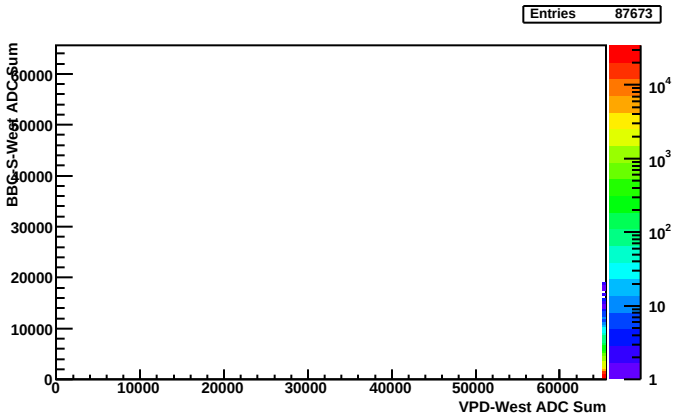

Entries 87673
BBC-L-East ADC Sum
VPD-East ADC Sum

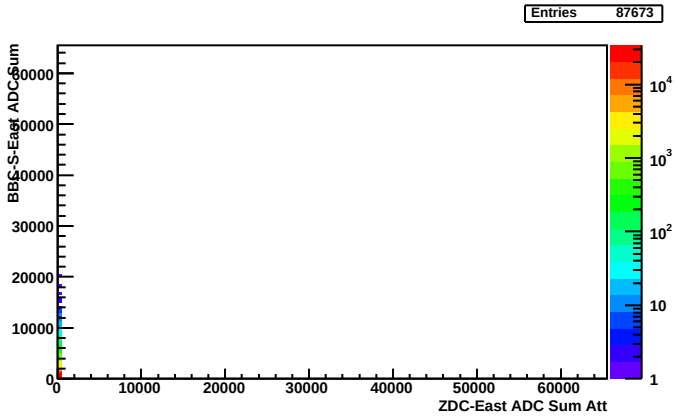

Entries 87673
BBC-L-West ADC Sum
VPD-West ADC Sum

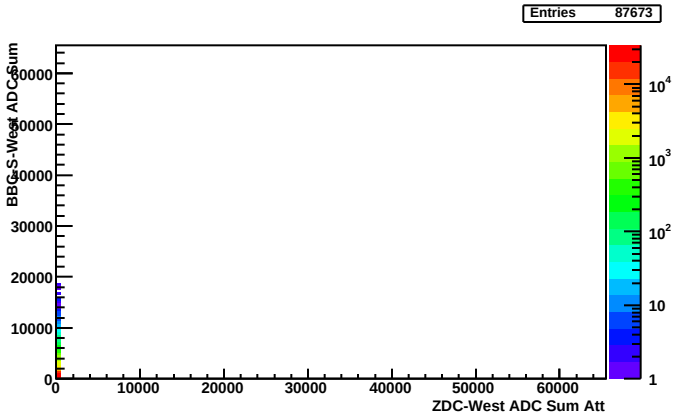

Entries 87673
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

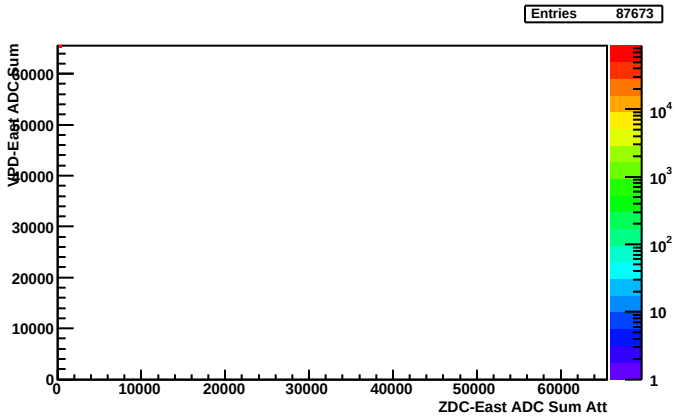

Entries 87673
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

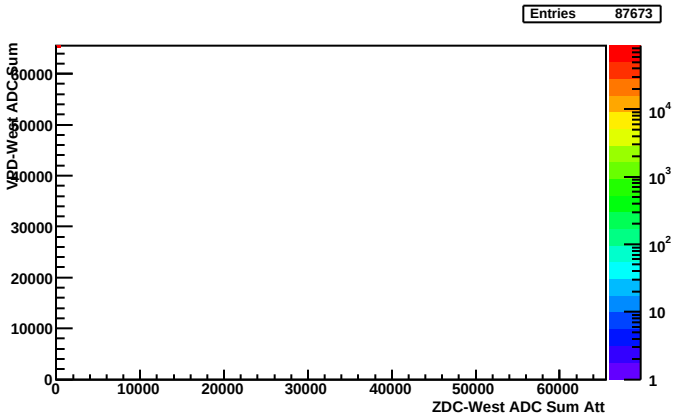

Entries 87673
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 87673
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 87673
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 87673
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 87673
VPD-East ADC Sum
ZDC-East ADC Sum Att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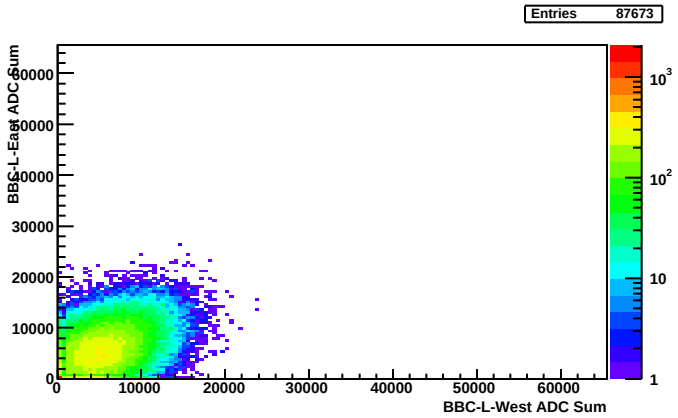
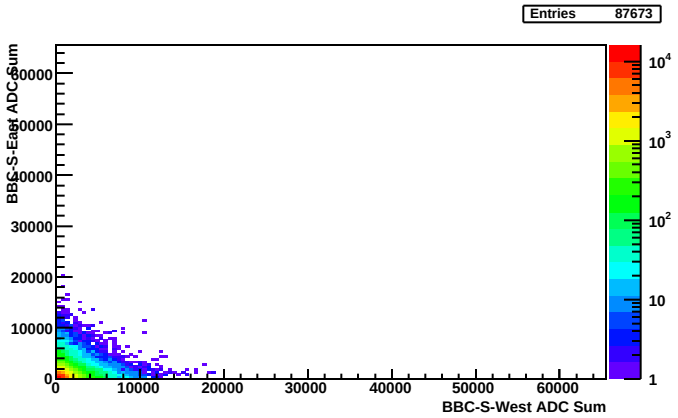
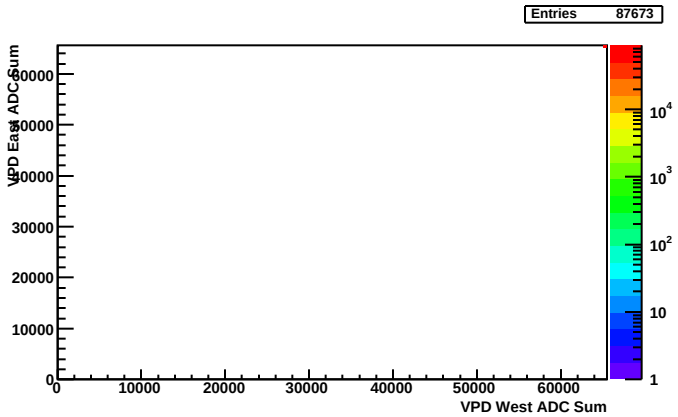
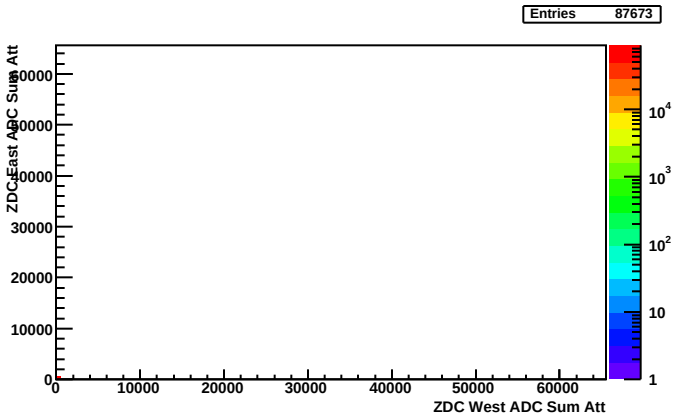
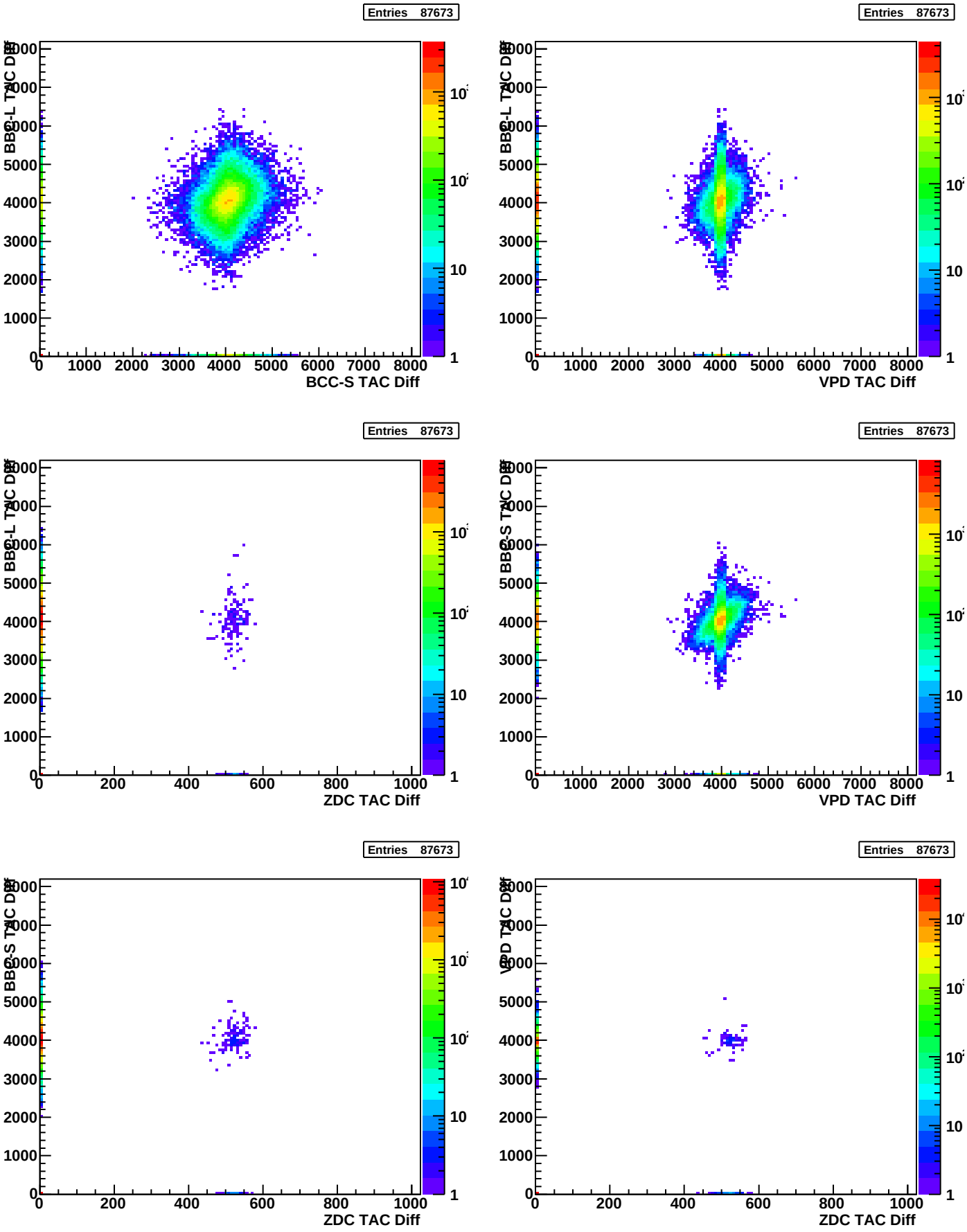
VPD-West ADC Sum
ZDC-West ADC Sum Att

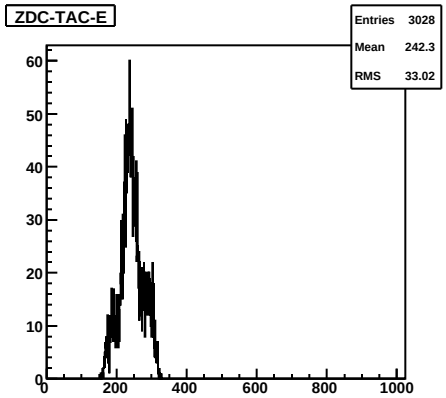

Entries 87673
BBC-L-East ADC Sum
BBC-L-West ADC Sum

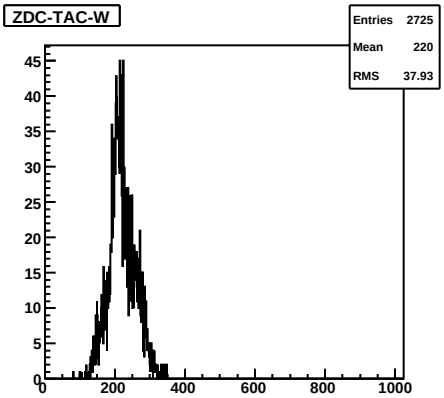

Entries 87673
BBC-S-East ADC Sum
BBC-S-West ADC Sum

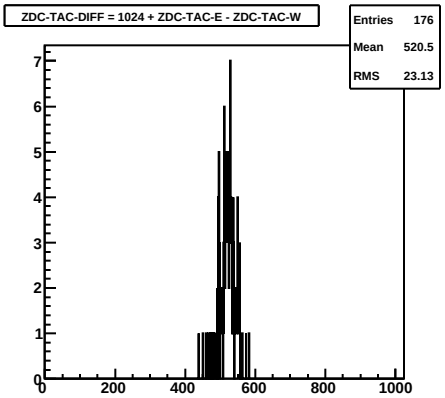

Entries 87673
VPD East ADC Sum
VPD West ADC Sum

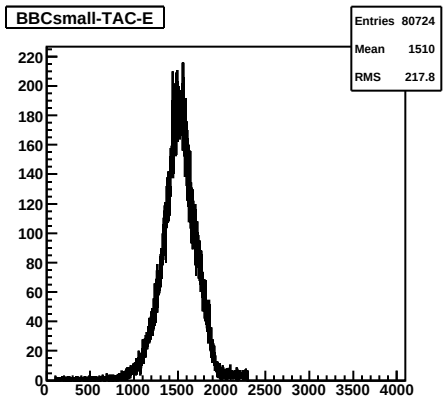

Entries 87673
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

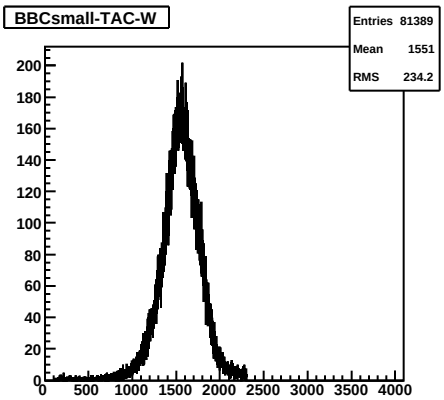

Entries 87673
BBC-L TAC Diff
BCC-S TAC Diff
Entries 87673
BBC-L TAC Diff
VPD TAC Diff
Entries 87673
BBC-L TAC Diff
ZDC TAC Diff
Entries 87673
BBC-S TAC Diff
VPD TAC Diff
Entries 87673
BBC-S TAC Diff
ZDC TAC Diff
Entries 87673
VPD TAC Diff
ZDC TAC Diff

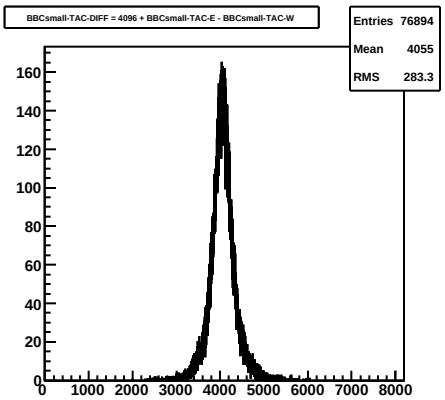

ZDC-TAC-E
Entries 3028
Mean 242.3
RMS 33.02

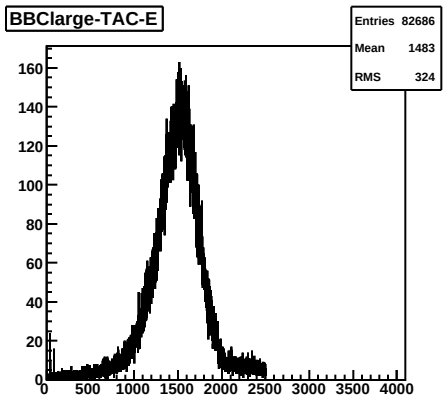

ZDC-TAC-W
Entries 2725
Mean 220
RMS 37.93

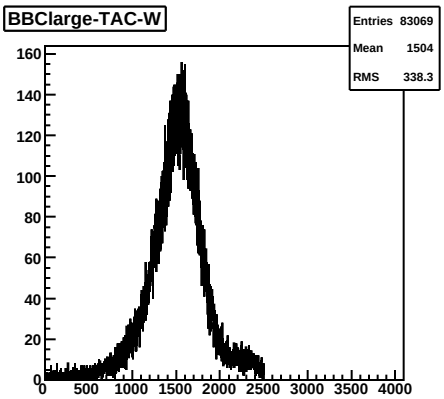

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 176
Mean 520.5
RMS 23.13

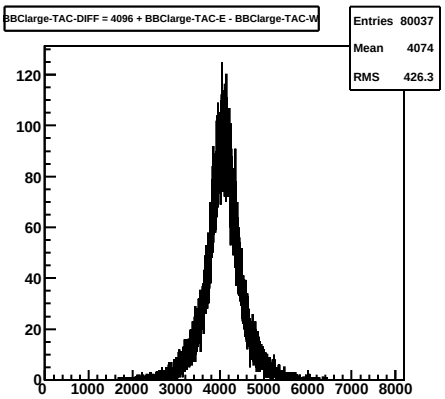

BBCsmall-TAC-E
Entries 80724
Mean 1510
RMS 217.8

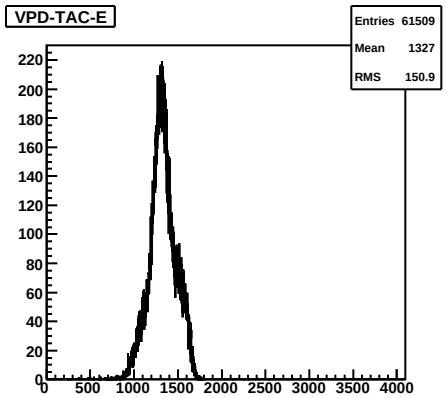

BBCsmall-TAC-W
Entries 81389
Mean 1551
RMS 234.2

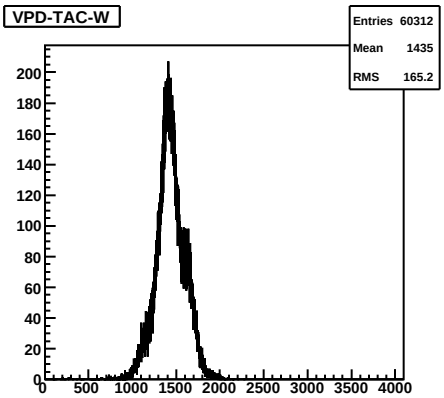

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 76894
Mean 4055
RMS 283.3

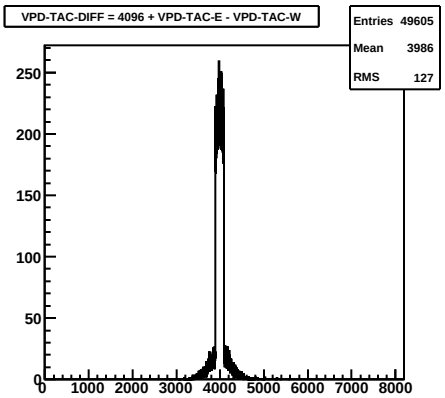

BBClarge-TAC-E
Entries 82686
Mean 1483
RMS 324

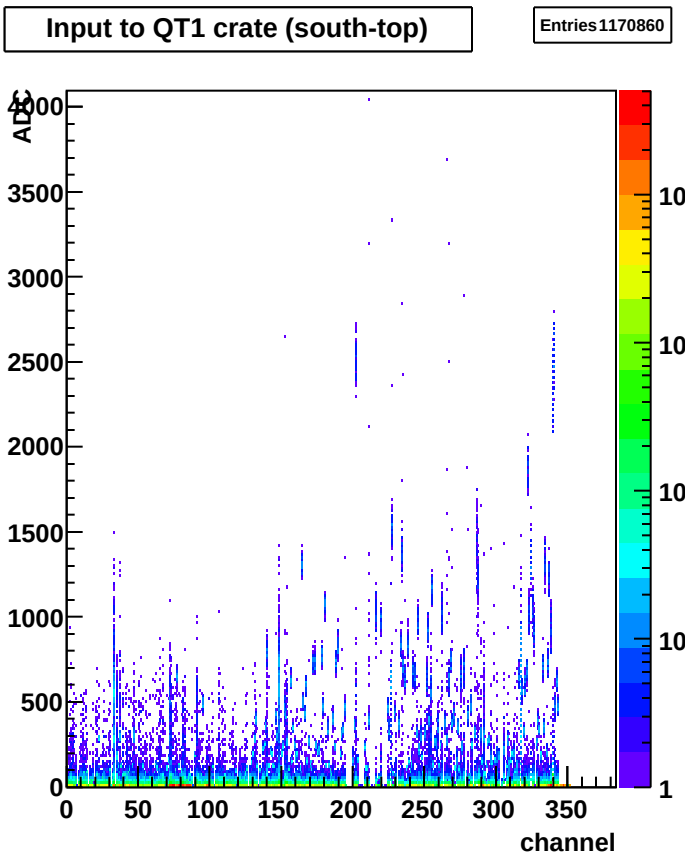

BBClarge-TAC-W
Entries 83069
Mean 1504
RMS 338.3

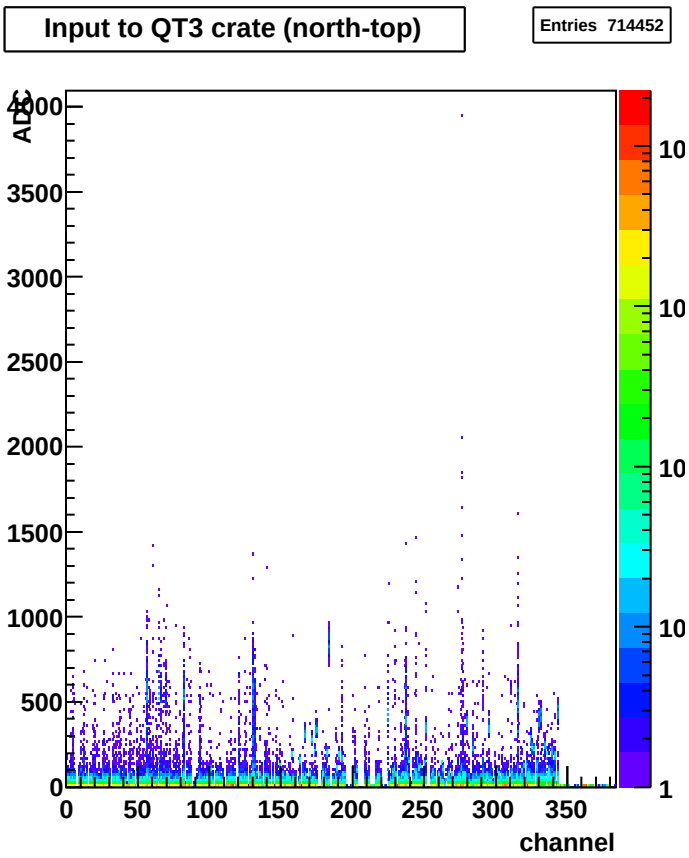

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 80037
Mean 4074
RMS 426.3

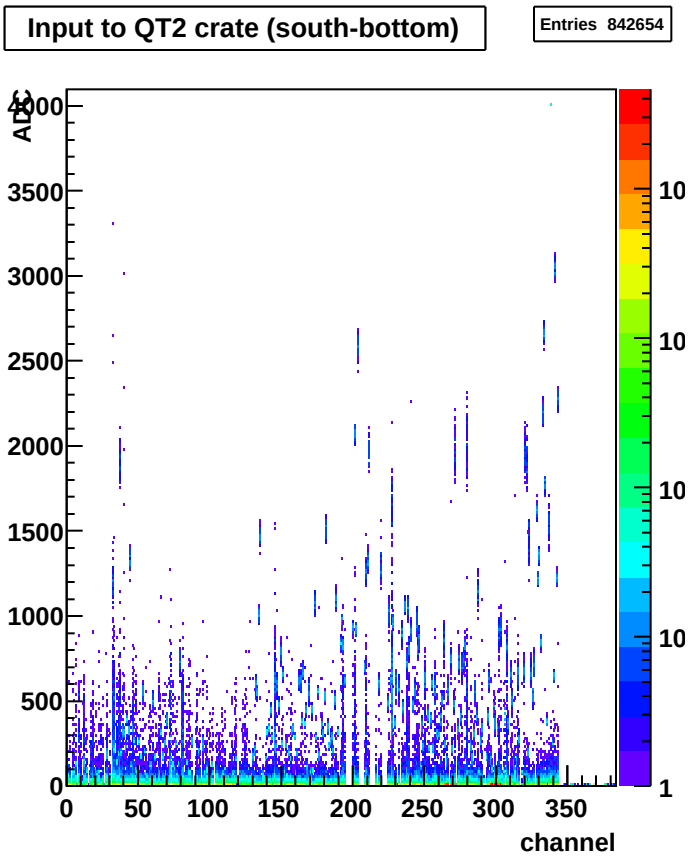

VPD-TAC-E
Entries 61509
Mean 1327
RMS 150.9

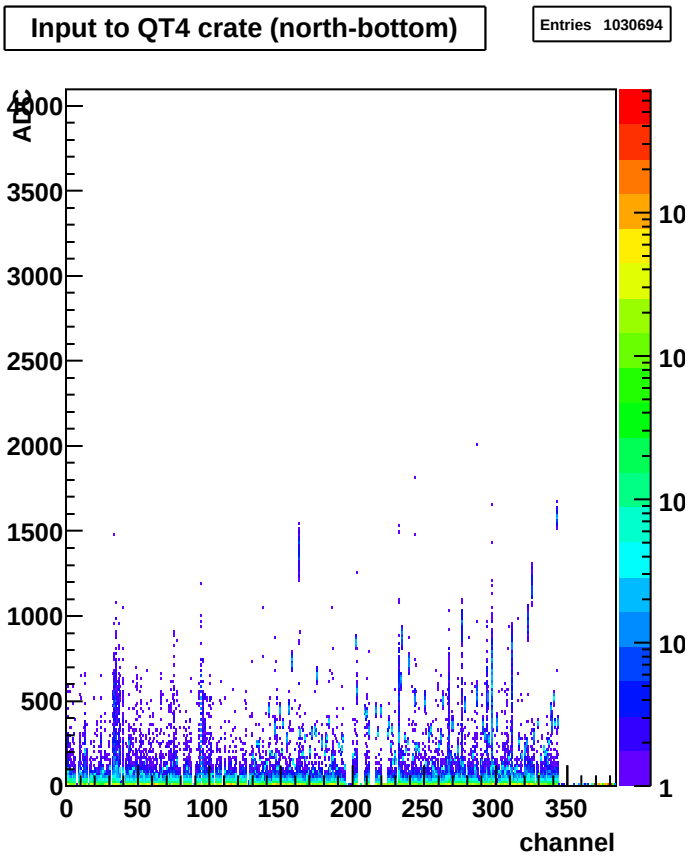

VPD-TAC-W
Entries 60312
Mean 1435
RMS 165.2

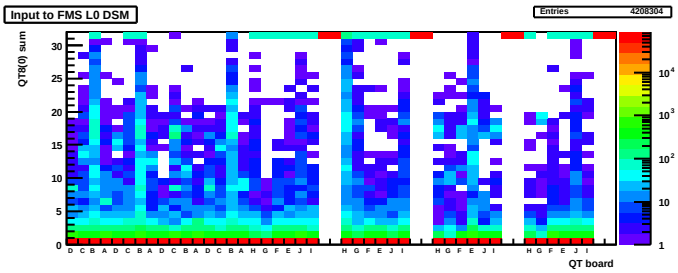

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 49605
Mean 3986
RMS 127

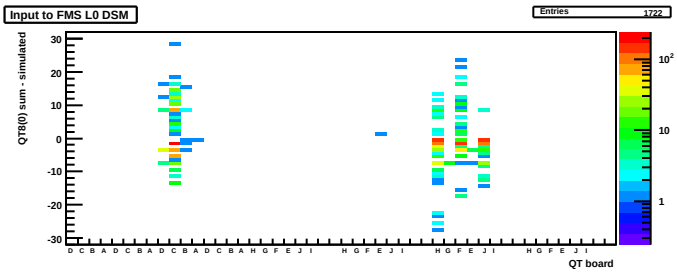

Input to QT1 crate (south-top)
Entries 1170860
ADC
channel

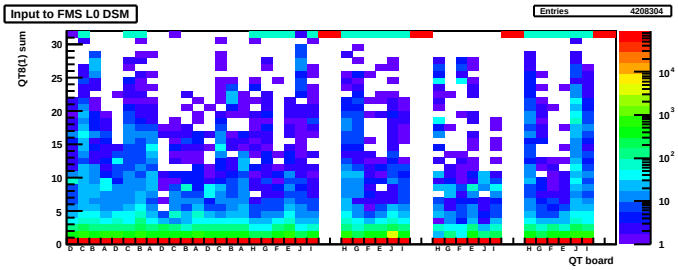

Input to QT3 crate (north-top)
Entries 714452
ADC
channel

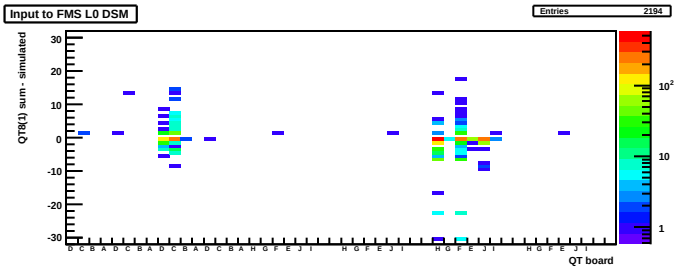

Input to QT2 crate (south-bottom)
Entries 842654
ADC
channel

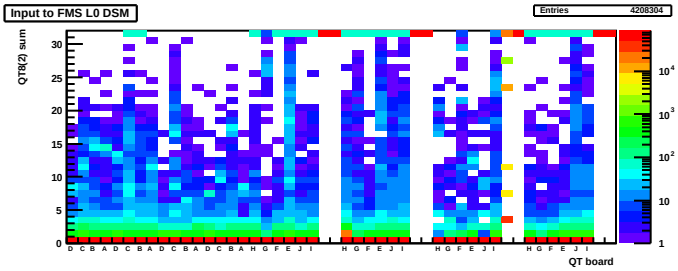

Input to QT4 crate (north-bottom)
Entries 1030694
ADC
channel

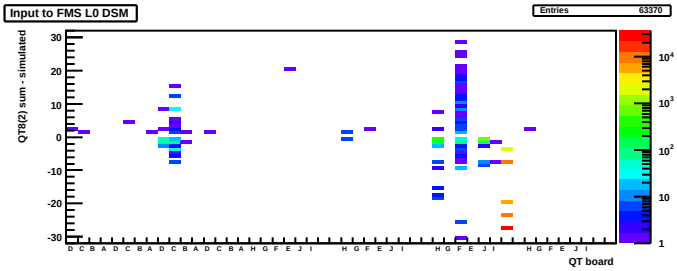

Input to FMS L0 DSM
Entries 4208304
QT8(0) sum
QT board

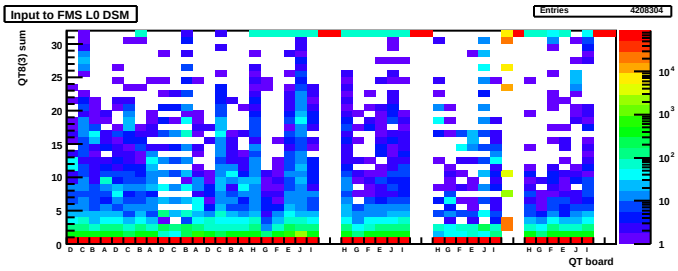

Input to FMS L0 DSM
Entries 1722
QT8(0) sum - simulated
QT board

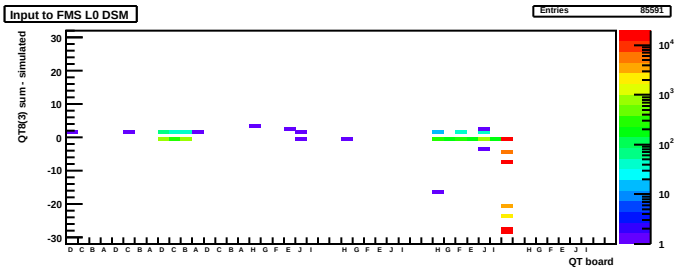

Input to FMS L0 DSM
Entries 4208304
QT8(1) sum
QT board

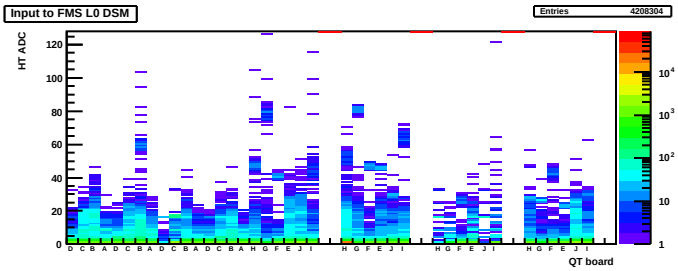

Input to FMS L0 DSM
Entries 2194
QT8(1) sum - simulated
QT board

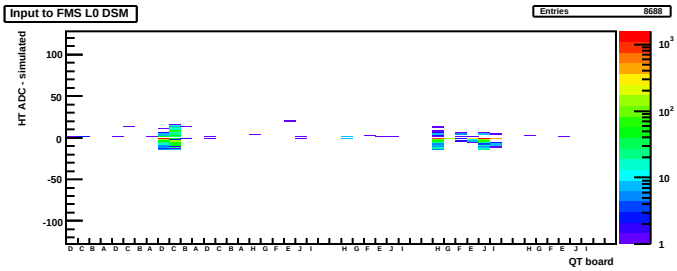

Input to FMS L0 DSM
Entries 4208304
QT8(2) sum
QT board

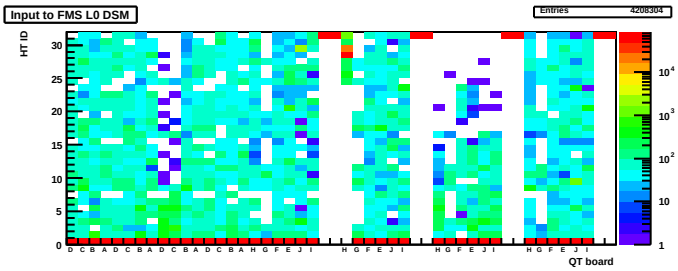

Input to FMS L0 DSM
Entries 63370
QT8(2) sum - simulated
QT board

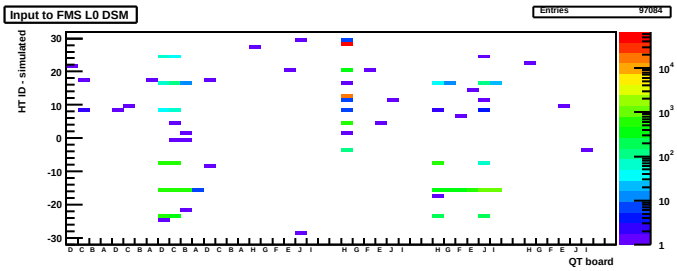

Input to FMS L0 DSM
Entries 4208304
QT8(3) sum
QT board

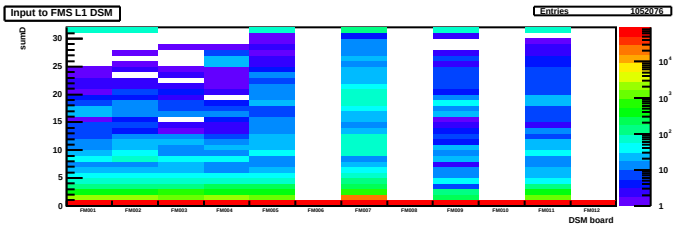

Input to FMS L0 DSM
Entries 85591
QT8(3) sum - simulated
QT board

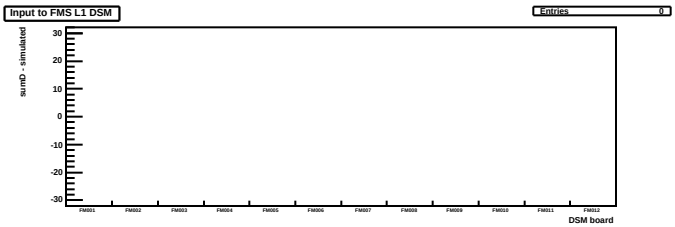

Input to FMS L0 DSM
Entries 4208304
HT ADC
QT board

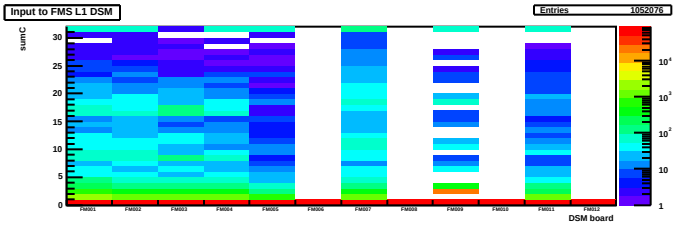

Input to FMS L0 DSM
Entries 8688
HT ADC - simulated
QT board

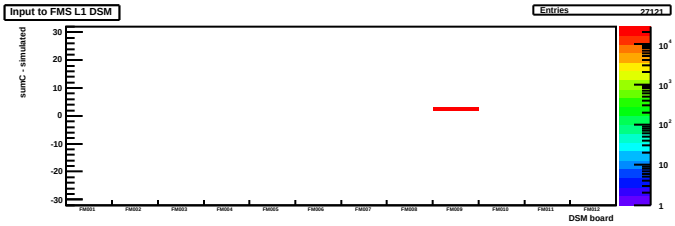

Input to FMS L0 DSM
Entries 4208304
HT ID
QT board

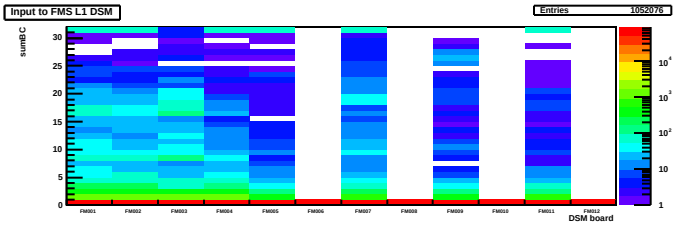

Input to FMS L0 DSM
Entries 97084
HT ID - simulated
QT board

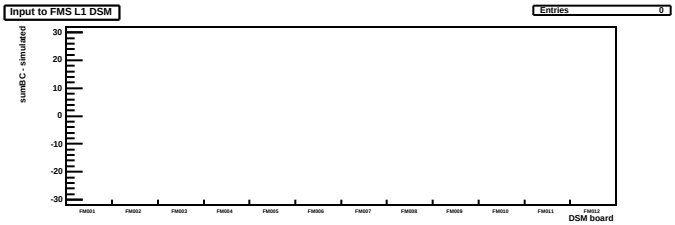

Input to FMS L1 DSM
Entries 1052076
sumD
DSM board

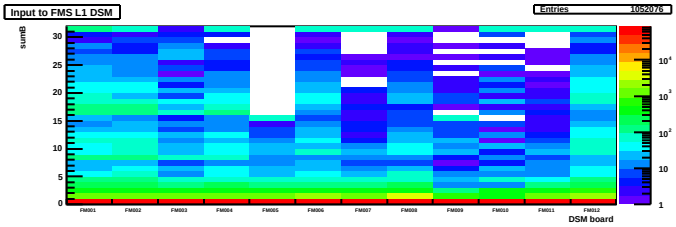

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

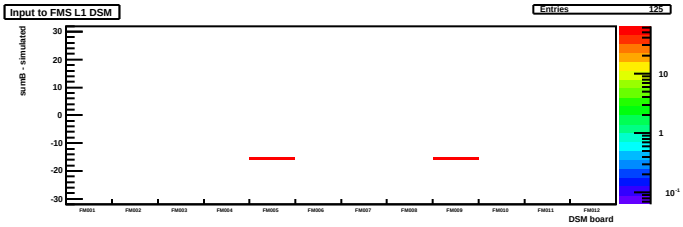

Input to FMS L1 DSM
Entries 1052076
sumC
DSM board

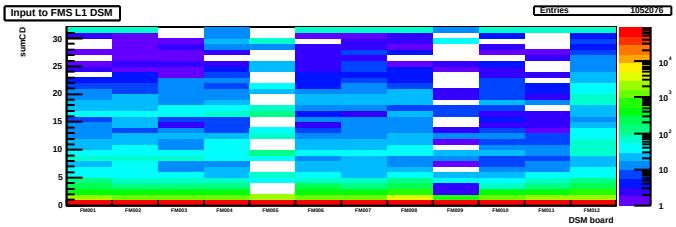

Input to FMS L1 DSM
Entries 27121
sumC - simulated
DSM board

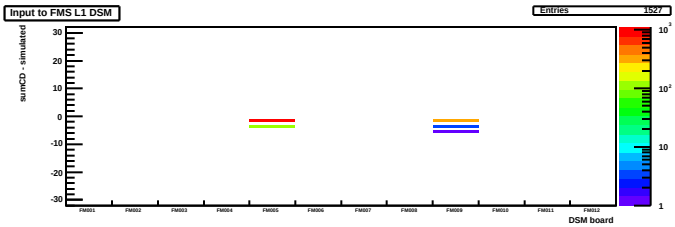

Input to FMS L1 DSM
Entries 1052076
sumBC
DSM board

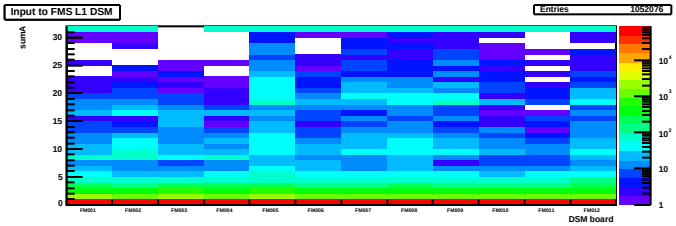

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

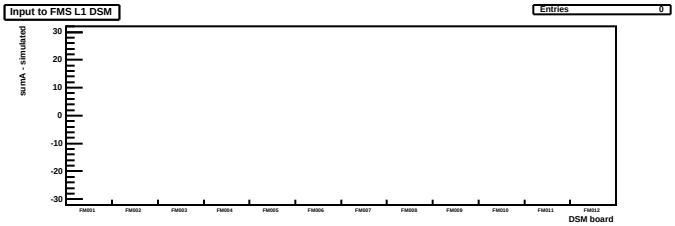

Input to FMS L1 DSM
Entries 1052076
sumB
DSM board

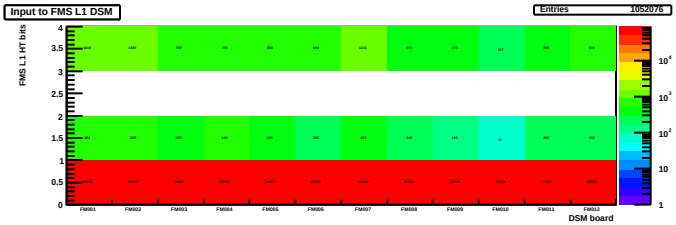

Input to FMS L1 DSM
Entries 125
sumB - simulated
DSM board

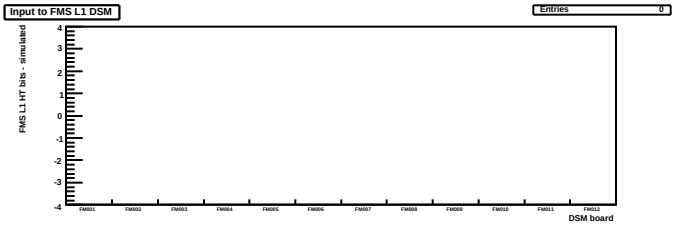

Input to FMS L1 DSM
Entries 1052076
sumCD
DSM board

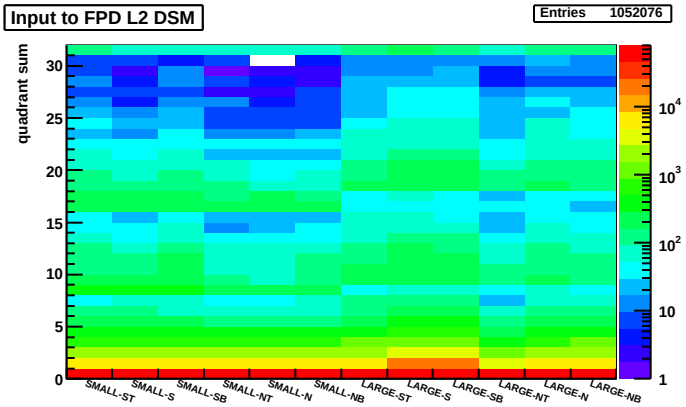

Input to FMS L1 DSM
Entries 1527
sumCD - simulated
DSM board

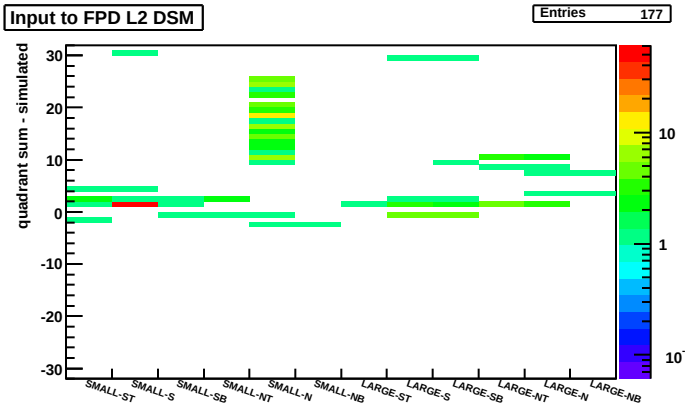

Input to FMS L1 DSM
Entries 1052076
sumA
DSM board

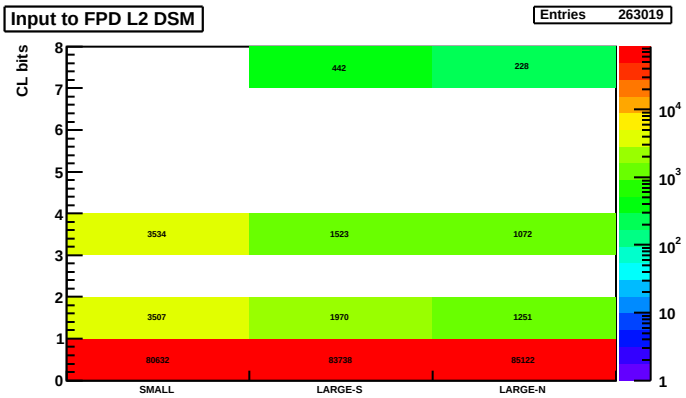

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

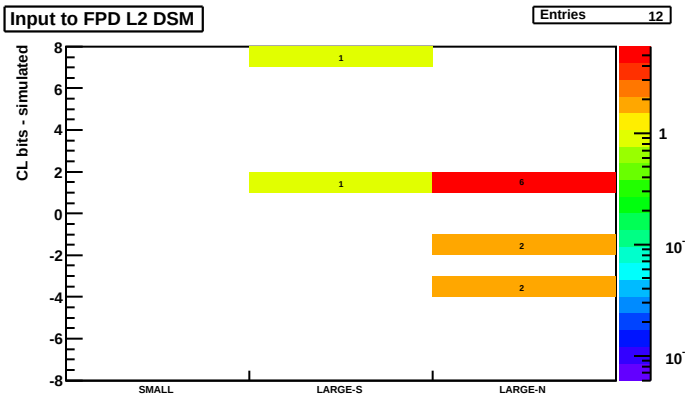

Input to FMS L1 DSM
Entries 1052076
FMS L1 HT bits
DSM board

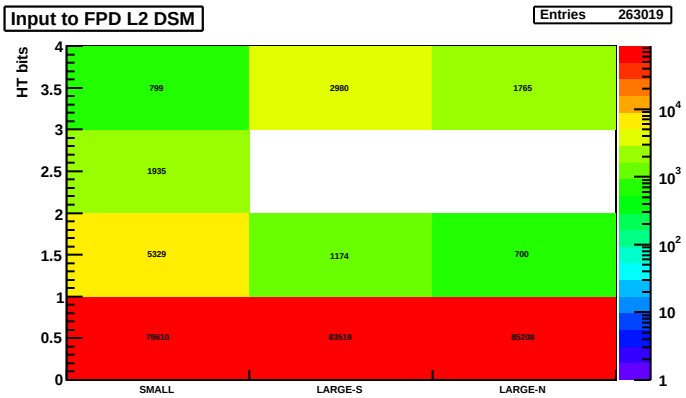

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

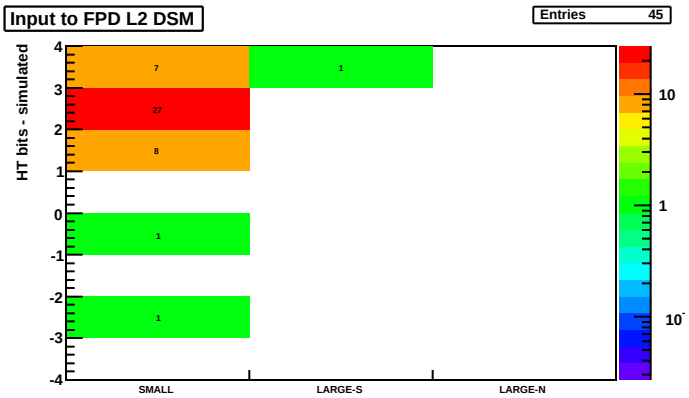

Input to FPD L2 DSM
Entries 1052076
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

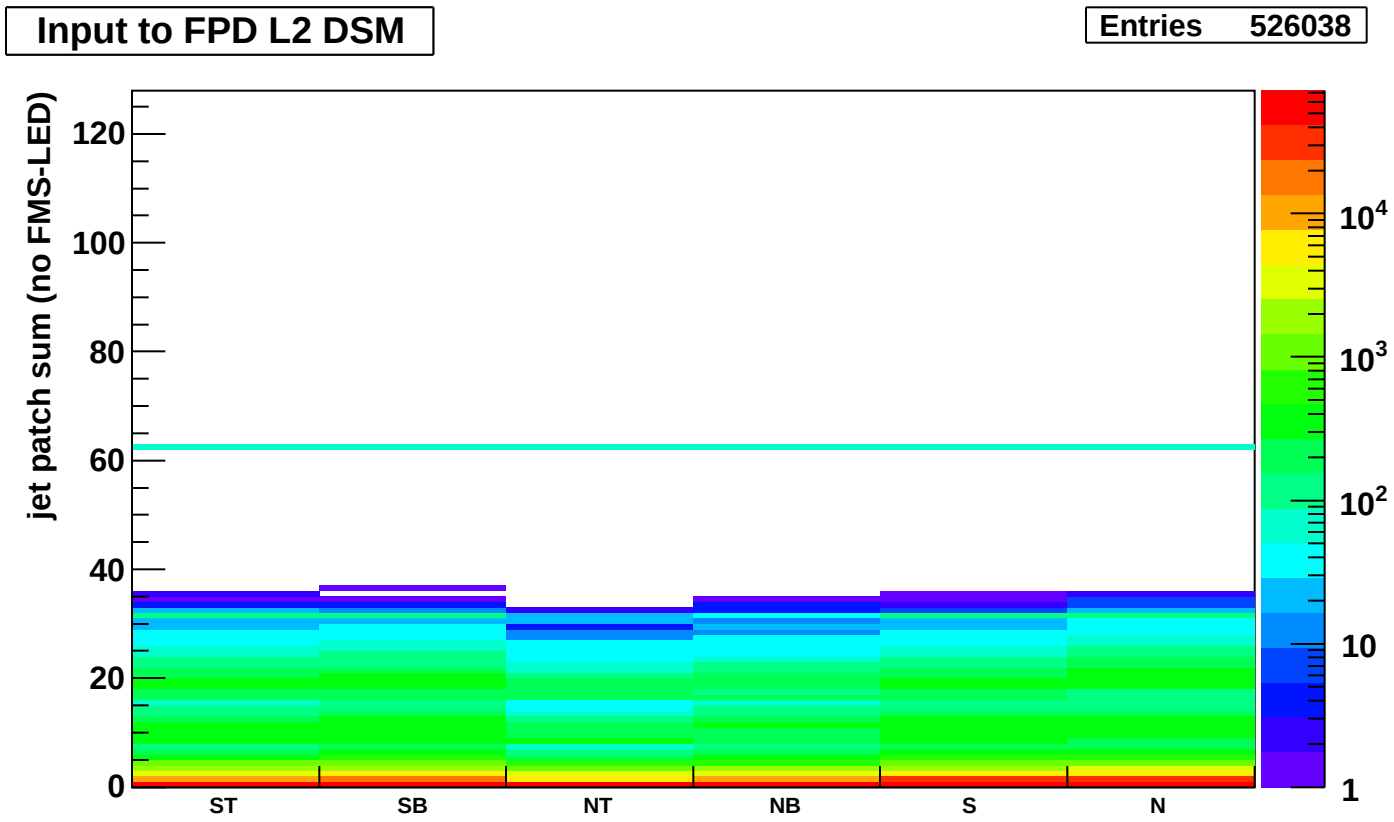

Input to FPD L2 DSM
Entries 177
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

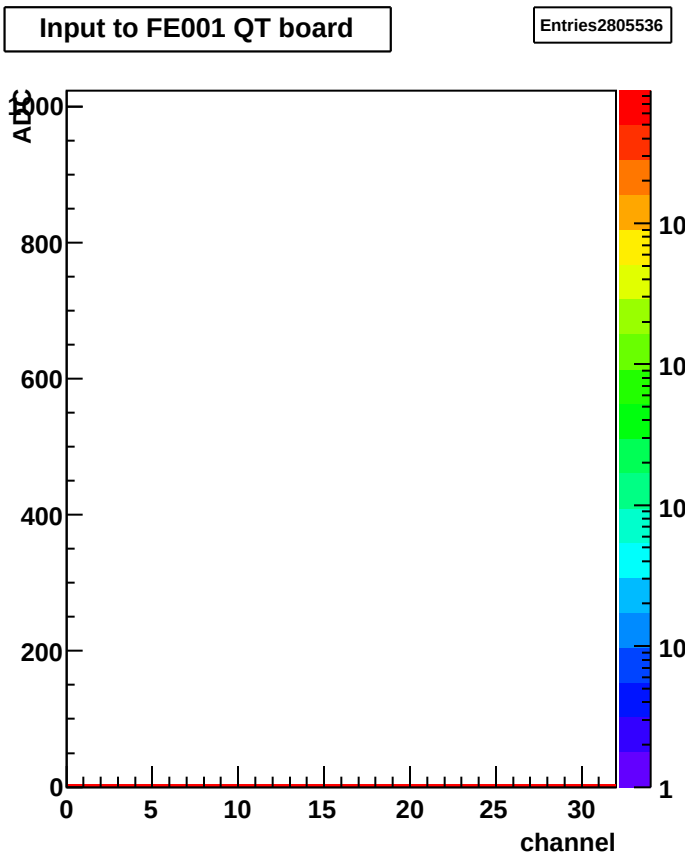

Input to FPD L2 DSM
Entries 263019
CL bits
442
228
3534
1523
1072
3507
1970
1251
80632
83738
85122
SMALL
LARGE-S
LARGE-N

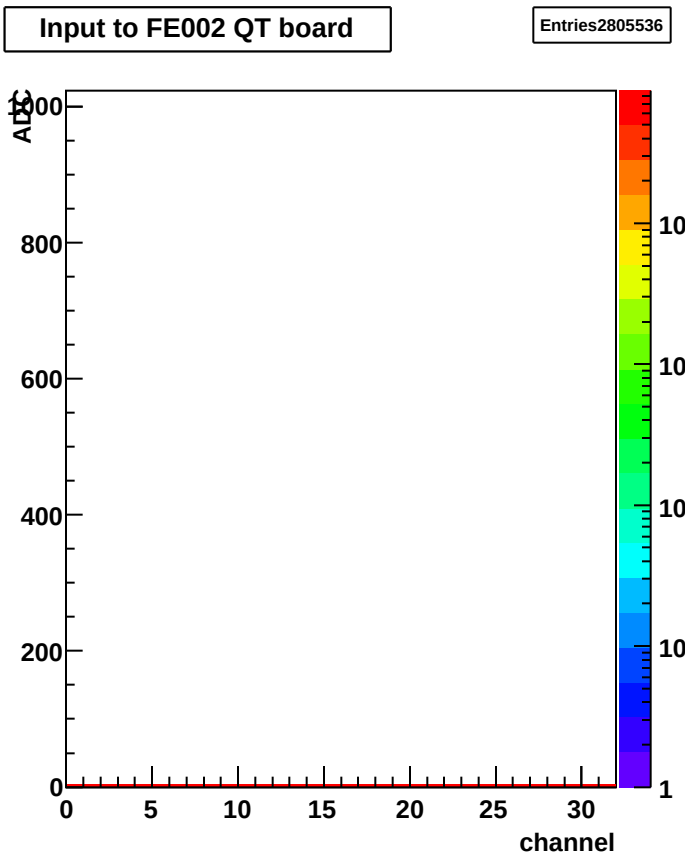

Input to FPD L2 DSM
Entries 12
CL bits - simulated
1
1
6
2
2
SMALL
LARGE-S
LARGE-N

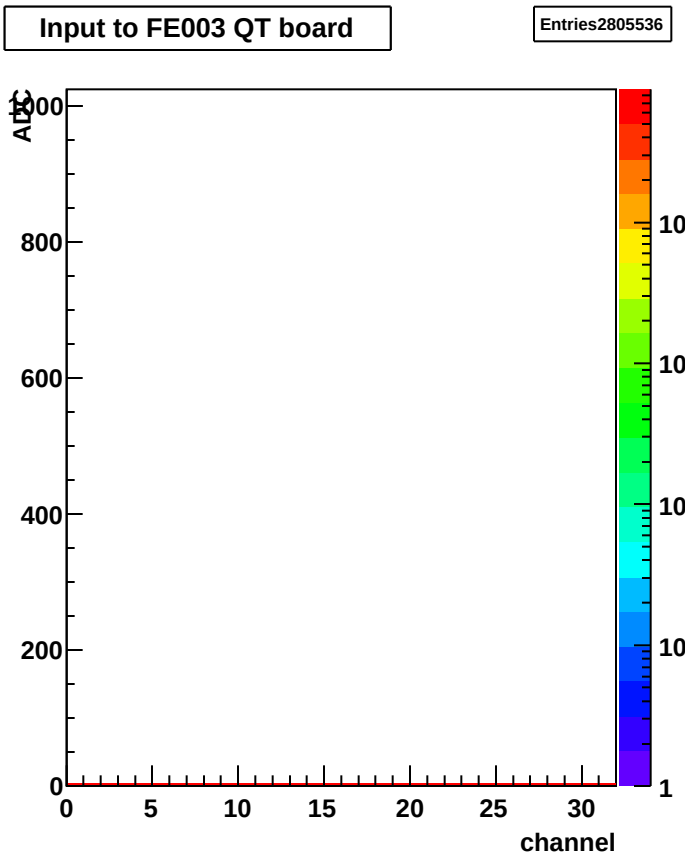

Input to FPD L2 DSM
Entries 263019
HT bits
799
2980
1765
1935
5329
1174
700
79610
83519
85208
SMALL
LARGE-S
LARGE-N

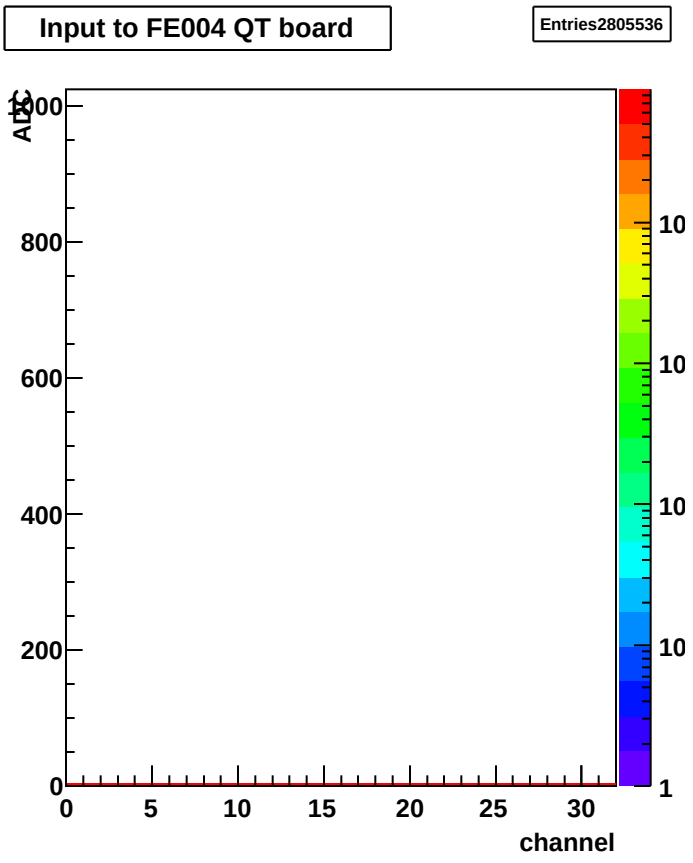

Input to FPD L2 DSM
Entries 45
HT bits - simulated
7
1
27
8
1
1
SMALL
LARGE-S
LARGE-N

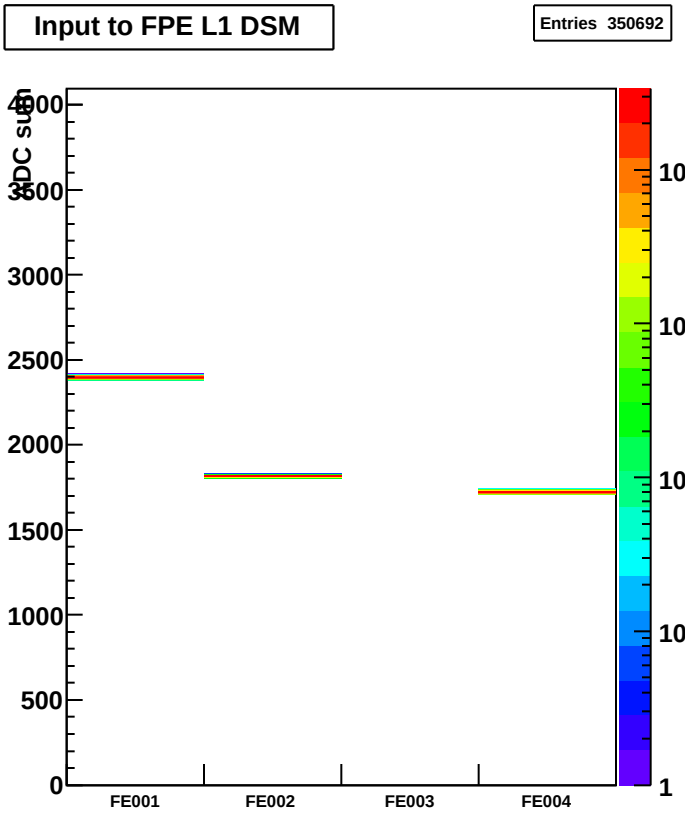

Input to FPD L2 DSM
Entries 526038
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

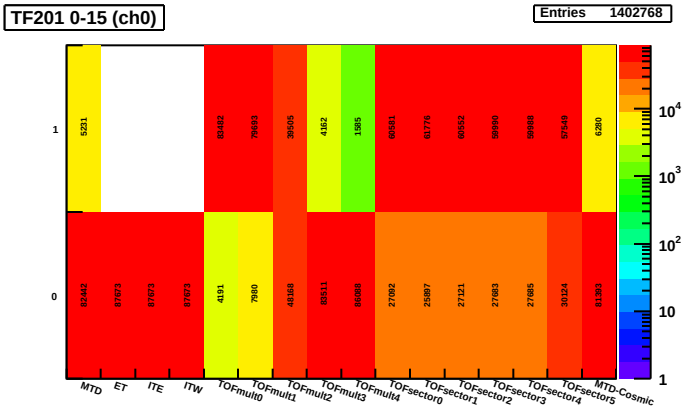

Input to FE001 QT board
Entries2805536
ADC
channel

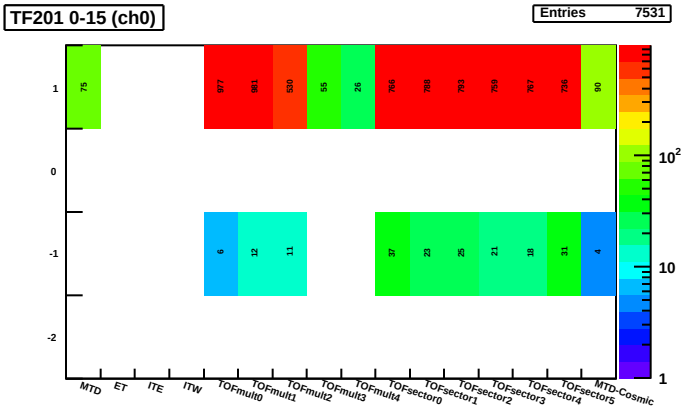

Input to FE002 QT board
Entries2805536
ADC
channel

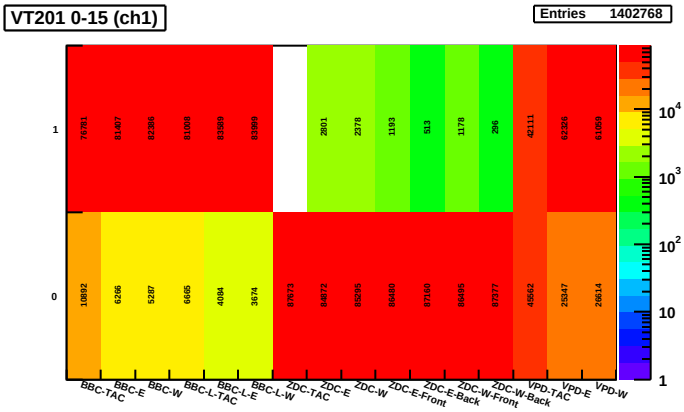

Input to FE003 QT board
Entries2805536
ADC
channel

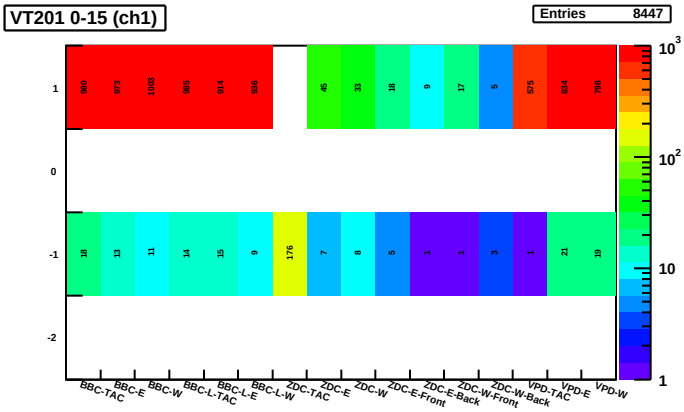

Input to FE004 QT board
Entries2805536
ADC
channel

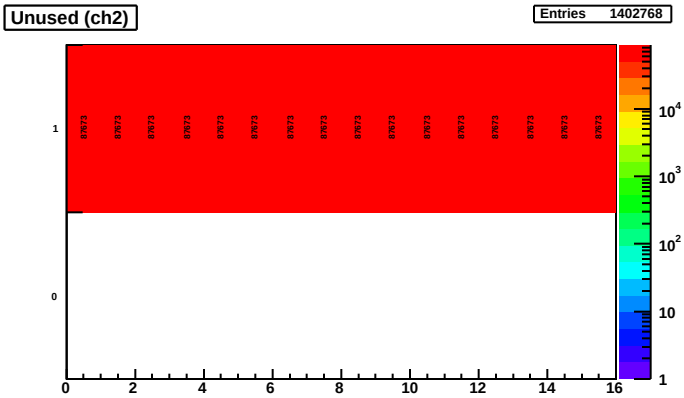

Input to FPE L1 DSM
Entries 350692
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

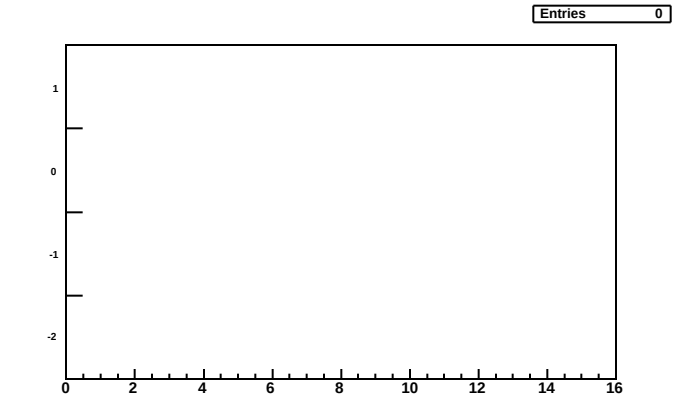

TF201 0-15 (ch0)
Entries 1402768
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 7531
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1402768
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 8447
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1402768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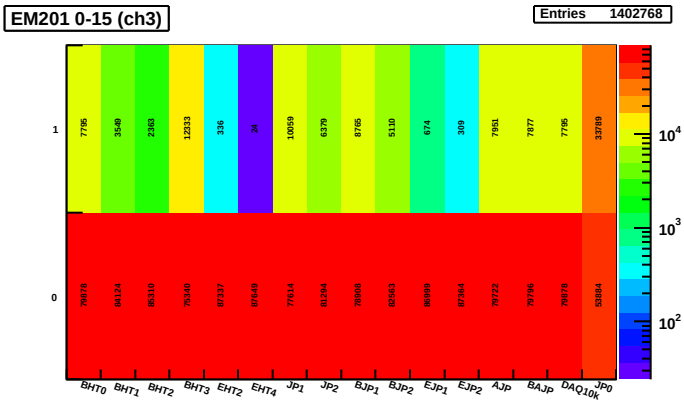
EM201 0-15 (ch3)
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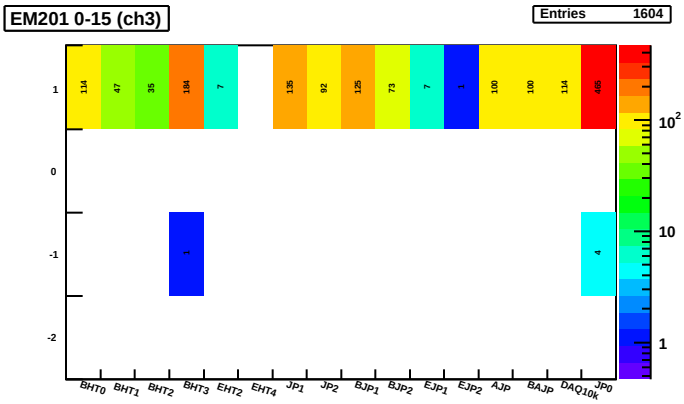
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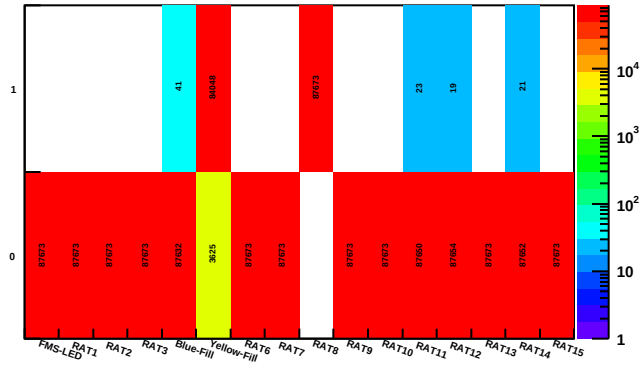
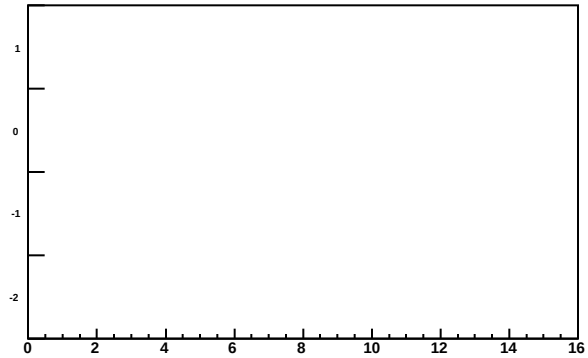
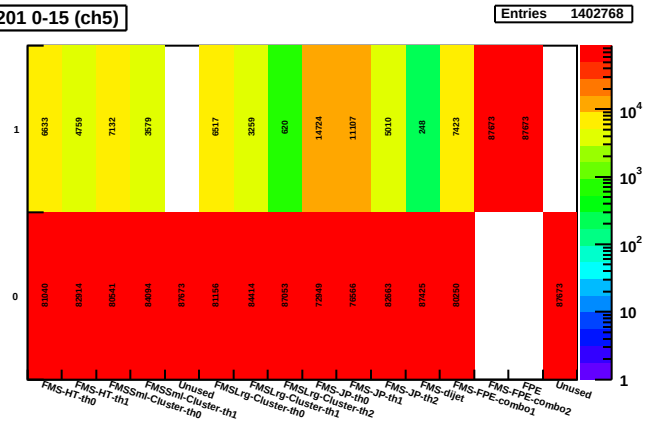
RAT board (ch4)





FP201 0-15 (ch5)



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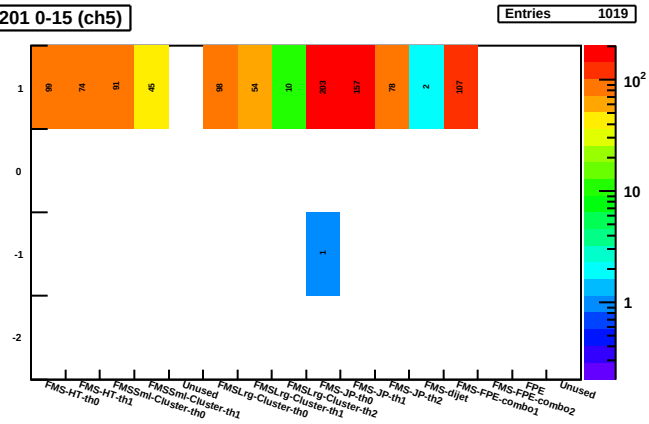

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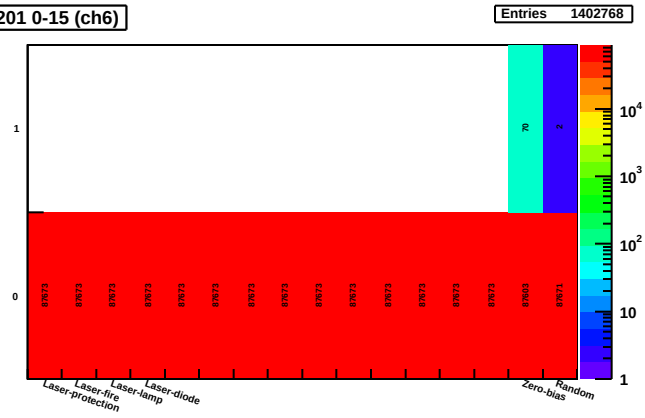

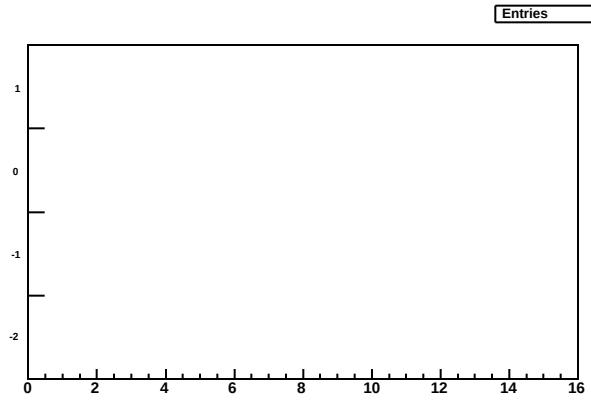
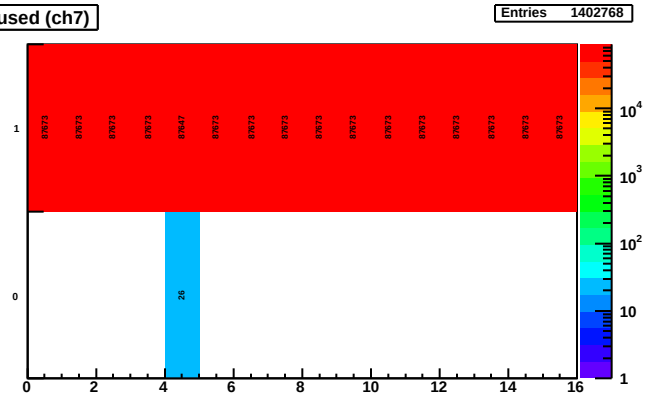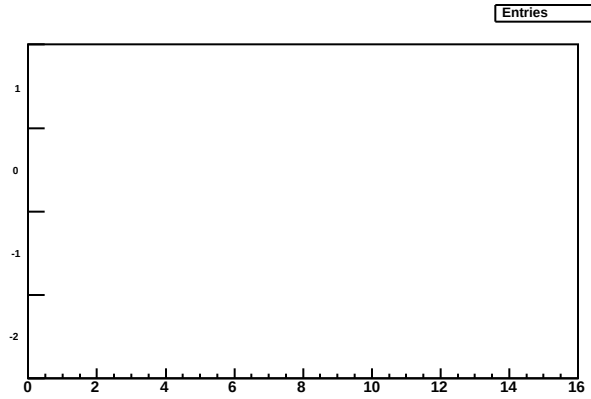
Unused (ch7)