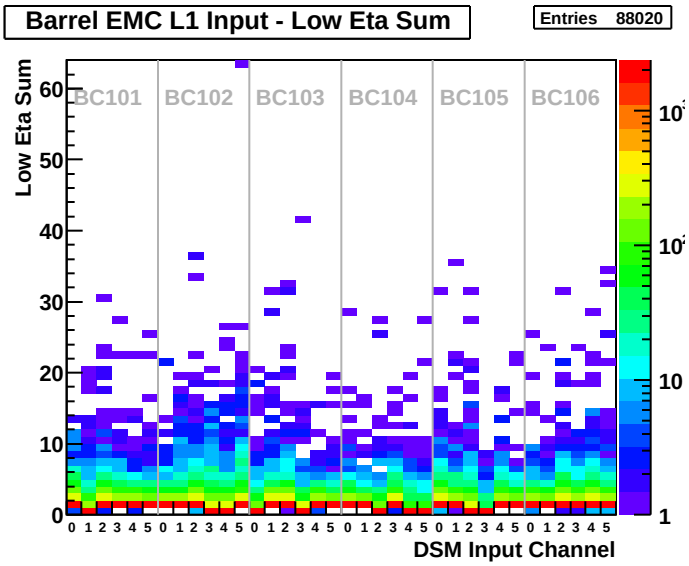
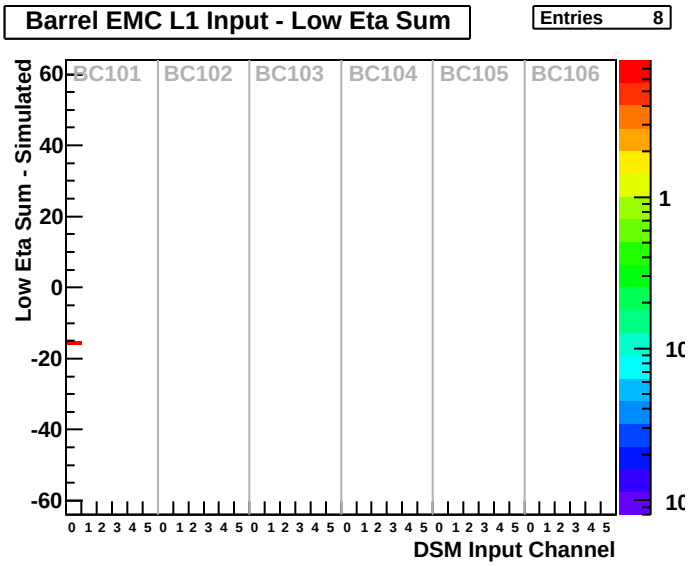
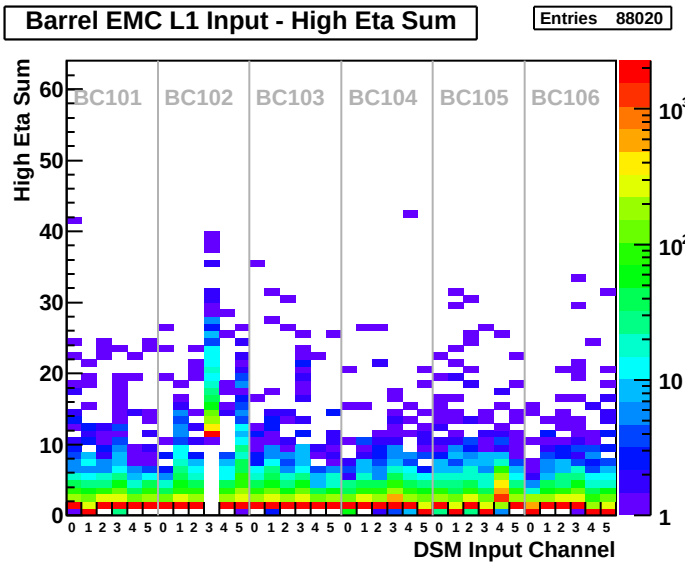
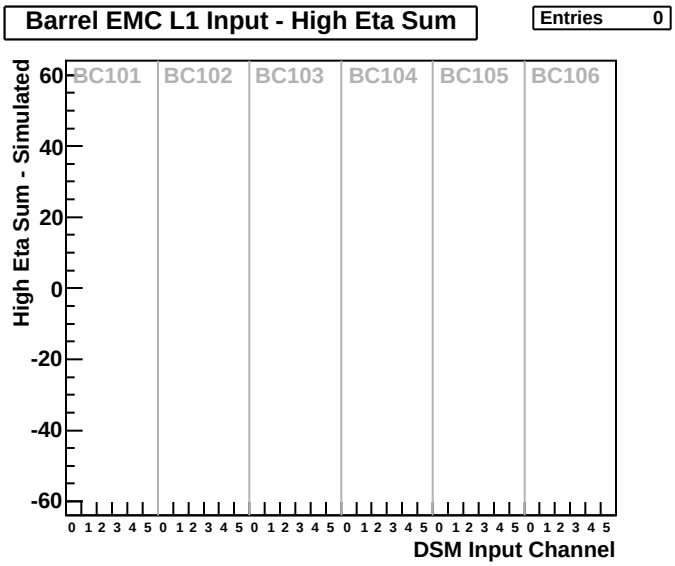
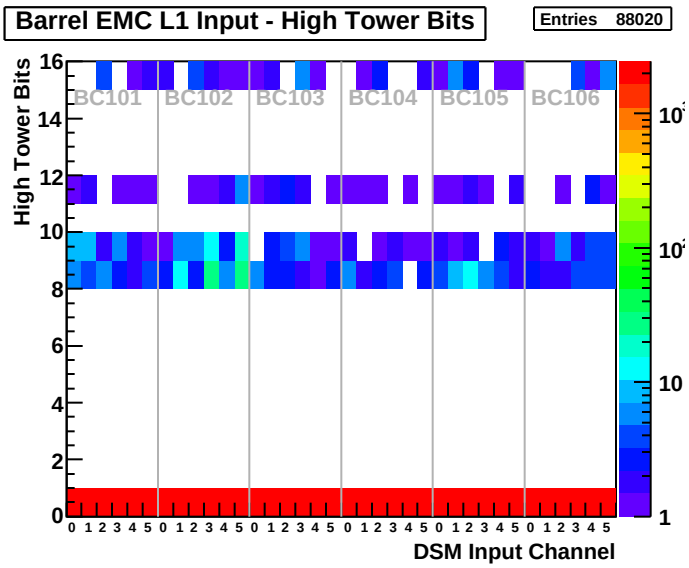
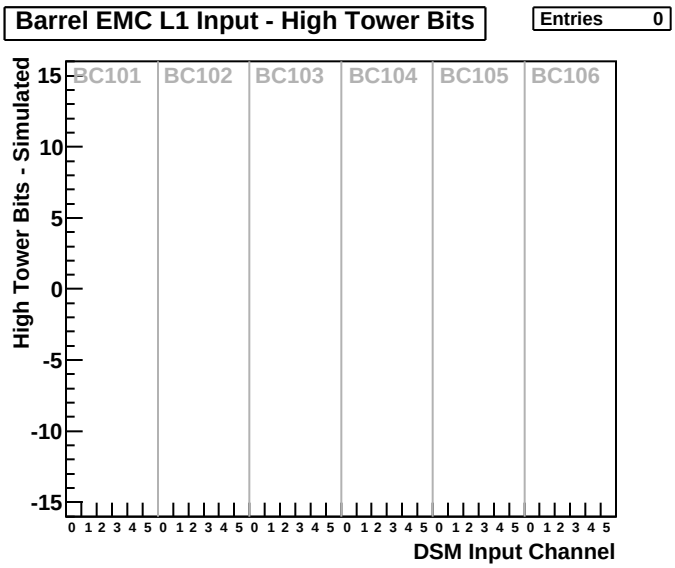

Barrel EMC L1 Input - Low Eta Sum
Entries 88020
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

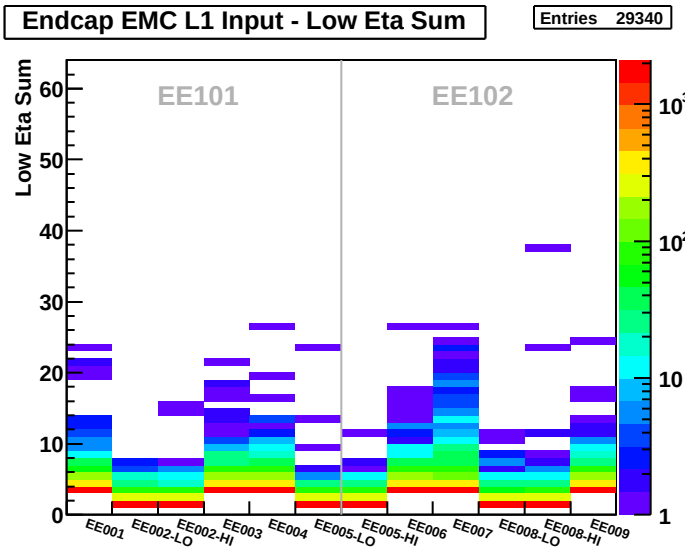

Barrel EMC L1 Input - Low Eta Sum
Entries 8
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

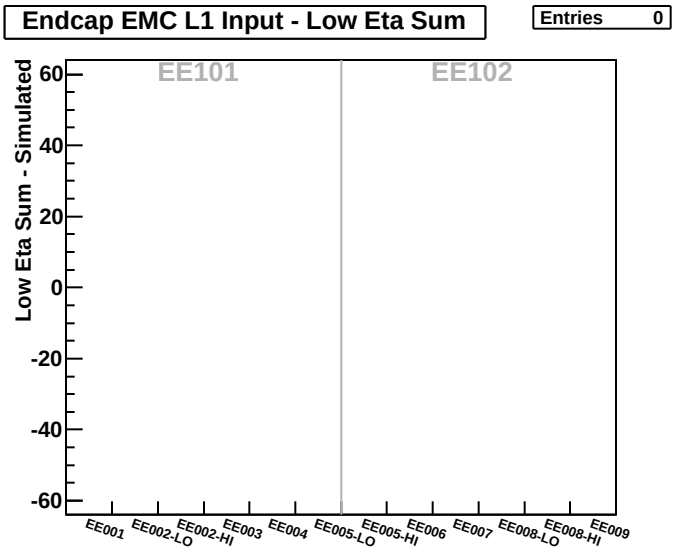

Barrel EMC L1 Input - High Eta Sum
Entries 88020
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

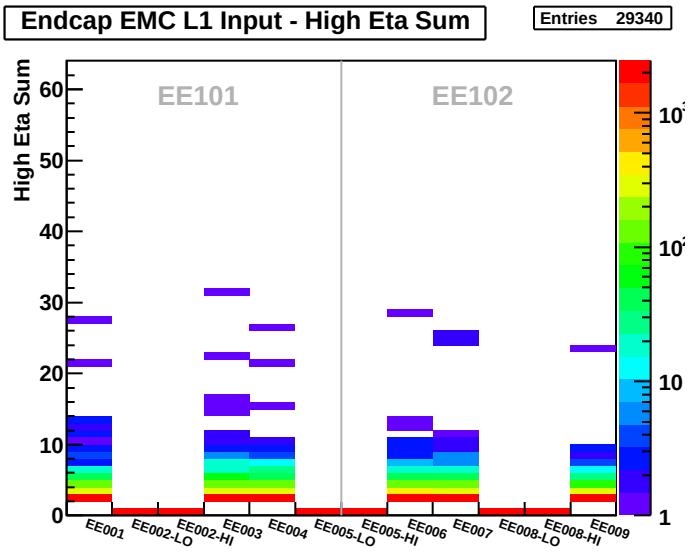

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

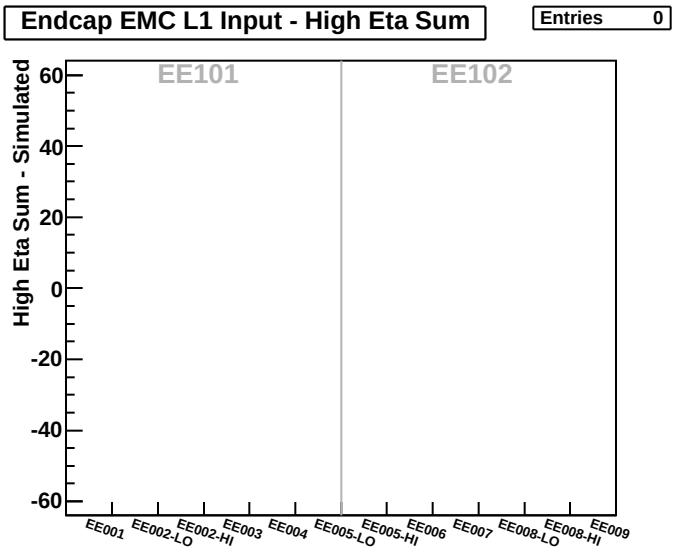

Barrel EMC L1 Input - High Tower Bits
Entries 88020
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

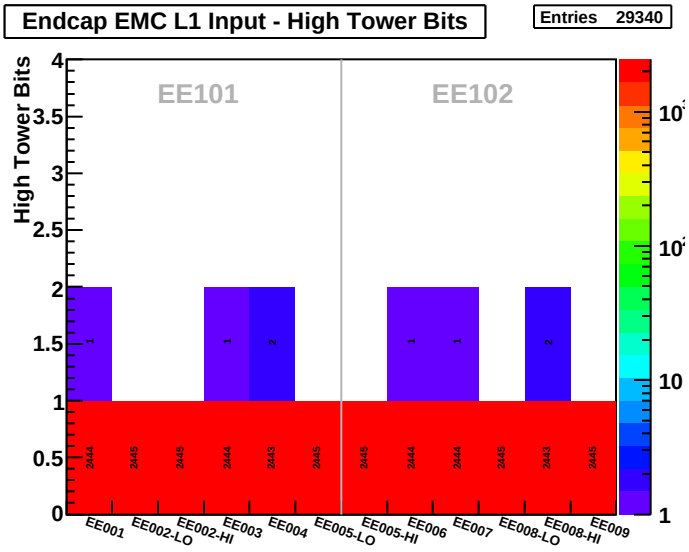

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

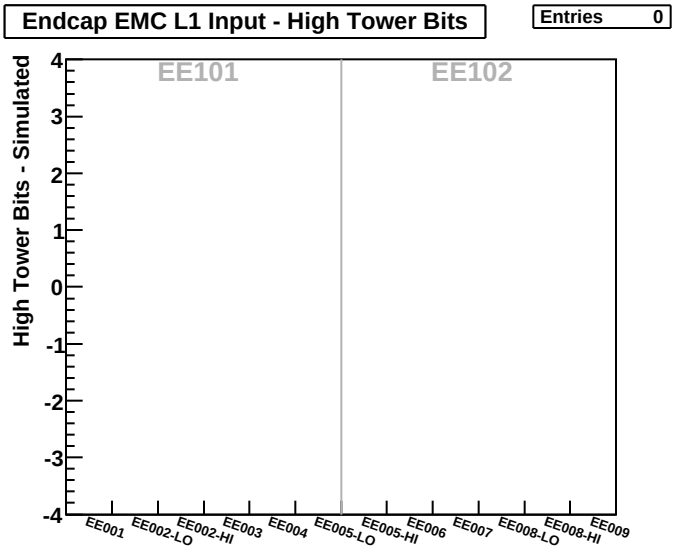

Endcap EMC L1 Input - Low Eta Sum
Entries 29340
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

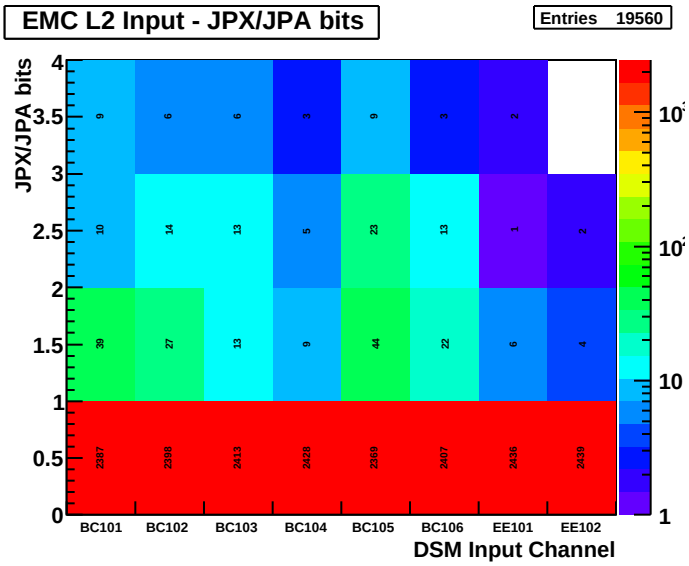

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

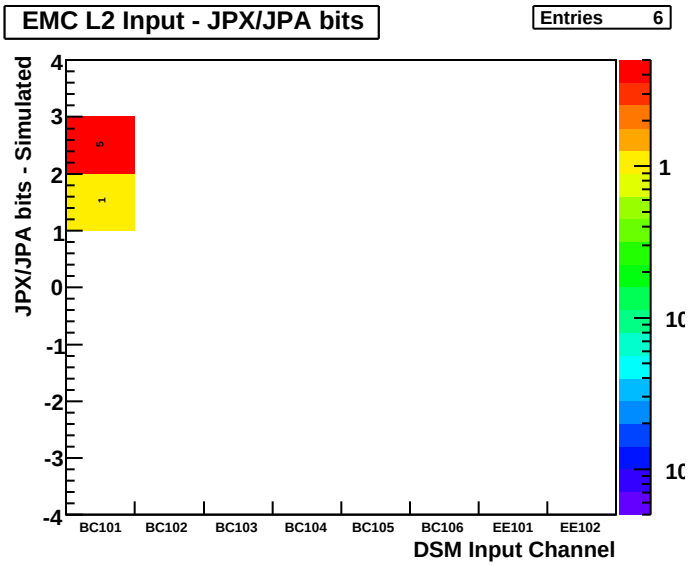

Endcap EMC L1 Input - High Eta Sum
Entries 29340
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

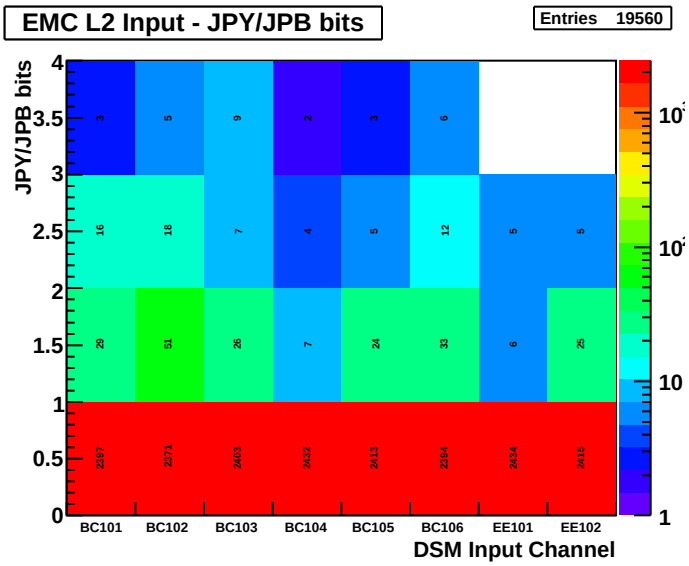

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

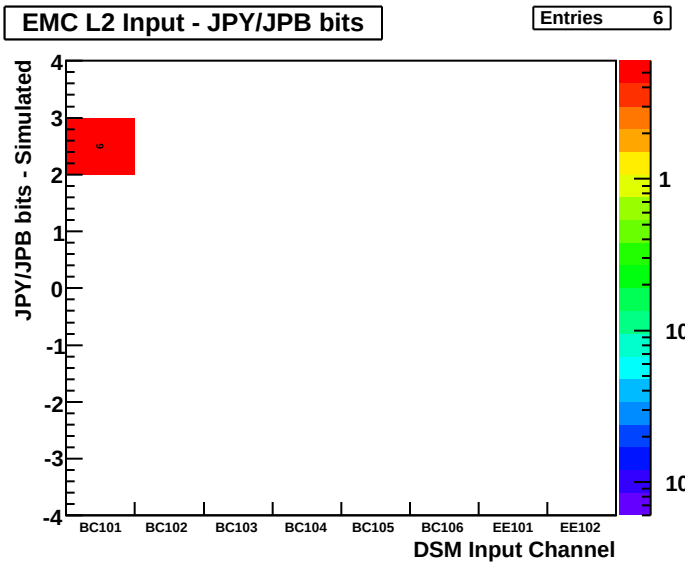

Endcap EMC L1 Input - High Tower Bits
Entries 29340
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

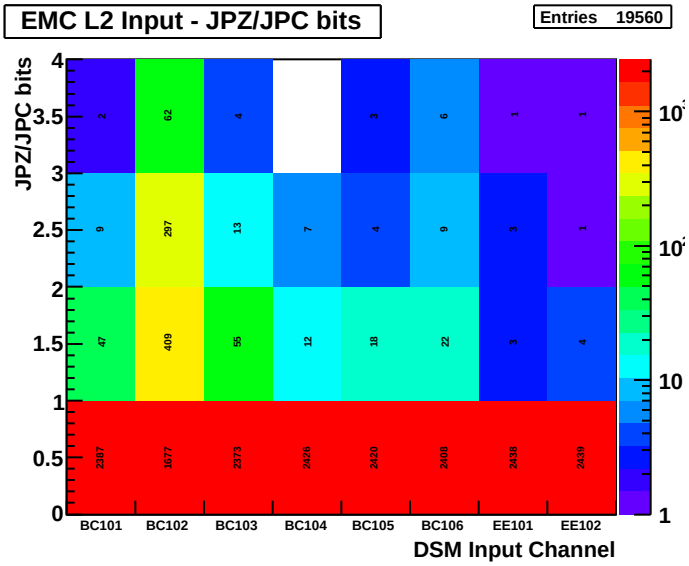

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

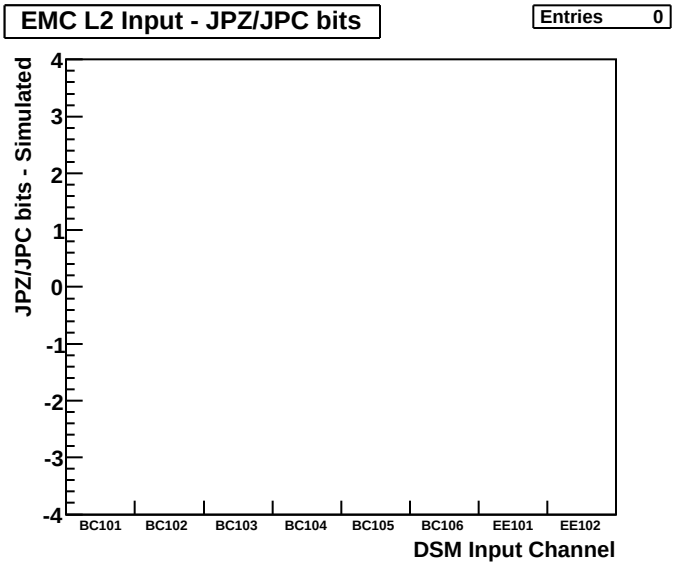

EMC L2 Input - JPX/JPA bits
Entries 19560
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

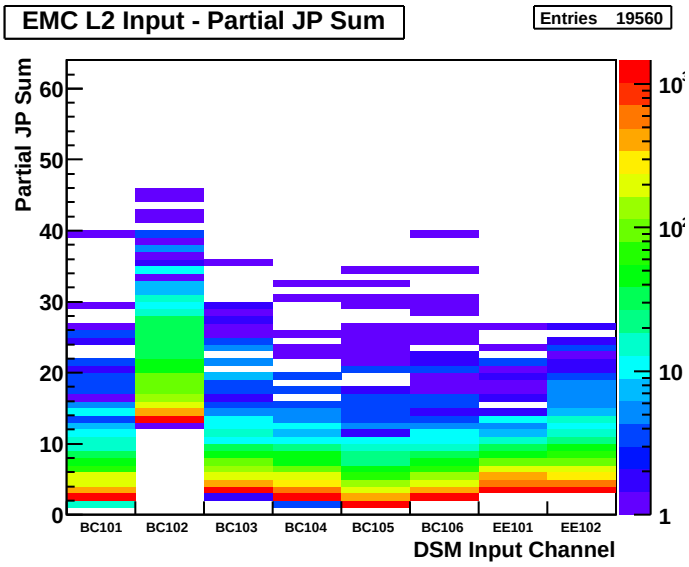

EMC L2 Input - JPX/JPA bits
Entries 6
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

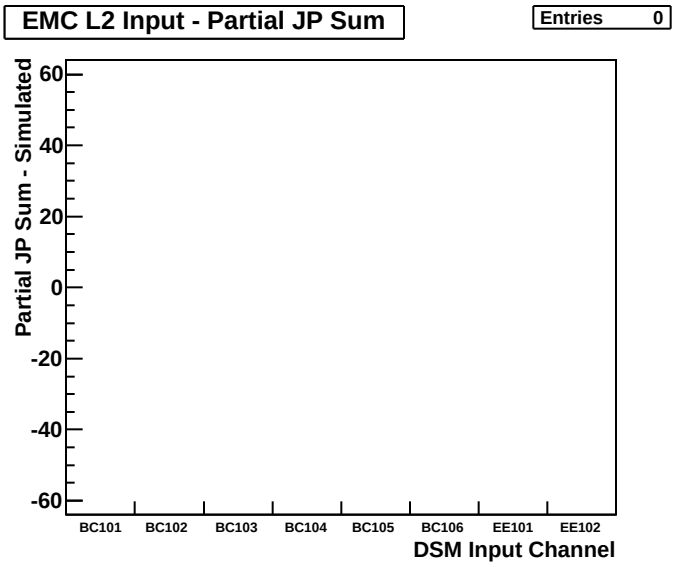

EMC L2 Input - JPY/JPB bits
Entries 19560
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

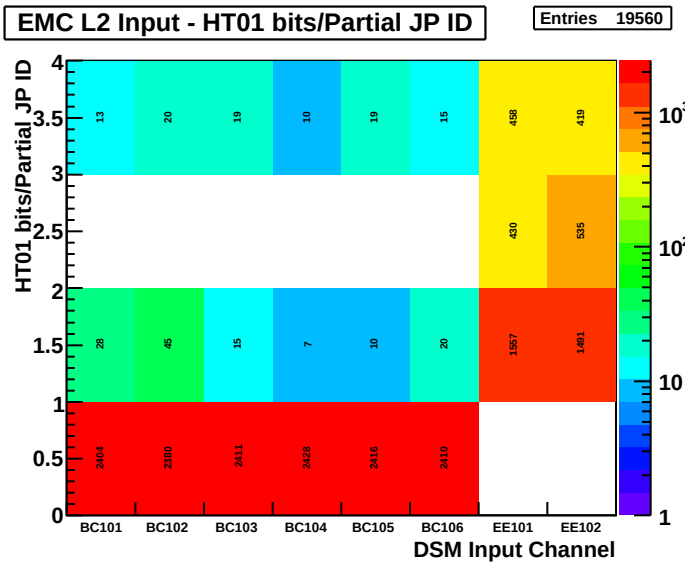

EMC L2 Input - JPY/JPB bits
Entries 6
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

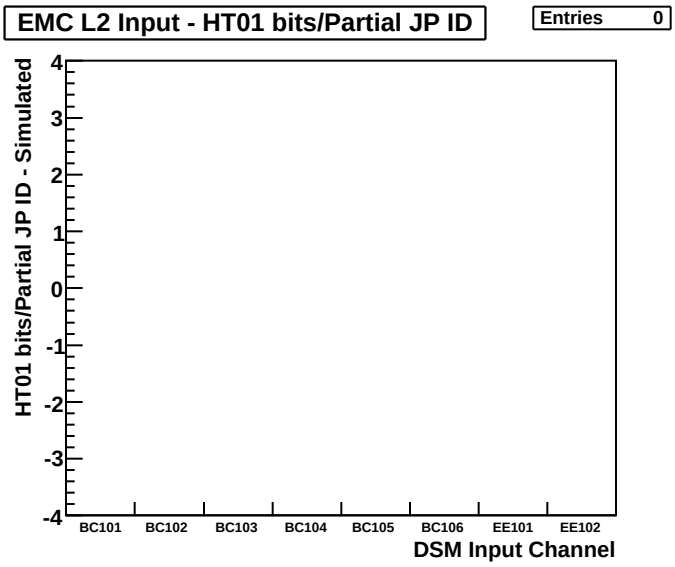

EMC L2 Input - JPZ/JPC bits
Entries 19560
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

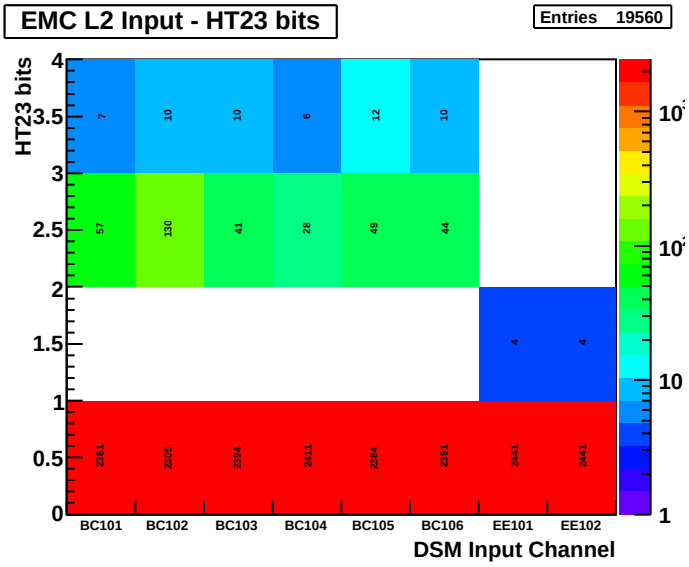

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

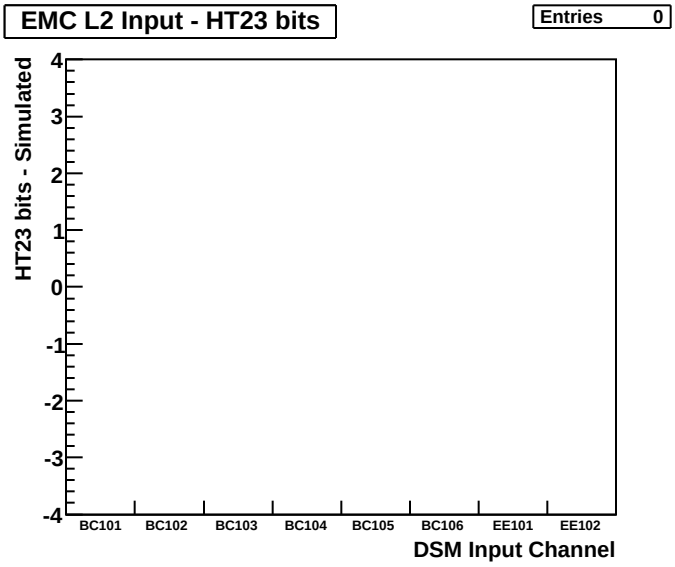

EMC L2 Input - Partial JP Sum
Entries 19560
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

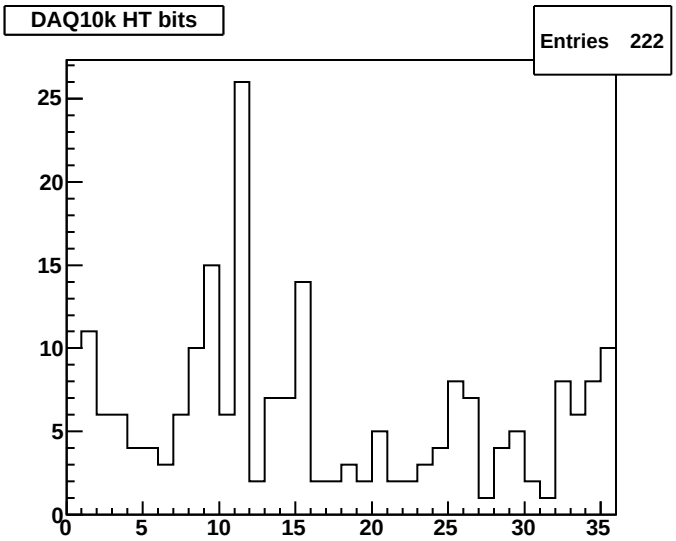

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

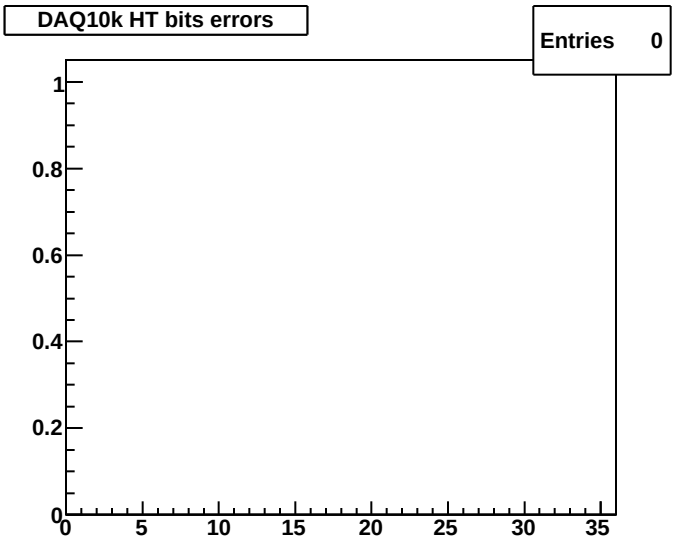

EMC L2 Input - HT01 bits/Partial JP ID
Entries 19560
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

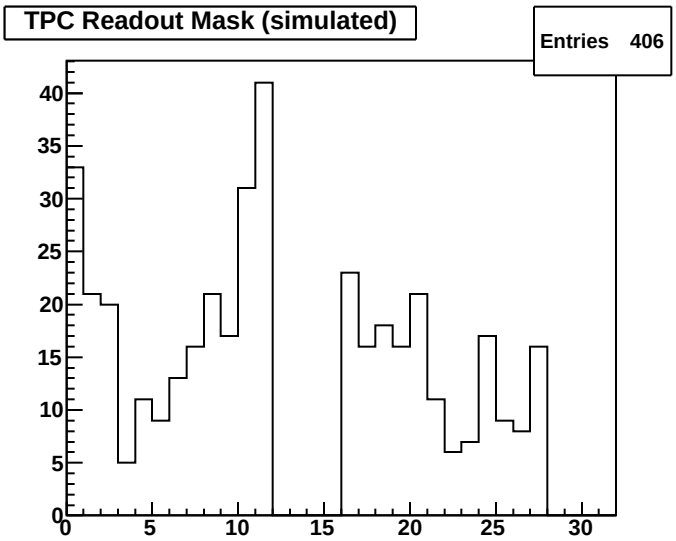

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

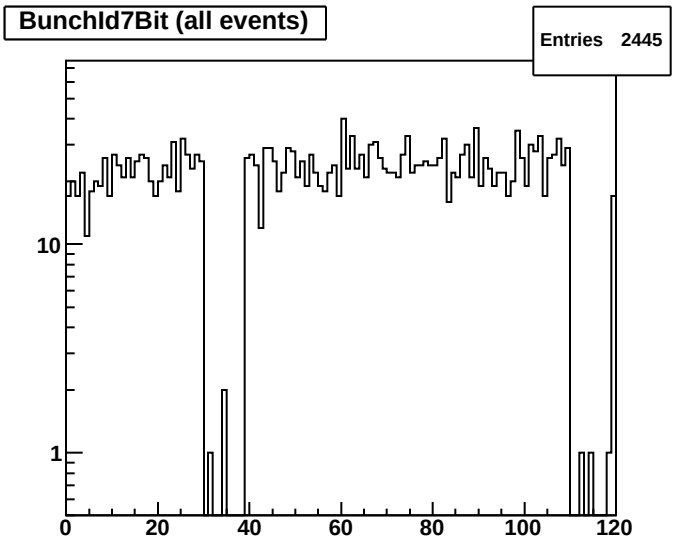

EMC L2 Input - HT23 bits
Entries 19560
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

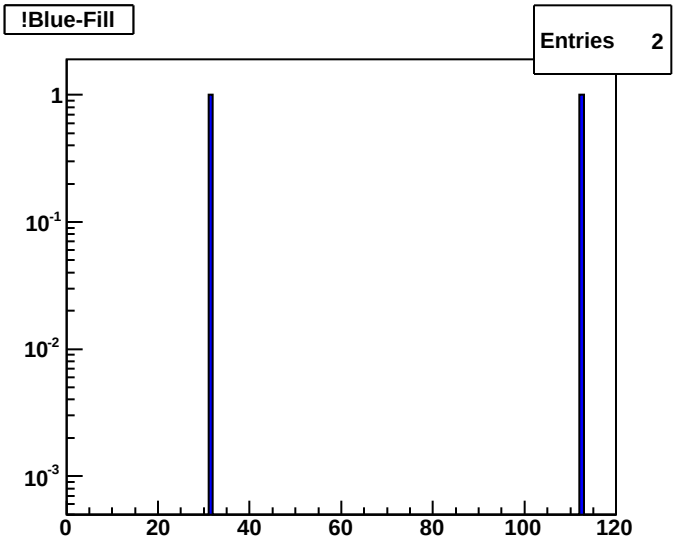

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

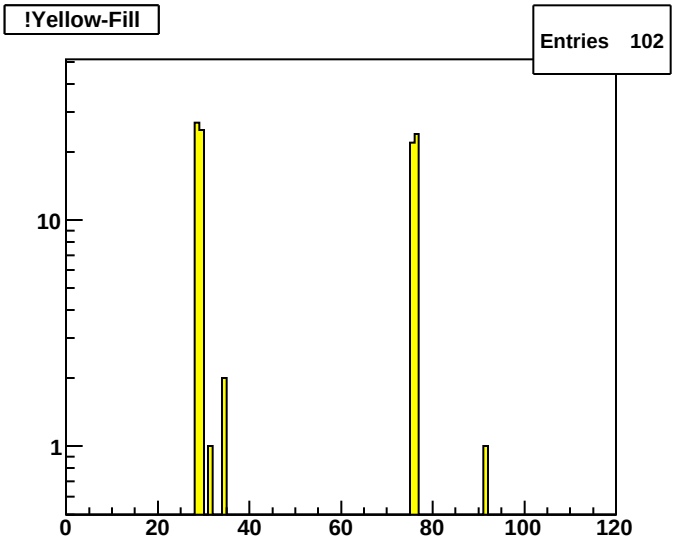

DAQ10k HT bits
Entries 222

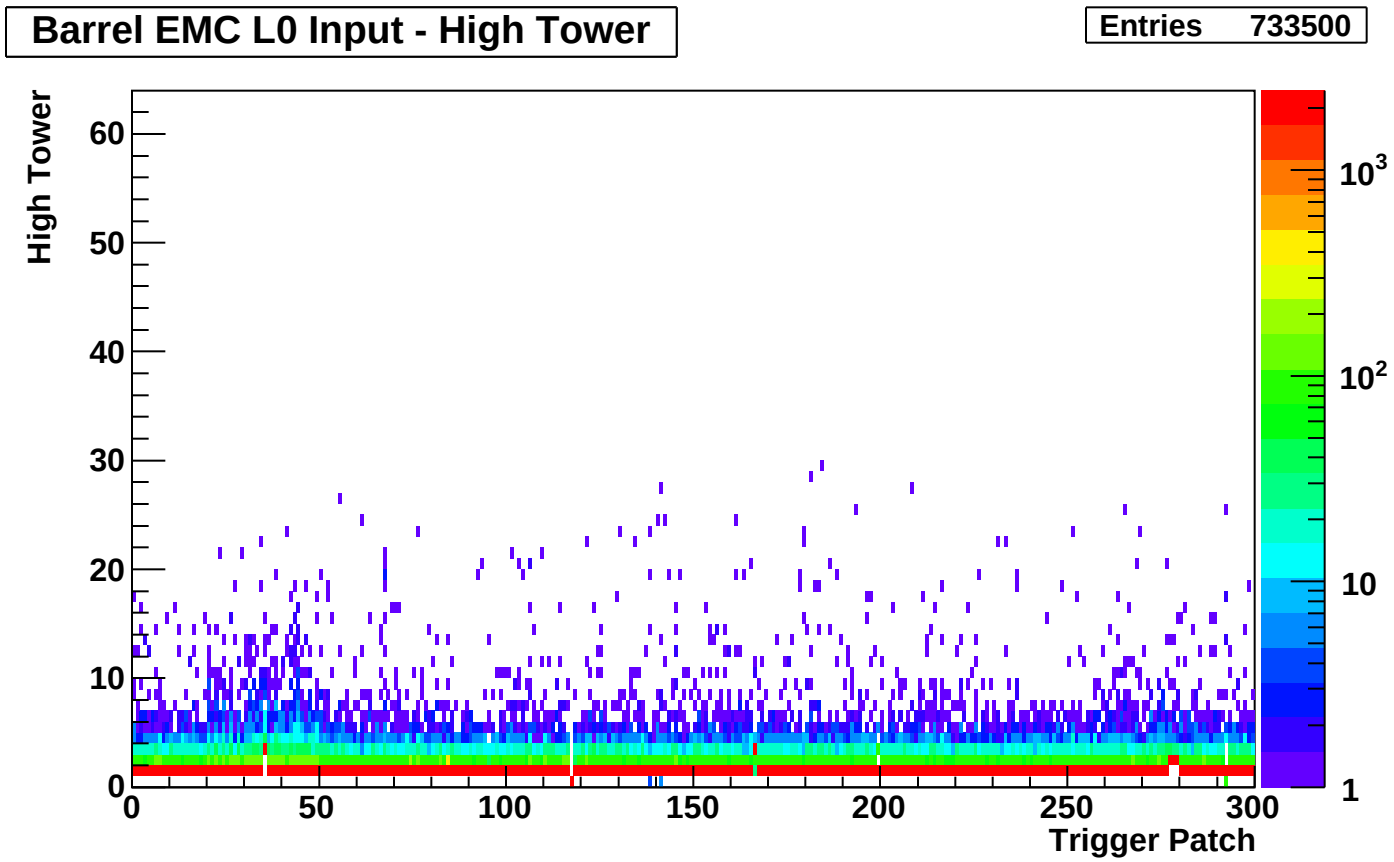

DAQ10k HT bits errors
Entries 0

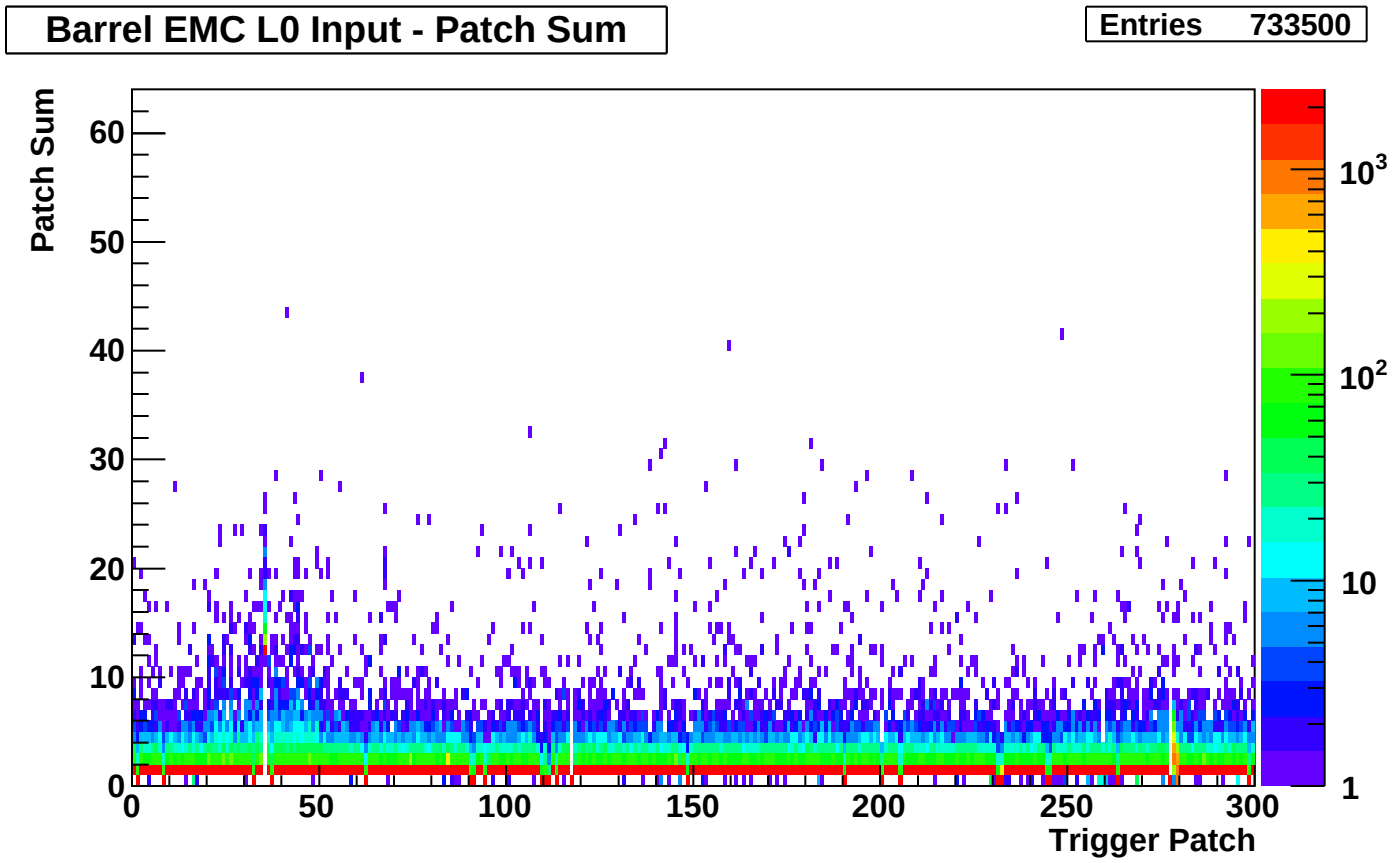

TPC Readout Mask (simulated)
Entries 406

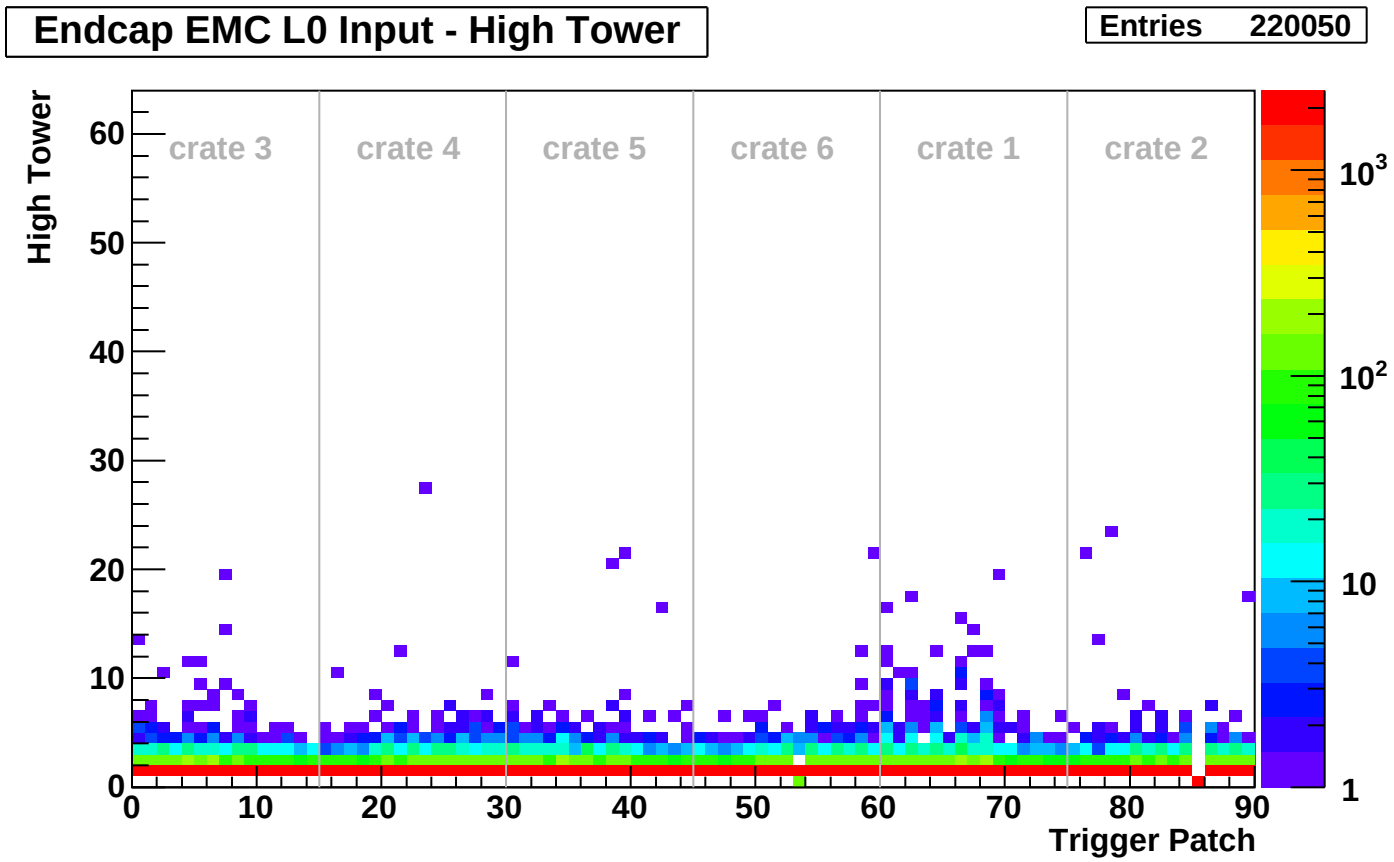

BunchId7Bit (all events)
Entries 2445

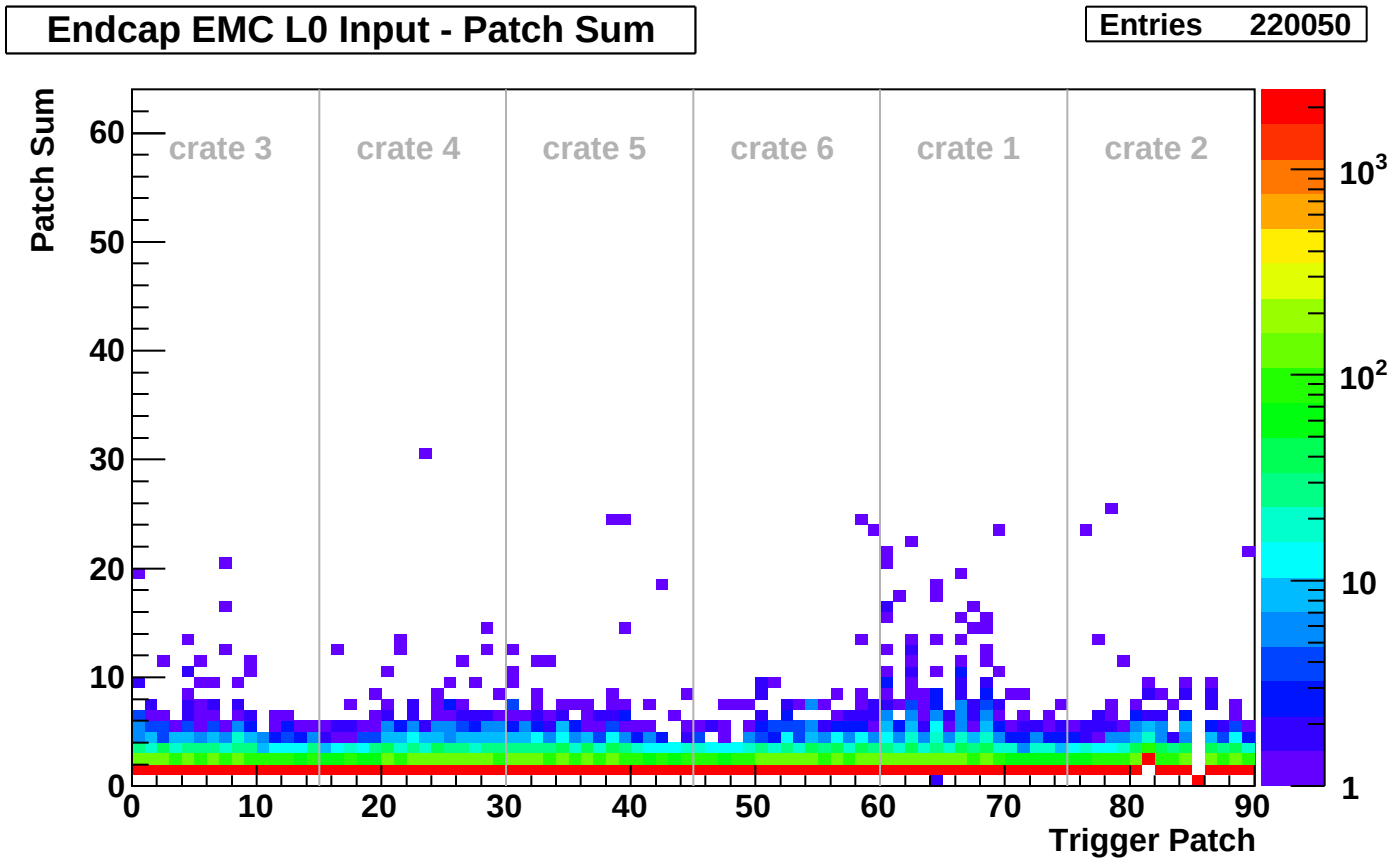

!Blue-Fill
Entries 2

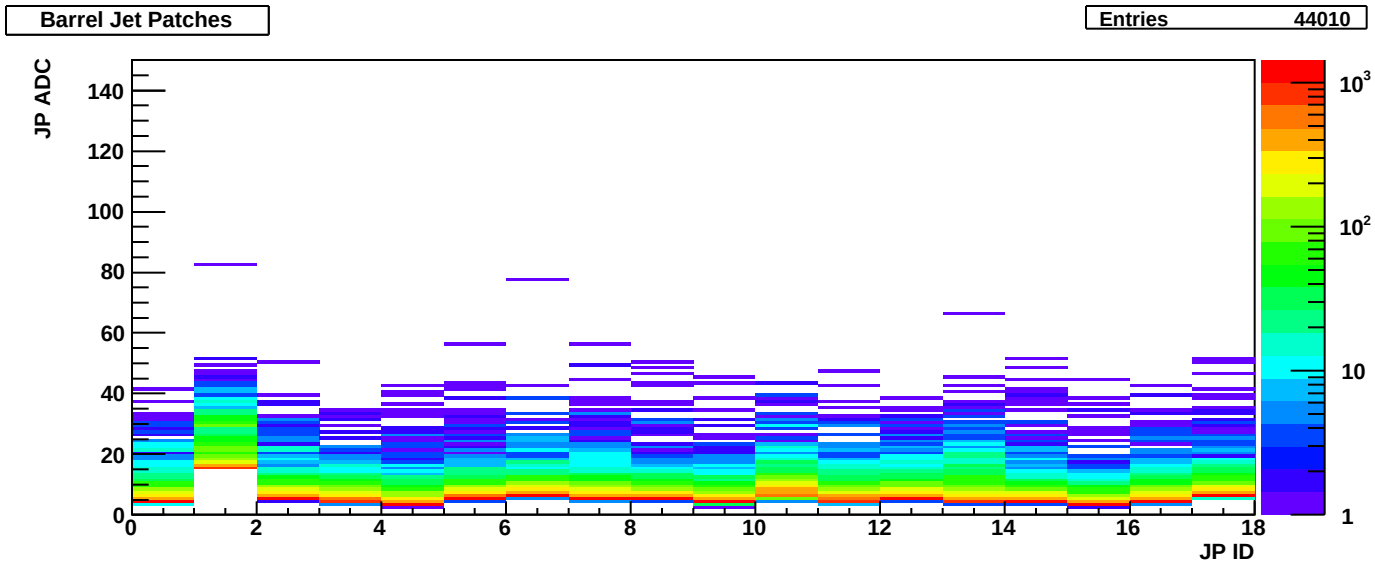

!Yellow-Fill
Entries 102

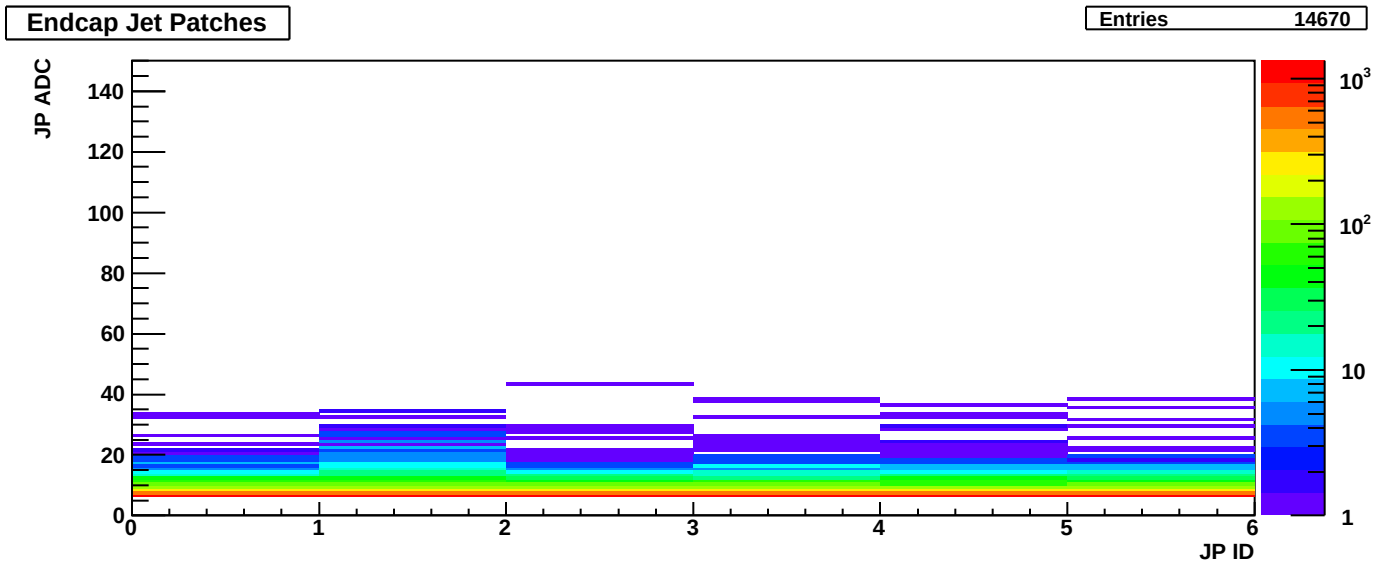

Barrel EMC L0 Input - High Tower
Entries 733500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

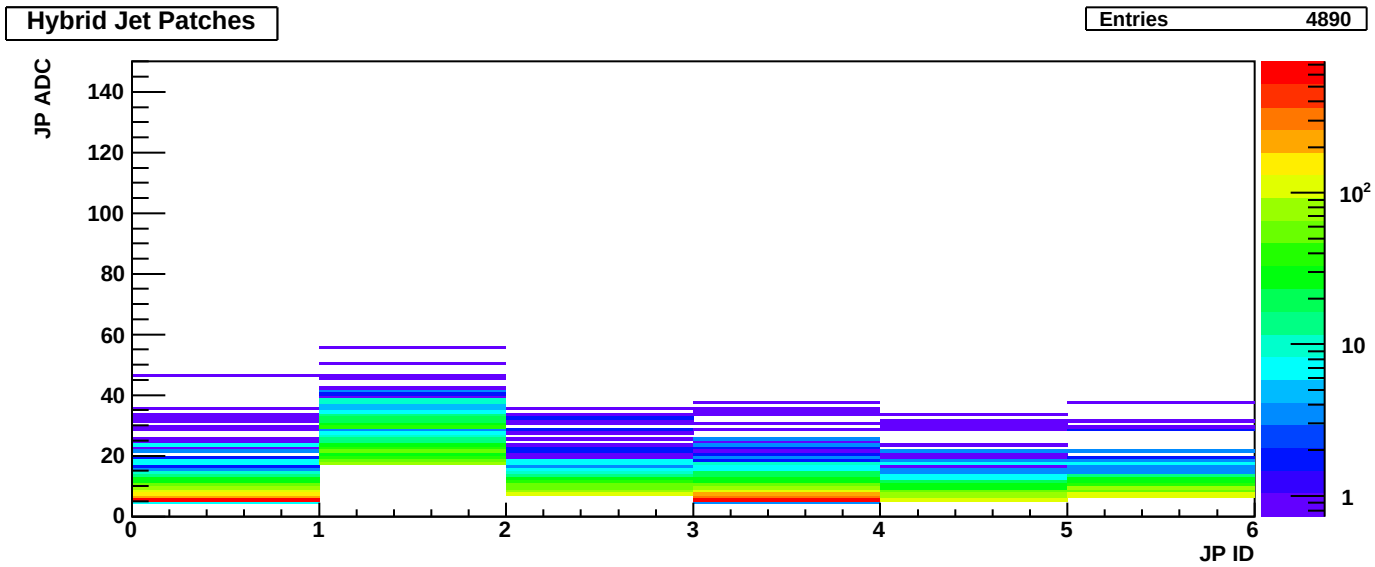

Barrel EMC L0 Input - Patch Sum
Entries 733500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

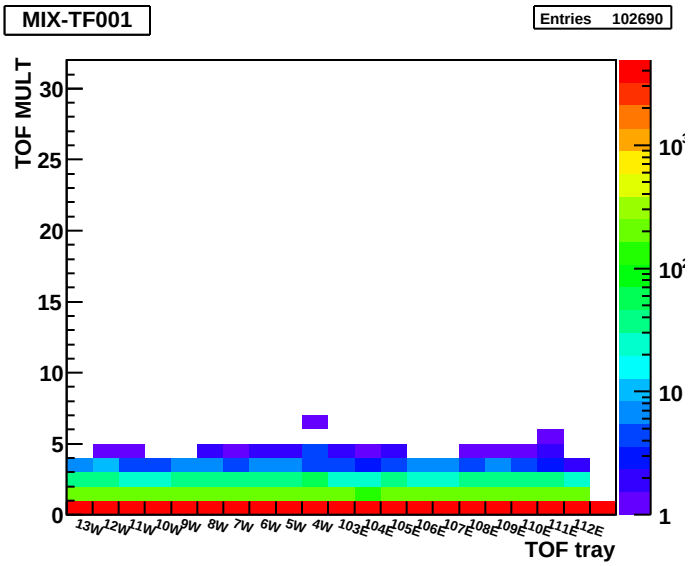

Endcap EMC L0 Input - High Tower
Entries 220050
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

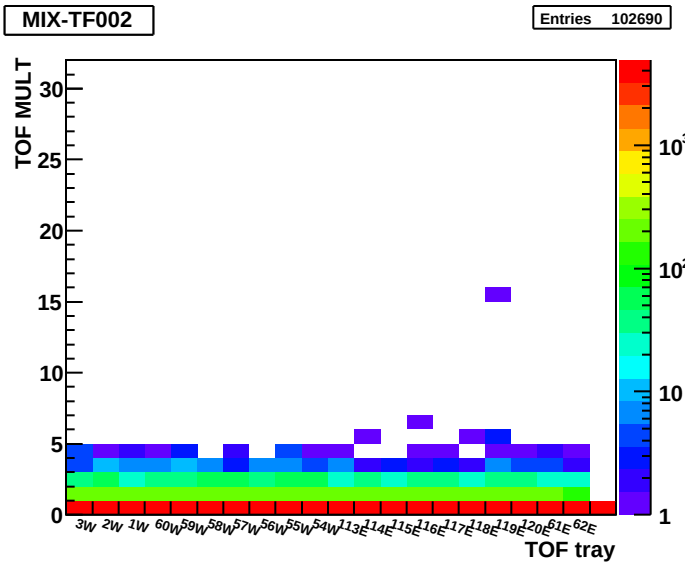

Endcap EMC L0 Input - Patch Sum
Entries 220050
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

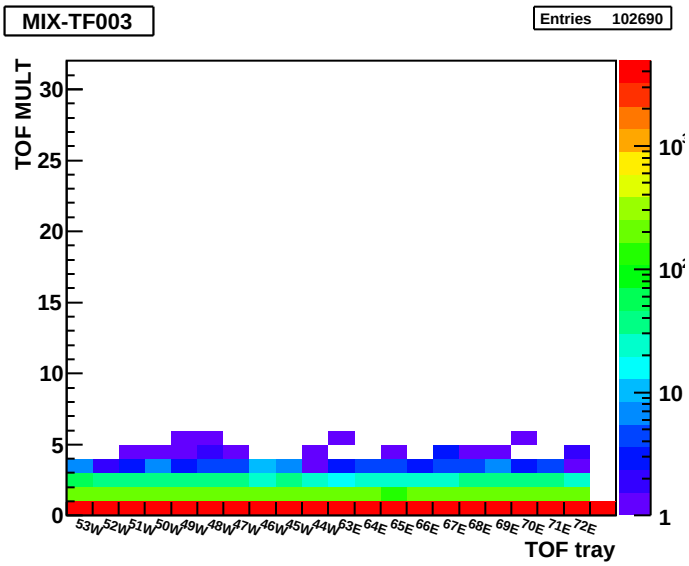

Barrel Jet Patches
Entries 44010
JP ADC
JP ID

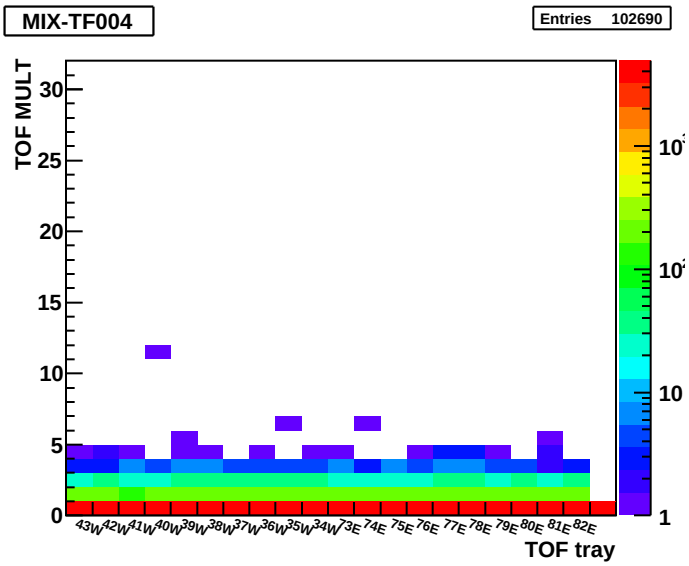

Endcap Jet Patches
Entries 14670
JP ADC
JP ID

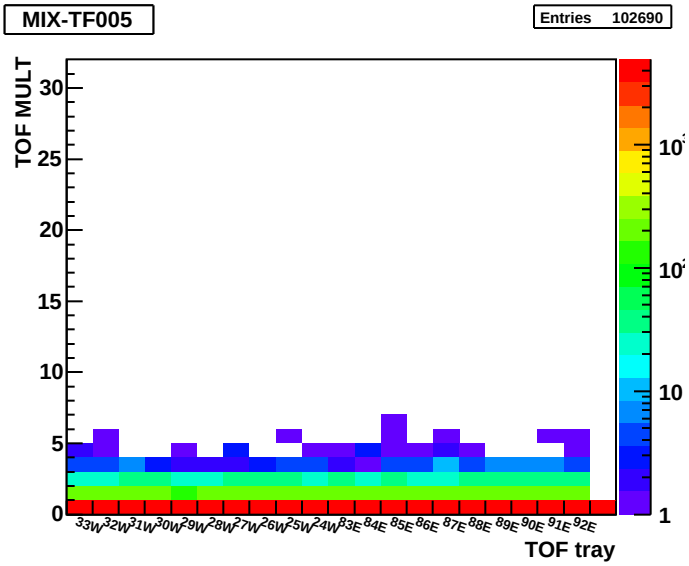

Hybrid Jet Patches
Entries 4890
JP ADC
JP ID

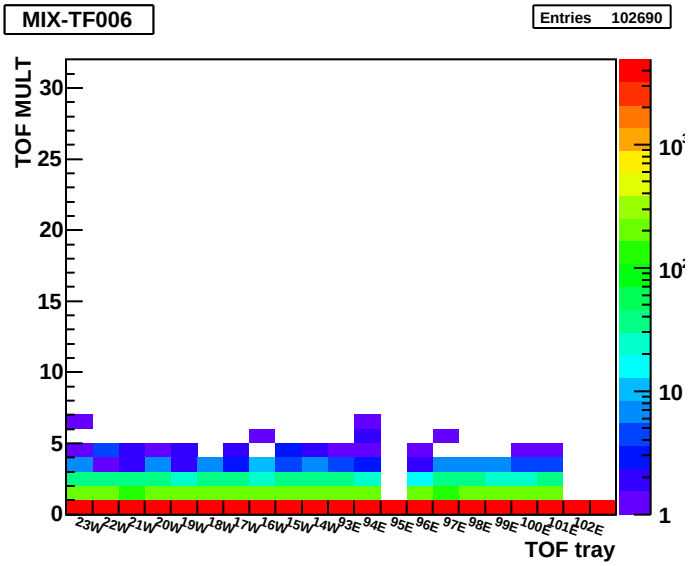

MIX-TF001
Entries 102690
TOF MULT
TOF tray

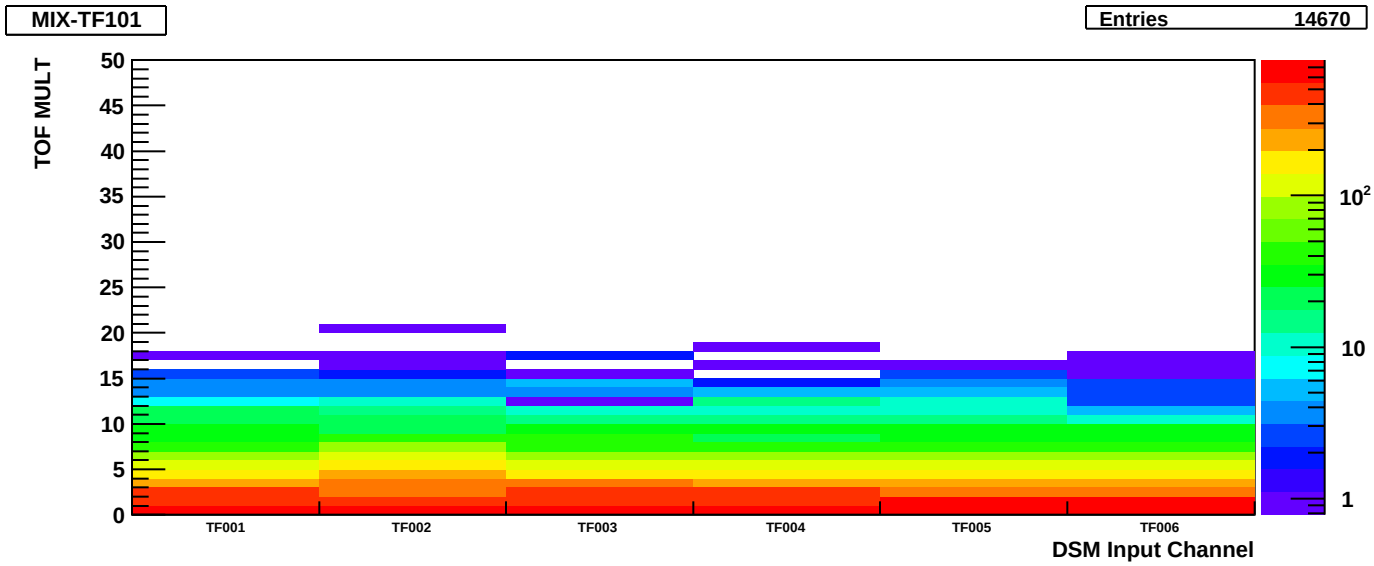

MIX-TF002
Entries 102690
TOF MULT
TOF tray

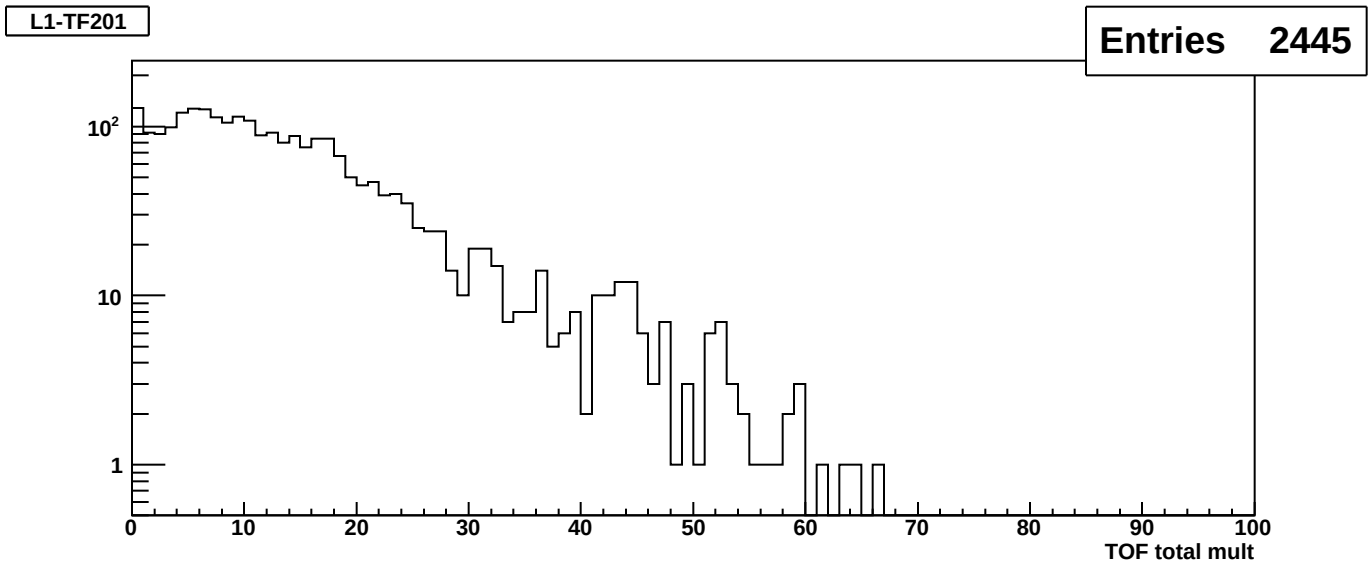

MIX-TF003
Entries 102690
TOF MULT
TOF tray

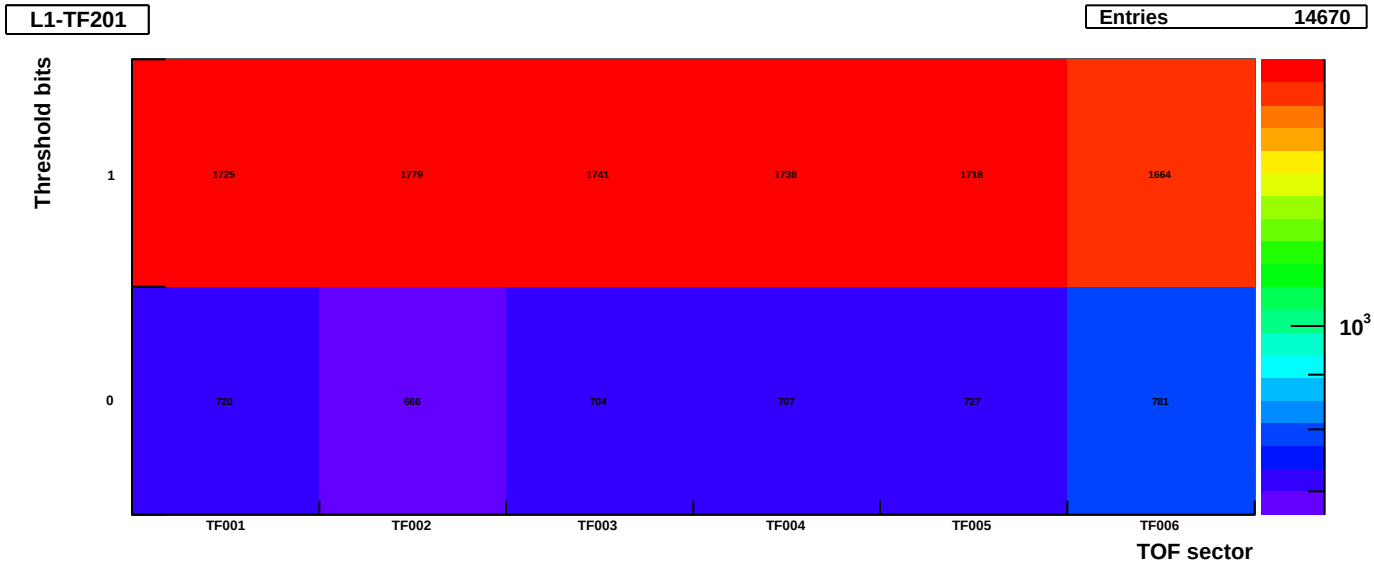

MIX-TF004
Entries 102690
TOF MULT
TOF tray

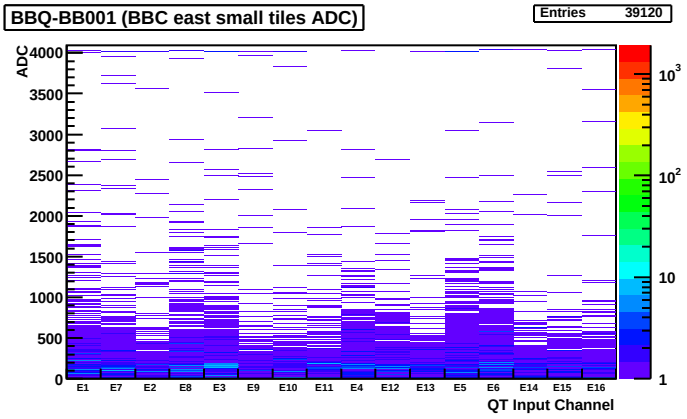

MIX-TF005
Entries 102690
TOF MULT
TOF tray

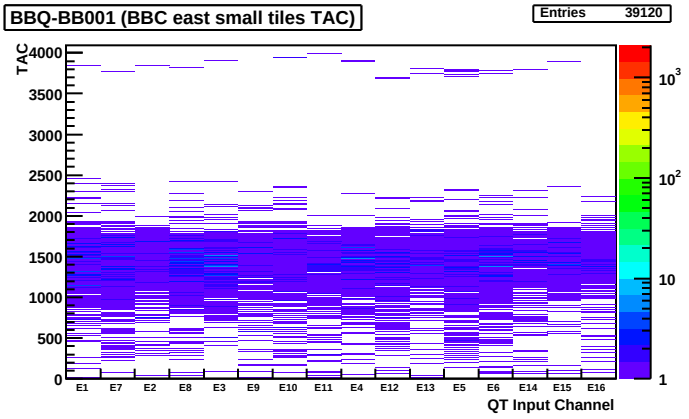

MIX-TF006
Entries 102690
TOF MULT
TOF tray

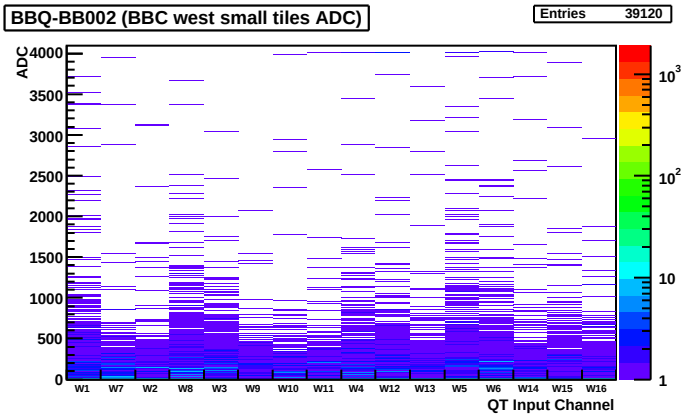

MIX-TF101
Entries 14670
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

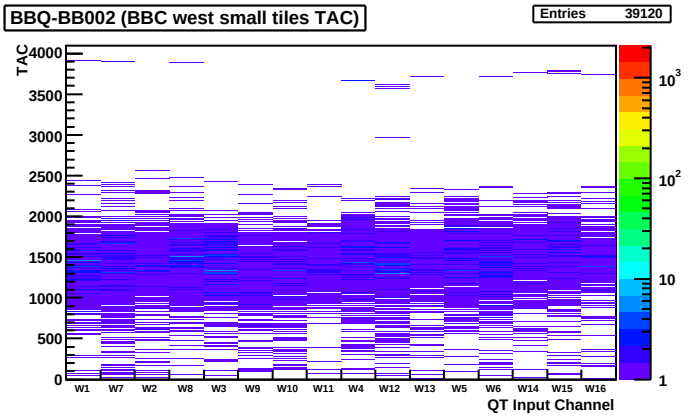

L1-TF201
Entries 2445
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

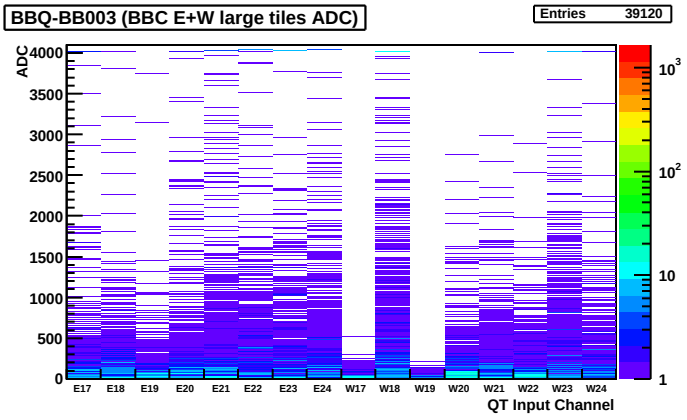

L1-TF201
Entries 14670
Threshold bits
1
0
1725
1779
1741
1738
1718
1664
720
666
704
707
727
781
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

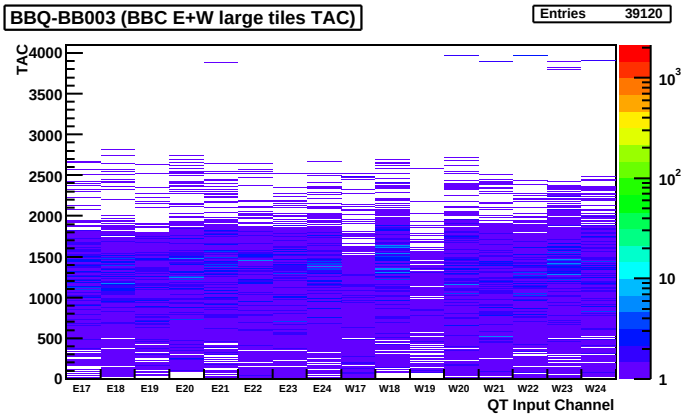

BBQ-BB001 (BBC east small tiles ADC)
Entries 39120
ADC
QT Input Channel

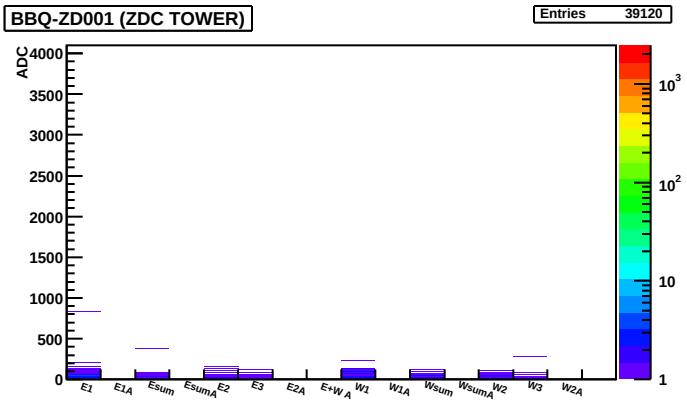

BBQ-BB001 (BBC east small tiles TAC)
Entries 39120
TAC
QT Input Channel

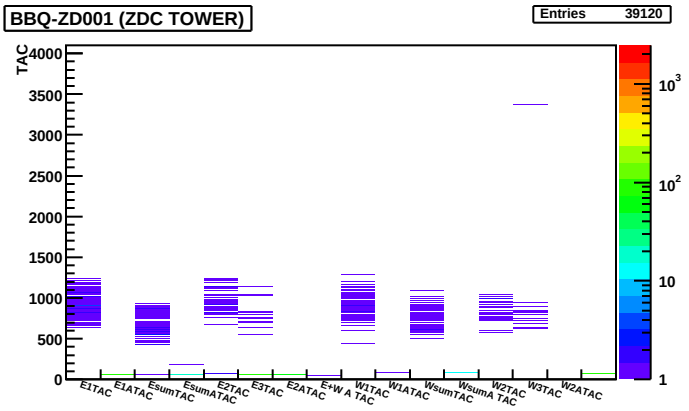

BBQ-BB002 (BBC west small tiles ADC)
Entries 39120
ADC
QT Input Channel

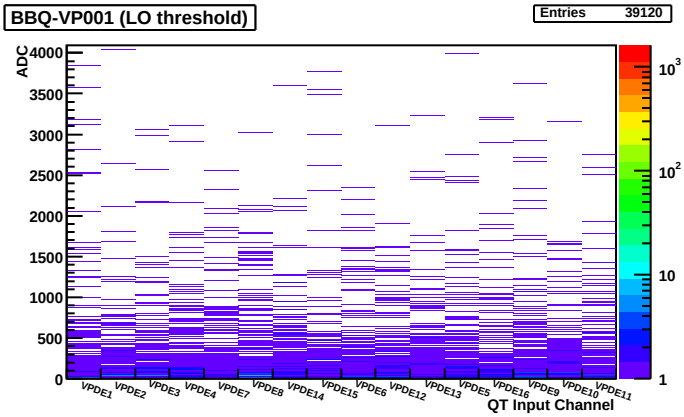

BBQ-BB002 (BBC west small tiles TAC)
Entries 39120
TAC
QT Input Channel

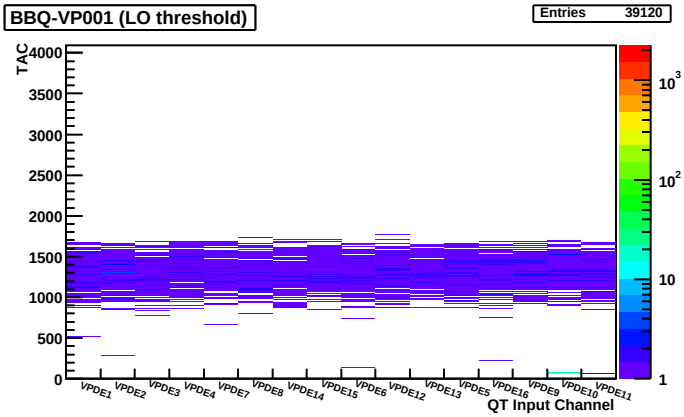

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 39120
ADC
QT Input Channel

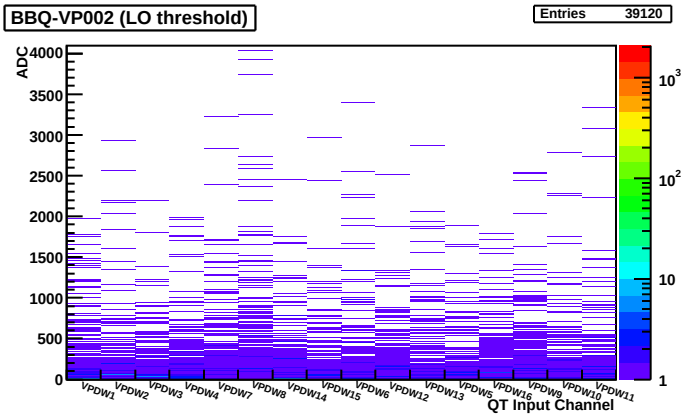

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 39120
TAC
QT Input Channel

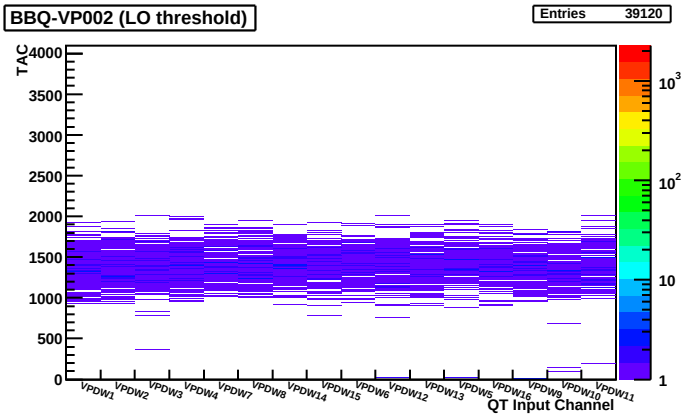

BBQ-ZD001 (ZDC TOWER)
Entries 39120
ADC

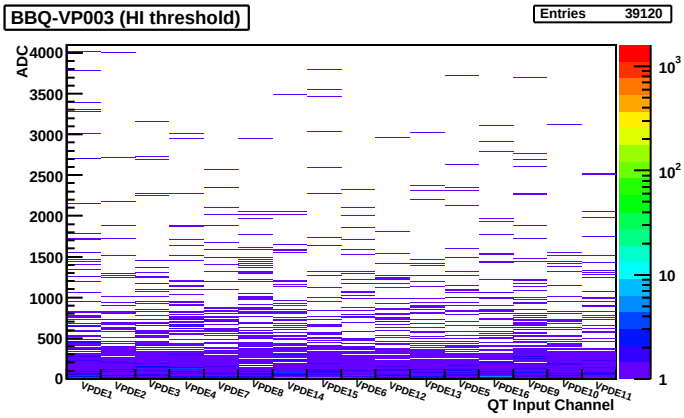

BBQ-ZD001 (ZDC TOWER)
Entries 39120
TAC

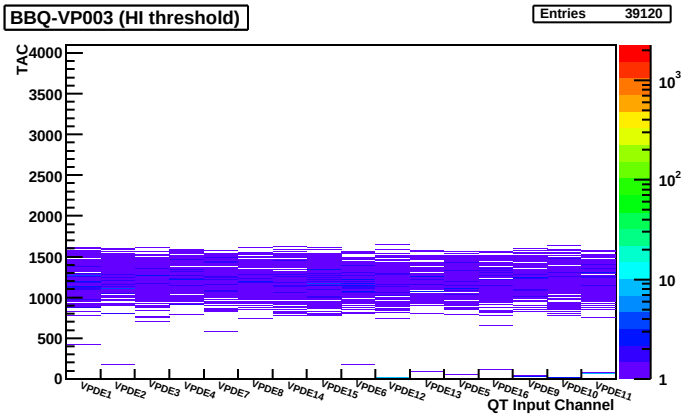

BBQ-VP001 (LO threshold)
Entries 39120
ADC
QT Input Channel

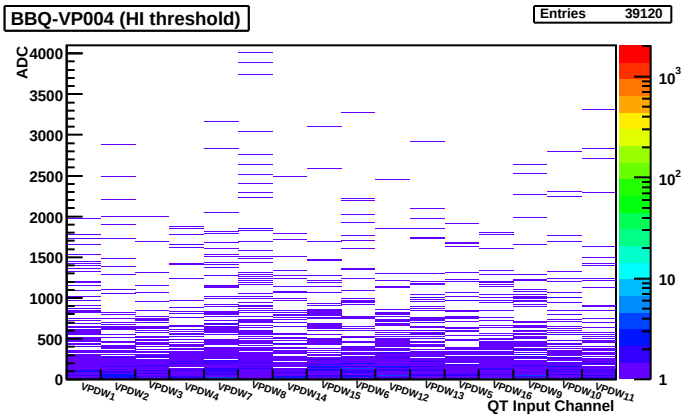

BBQ-VP001 (LO threshold)
Entries 39120
TAC
QT Input Channel

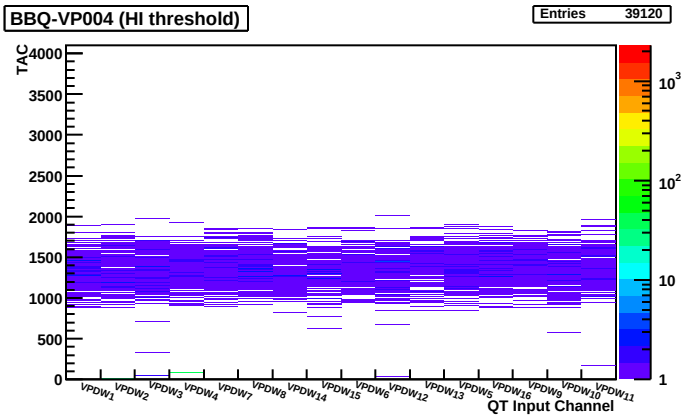

BBQ-VP002 (LO threshold)
Entries 39120
ADC
QT Input Channel

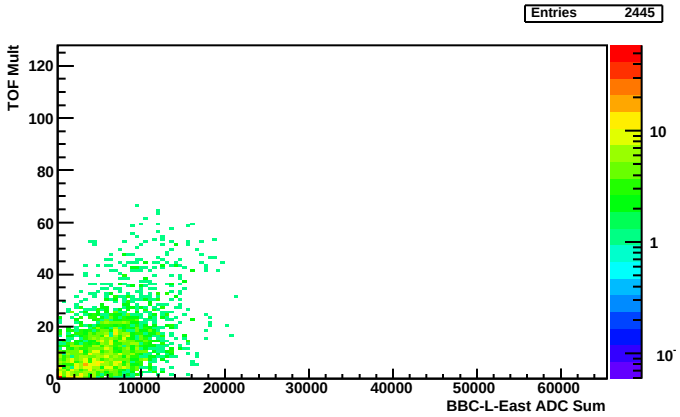

BBQ-VP002 (LO threshold)
Entries 39120
TAC
QT Input Channel

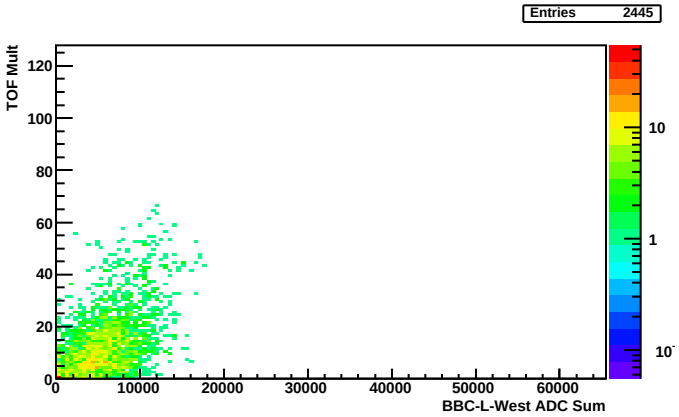

BBQ-VP003 (HI threshold)
Entries 39120
ADC
QT Input Channel

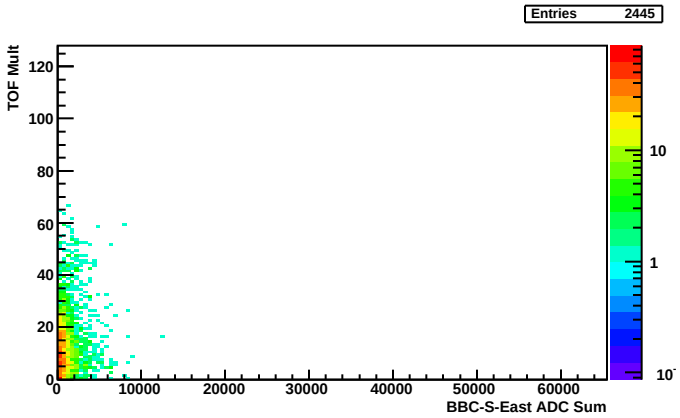

BBQ-VP003 (HI threshold)
Entries 39120
TAC
QT Input Channel

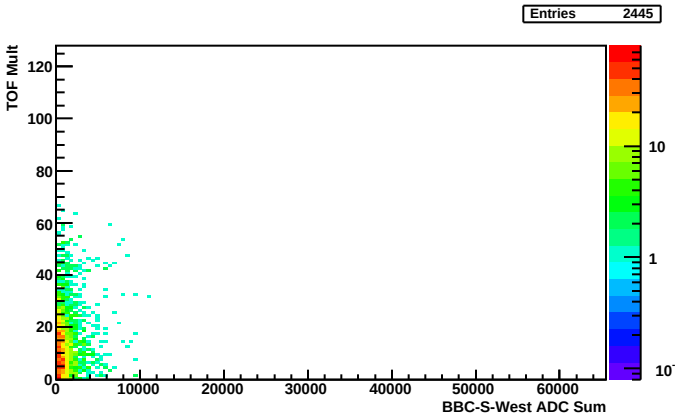

BBQ-VP004 (HI threshold)
Entries 39120
ADC
QT Input Channel

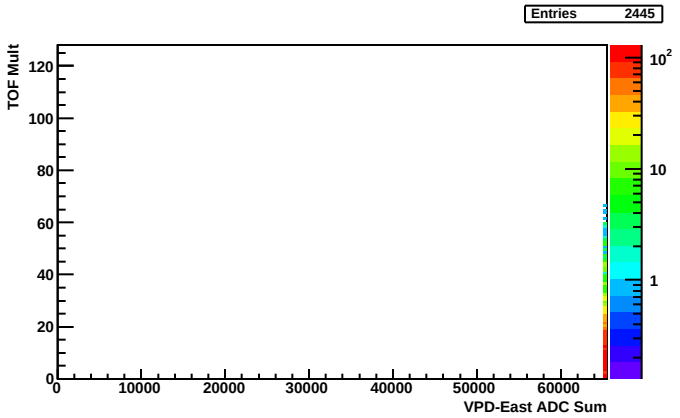

BBQ-VP004 (HI threshold)
Entries 39120
TAC
QT Input Channel

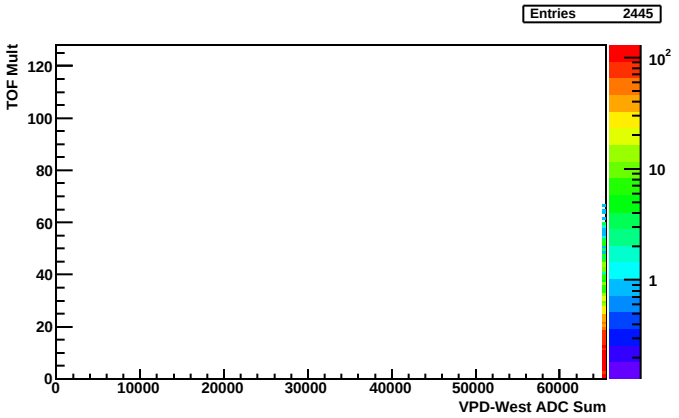

Entries 2445
TOF Mult
BBC-L-East ADC Sum

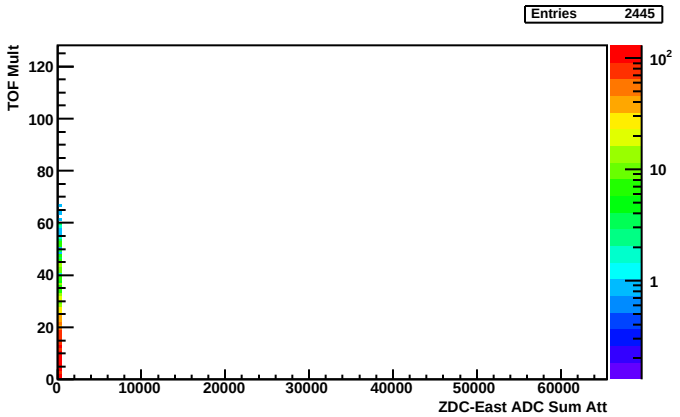

Entries 2445
TOF Mult
BBC-L-West ADC Sum

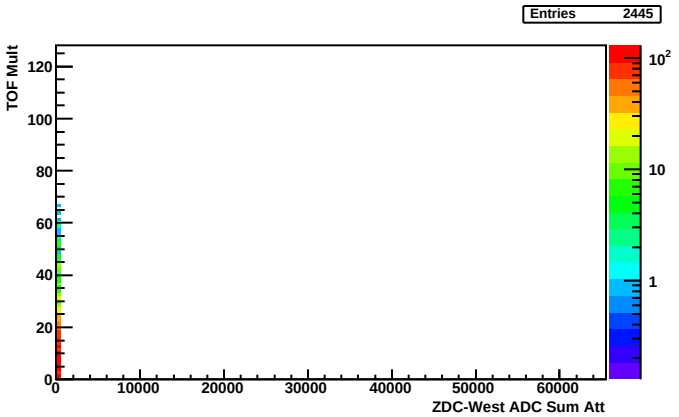

Entries 2445
TOF Mult
BBC-S-East ADC Sum

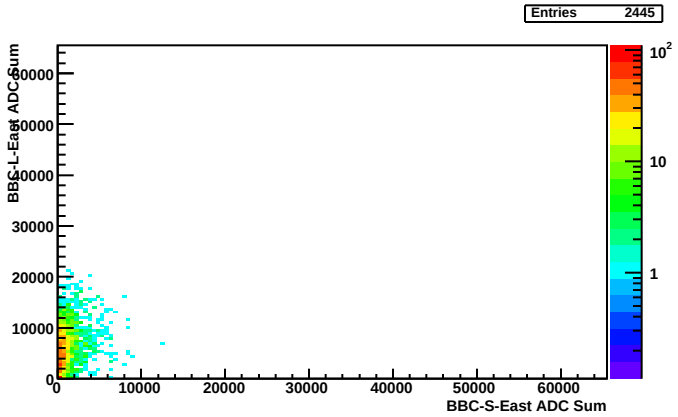

Entries 2445
TOF Mult
BBC-S-West ADC Sum

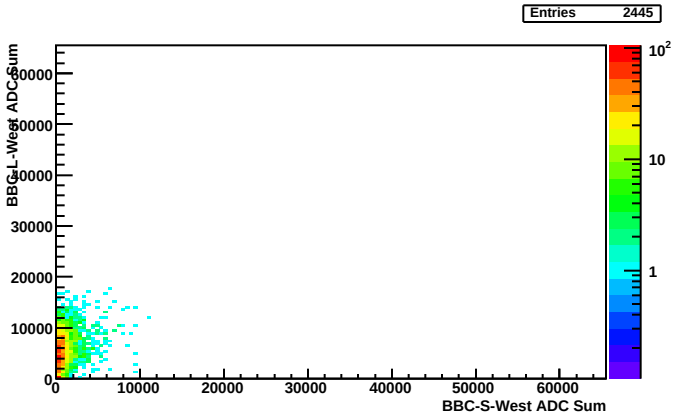

Entries 2445
TOF Mult
VPD-East ADC Sum

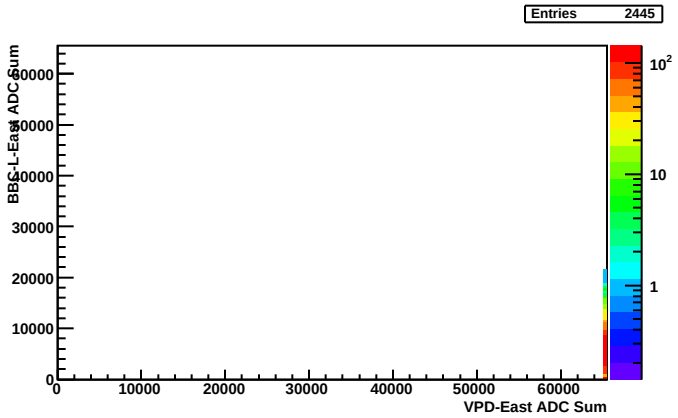

Entries 2445
TOF Mult
VPD-West ADC Sum

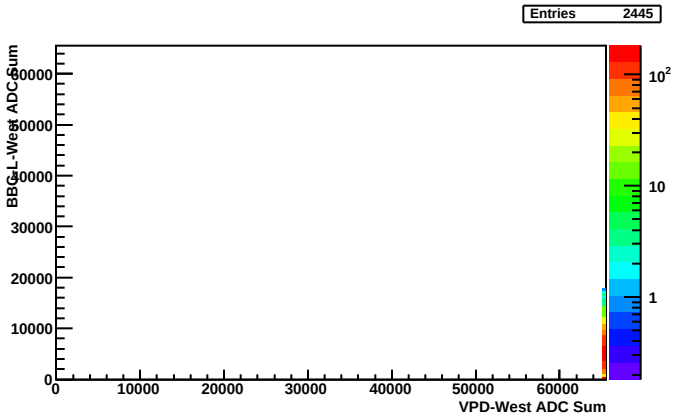

Entries 2445
TOF Mult
ZDC-East ADC Sum Att

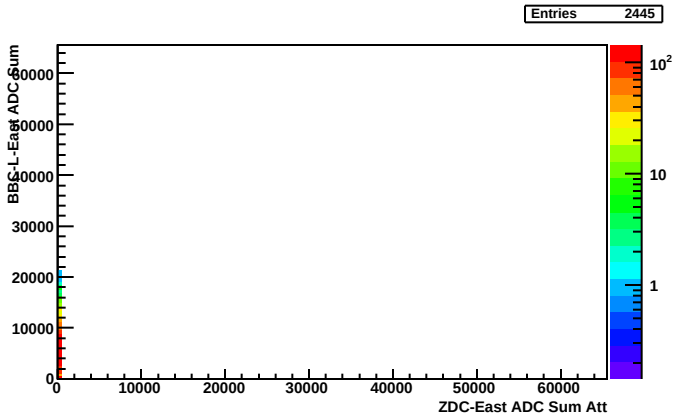

Entries 2445
TOF Mult
ZDC-West ADC Sum Att

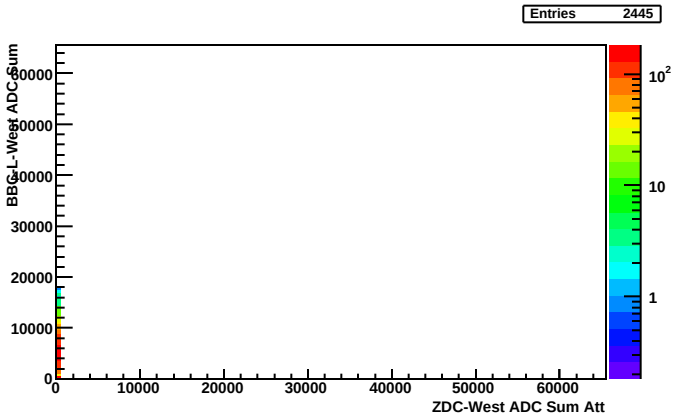

Entries 2445
BBC-L-East ADC Sum
BBC-S-East ADC Sum

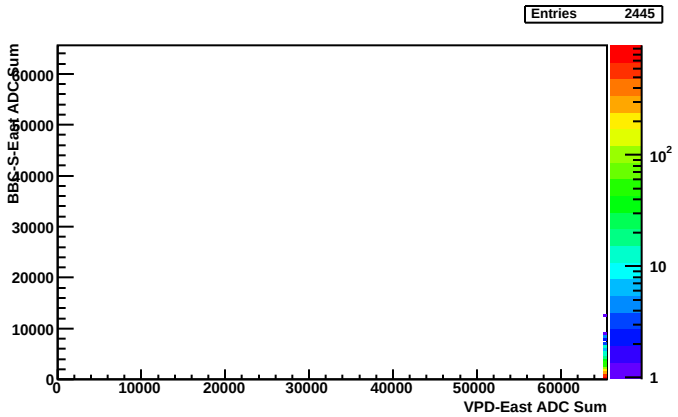

Entries 2445
BBC-L-West ADC Sum
BBC-S-West ADC Sum

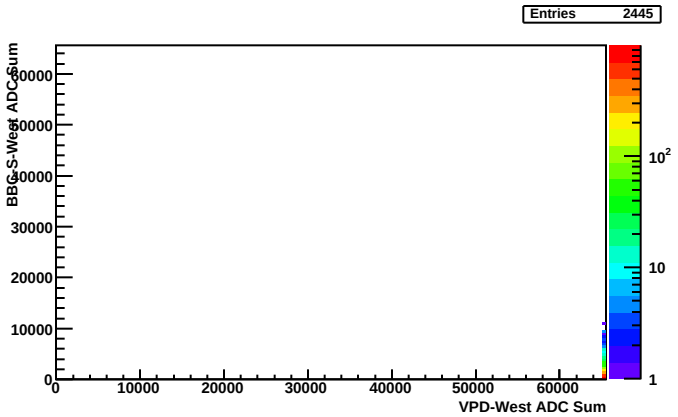

Entries 2445
BBC-L-East ADC Sum
VPD-East ADC Sum

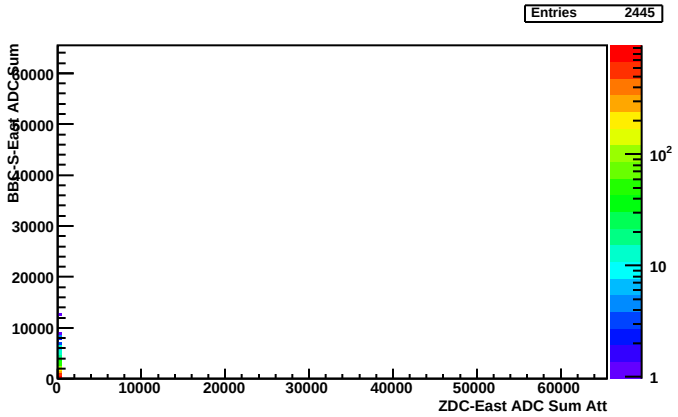

Entries 2445
BBC-L-West ADC Sum
VPD-West ADC Sum

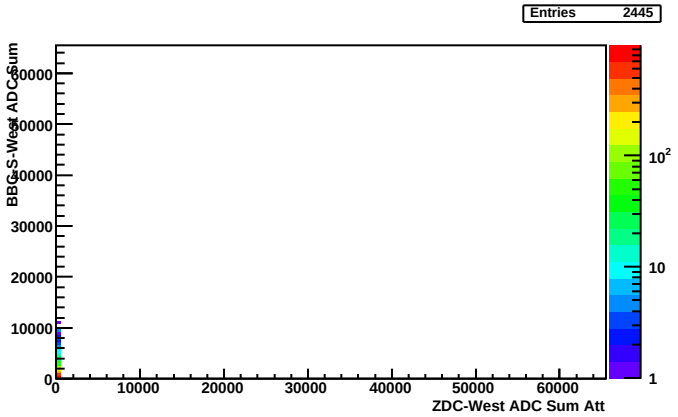

Entries 2445
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

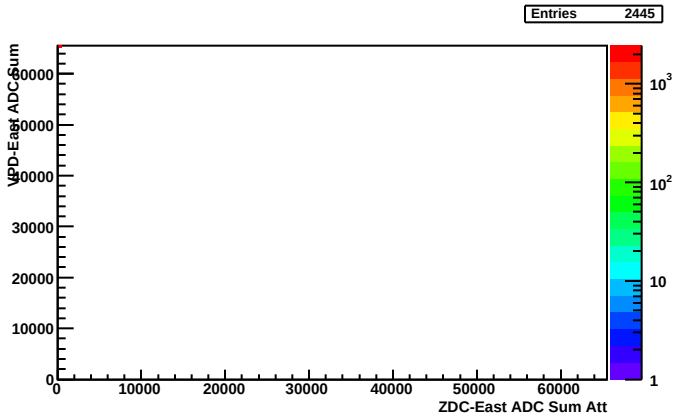

Entries 2445
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

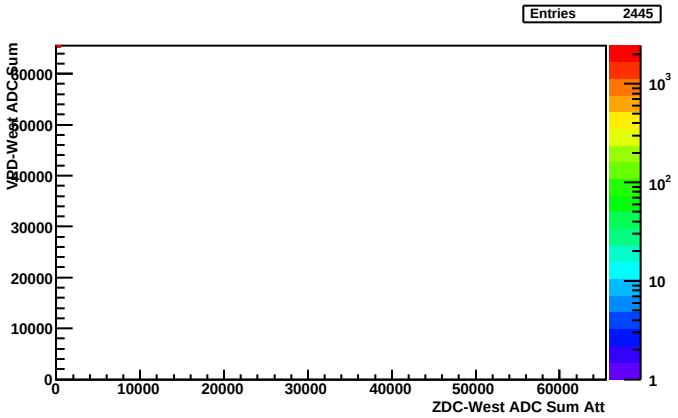

Entries 2445
BBC-S-East ADC Sum
VPD-East ADC Sum

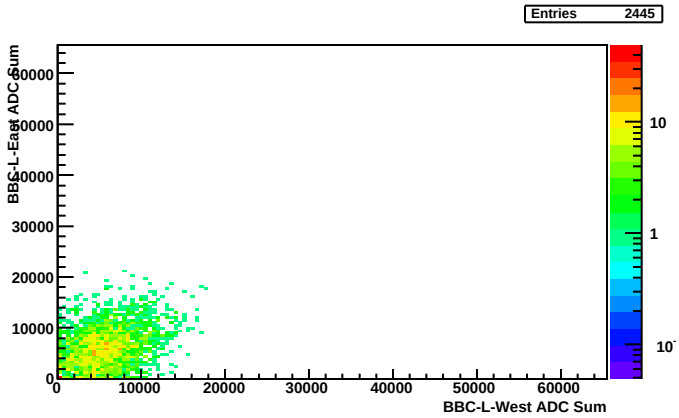

Entries 2445
BBC-S-West ADC Sum
VPD-West ADC Sum

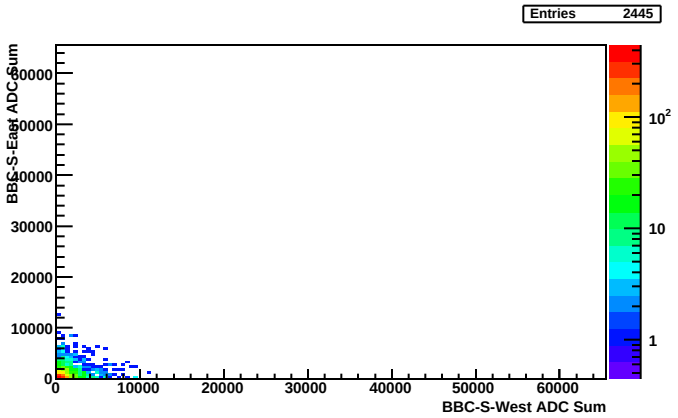

Entries 2445
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

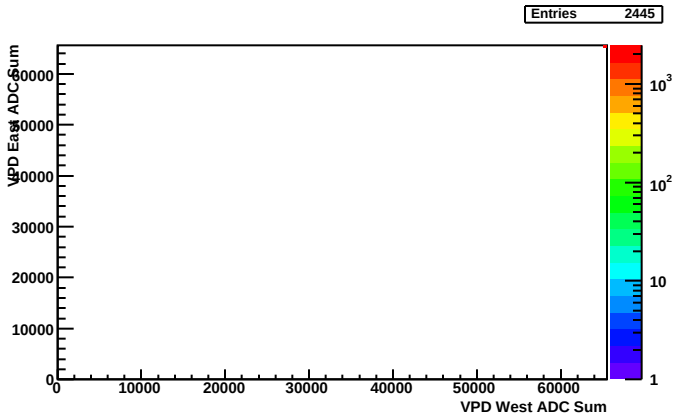

Entries 2445
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

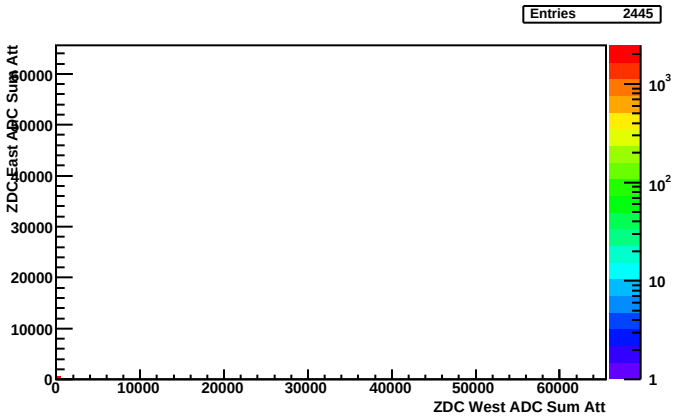

Entries 2445
VPD-East ADC Sum
ZDC-East ADC Sum Att

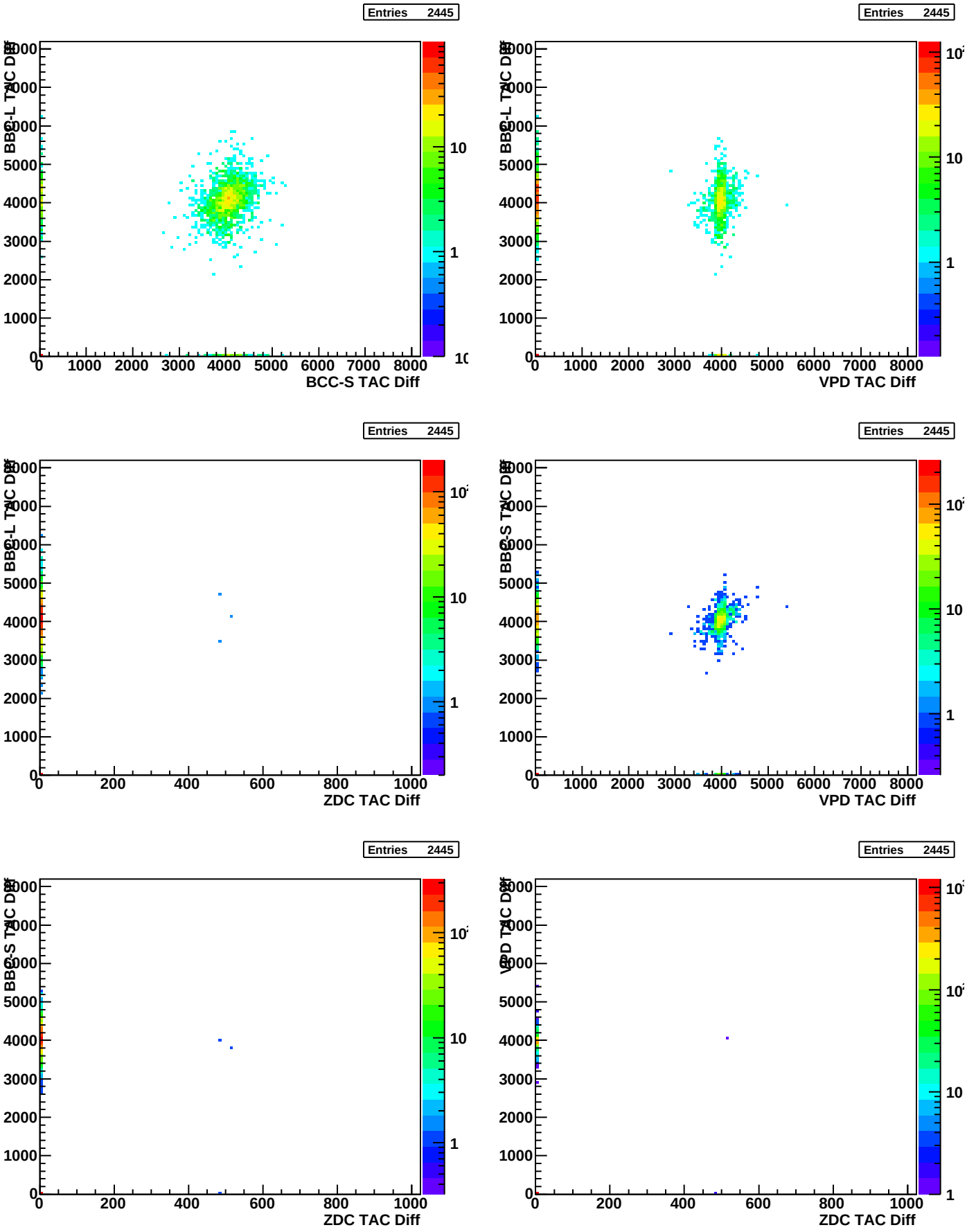

Entries 2445
VPD-West ADC Sum
ZDC-West ADC Sum Att

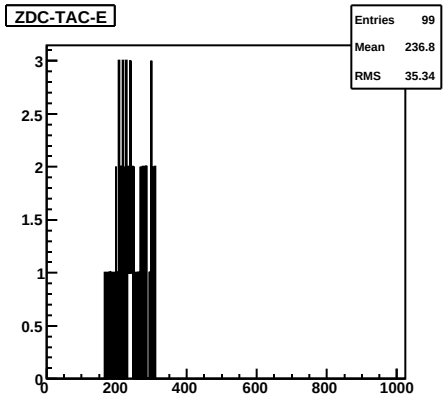

Entries 2445
BBC-L-East ADC Sum
BBC-L-West ADC Sum

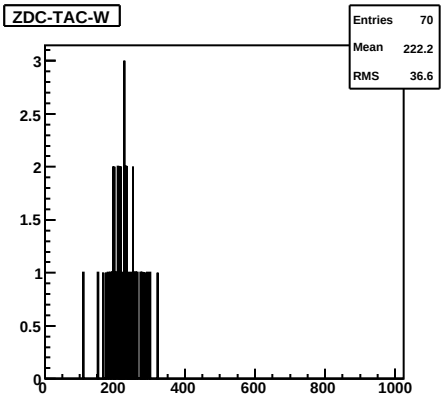

Entries 2445
BBC-S-East ADC Sum
BBC-S-West ADC Sum

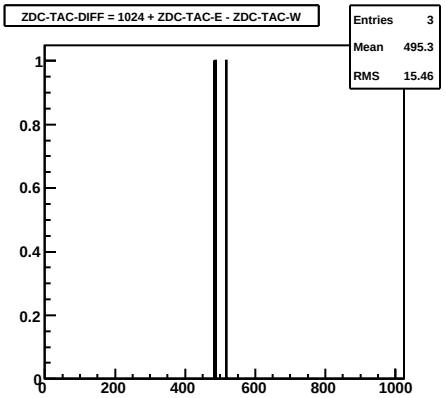

Entries 2445
VPD East ADC Sum
VPD West ADC Sum

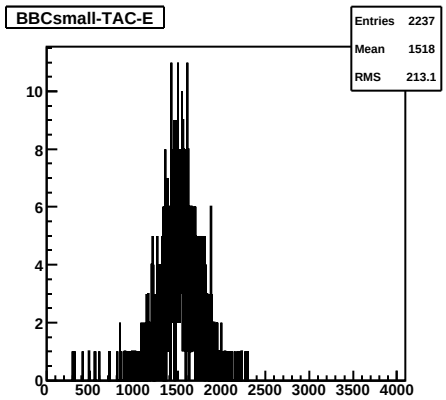

Entries 2445
ZDC East ADC Sum Att
ZDC West ADC Sum Att

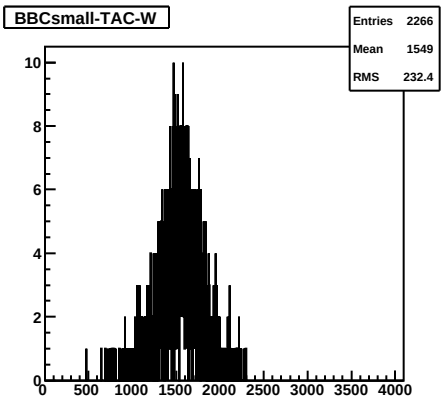

Entries 2445
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2445
BBC-L TAC Diff
VPD TAC Diff
Entries 2445
BBC-L TAC Diff
ZDC TAC Diff
Entries 2445
BBC-S TAC Diff
VPD TAC Diff
Entries 2445
BBC-S TAC Diff
ZDC TAC Diff
Entries 2445
VPD TAC Diff
ZDC TAC Diff

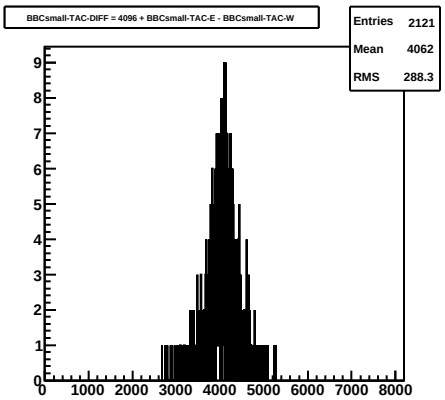

ZDC-TAC-E
Entries 99
Mean 236.8
RMS 35.34

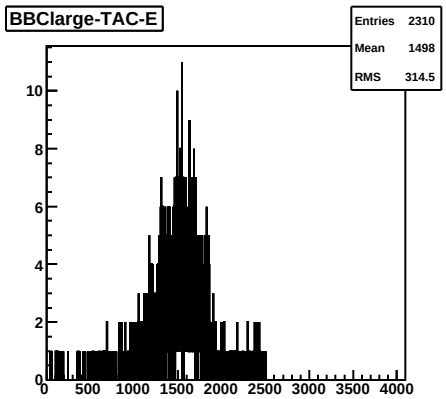

ZDC-TAC-W
Entries 70
Mean 222.2
RMS 36.6

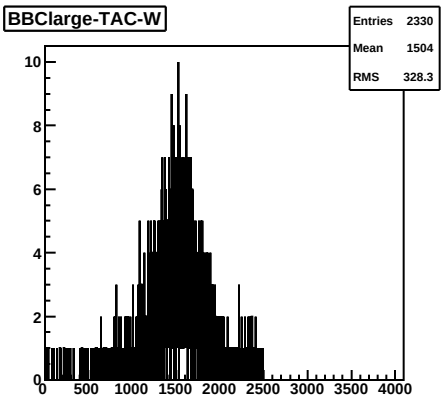

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3
Mean 495.3
RMS 15.46

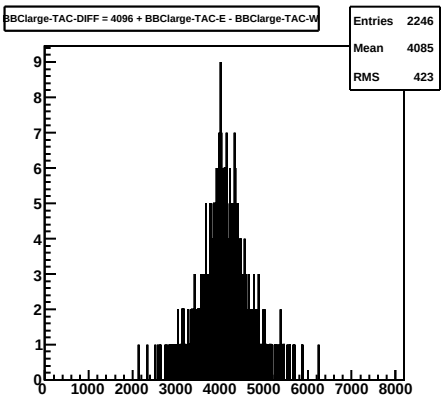

BBCsmall-TAC-E
Entries 2237
Mean 1518
RMS 213.1

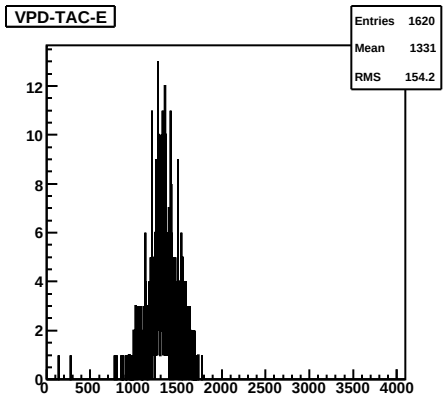

BBCsmall-TAC-W
Entries 2266
Mean 1549
RMS 232.4

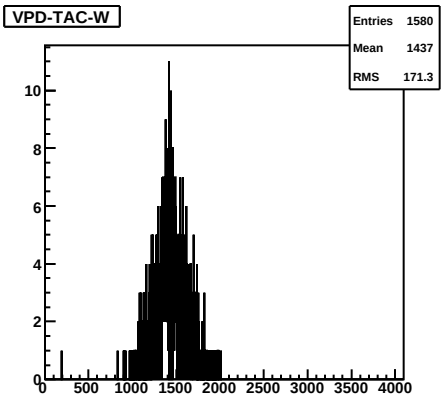

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2121
Mean 4062
RMS 288.3

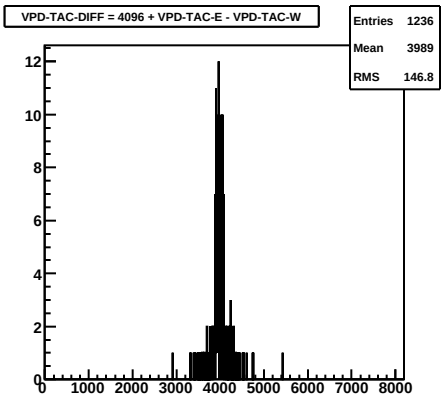

BBClarge-TAC-E
Entries 2310
Mean 1498
RMS 314.5

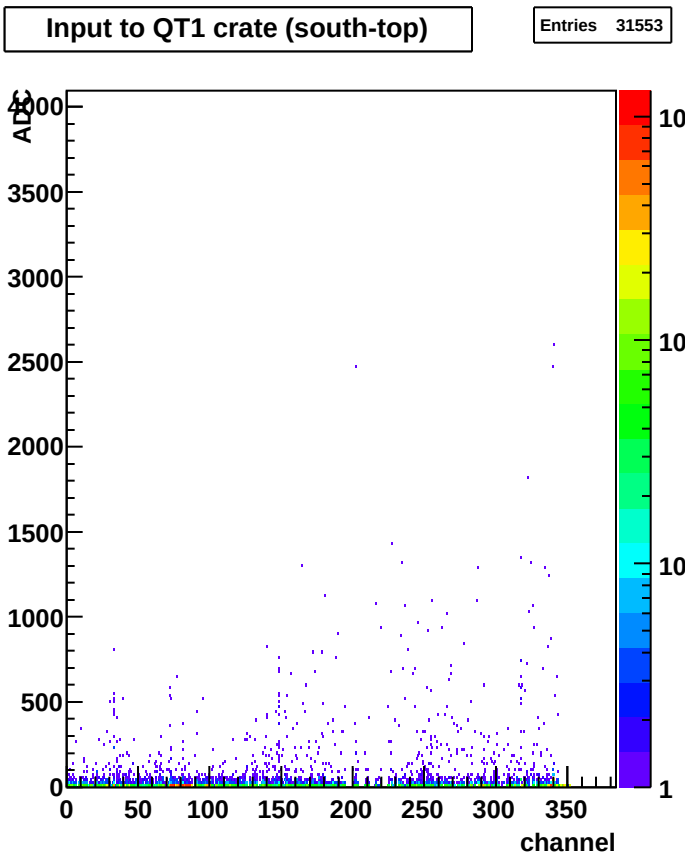

BBClarge-TAC-W
Entries 2330
Mean 1504
RMS 328.3

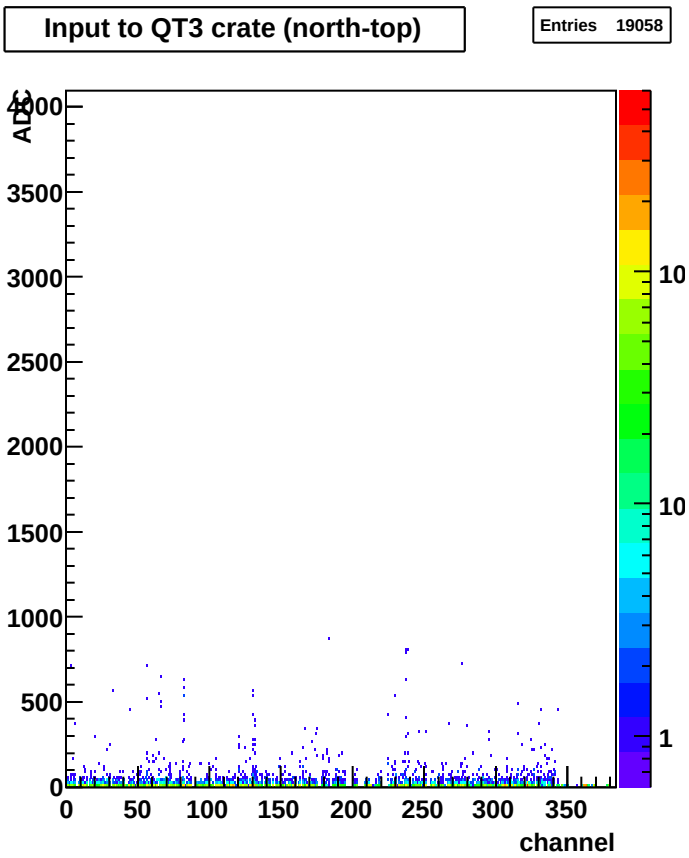

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2246
Mean 4085
RMS 423

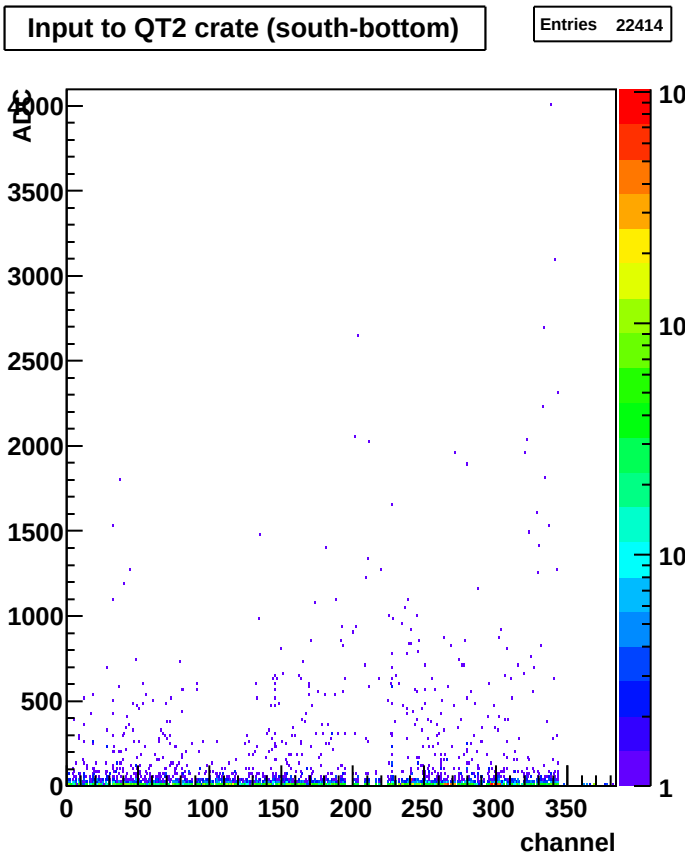

VPD-TAC-E
Entries 1620
Mean 1331
RMS 154.2

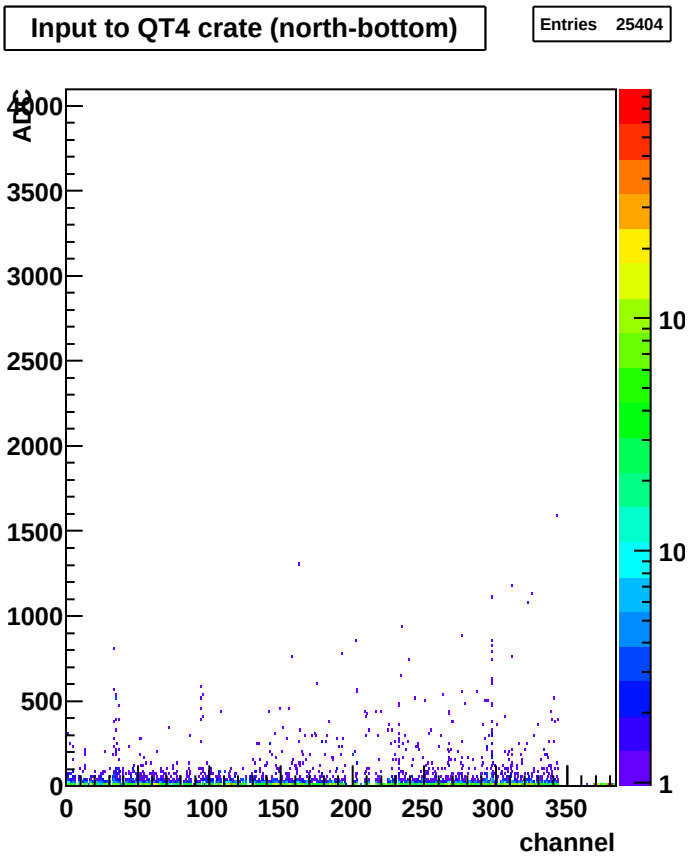

VPD-TAC-W
Entries 1580
Mean 1437
RMS 171.3

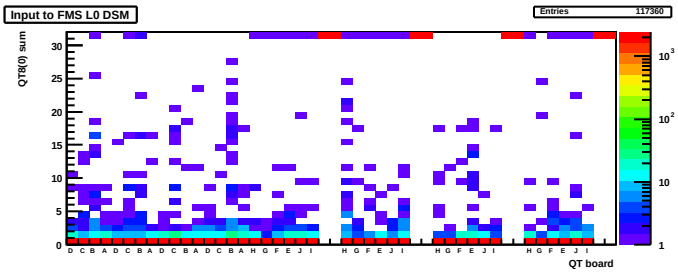

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1236
Mean 3989
RMS 146.8

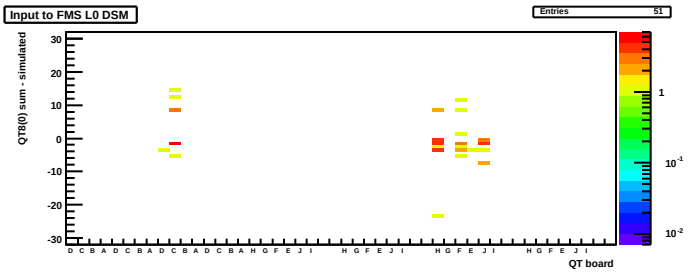

Input to QT1 crate (south-top)
Entries 31553
ADC
channel

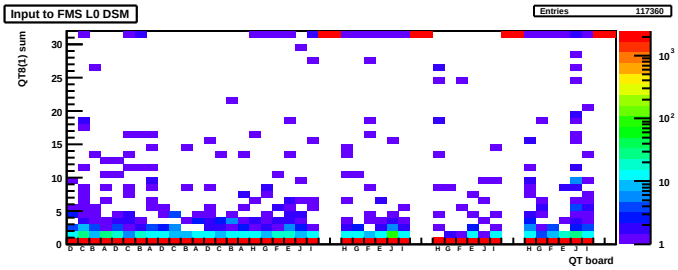

Input to QT3 crate (north-top)
Entries 19058
ADC
channel

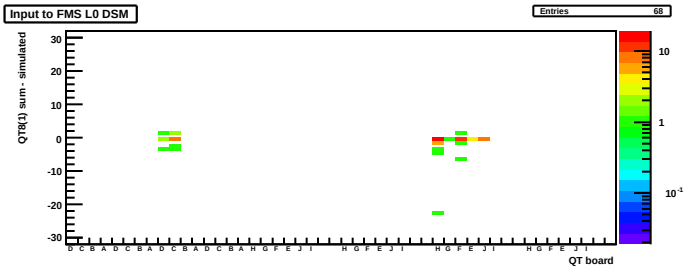

Input to QT2 crate (south-bottom)
Entries 22414
ADC
channel

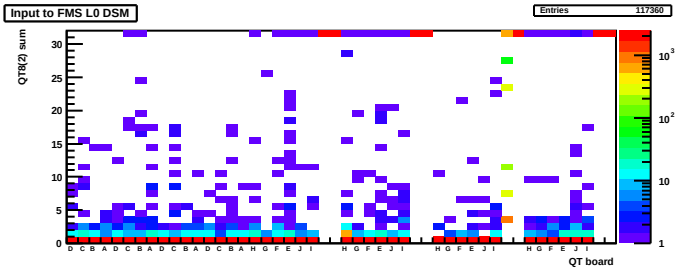

Input to QT4 crate (north-bottom)
Entries 25404
ADC
channel

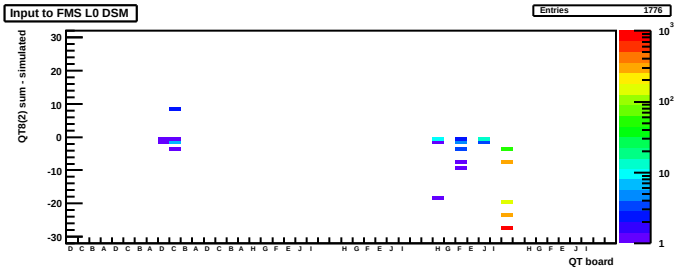

Input to FMS L0 DSM
Entries 117360
QT8(0) sum
QT board

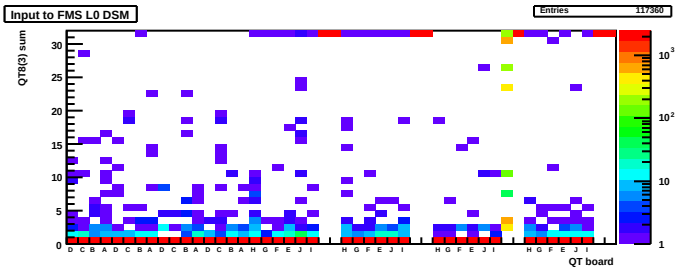

Input to FMS L0 DSM
Entries 51
QT8(0) sum - simulated
QT board

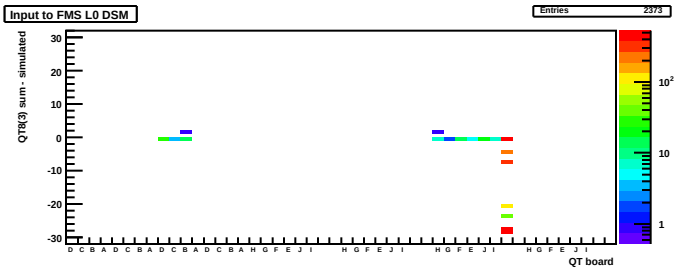

Input to FMS L0 DSM
Entries 117360
QT8(1) sum
QT board

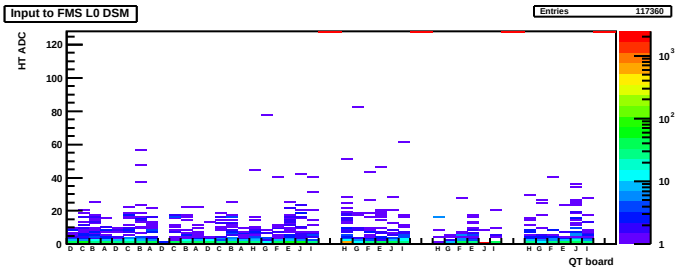

Input to FMS L0 DSM
Entries 68
QT8(1) sum - simulated
QT board

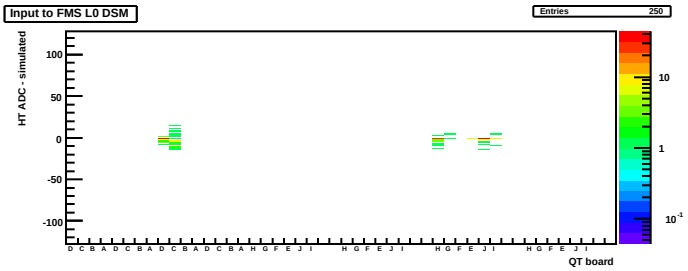

Input to FMS L0 DSM
Entries 117360
QT8(2) sum
QT board

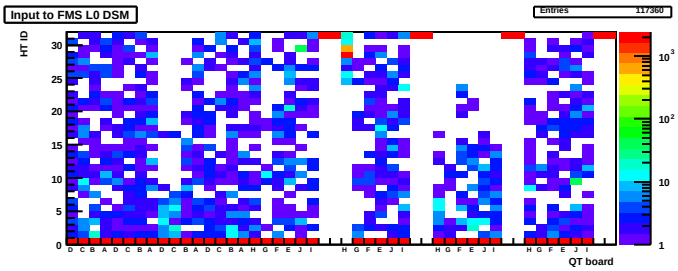

Input to FMS L0 DSM
Entries 1776
QT8(2) sum - simulated
QT board

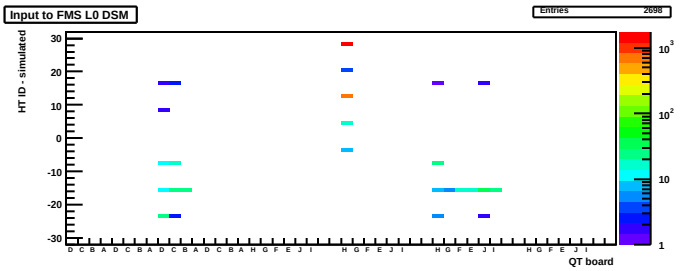

Input to FMS L0 DSM
Entries 117360
QT8(3) sum
QT board

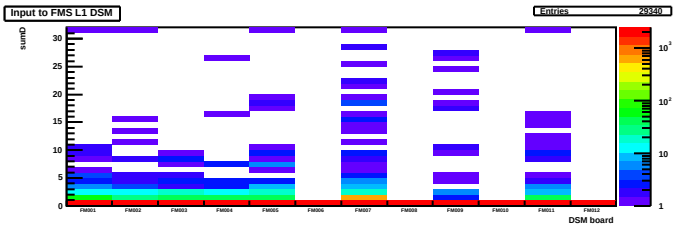

Input to FMS L0 DSM
Entries 2373
QT8(3) sum - simulated
QT board

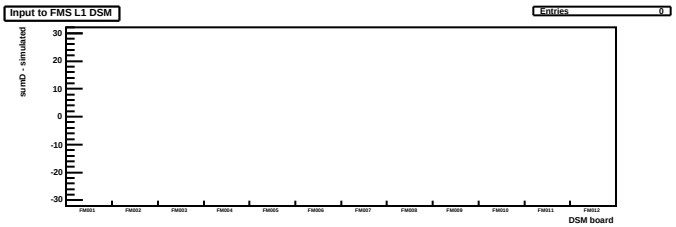

Input to FMS L0 DSM
Entries 117360
HT ADC
QT board

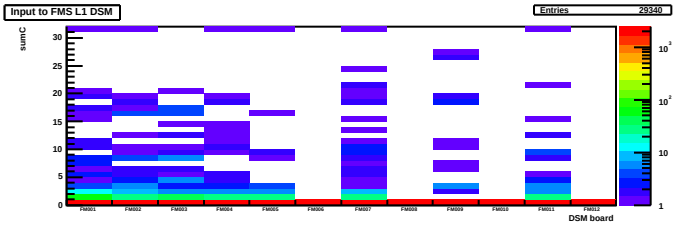

Input to FMS L0 DSM
Entries 250
HT ADC - simulated
QT board

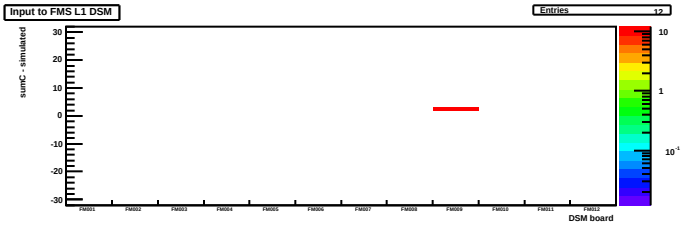

Input to FMS L0 DSM
Entries 117360
HT ID
QT board

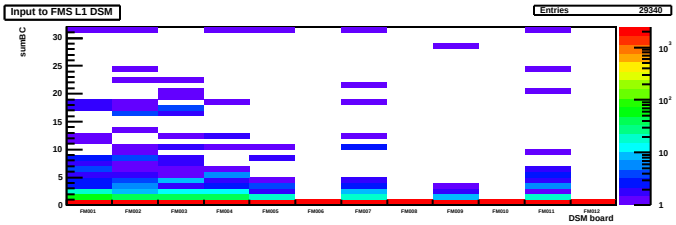

Input to FMS L0 DSM
Entries 2698
HT ID - simulated
QT board

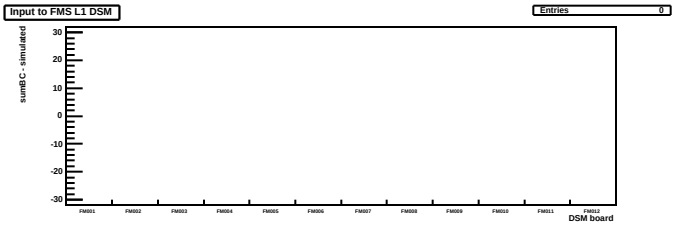

Input to FMS L1 DSM
Entries 29340
sumD
DSM board

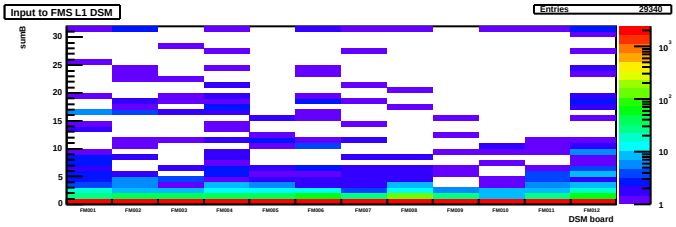

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

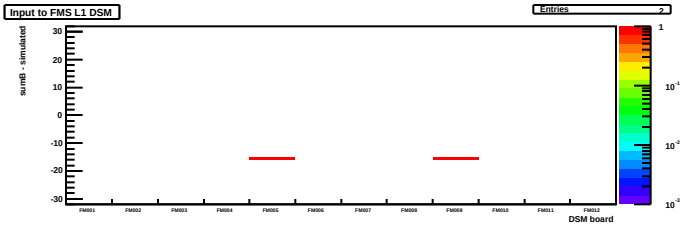

Input to FMS L1 DSM
Entries 29340
sumC
DSM board

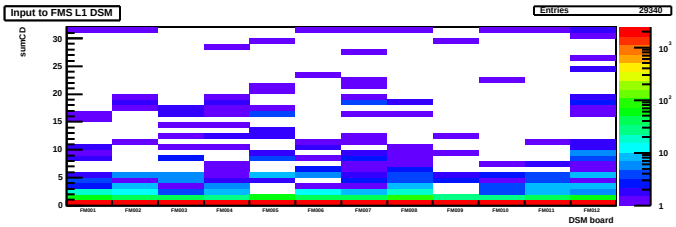

Input to FMS L1 DSM
Entries 12
sumC - simulated
DSM board

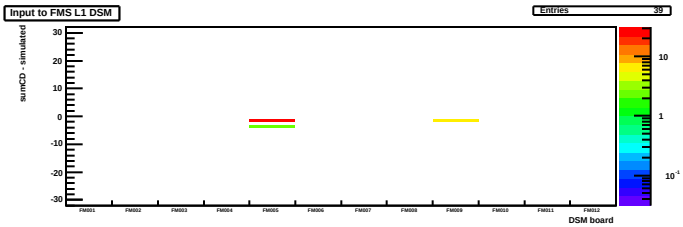

Input to FMS L1 DSM
Entries 29340
sumBC
DSM board

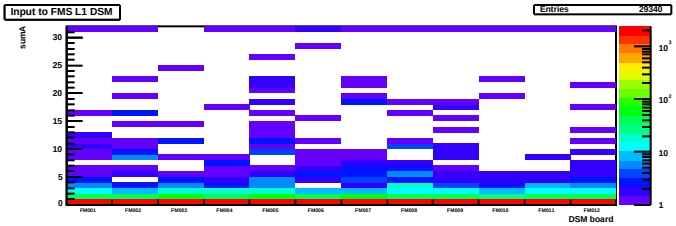

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

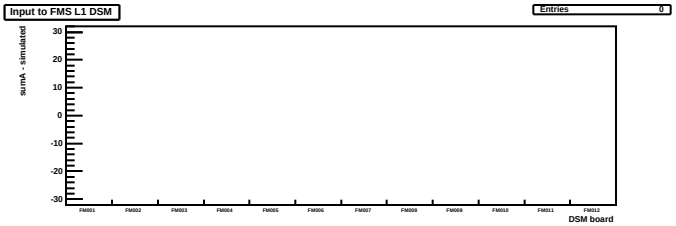

Input to FMS L1 DSM
Entries 29340
sumB
DSM board

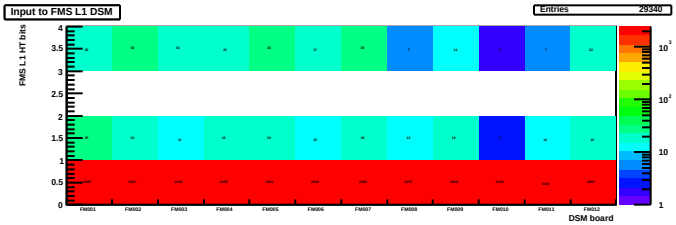

Input to FMS L1 DSM
Entries 2
sumB - simulated
DSM board

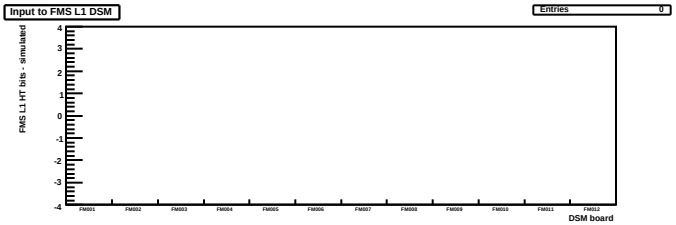

Input to FMS L1 DSM
Entries 29340
sumCD
DSM board

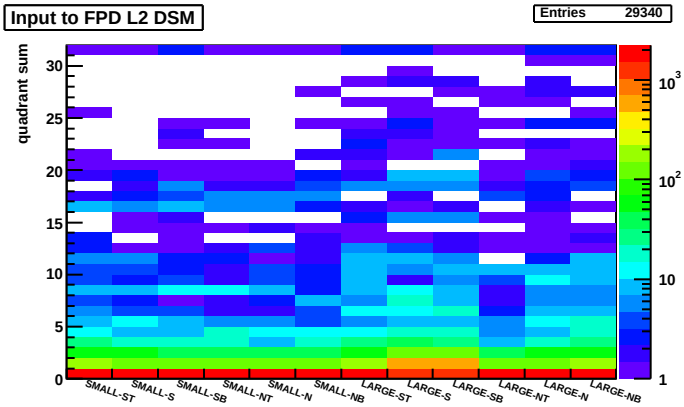

Input to FMS L1 DSM
Entries 39
sumCD - simulated
DSM board

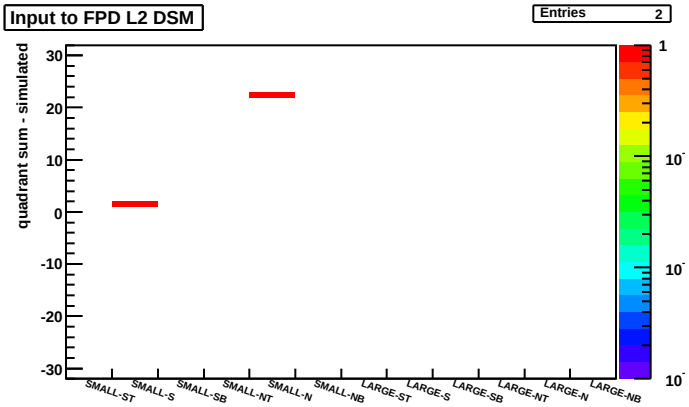

Input to FMS L1 DSM
Entries 29340
sumA
DSM board

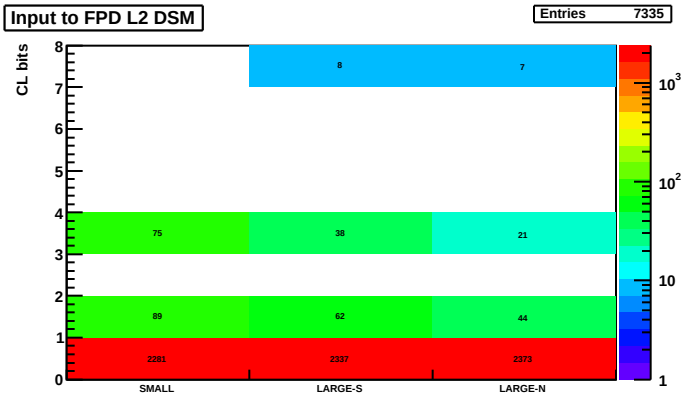

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

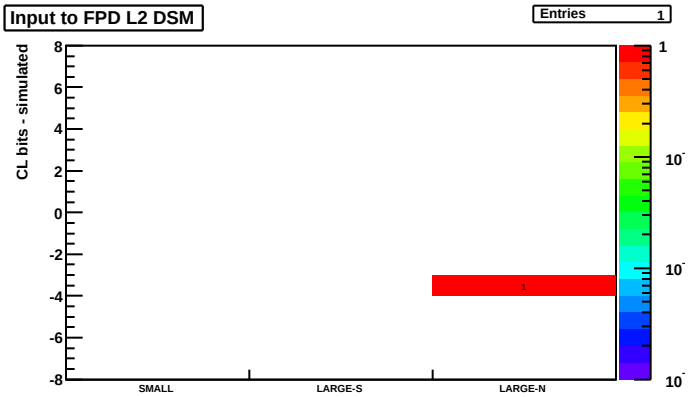

Input to FMS L1 DSM
Entries 29340
FMS L1 HT bits
DSM board

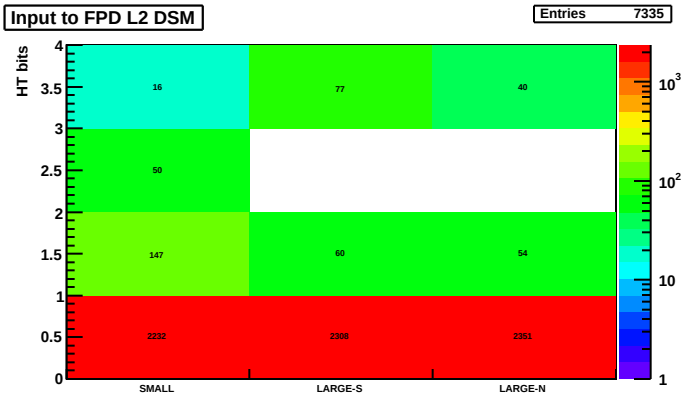

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

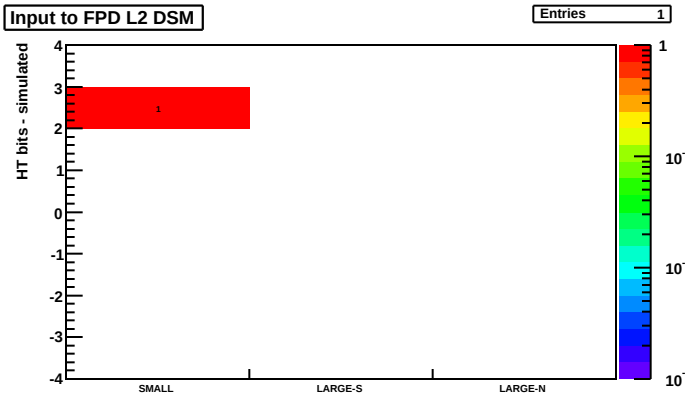

Input to FPD L2 DSM
Entries 29340
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

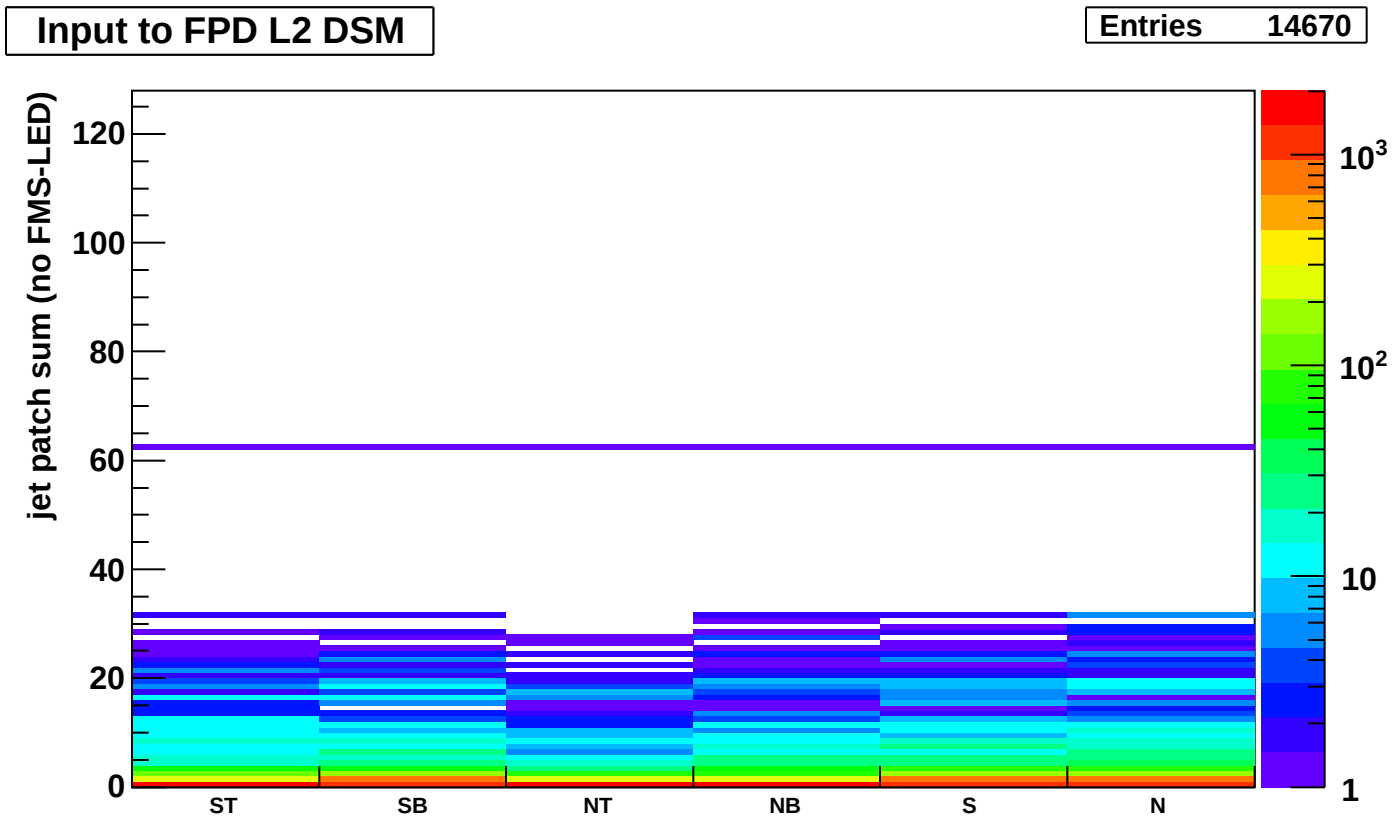

Input to FPD L2 DSM
Entries 2
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

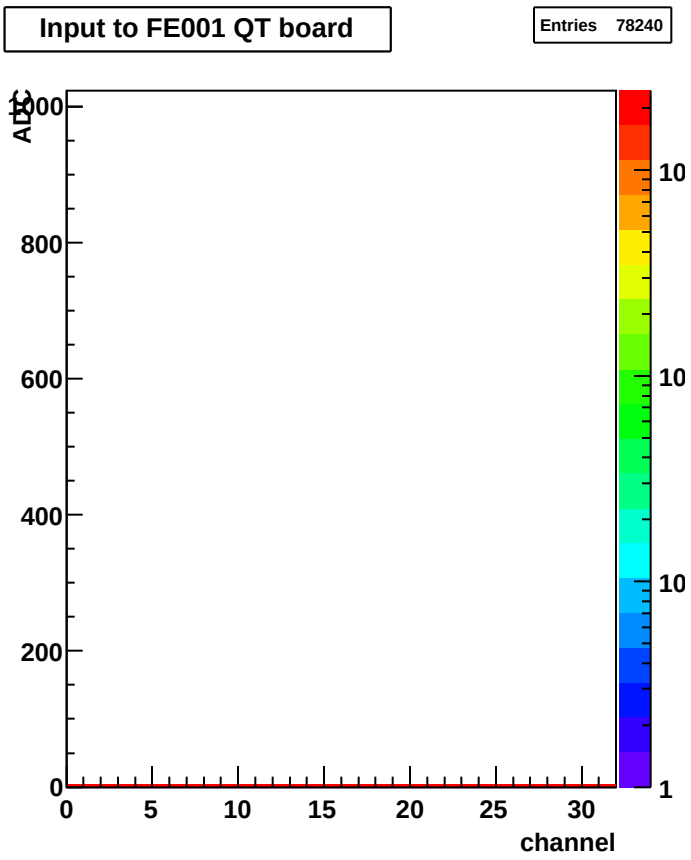

Input to FPD L2 DSM
Entries 7335
CL bits
8
7
75
38
21
89
62
44
2281
2337
2373
SMALL
LARGE-S
LARGE-N

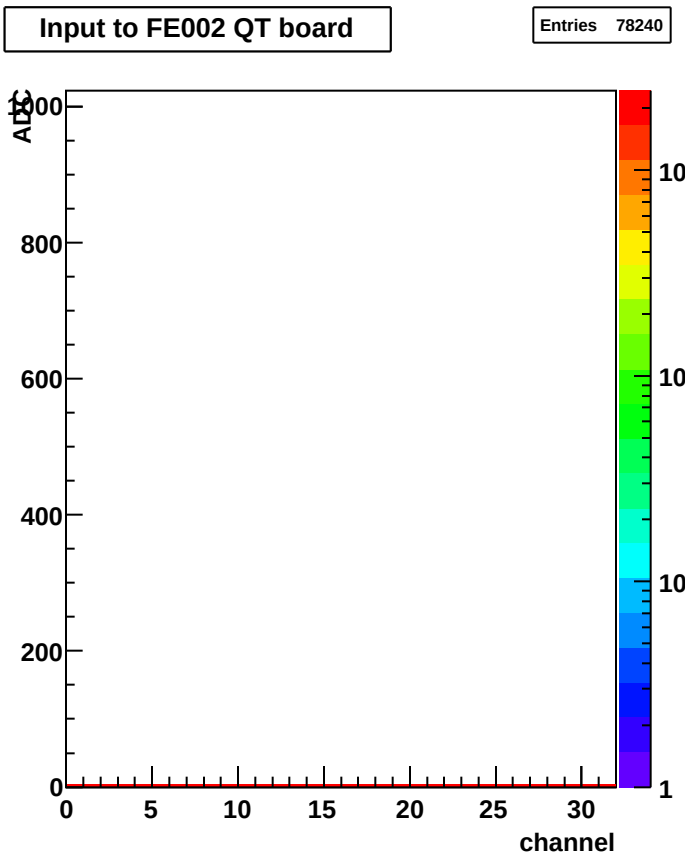

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

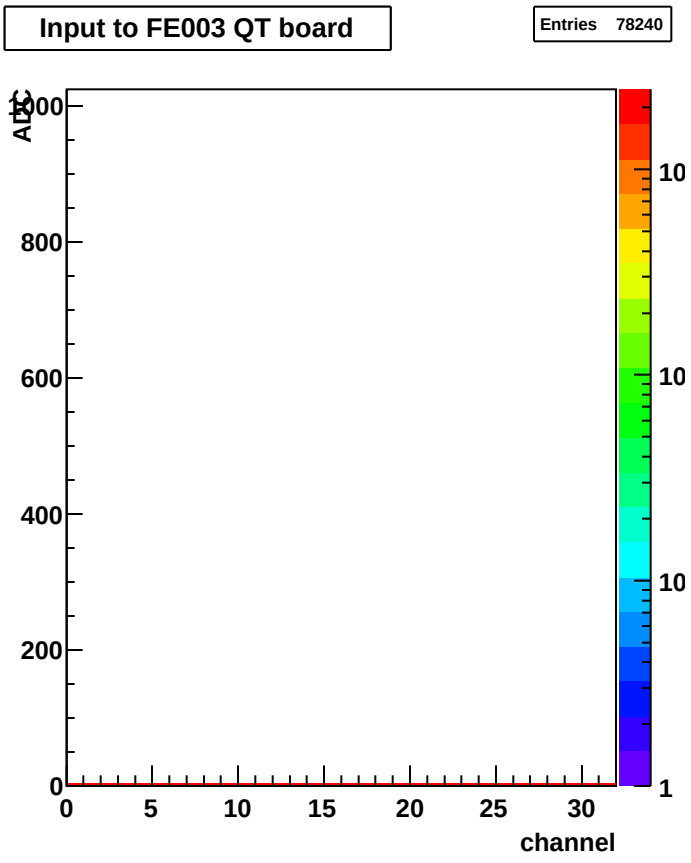

Input to FPD L2 DSM
Entries 7335
HT bits
16
77
40
50
147
60
54
2232
2308
2351
SMALL
LARGE-S
LARGE-N

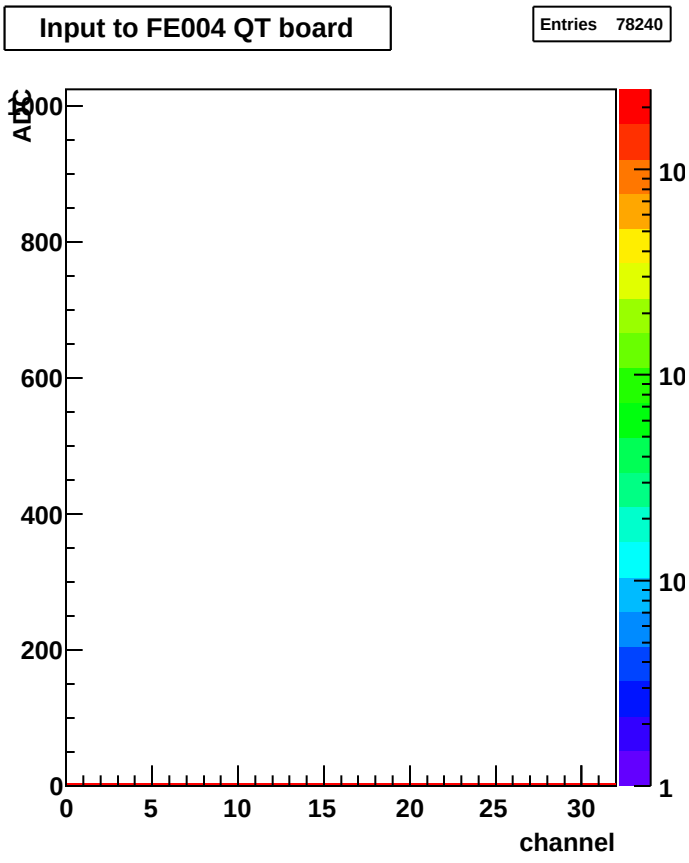

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

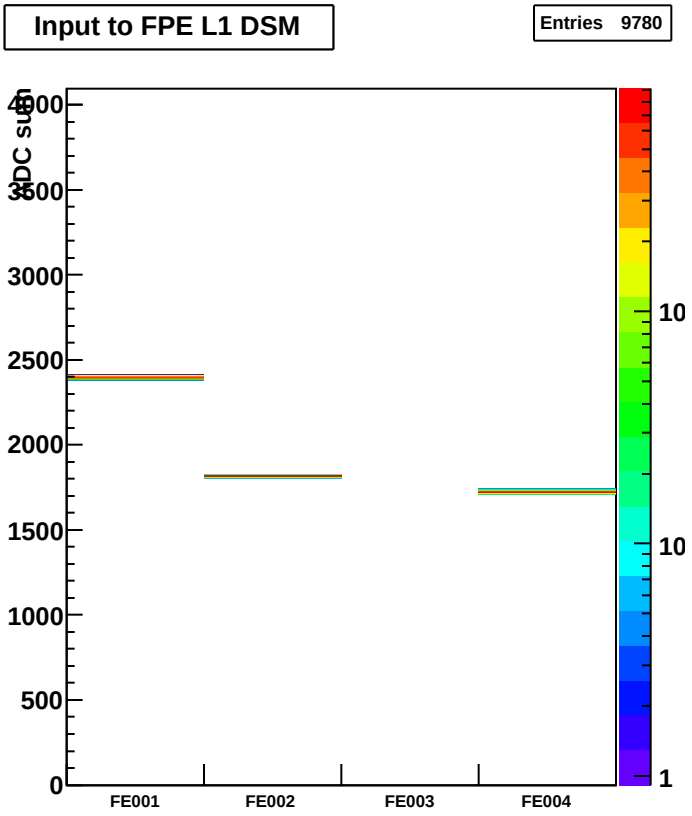

Input to FPD L2 DSM
Entries 14670
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3

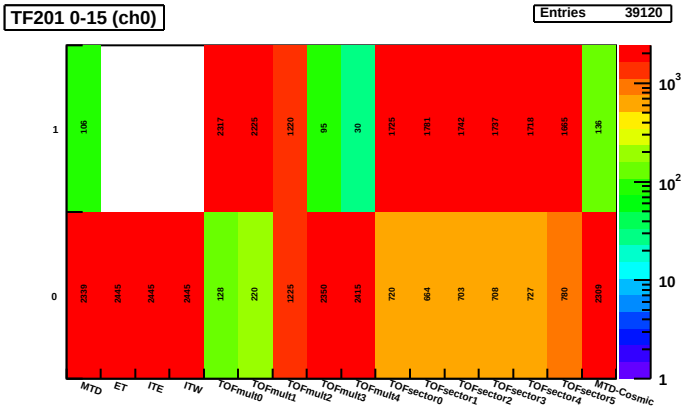

Input to FE001 QT board
Entries 78240
ADC
channel

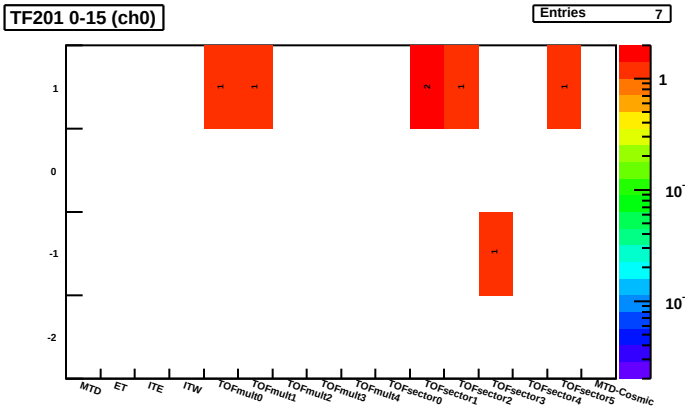

Input to FE002 QT board
Entries 78240
ADC
channel

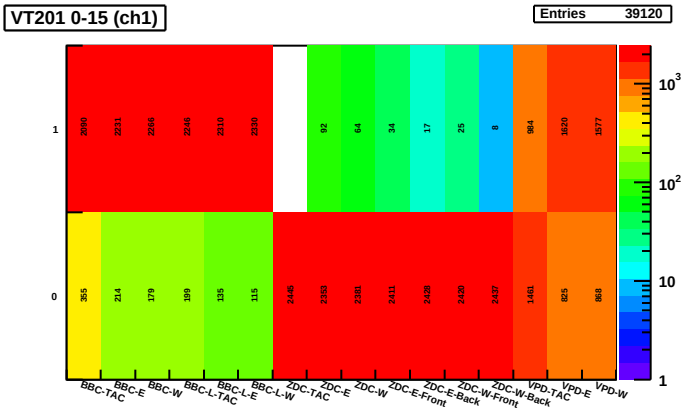

Input to FE003 QT board
Entries 78240
ADC
channel

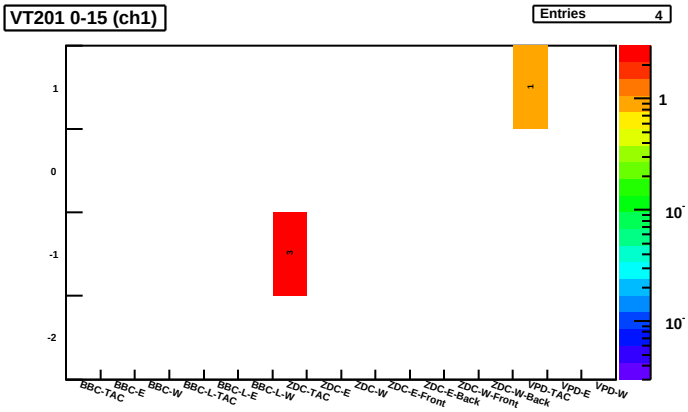

Input to FE004 QT board
Entries 78240
ADC
channel

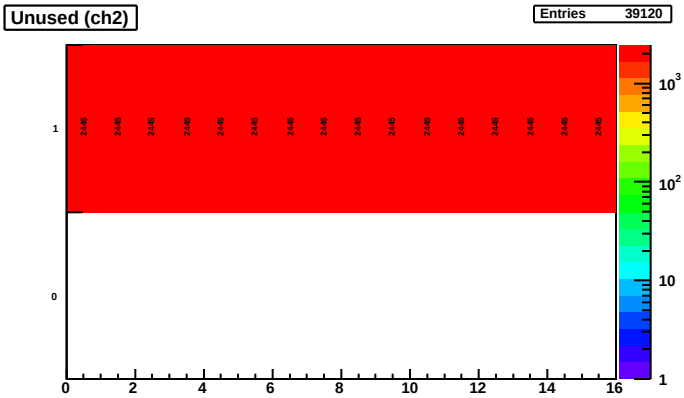

Input to FPE L1 DSM
Entries 9780
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
1

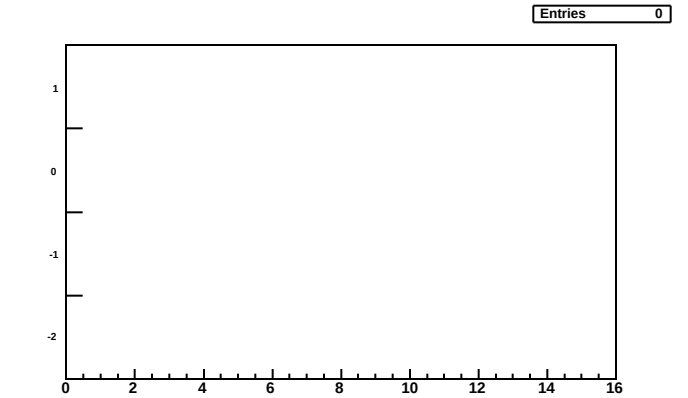

TF201 0-15 (ch0)
Entries 39120
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

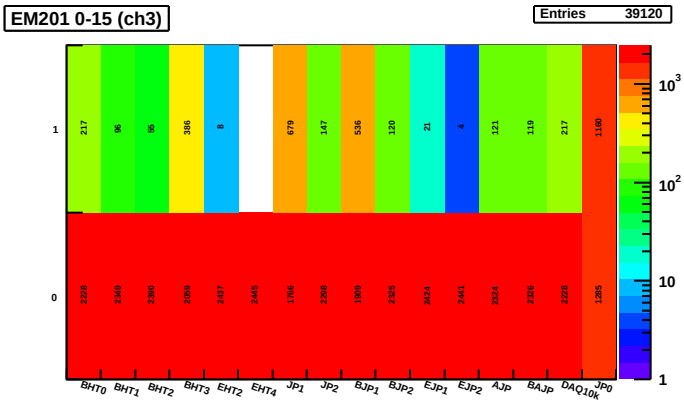

TF201 0-15 (ch0)
Entries 7
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 39120
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 39120


Entries 0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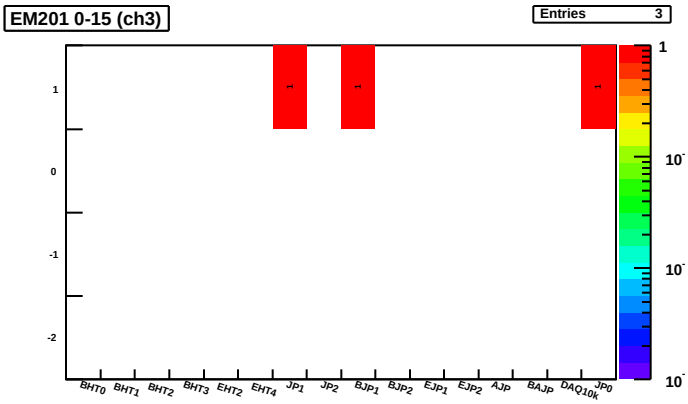
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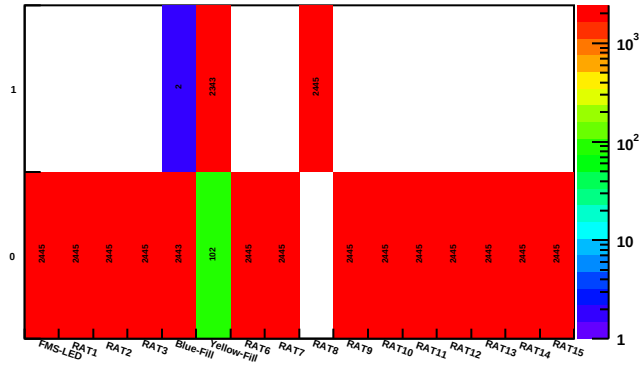
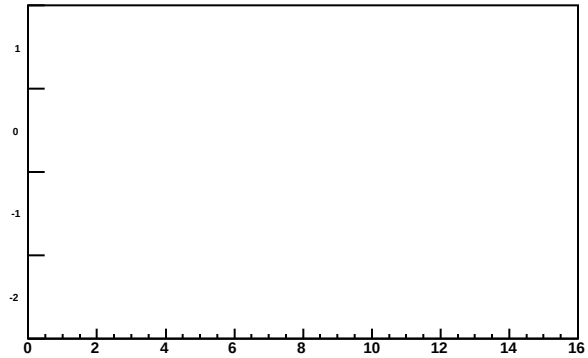
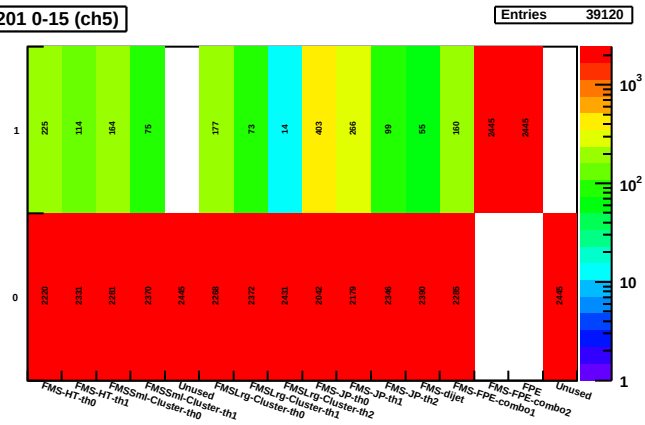
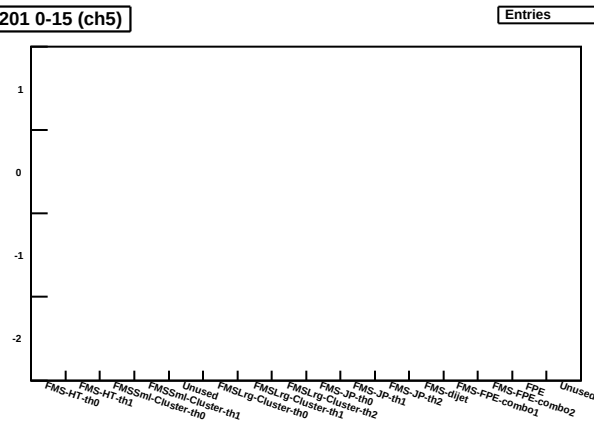
RAT board (ch4)



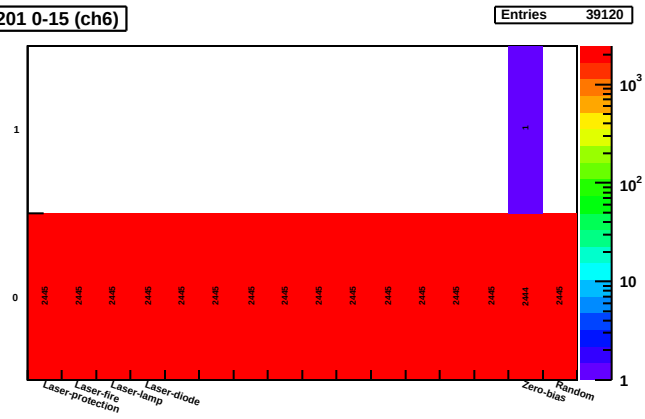


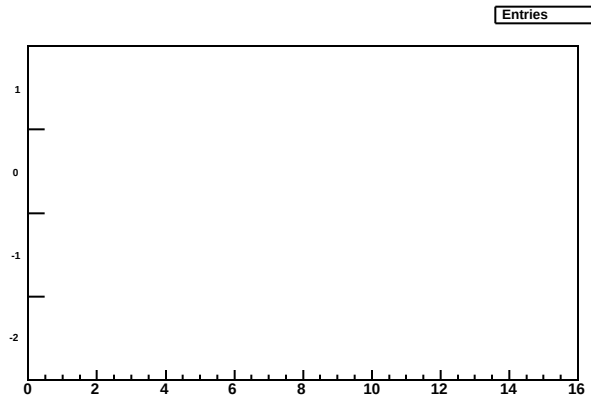
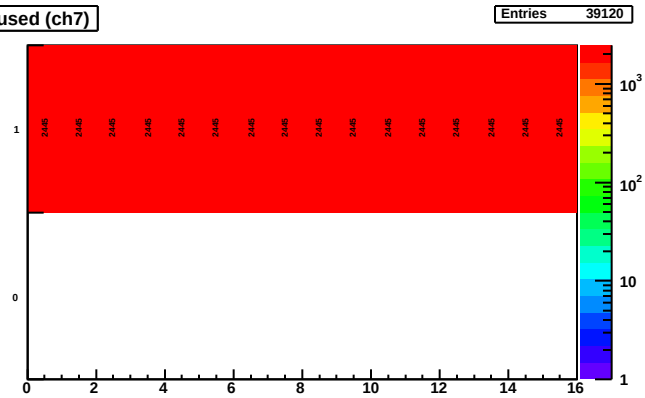
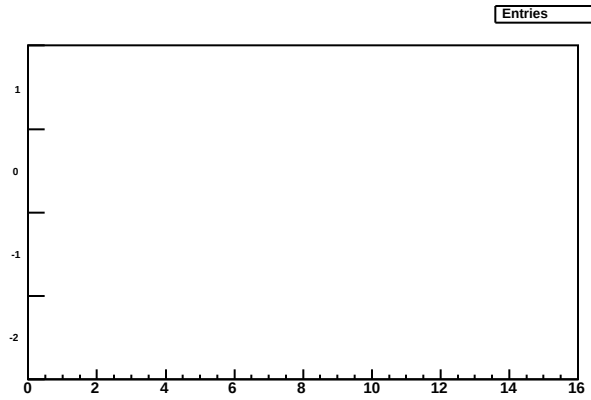
FP201 0-15 (ch5)





ST201 0-15 (ch6)





Unused (ch7)