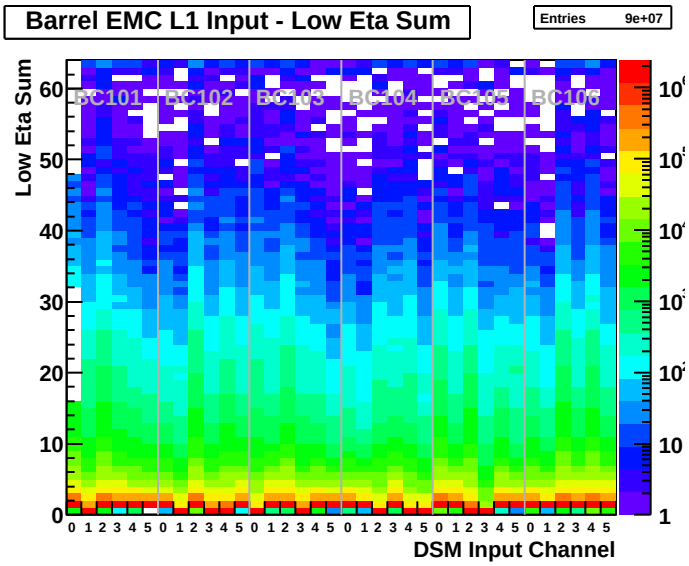
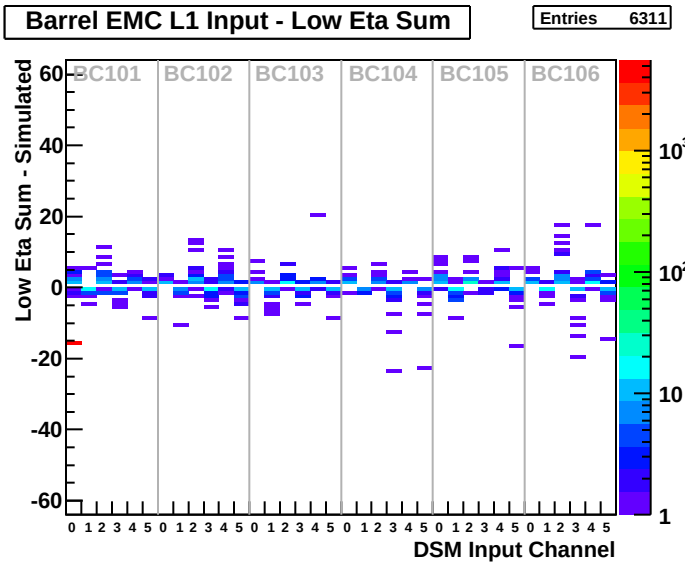
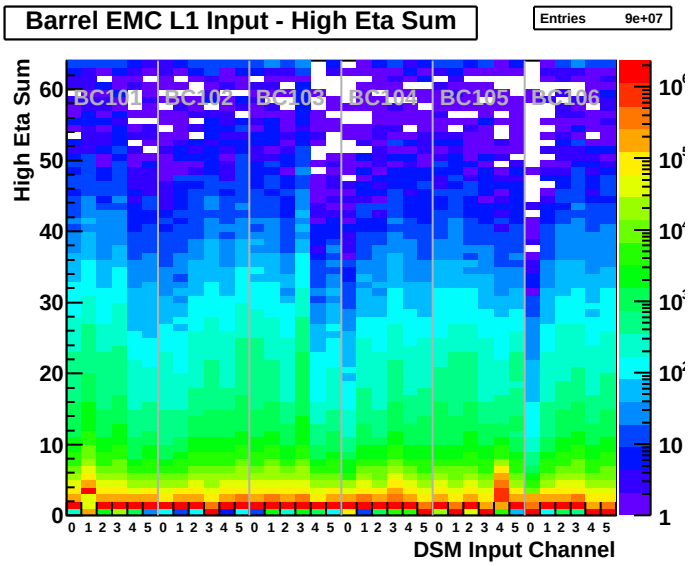
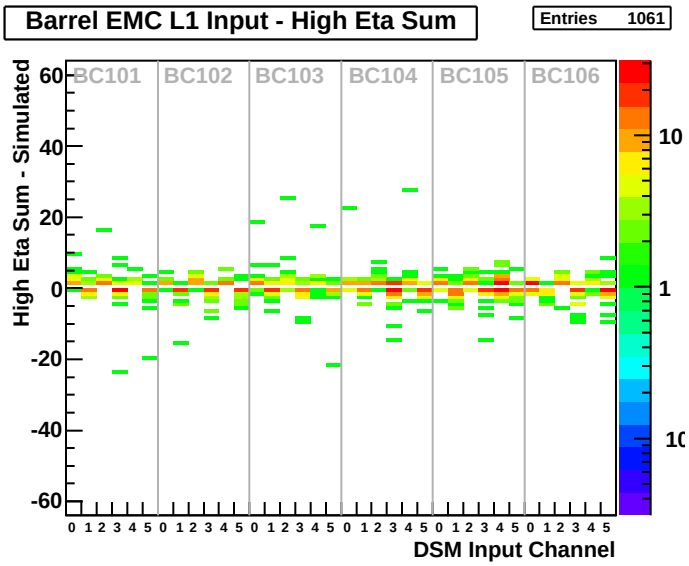
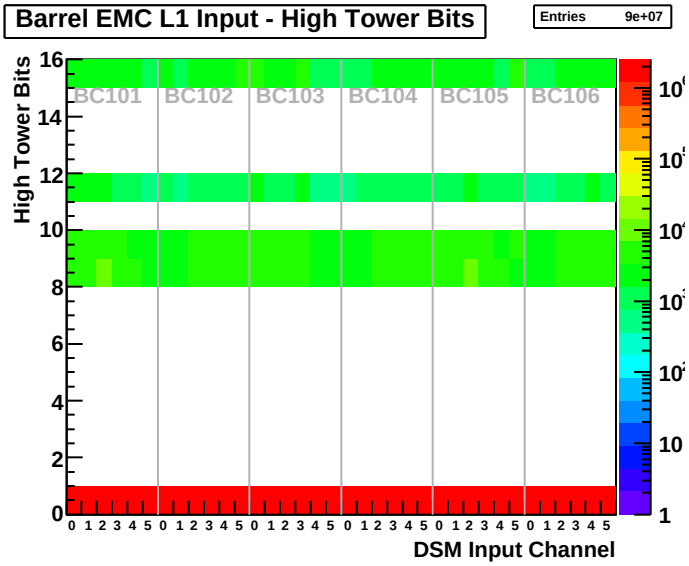
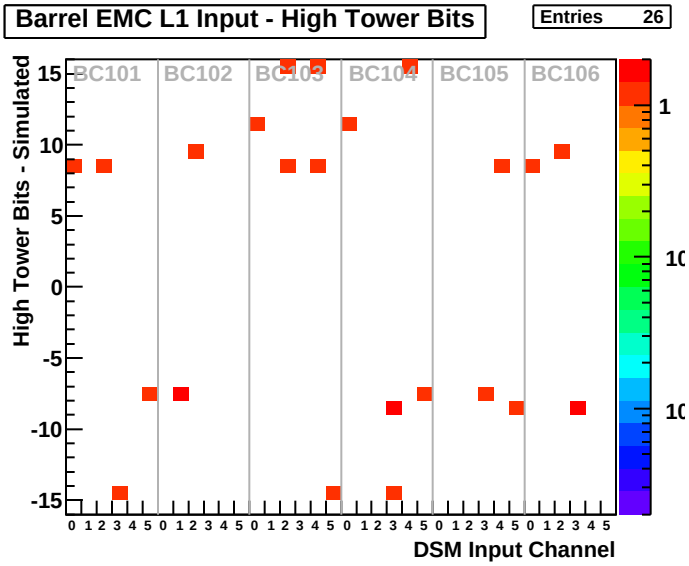

Barrel EMC L1 Input - Low Eta Sum
Entries 9e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

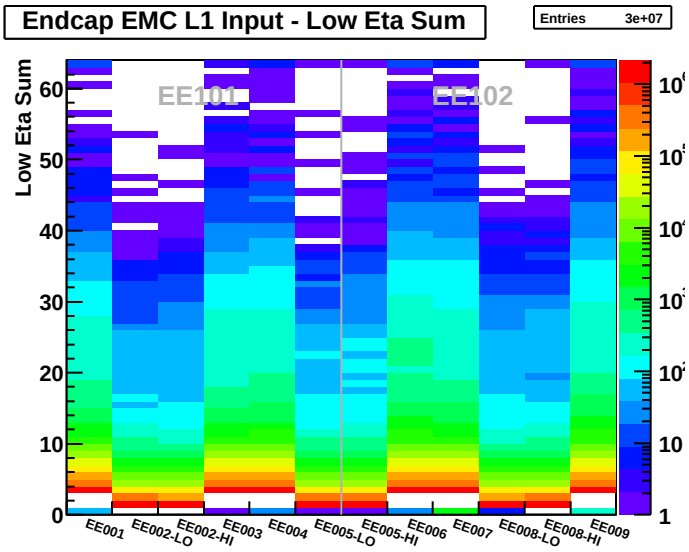

Barrel EMC L1 Input - Low Eta Sum
Entries 6311
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

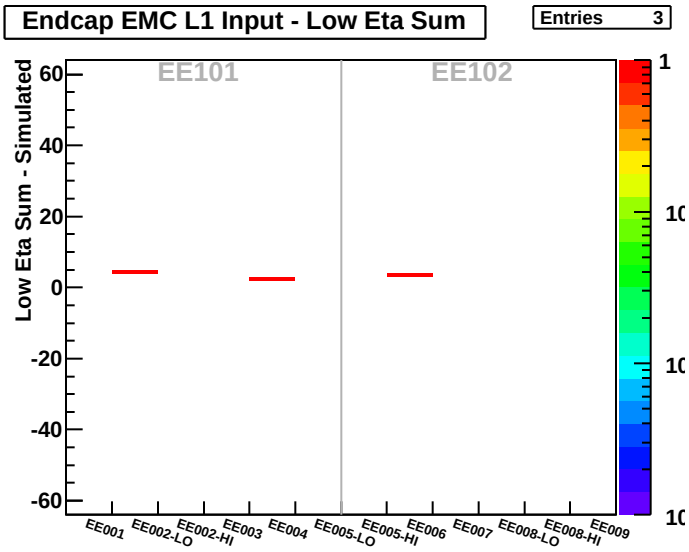

Barrel EMC L1 Input - High Eta Sum
Entries 9e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

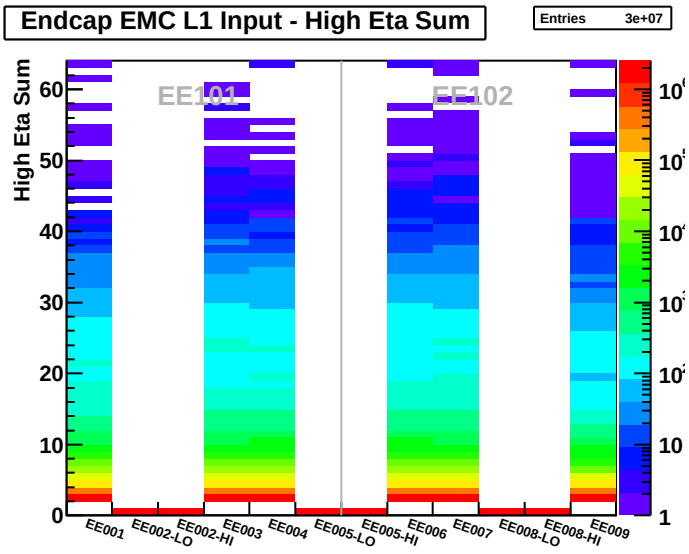

Barrel EMC L1 Input - High Eta Sum
Entries 1061
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

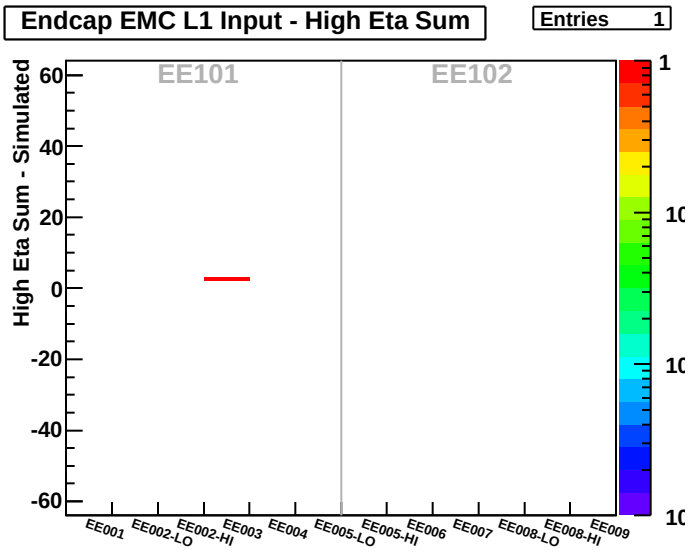

Barrel EMC L1 Input - High Tower Bits
Entries 9e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

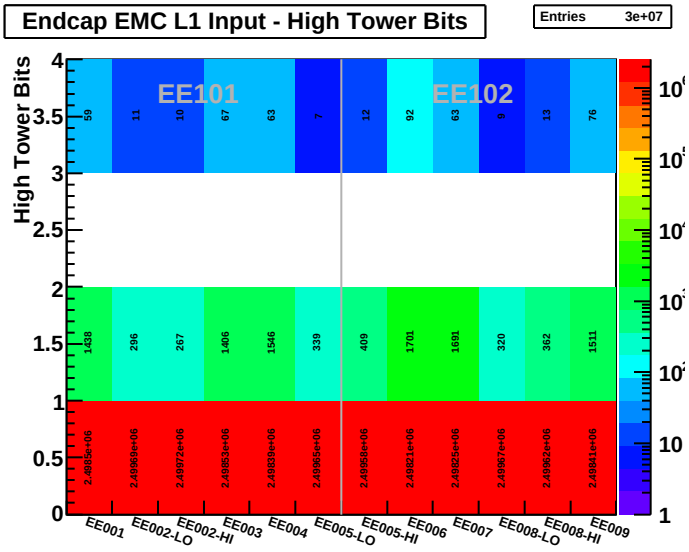

Barrel EMC L1 Input - High Tower Bits
Entries 26
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

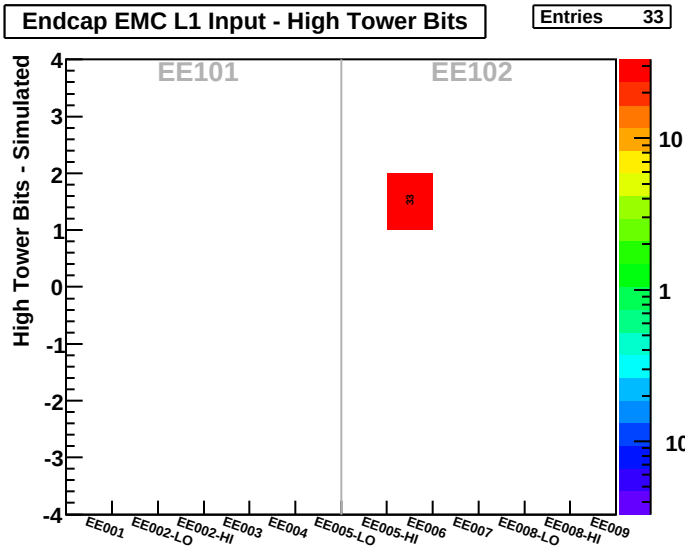

Endcap EMC L1 Input - Low Eta Sum
Entries 3e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

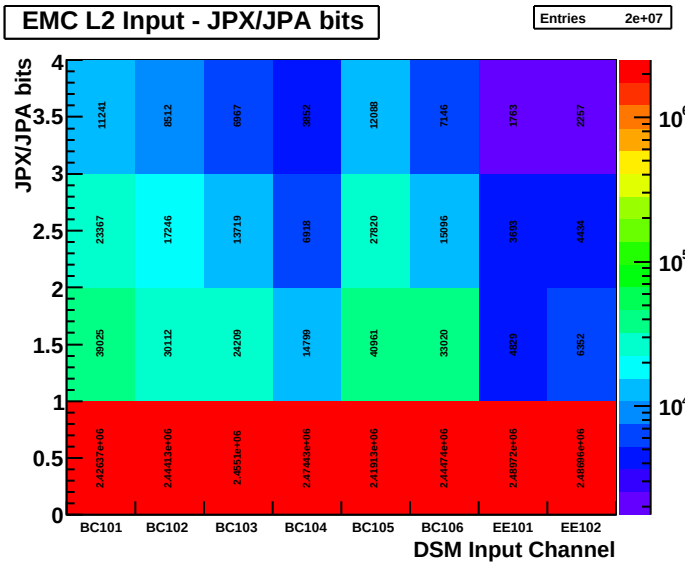

Endcap EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

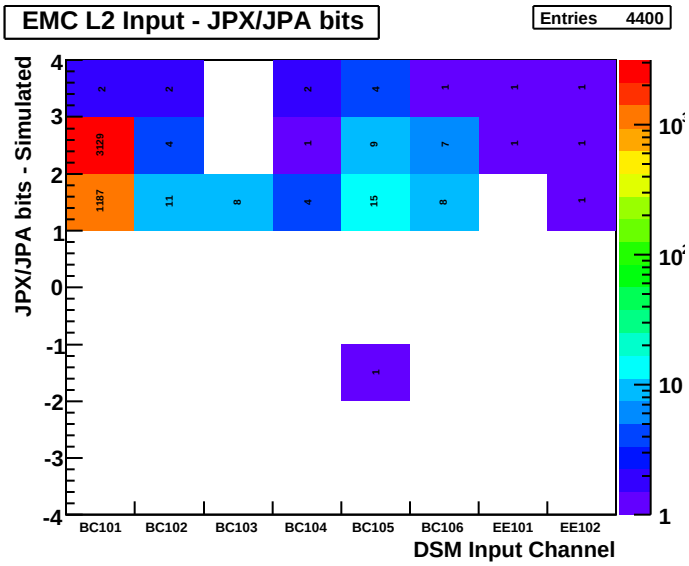

Endcap EMC L1 Input - High Eta Sum
Entries 3e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

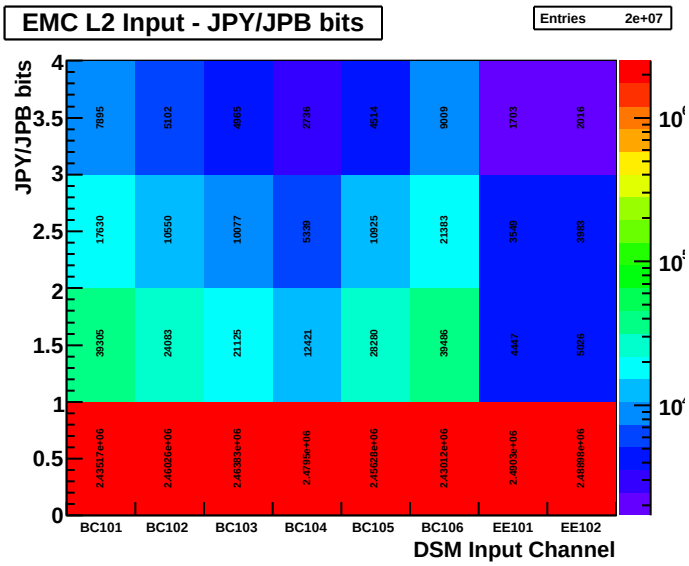

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

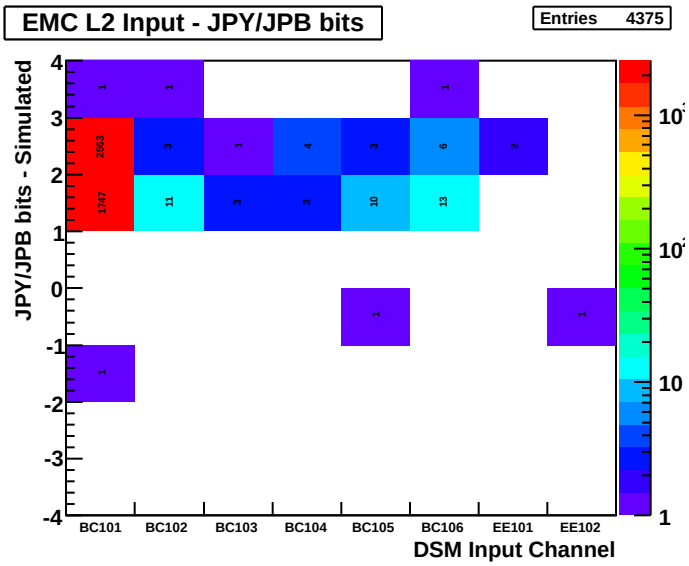

Endcap EMC L1 Input - High Tower Bits
Entries 3e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

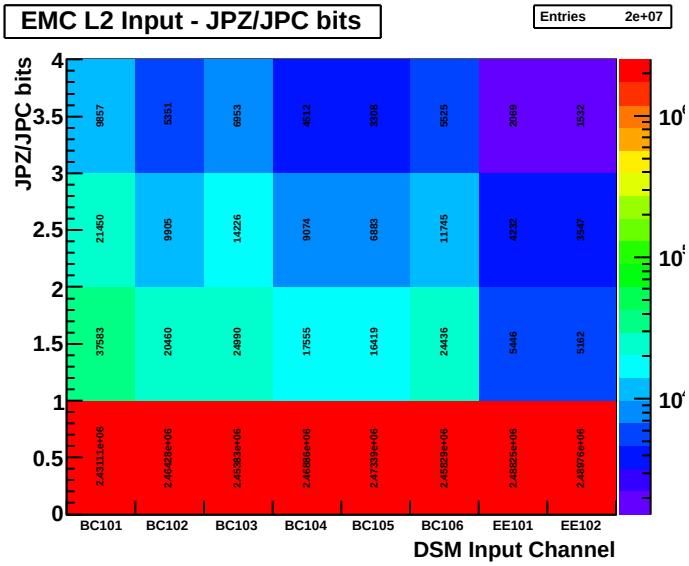

Endcap EMC L1 Input - High Tower Bits
Entries 33
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

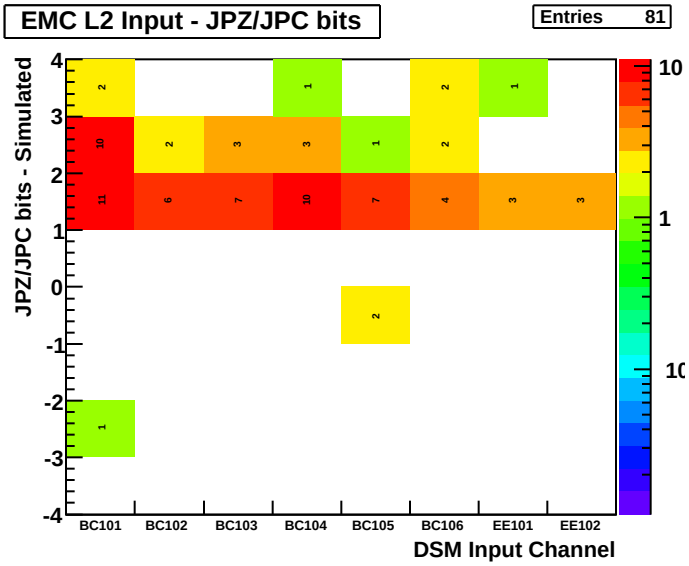

EMC L2 Input - JPX/JPA bits
Entries 2e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

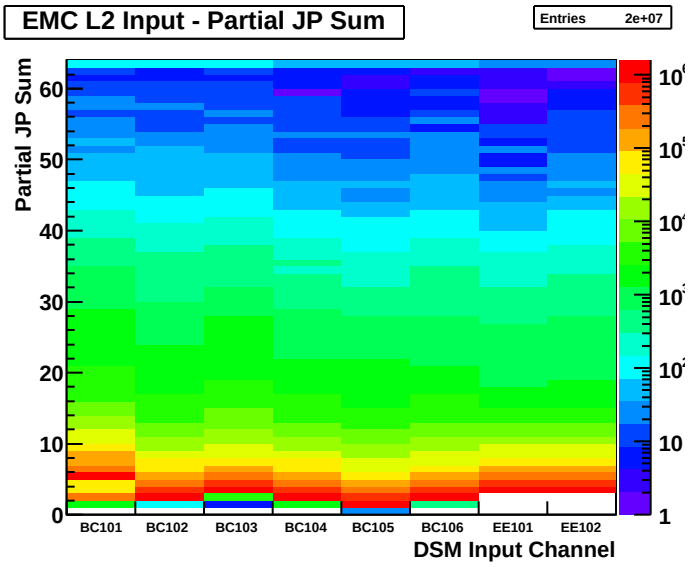

EMC L2 Input - JPX/JPA bits
Entries 4400
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

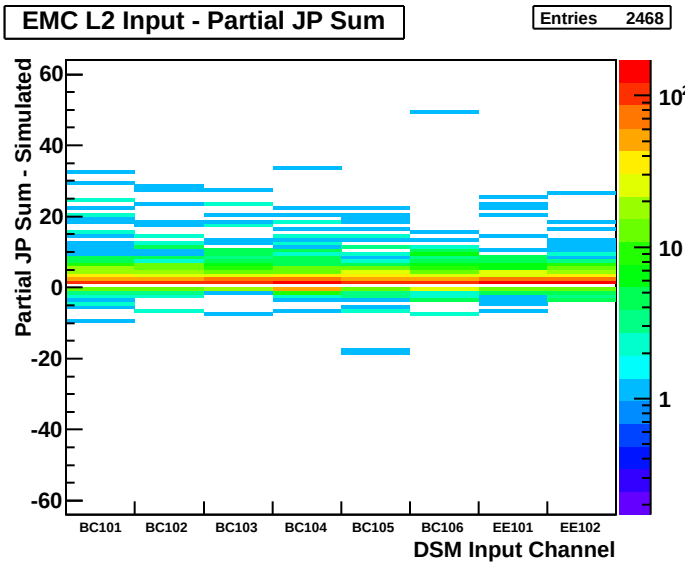

EMC L2 Input - JPY/JPB bits
Entries 2e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

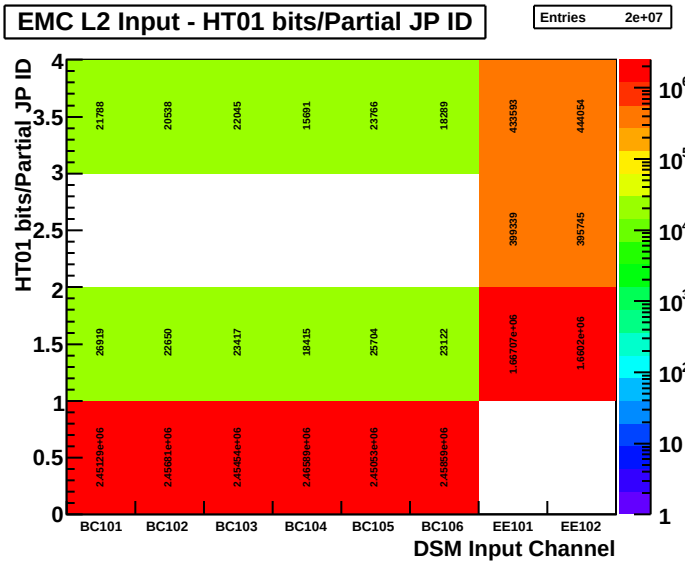

EMC L2 Input - JPY/JPB bits
Entries 4375
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

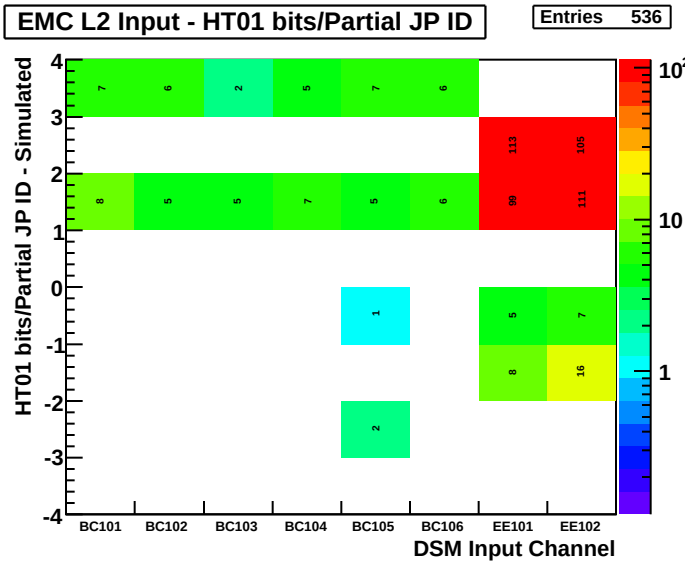

EMC L2 Input - JPZ/JPC bits
Entries 2e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

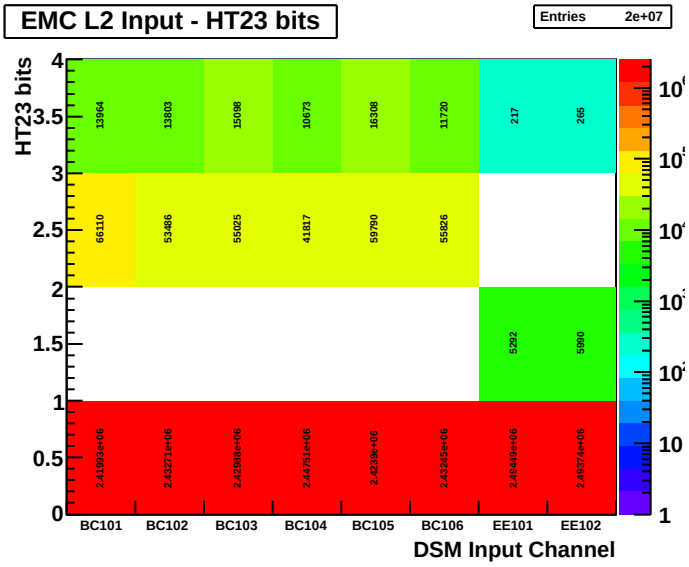

EMC L2 Input - JPZ/JPC bits
Entries 81
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

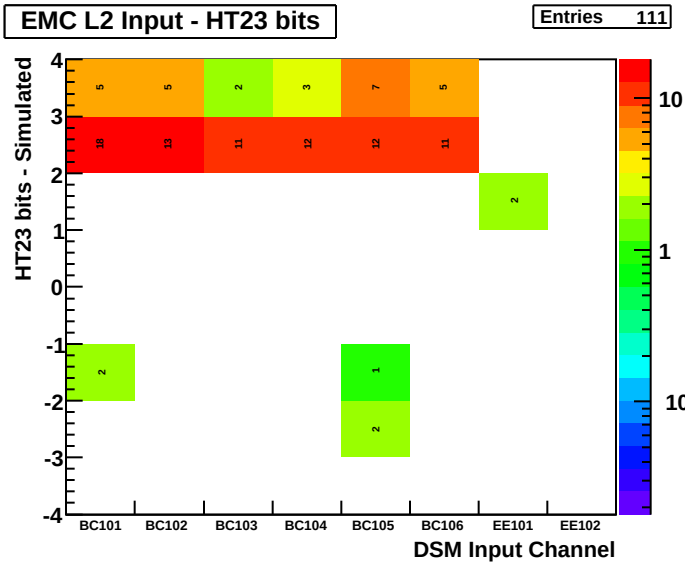

EMC L2 Input - Partial JP Sum
Entries 2e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

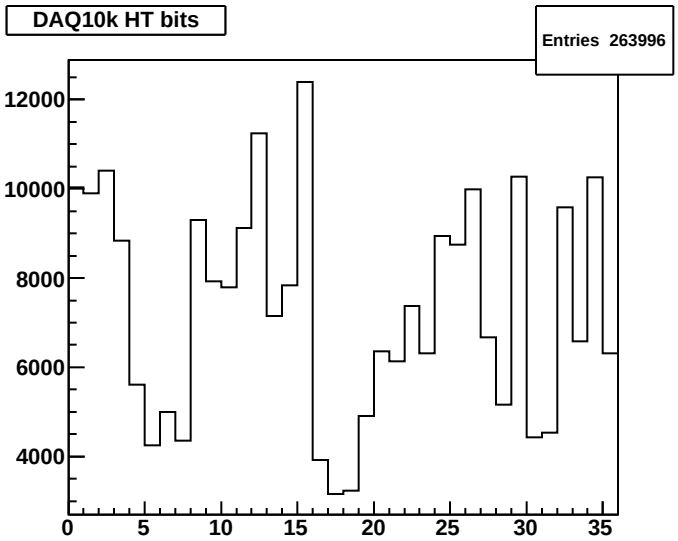

EMC L2 Input - Partial JP Sum
Entries 2468
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

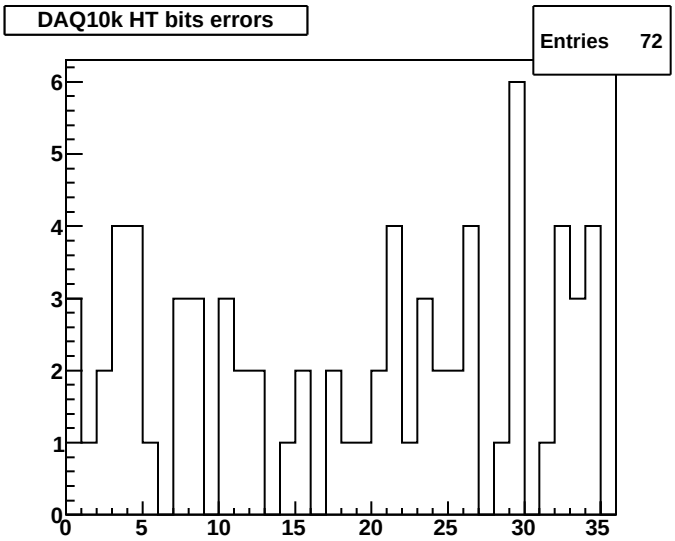

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

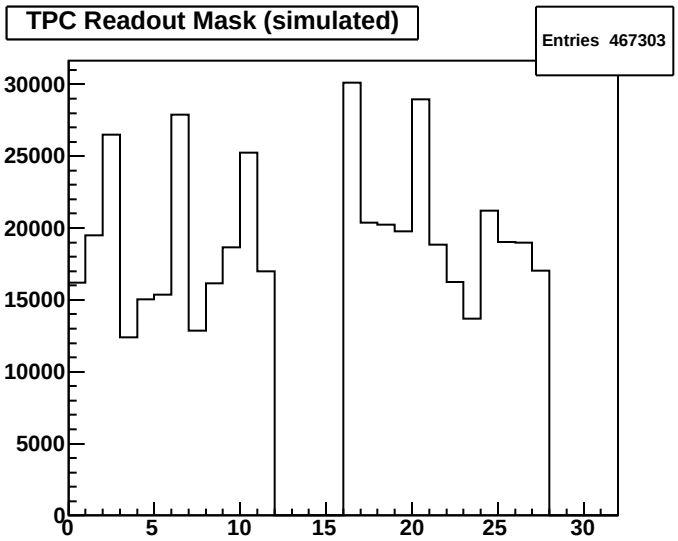

EMC L2 Input - HT01 bits/Partial JP ID
Entries 536
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

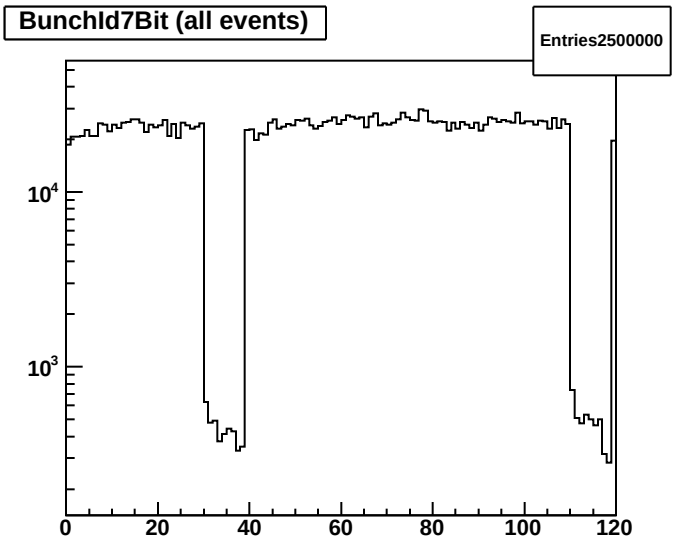

EMC L2 Input - HT23 bits
Entries 2e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

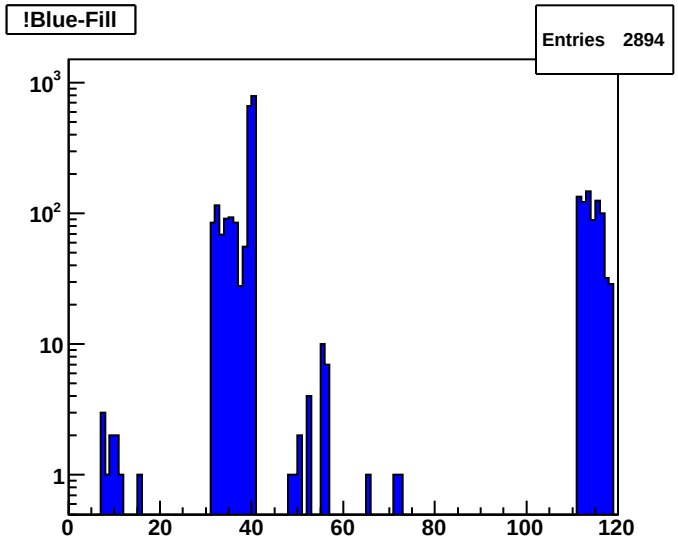

EMC L2 Input - HT23 bits
Entries 111
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

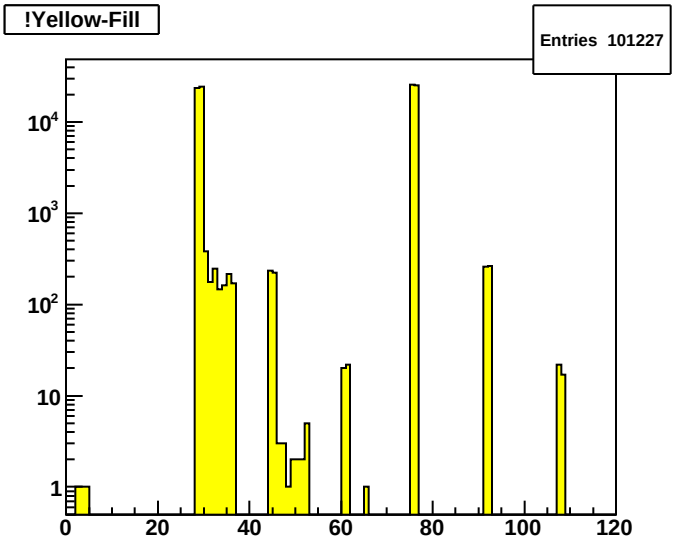

DAQ10k HT bits
Entries 263996

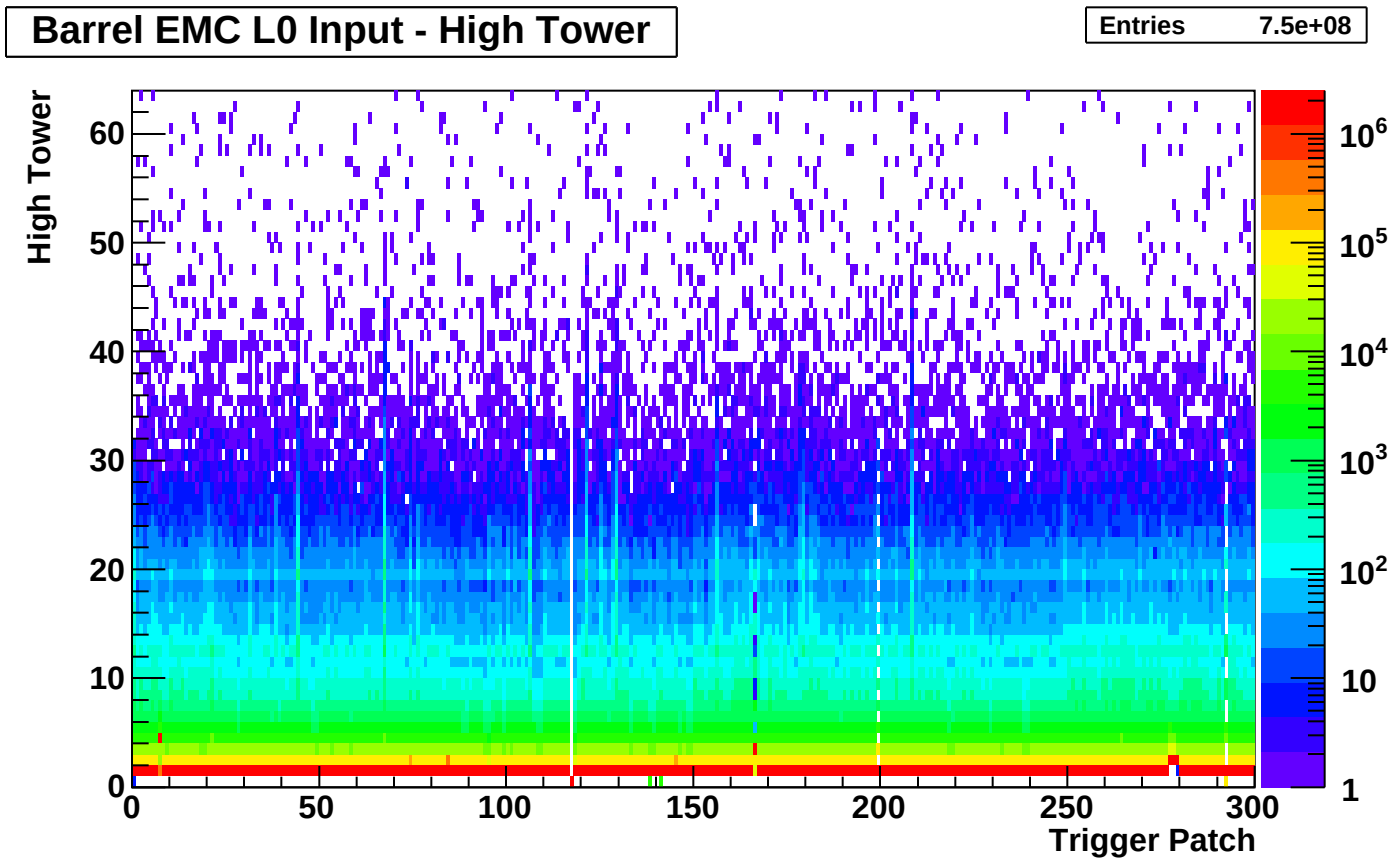

DAQ10k HT bits errors
Entries 72

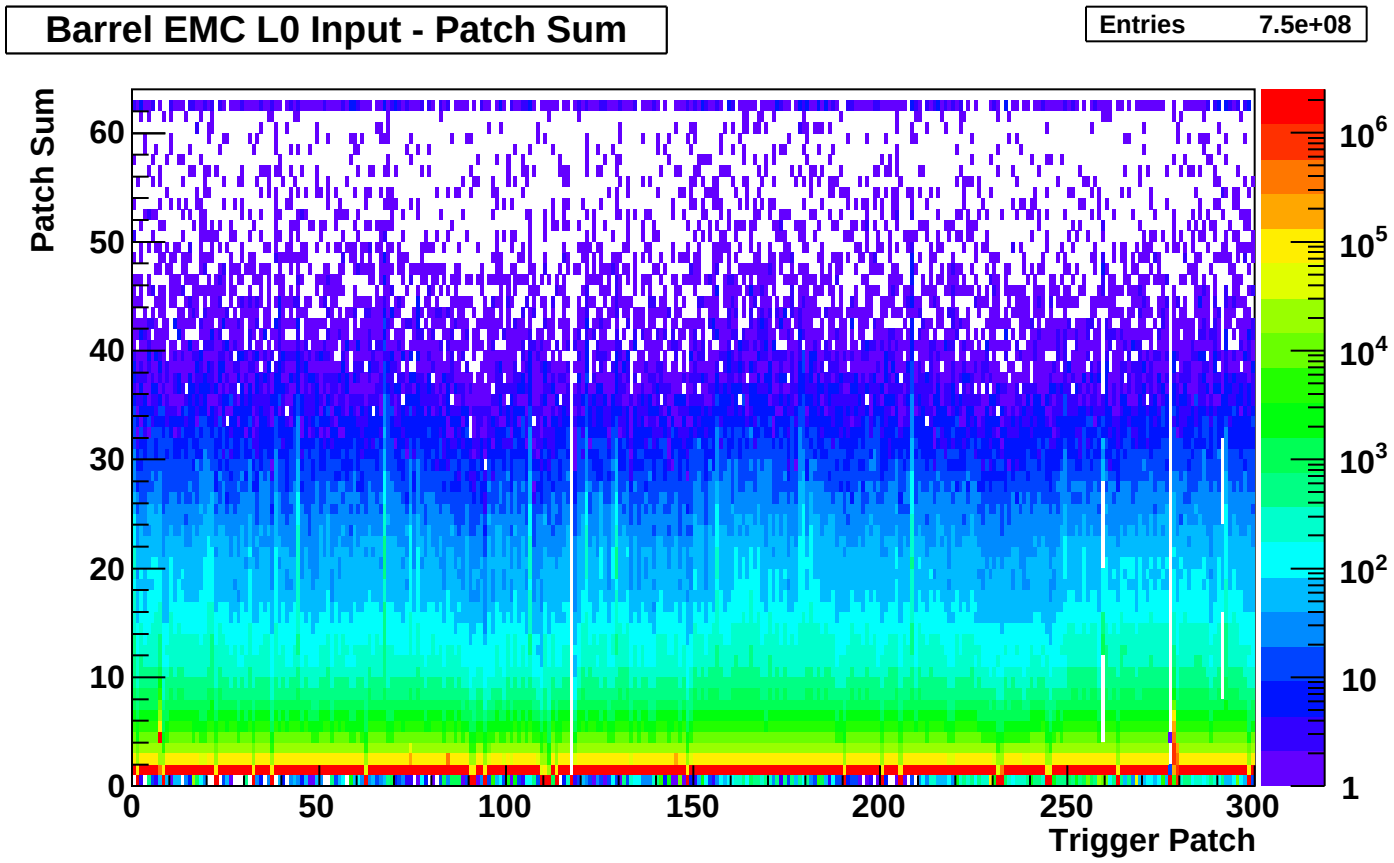

TPC Readout Mask (simulated)
Entries 467303

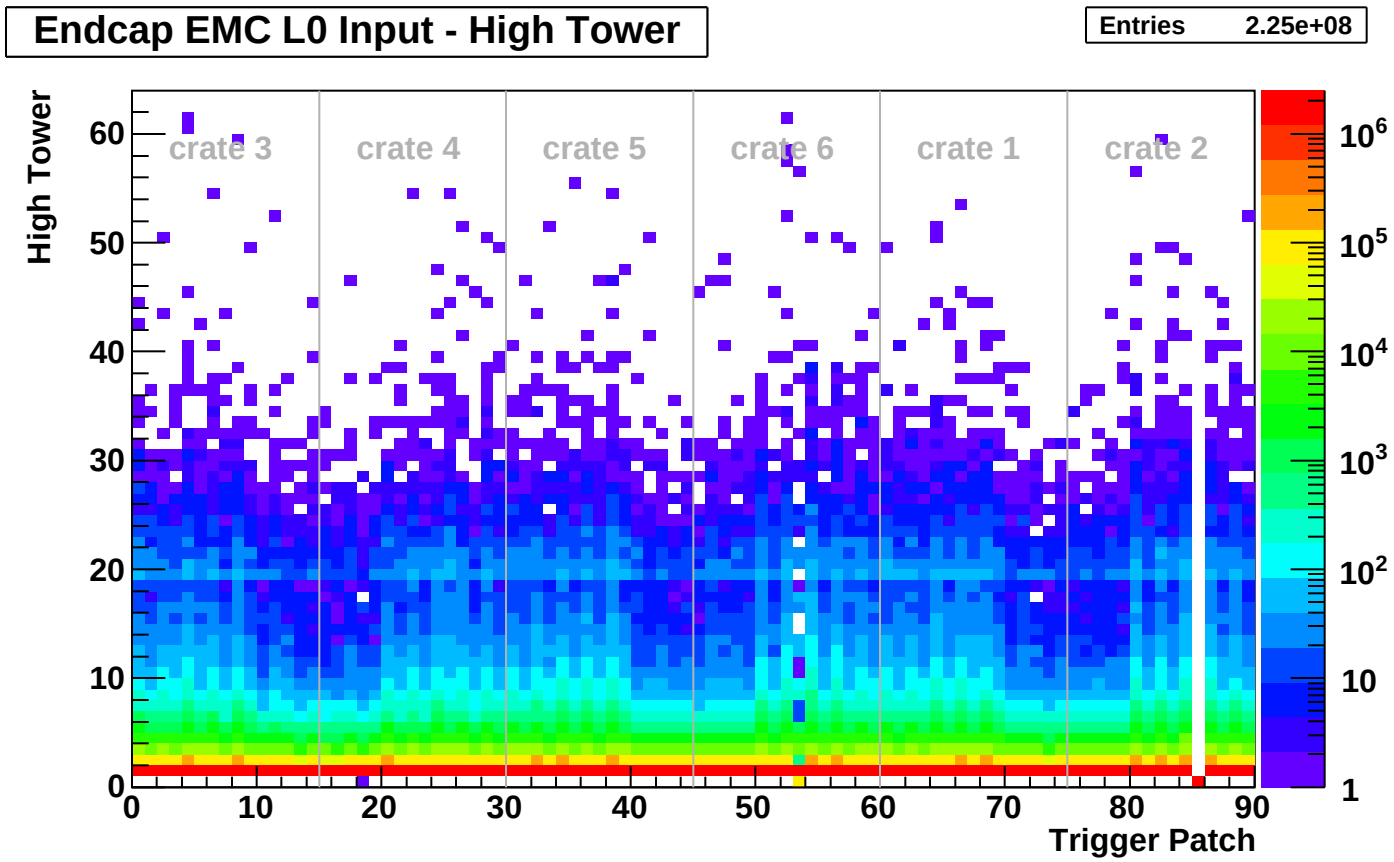

BunchId7Bit (all events)
Entries2500000

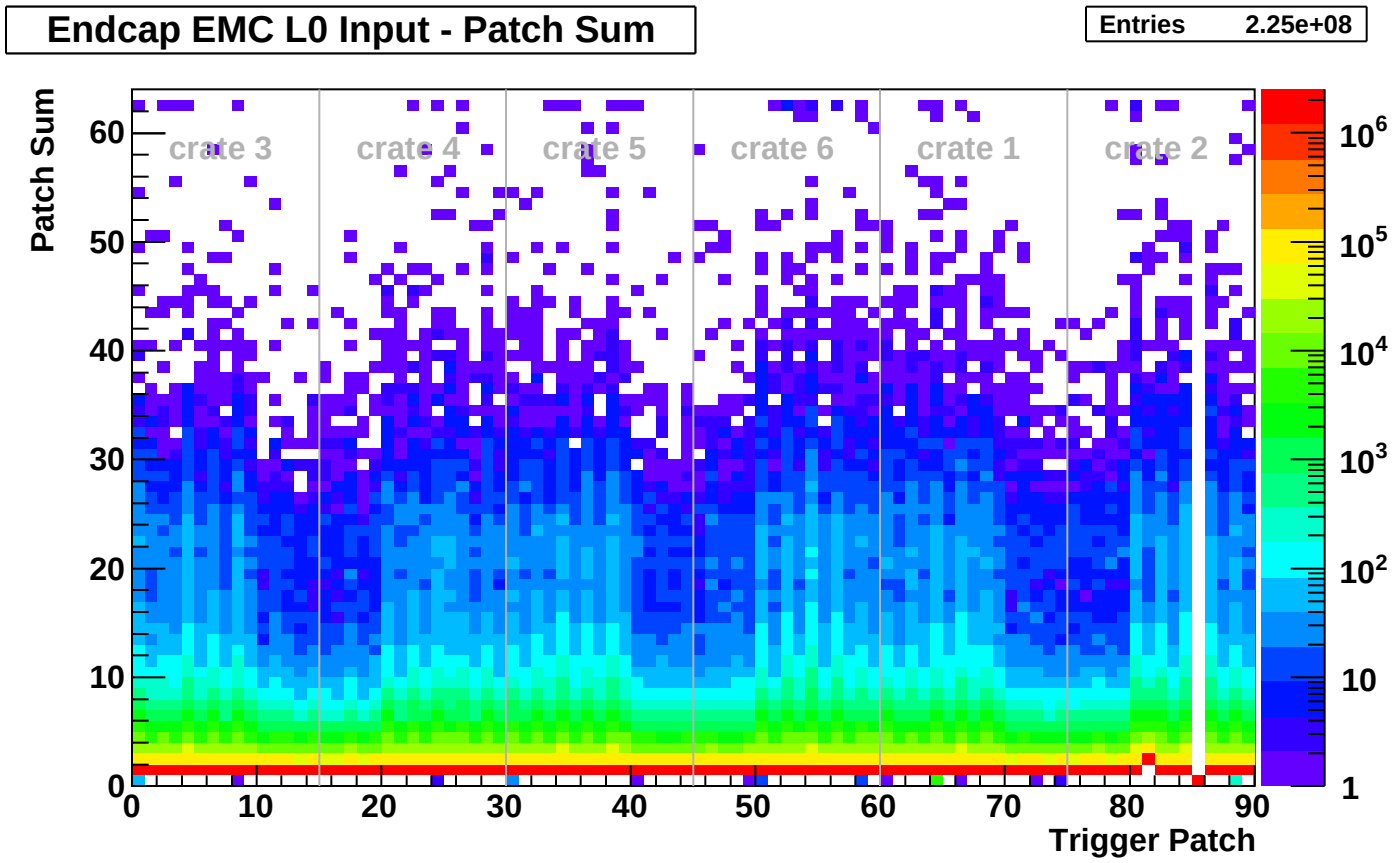

!Blue-Fill
Entries 2894

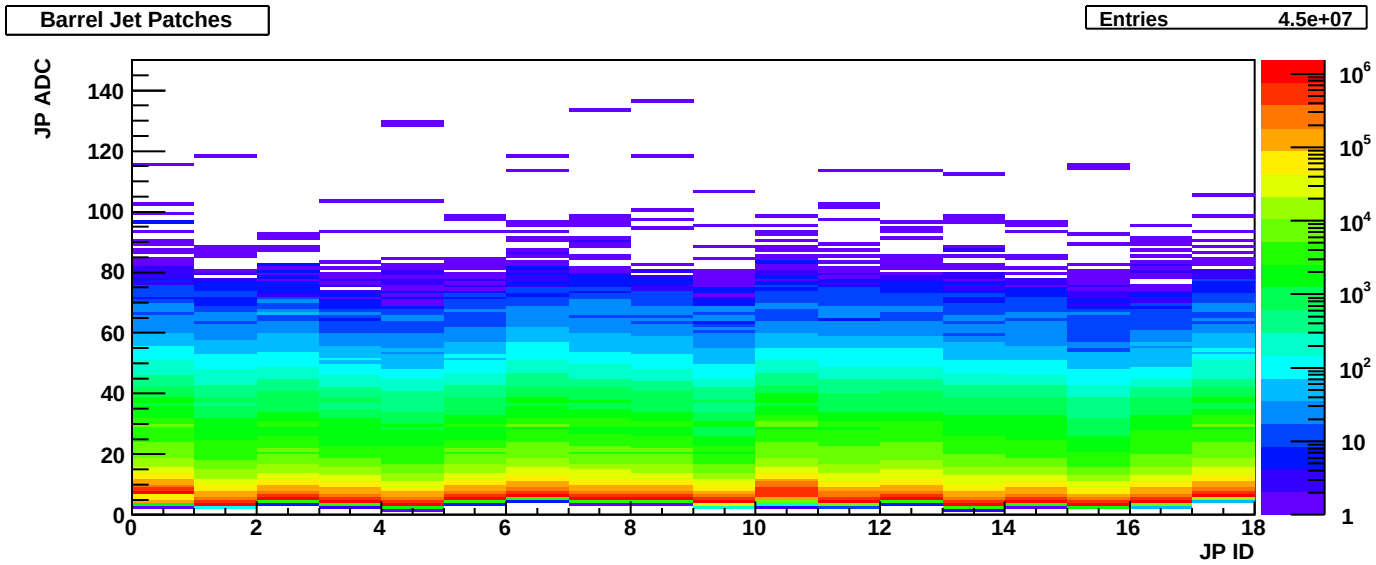

!Yellow-Fill
Entries 101227

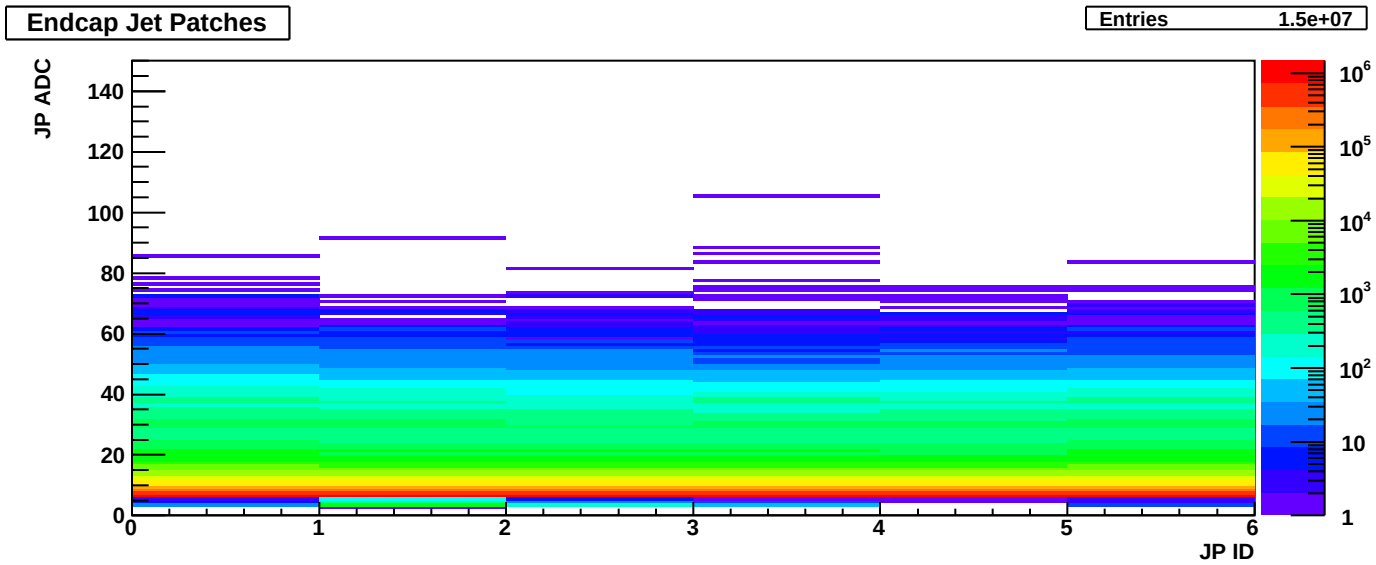

Barrel EMC L0 Input - High Tower
Entries 7.5e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

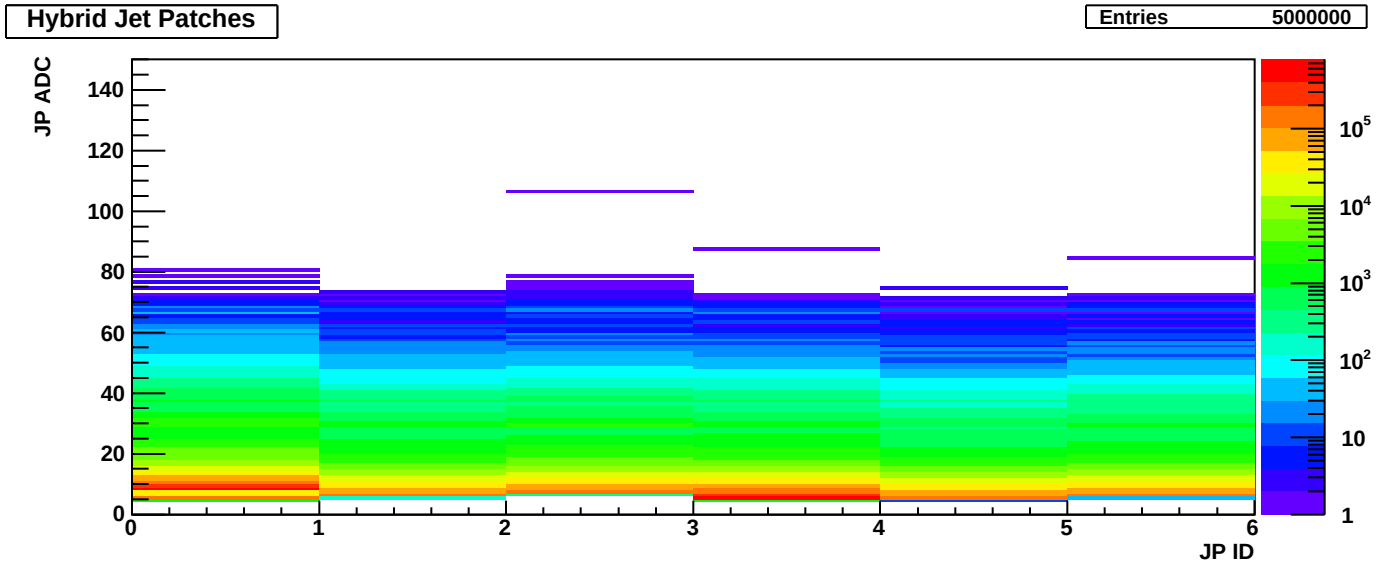

Barrel EMC L0 Input - Patch Sum
Entries 7.5e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

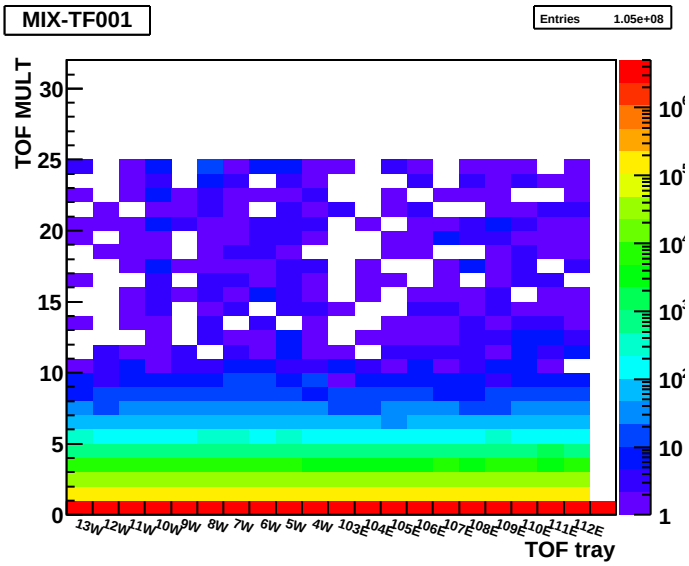

Endcap EMC L0 Input - High Tower
Entries 2.25e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

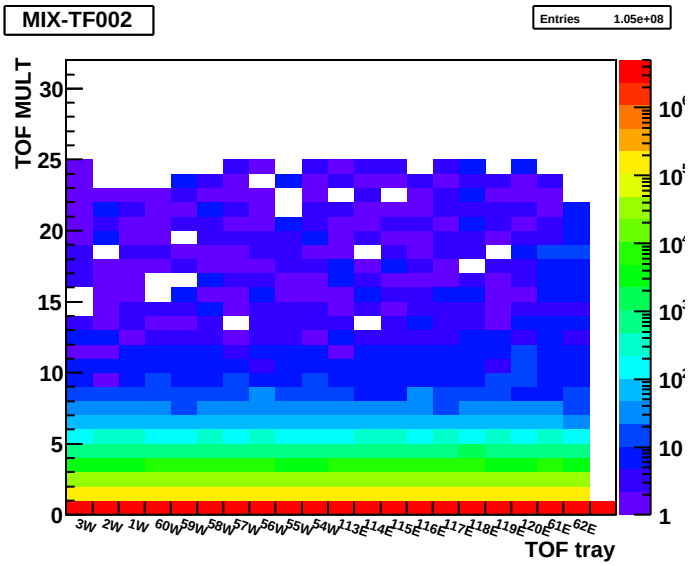

Endcap EMC L0 Input - Patch Sum
Entries 2.25e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

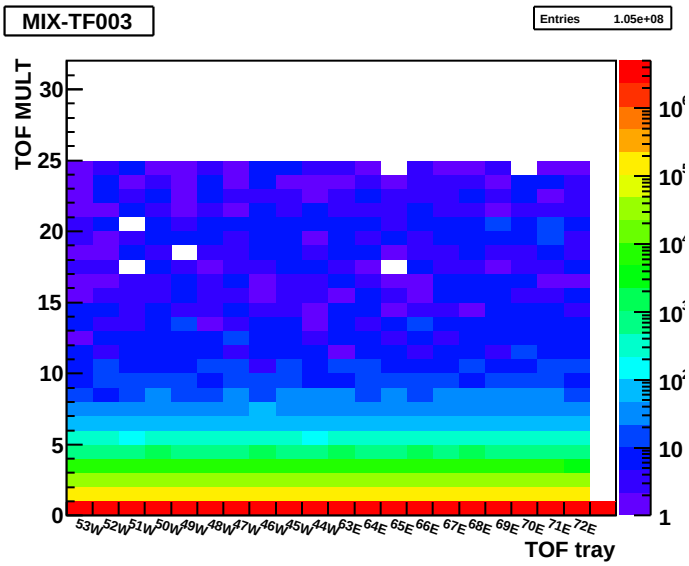

Barrel Jet Patches
Entries 4.5e+07
JP ADC
JP ID

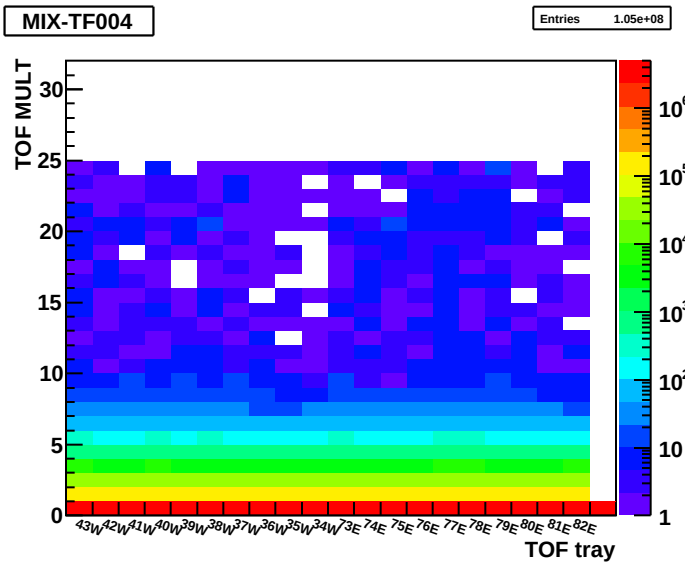

Endcap Jet Patches
Entries 1.5e+07
JP ADC
JP ID

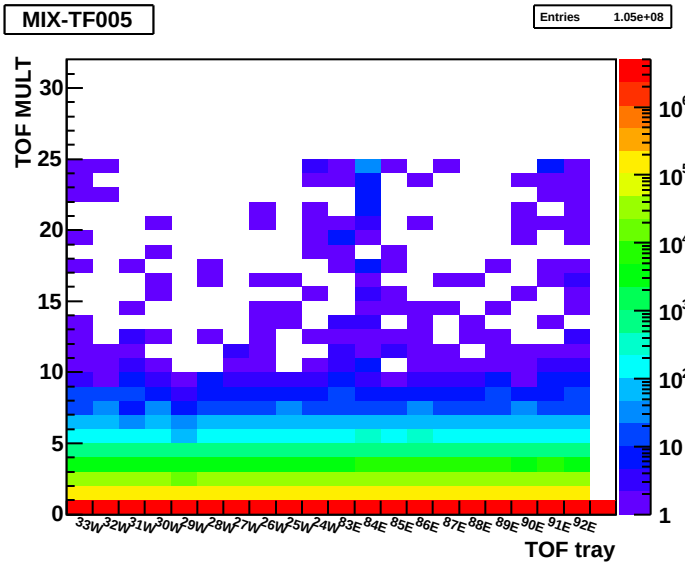

Hybrid Jet Patches
Entries 5000000
JP ADC
JP ID

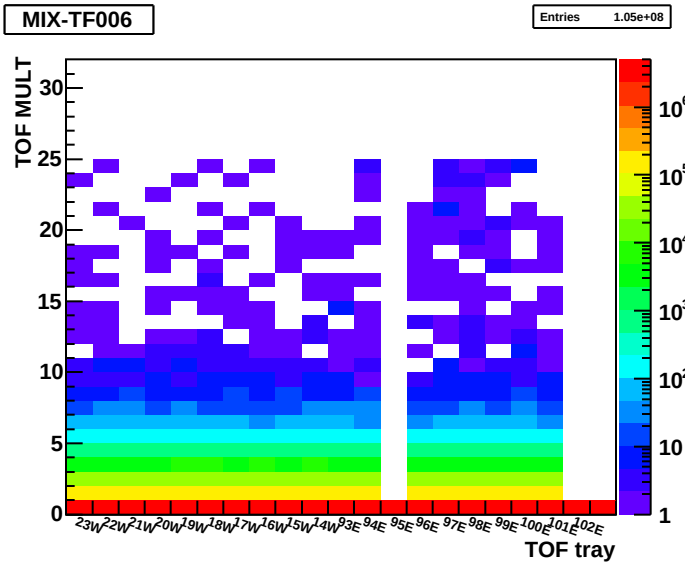

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

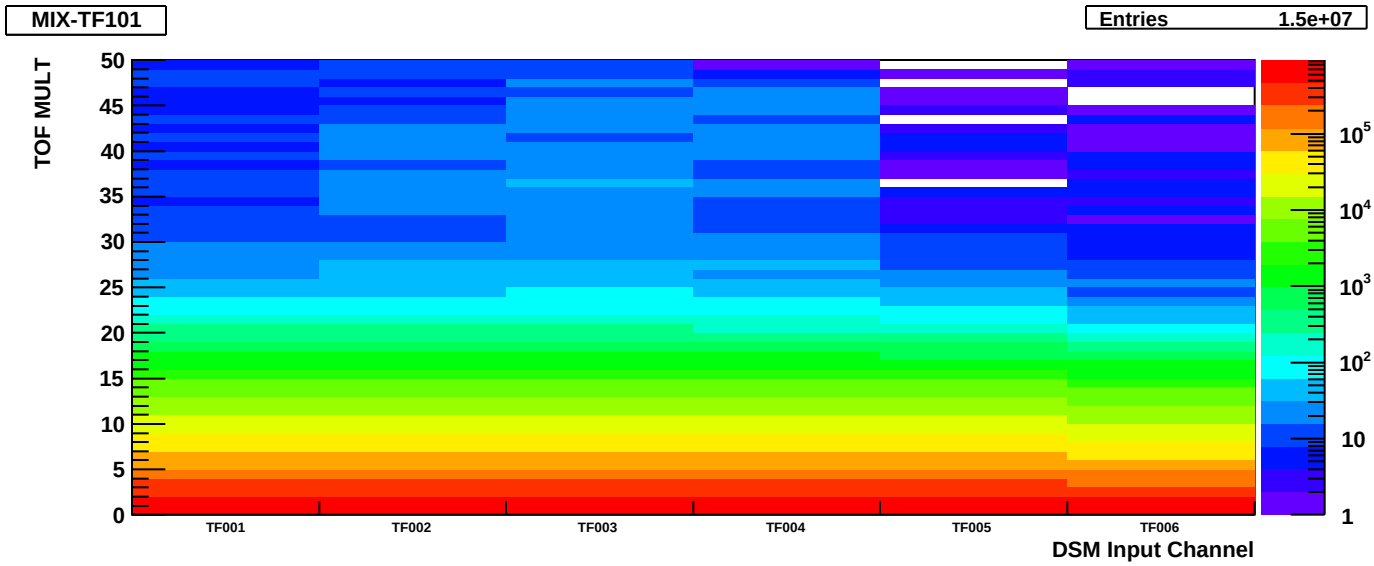

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

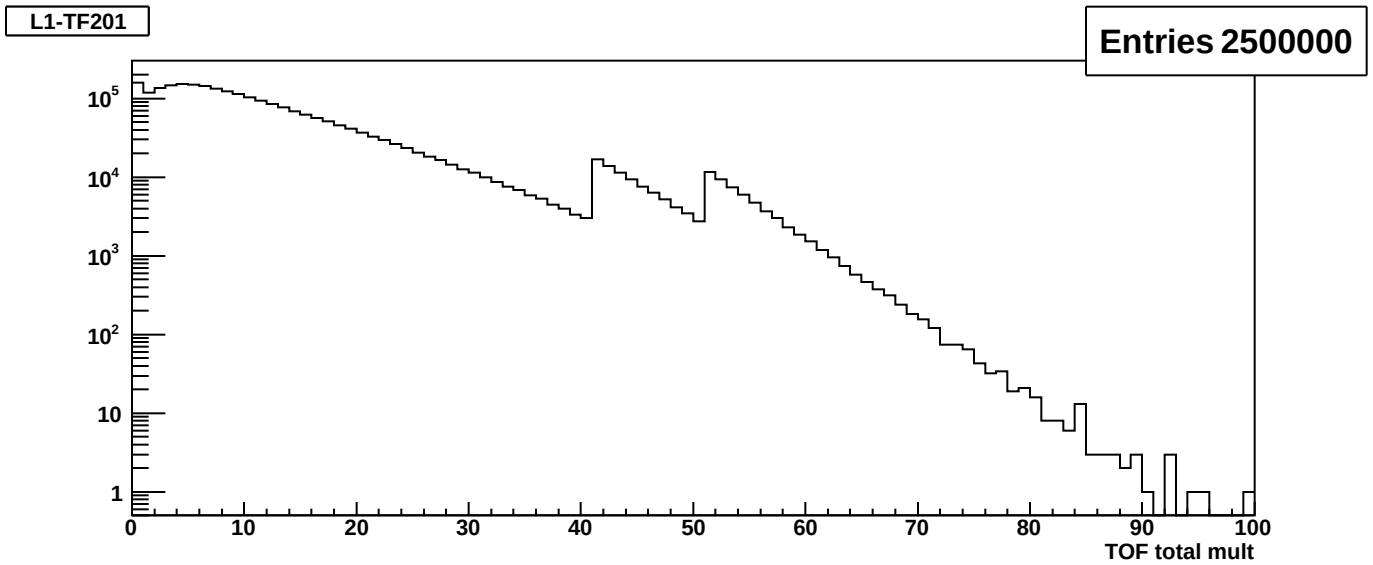

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

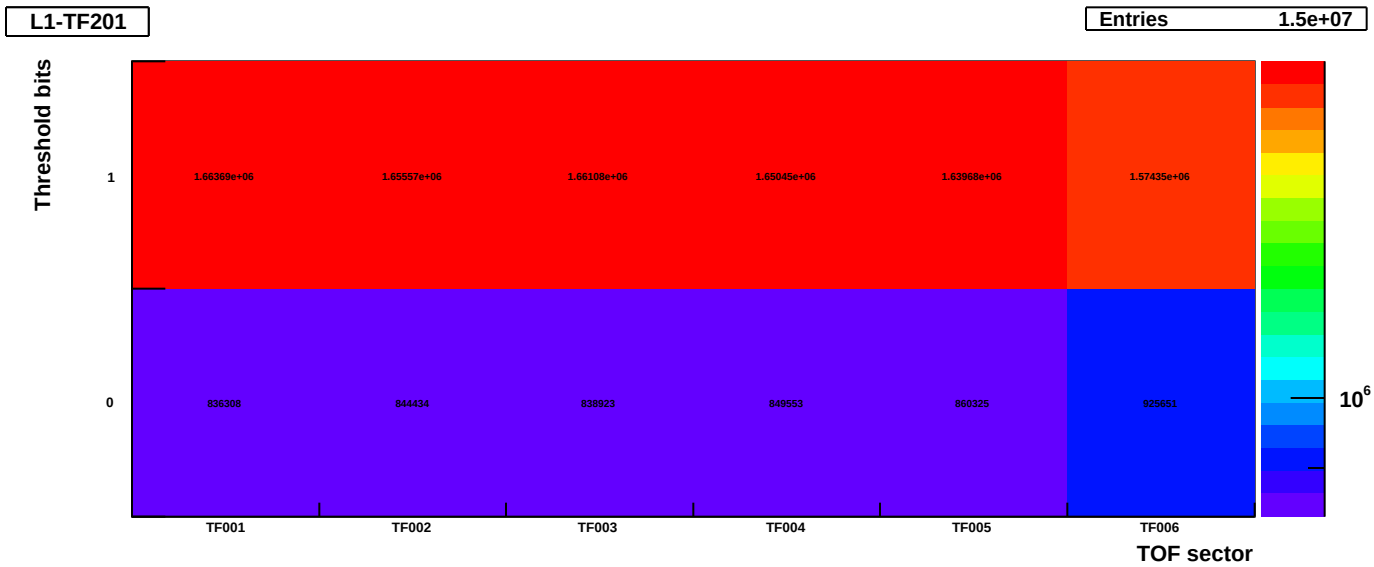

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

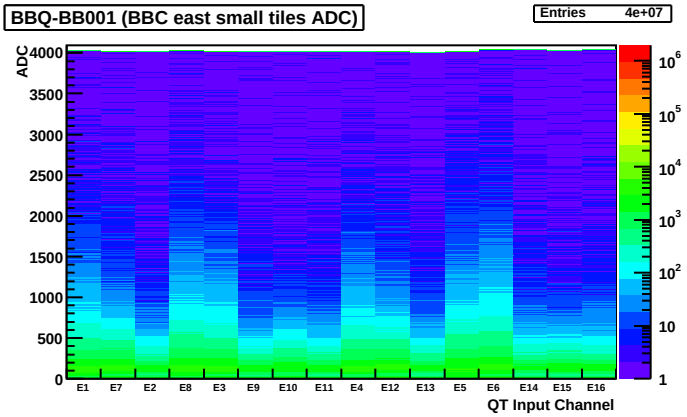

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

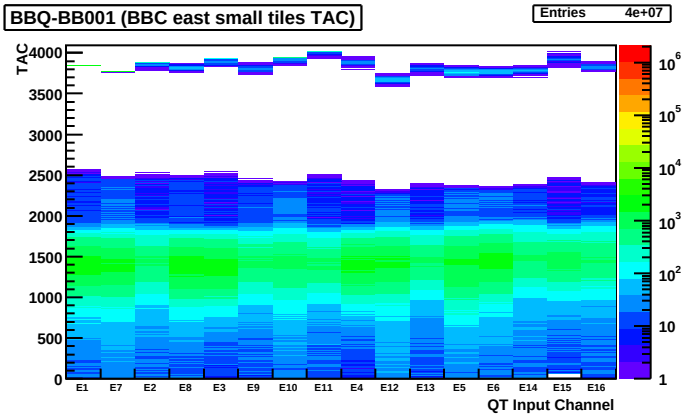

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

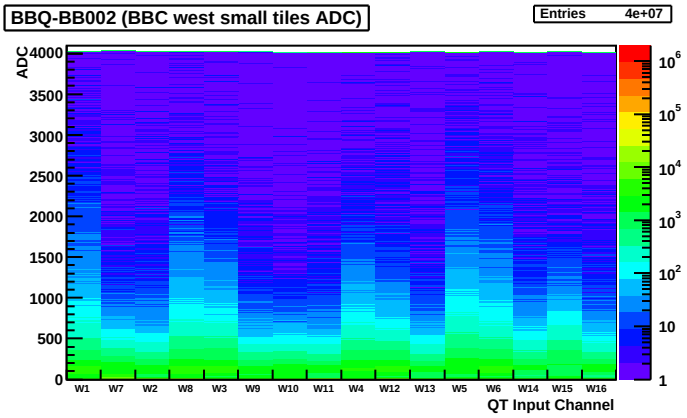

MIX-TF101
Entries 1.5e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

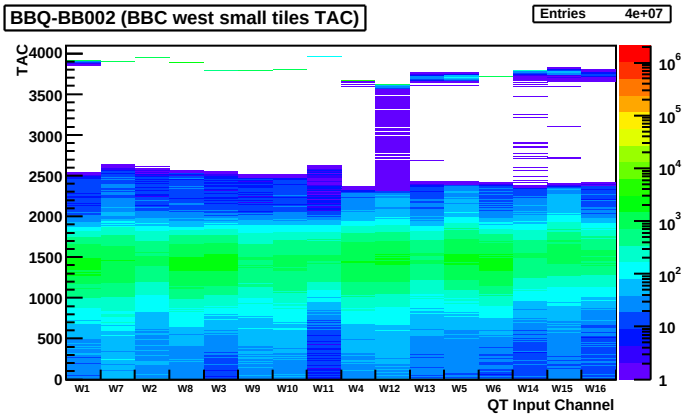

L1-TF201
Entries 2500000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

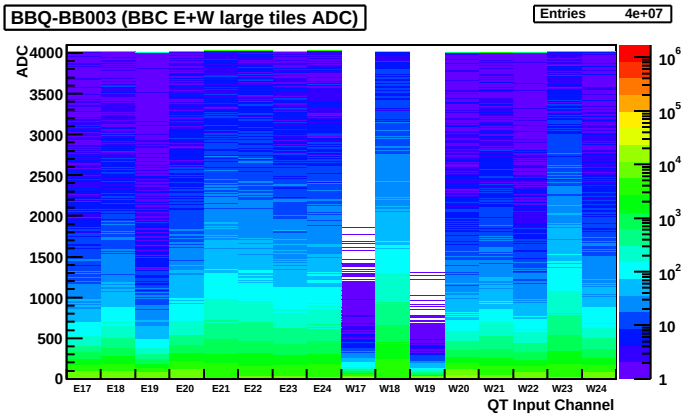

L1-TF201
Entries 1.5e+07
Threshold bits
1
0
1.66369e+06
1.65557e+06
1.66108e+06
1.65045e+06
1.63968e+06
1.57435e+06
836308
844434
838923
849553
860325
925651
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

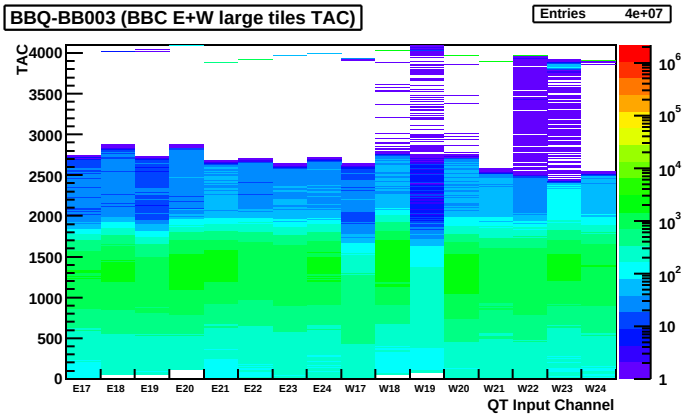

BBQ-BB001 (BBC east small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

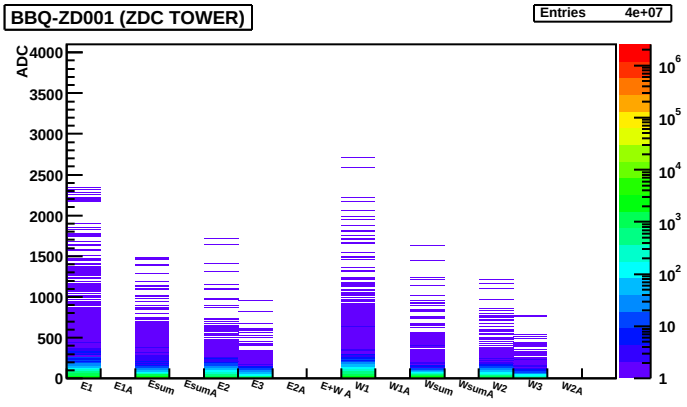

BBQ-BB001 (BBC east small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

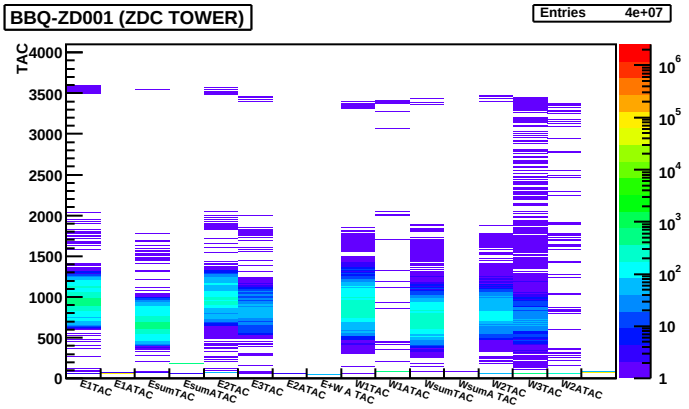

BBQ-BB002 (BBC west small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

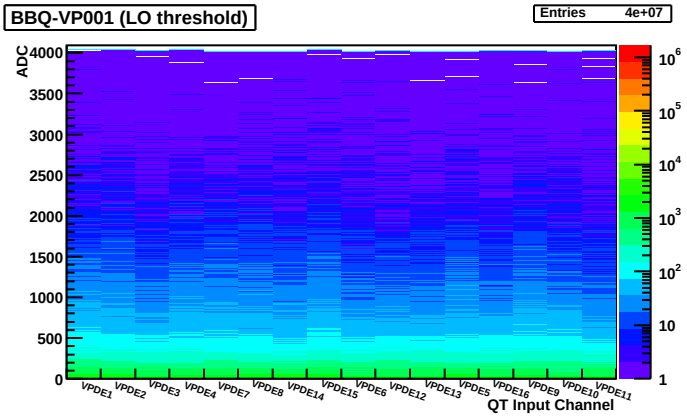

BBQ-BB002 (BBC west small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

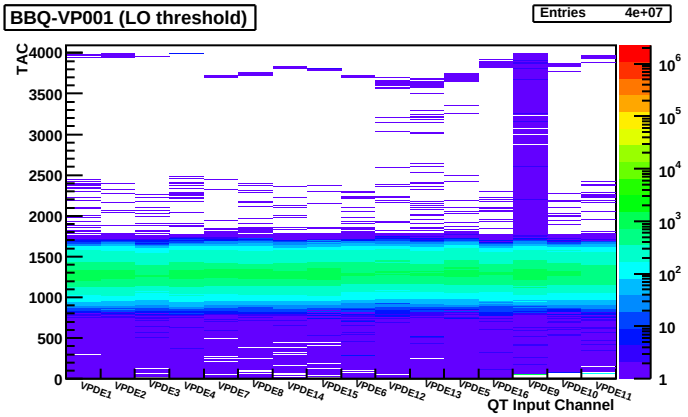

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4e+07
ADC
QT Input Channel

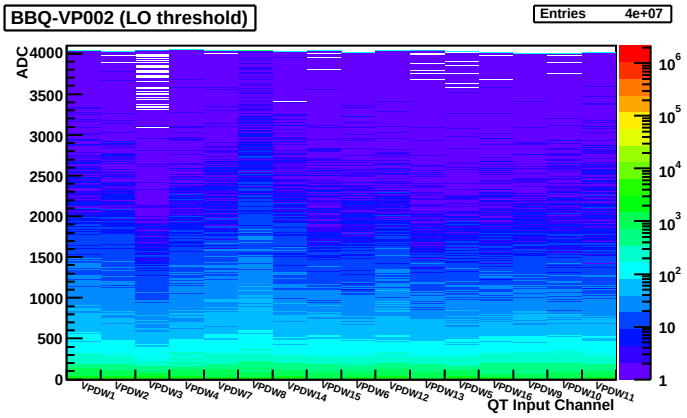

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4e+07
TAC
QT Input Channel

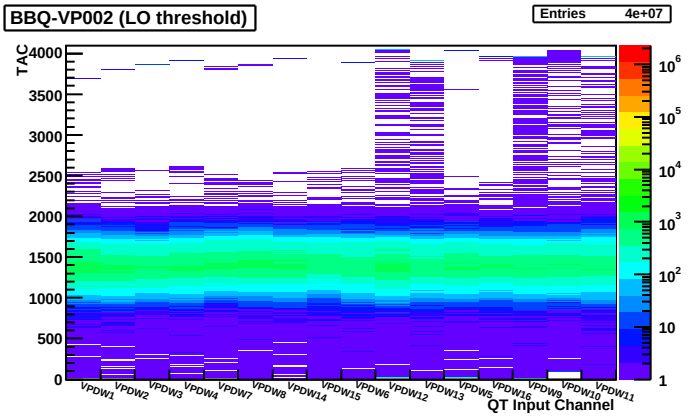

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
ADC

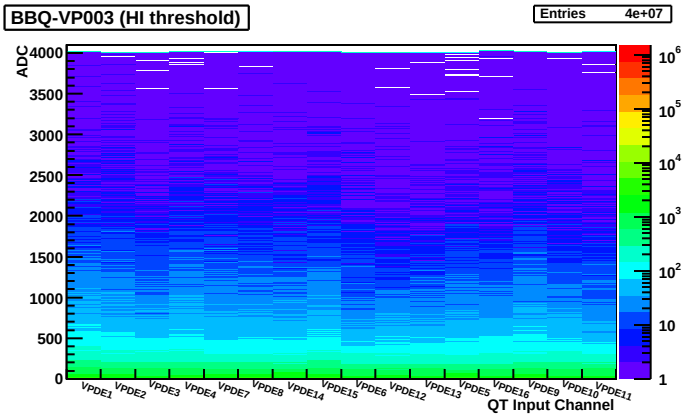

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
TAC

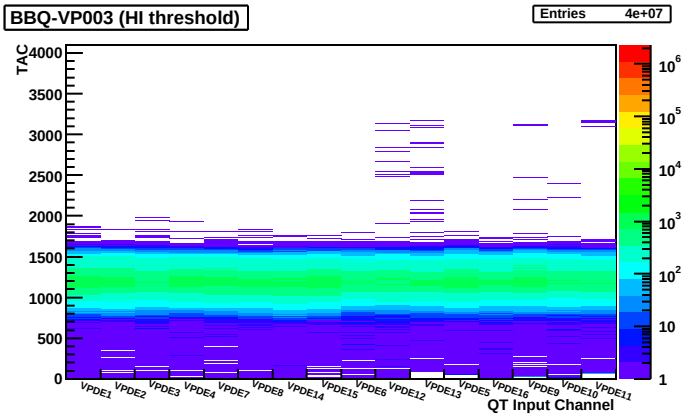

BBQ-VP001 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

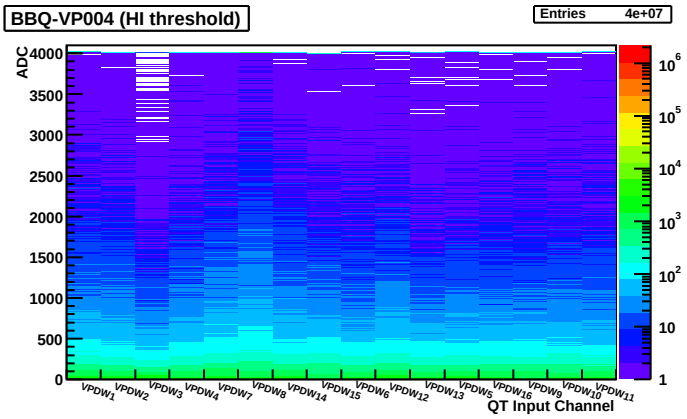

BBQ-VP001 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

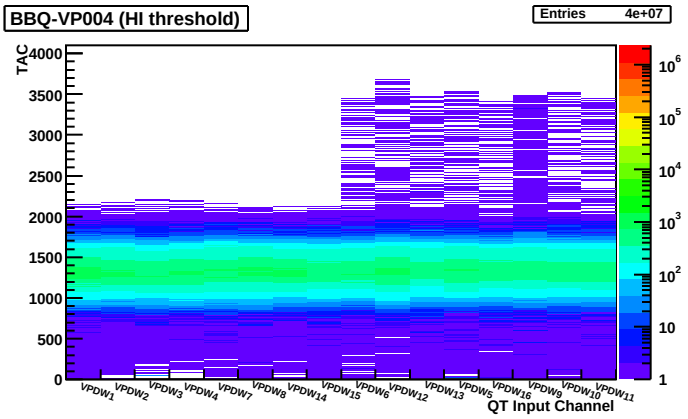

BBQ-VP002 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

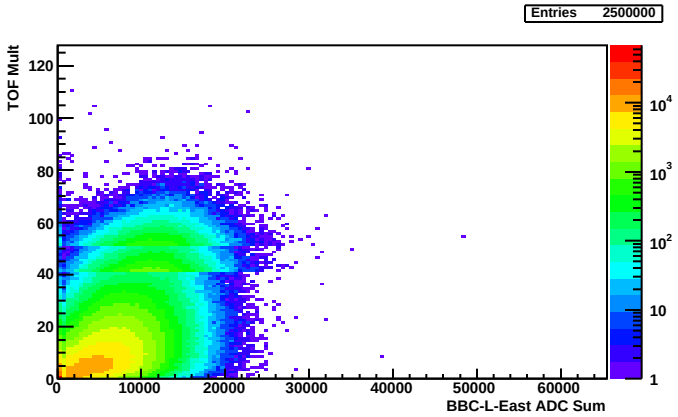

BBQ-VP002 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

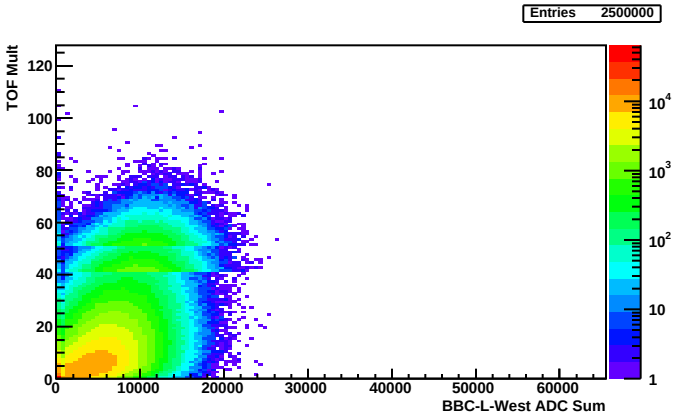

BBQ-VP003 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

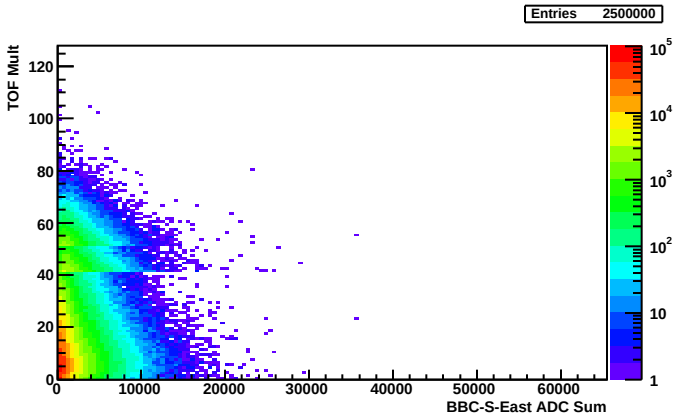

BBQ-VP003 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

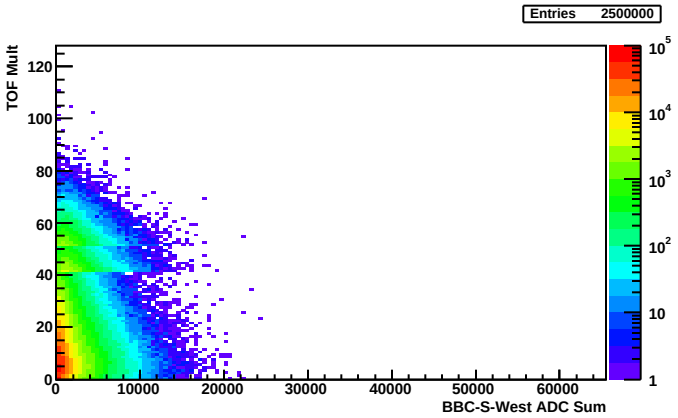

BBQ-VP004 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

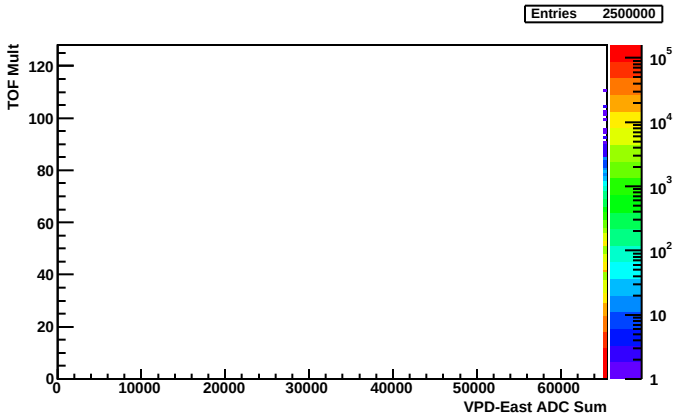

BBQ-VP004 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

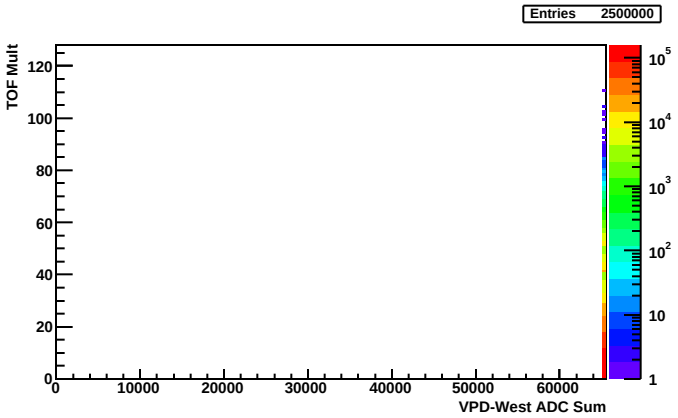

Entries 2500000
TOF Mult
BBC-L-East ADC Sum

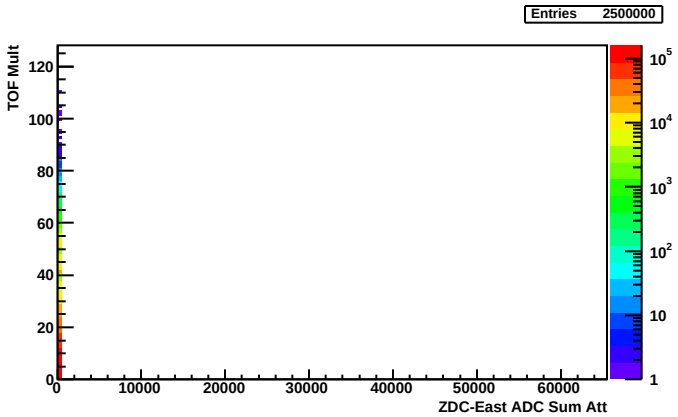

Entries 2500000
TOF Mult
BBC-L-West ADC Sum

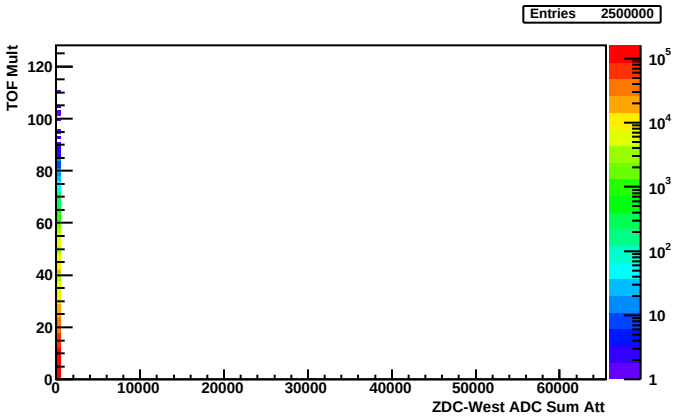

Entries 2500000
TOF Mult
BBC-S-East ADC Sum

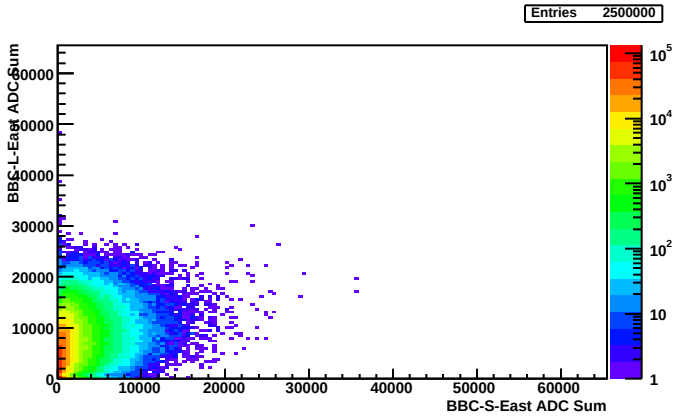

Entries 2500000
TOF Mult
BBC-S-West ADC Sum

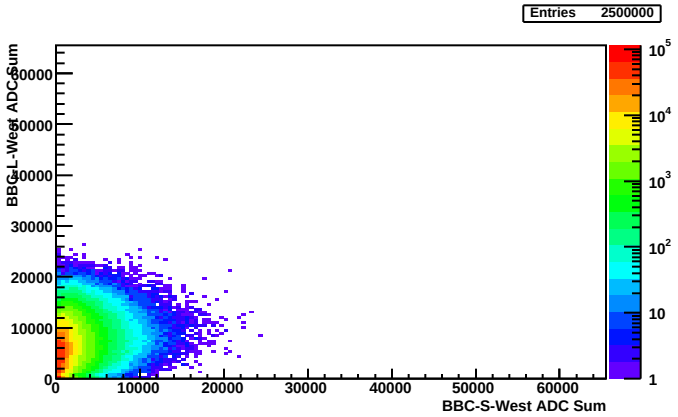

Entries 2500000
TOF Mult
VPD-East ADC Sum

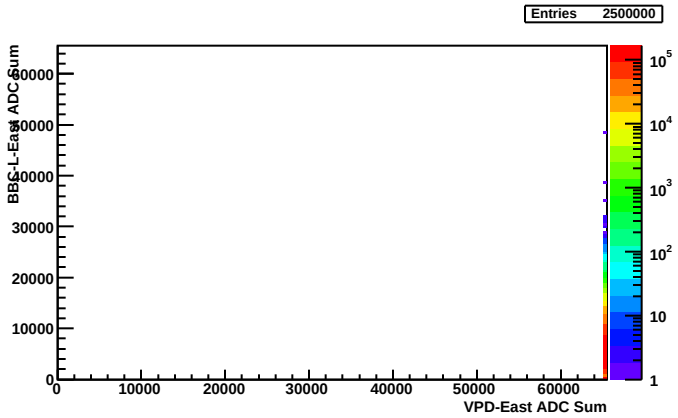

Entries 2500000
TOF Mult
VPD-West ADC Sum

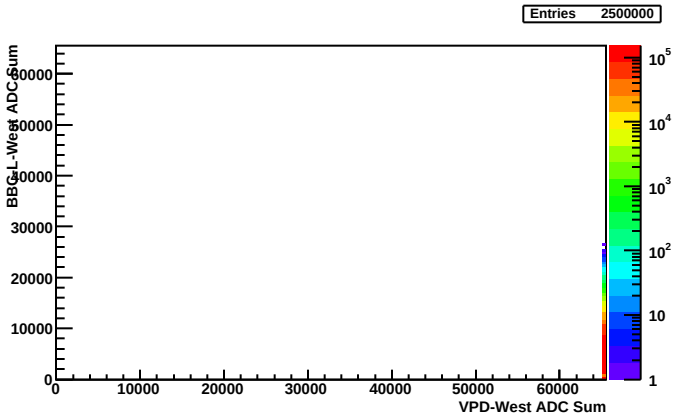

Entries 2500000
TOF Mult
ZDC-East ADC Sum Att

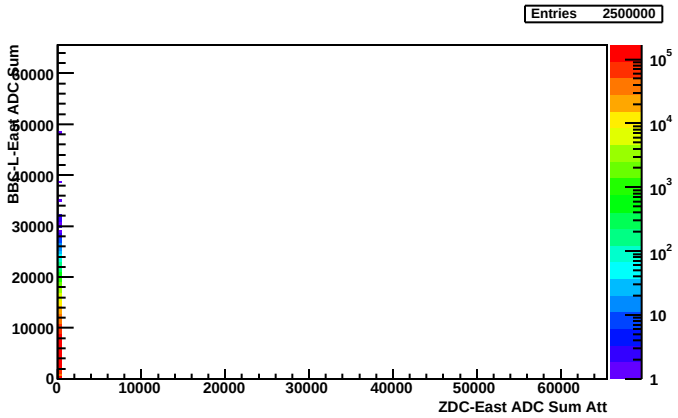

Entries 2500000
TOF Mult
ZDC-West ADC Sum Att

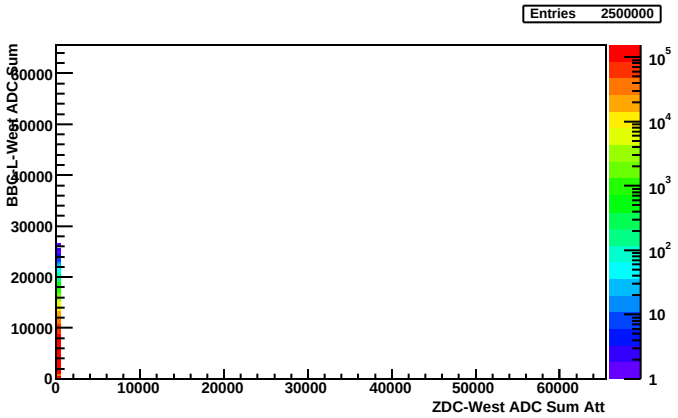

Entries 2500000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

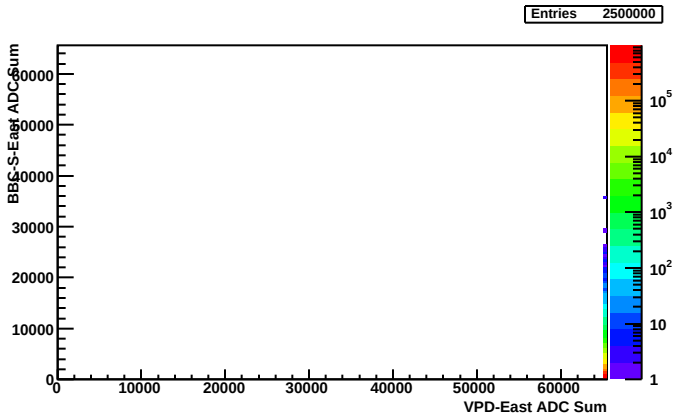

Entries 2500000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

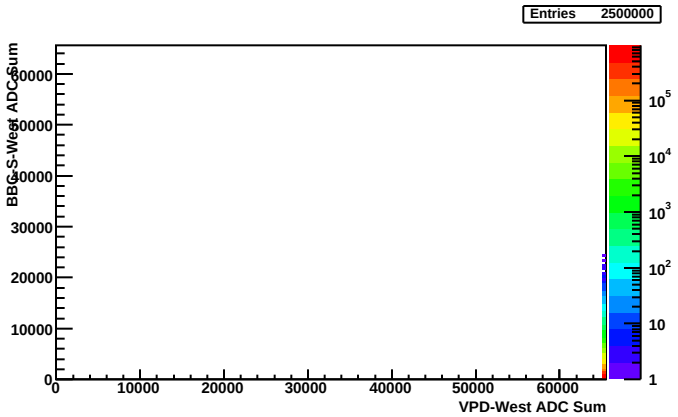

Entries 2500000
BBC-L-East ADC Sum
VPD-East ADC Sum

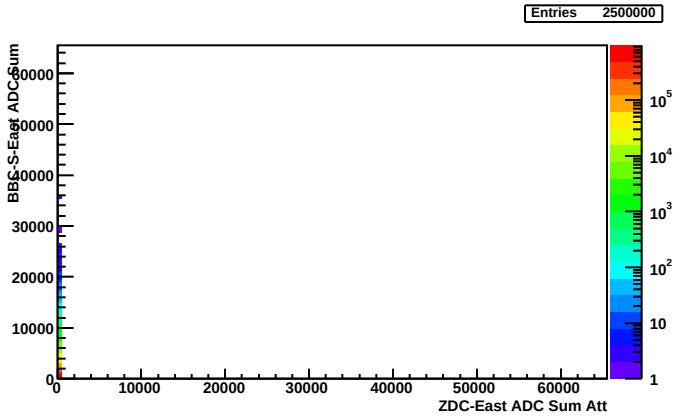

Entries 2500000
BBC-L-West ADC Sum
VPD-West ADC Sum

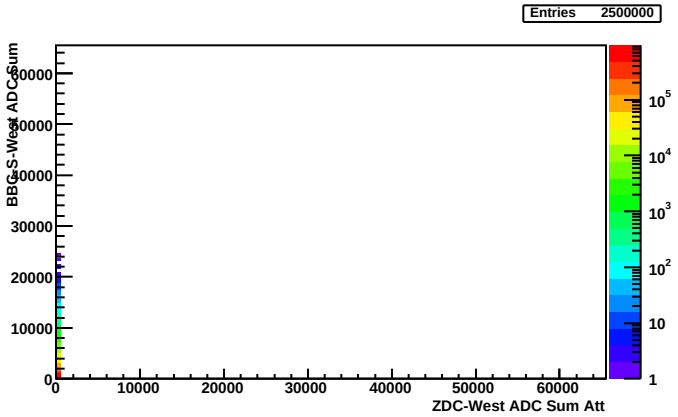

Entries 2500000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

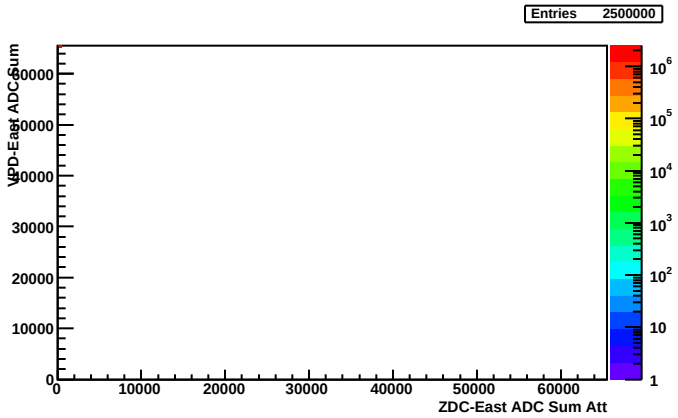

Entries 2500000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

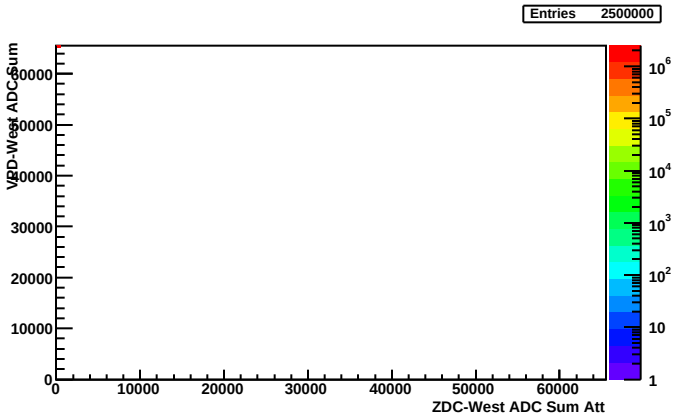

Entries 2500000
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2500000
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2500000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2500000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2500000
VPD-East ADC Sum
ZDC-East ADC Sum Att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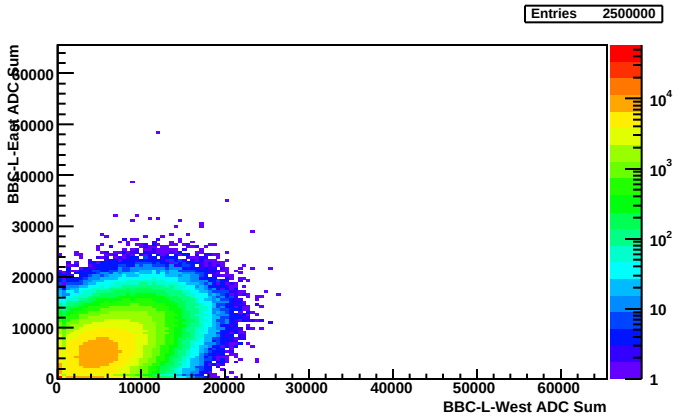
VPD-West ADC Sum
ZDC-West ADC Sum Att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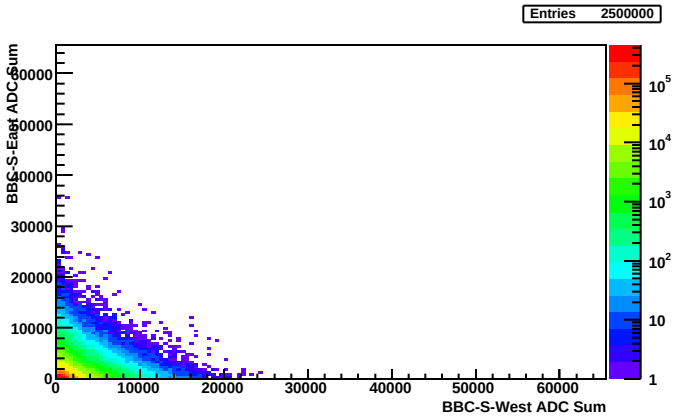
BBC-L-East ADC Sum
BBC-L-West ADC Sum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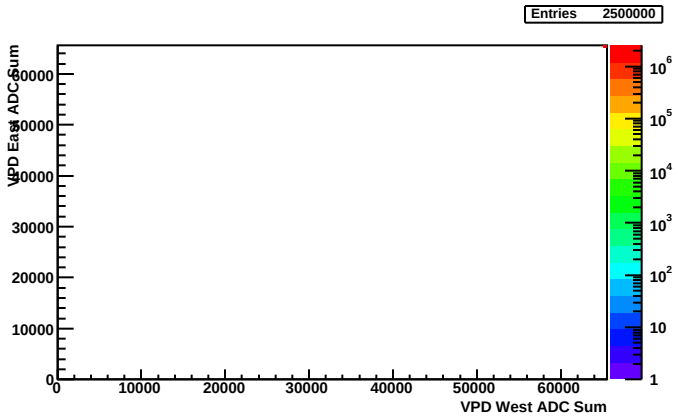
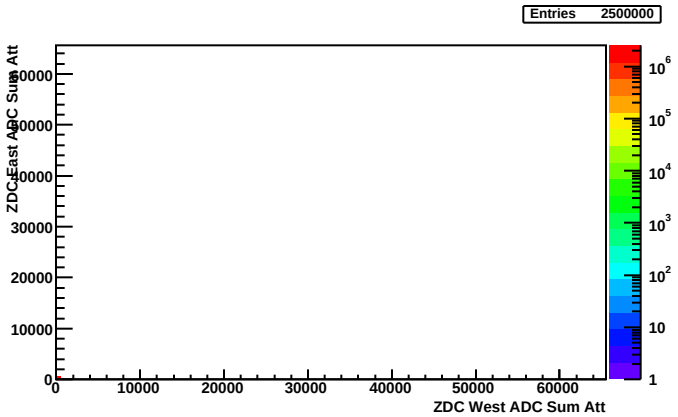
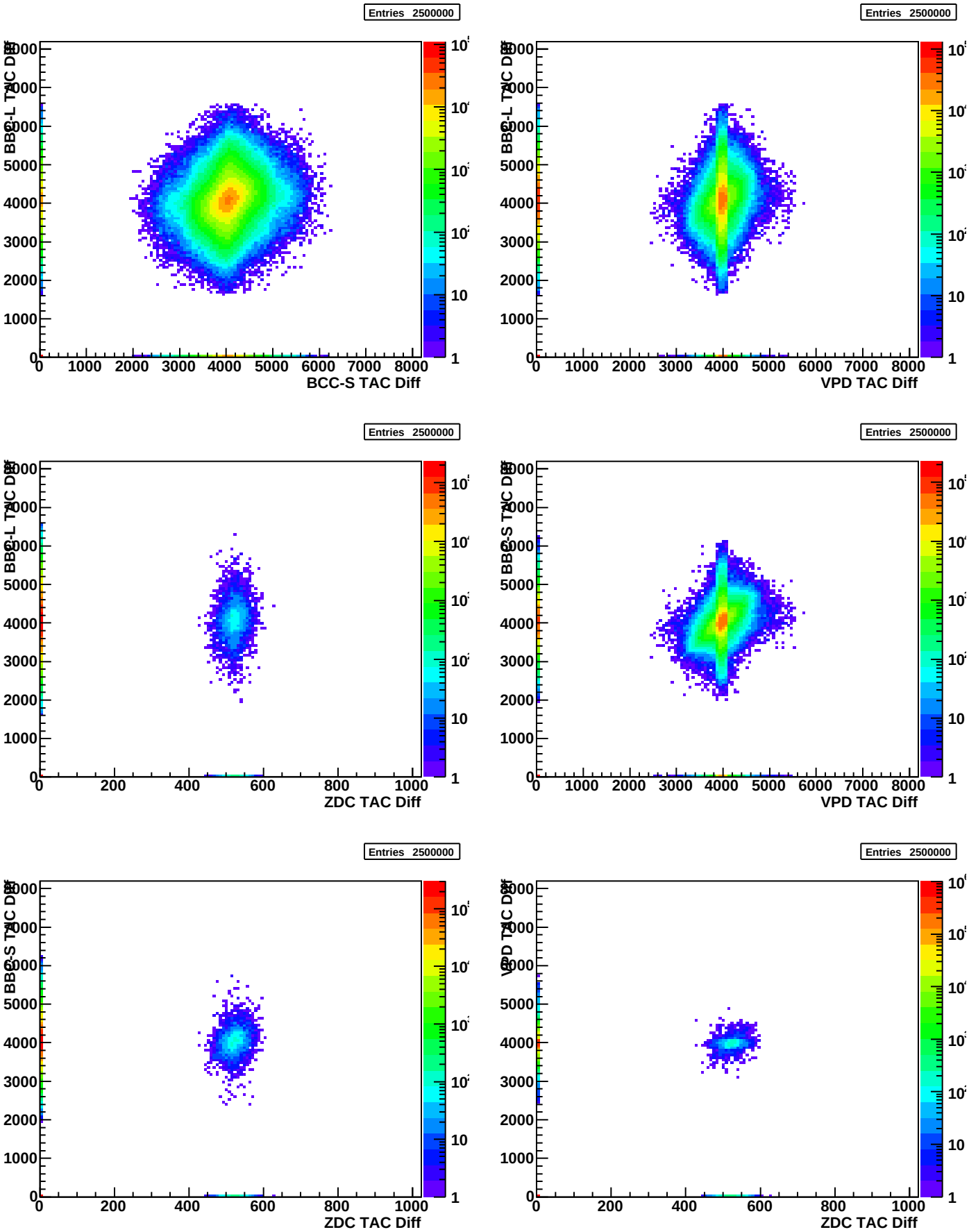
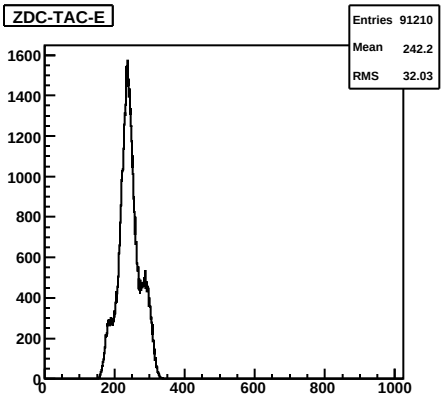
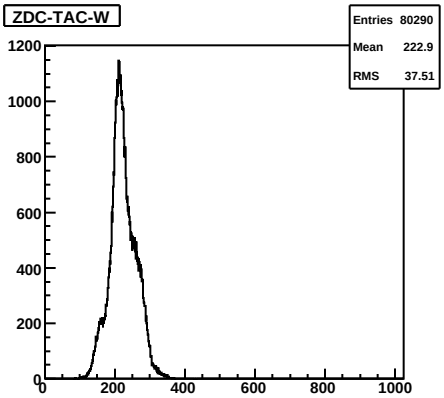
BBC-S-East ADC Sum
BBC-S-West ADC Sum

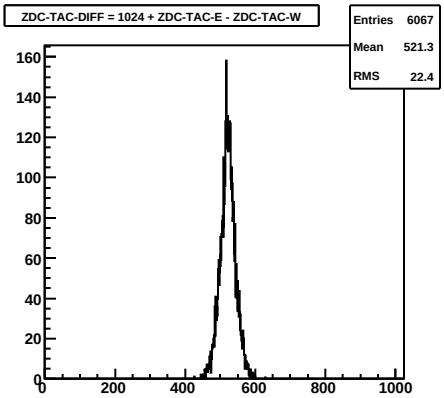

Entries 2500000
VPD East ADC Sum
VPD West ADC Sum

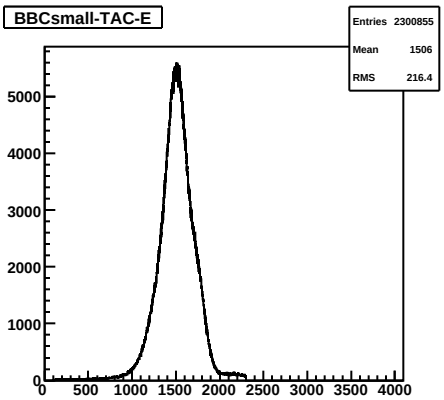

Entries 2500000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

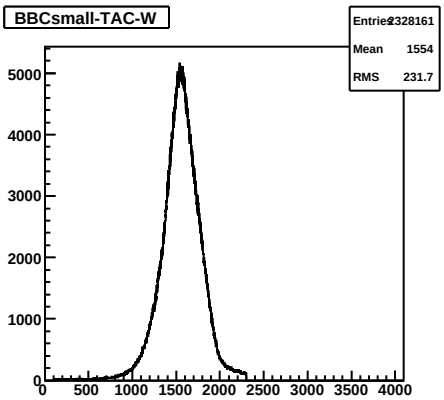

Entries 2500000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2500000
BBC-L TAC Diff
VPD TAC Diff
Entries 2500000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2500000
BBC-S TAC Diff
VPD TAC Diff
Entries 2500000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2500000
VPD TAC Diff
ZDC TAC Diff

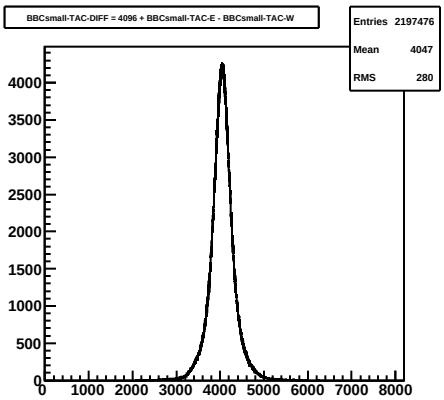

ZDC-TAC-E
Entries 91210
Mean 242.2
RMS 32.03

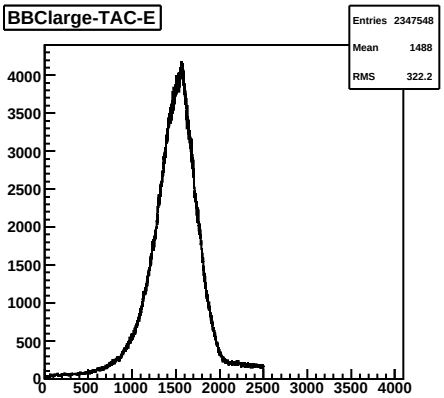

ZDC-TAC-W
Entries 80290
Mean 222.9
RMS 37.51

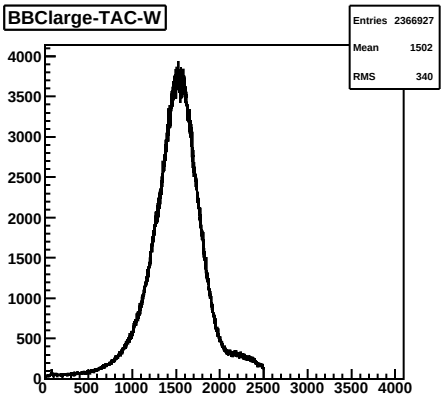

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 6067
Mean 521.3
RMS 22.4

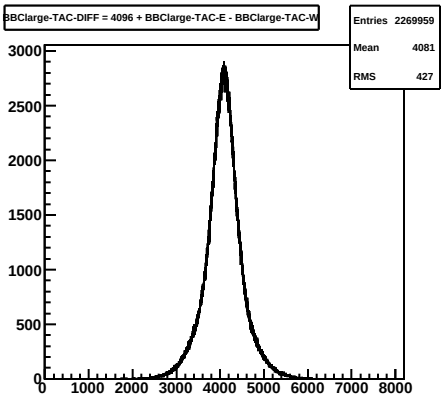

BBCsmall-TAC-E
Entries 2300855
Mean 1506
RMS 216.4

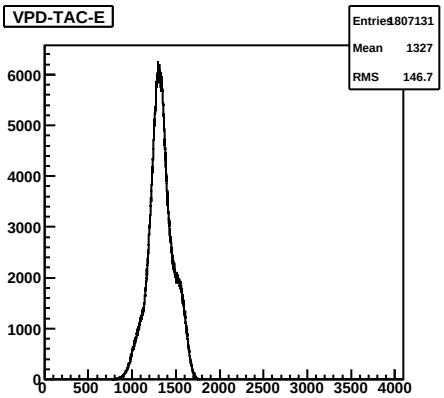

BBCsmall-TAC-W
Entries 2328161
Mean 1554
RMS 231.7

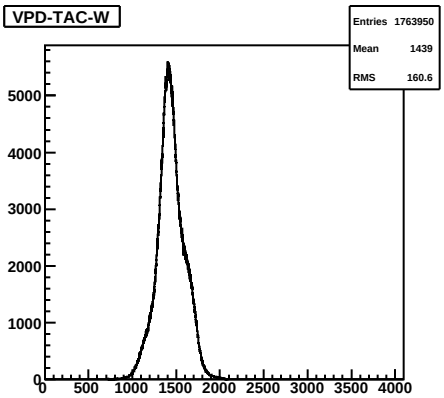

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2197476
Mean 4047
RMS 280

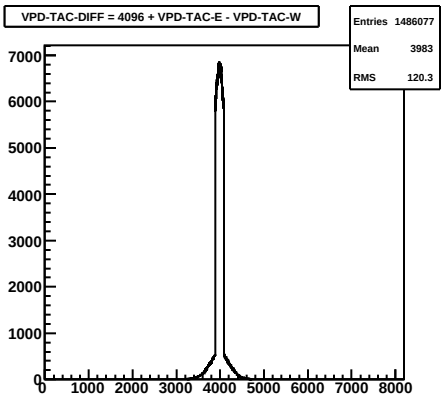

BBClarge-TAC-E
Entries 2347548
Mean 1488
RMS 322.2

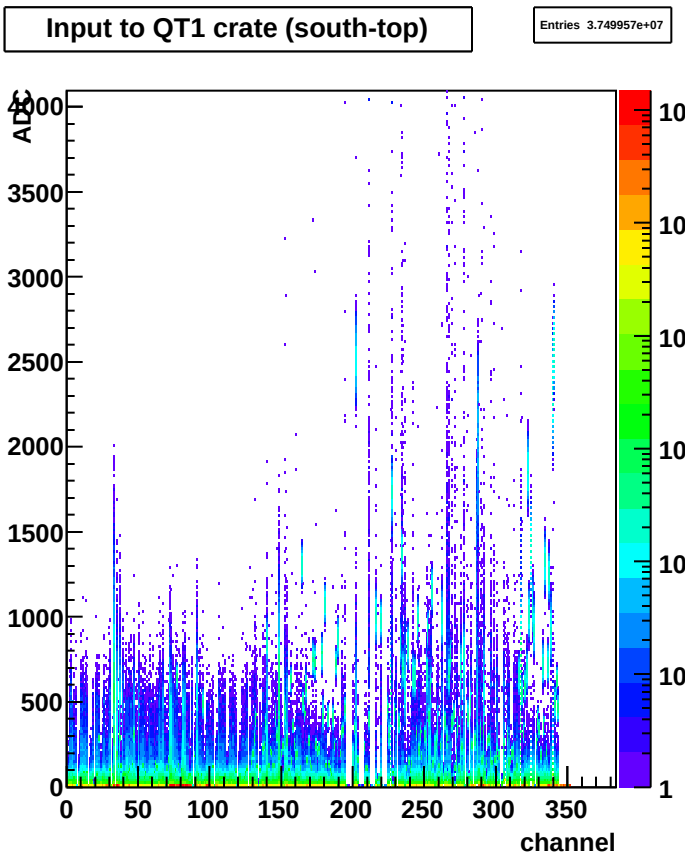

BBClarge-TAC-W
Entries 2366927
Mean 1502
RMS 340

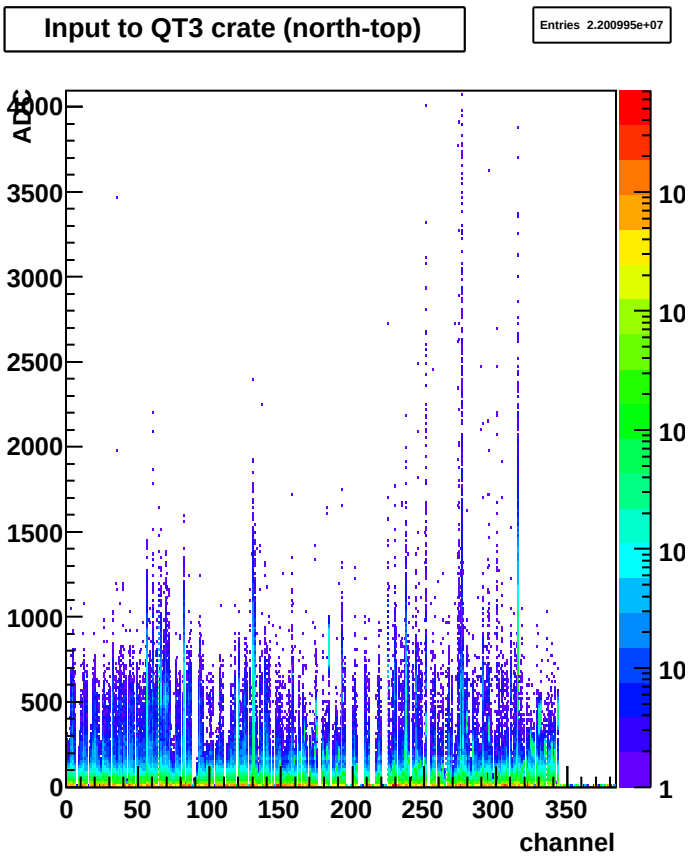

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2269959
Mean 4081
RMS 427

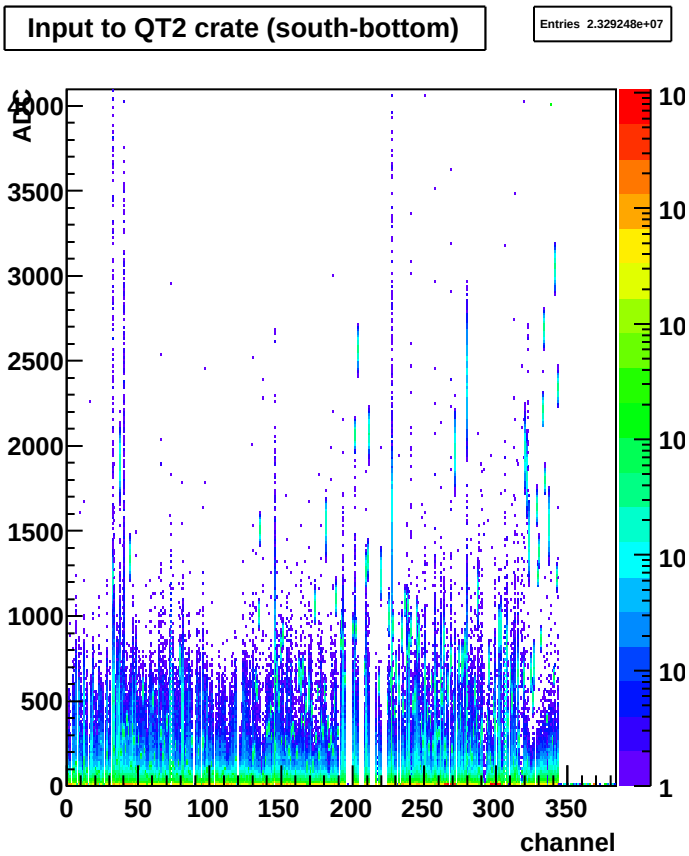

VPD-TAC-E
Entries 1807131
Mean 1327
RMS 146.7

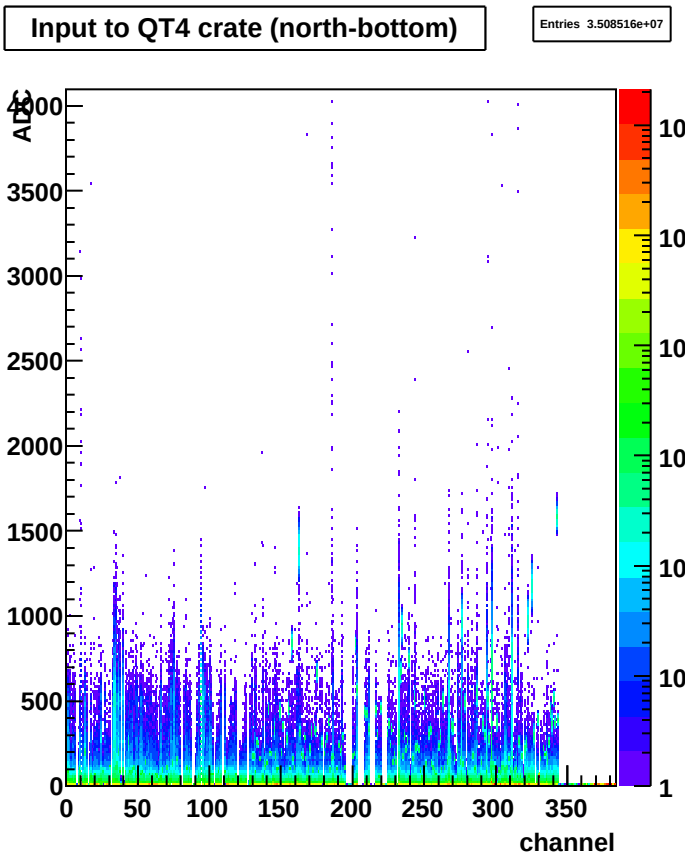

VPD-TAC-W
Entries 1763950
Mean 1439
RMS 160.6

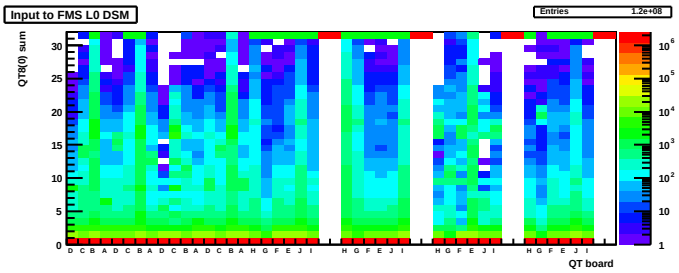

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1486077
Mean 3983
RMS 120.3

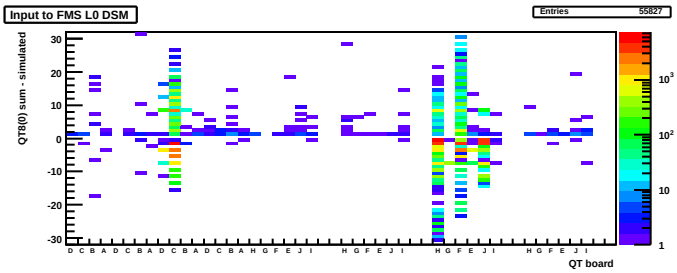

Input to QT1 crate (south-top)
Entries 3.749957e+07
ADC
channel

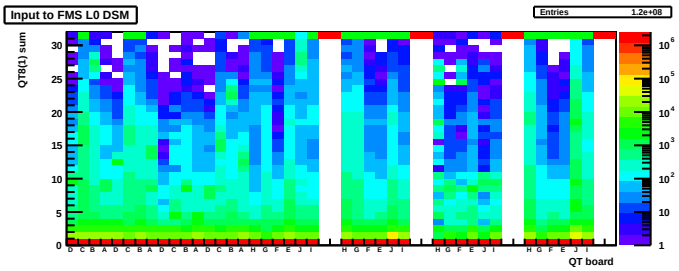

Input to QT3 crate (north-top)
Entries 2.200995e+07
ADC
channel

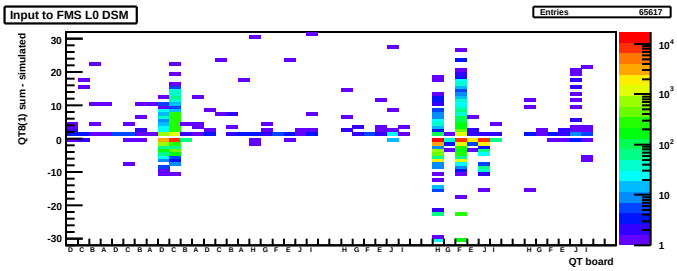

Input to QT2 crate (south-bottom)
Entries 2.329248e+07
ADC
channel

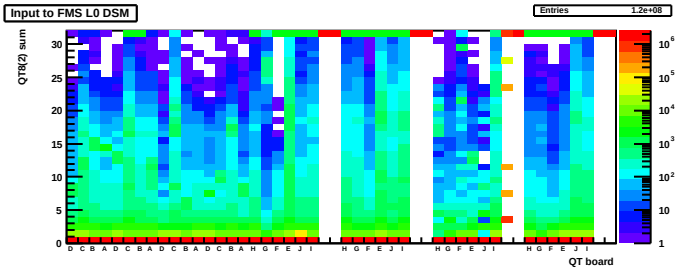

Input to QT4 crate (north-bottom)
Entries 3.508516e+07
ADC
channel

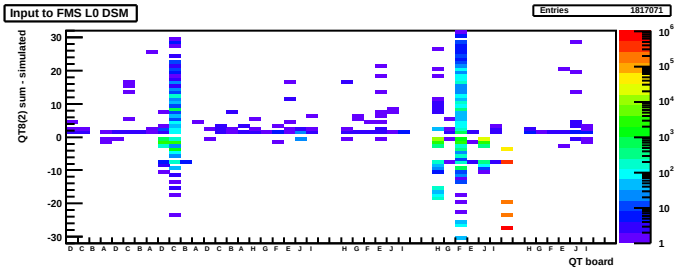

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

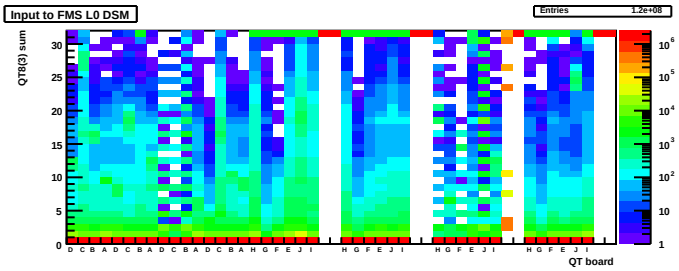

Input to FMS L0 DSM
Entries 55827
QT8(0) sum - simulated
QT board

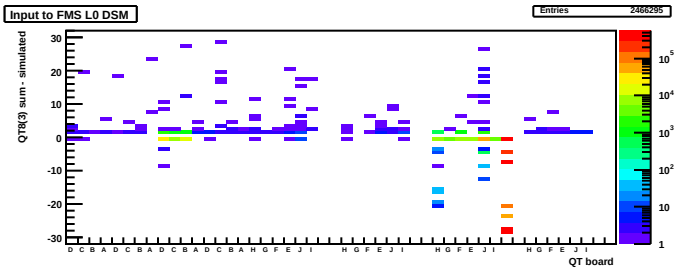

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

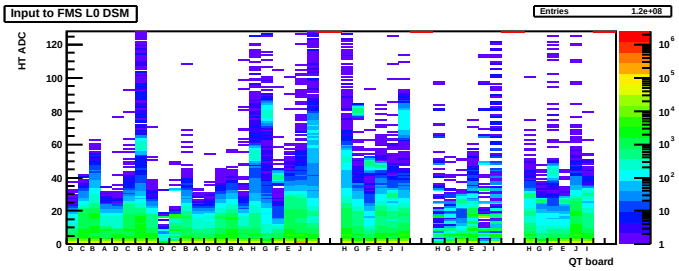

Input to FMS L0 DSM
Entries 65617
QT8(1) sum - simulated
QT board

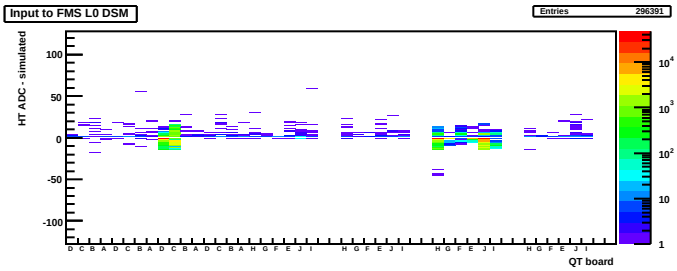

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

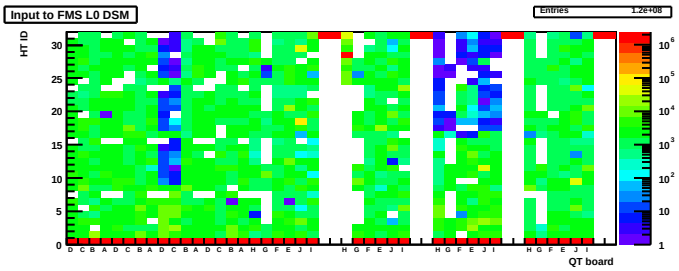

Input to FMS L0 DSM
Entries 1817071
QT8(2) sum - simulated
QT board

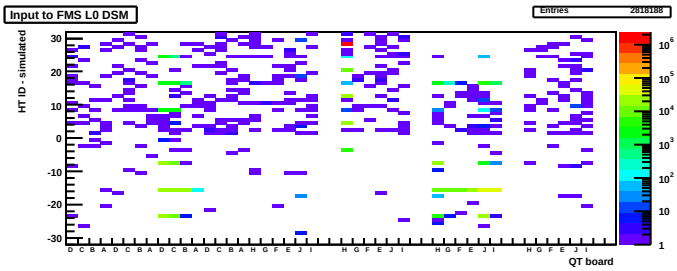

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

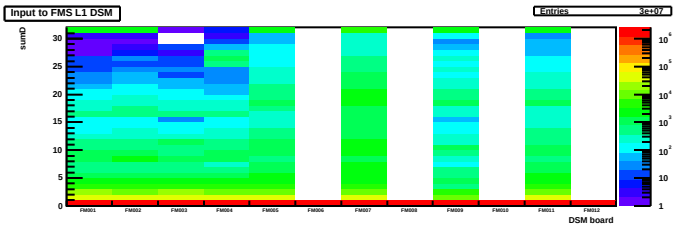

Input to FMS L0 DSM
Entries 2466295
QT8(3) sum - simulated
QT board

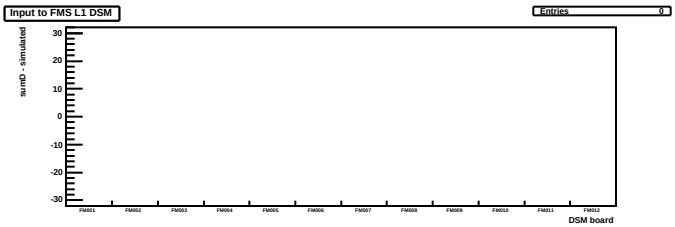

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

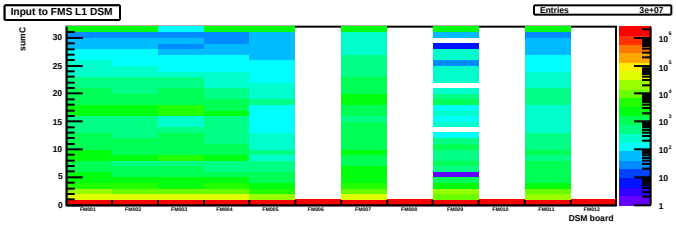

Input to FMS L0 DSM
Entries 296391
HT ADC - simulated
QT board

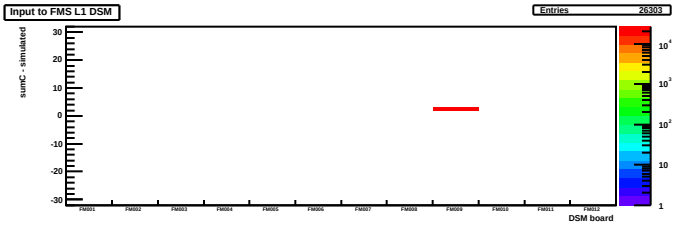

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

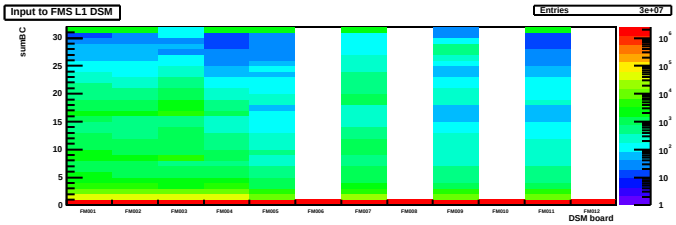

Input to FMS L0 DSM
Entries 2818188
HT ID - simulated
QT board

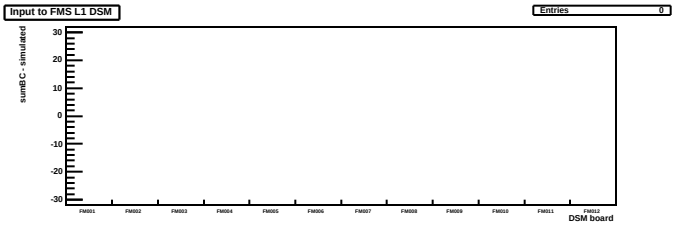

Input to FMS L1 DSM
Entries 3e+07
sumD
DSM board

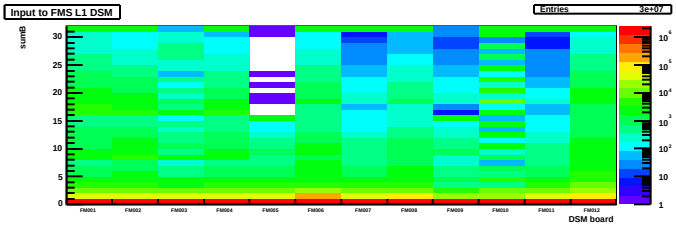

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

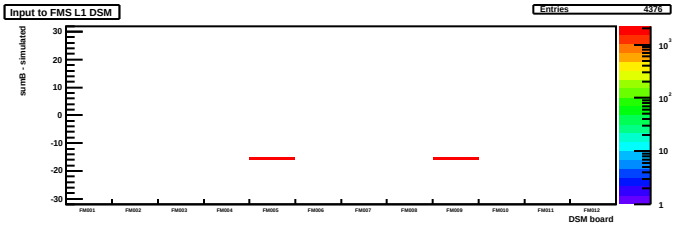

Input to FMS L1 DSM
Entries 3e+07
sumC
DSM board

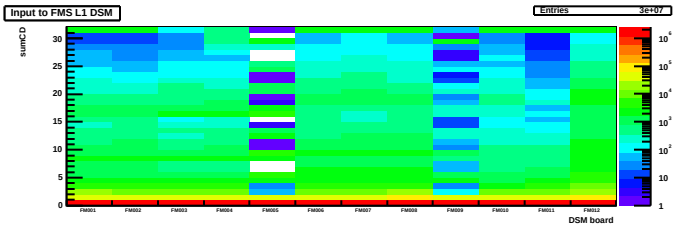

Input to FMS L1 DSM
Entries 26303
sumC - simulated
DSM board

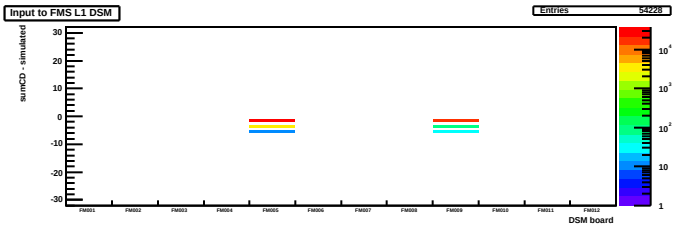

Input to FMS L1 DSM
Entries 3e+07
sumBC
DSM board

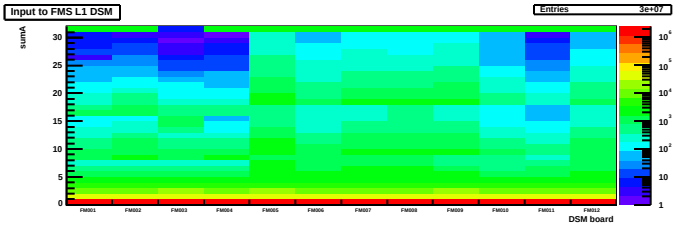

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

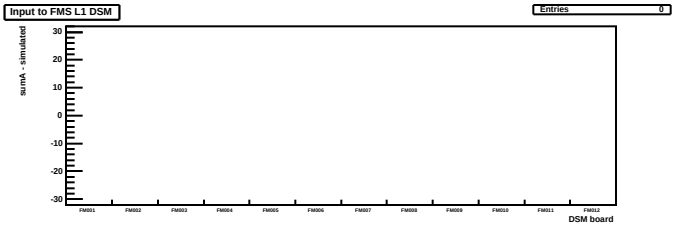

Input to FMS L1 DSM
Entries 3e+07
sumB
DSM board

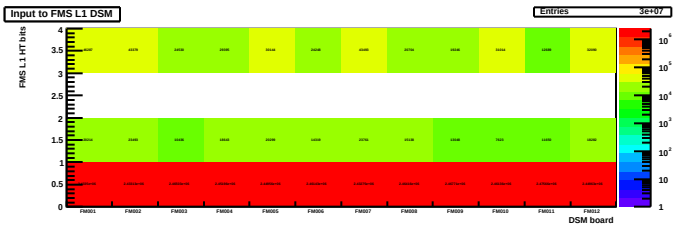

Input to FMS L1 DSM
Entries 4376
sumB - simulated
DSM board

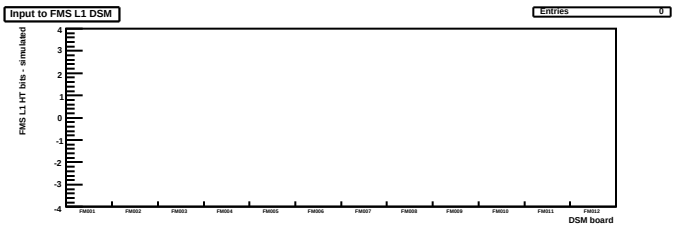

Input to FMS L1 DSM
Entries 3e+07
sumCD
DSM board

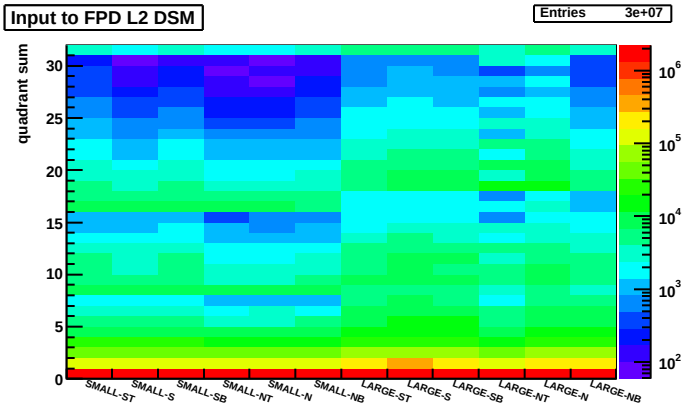

Input to FMS L1 DSM
Entries 54228
sumCD - simulated
DSM board

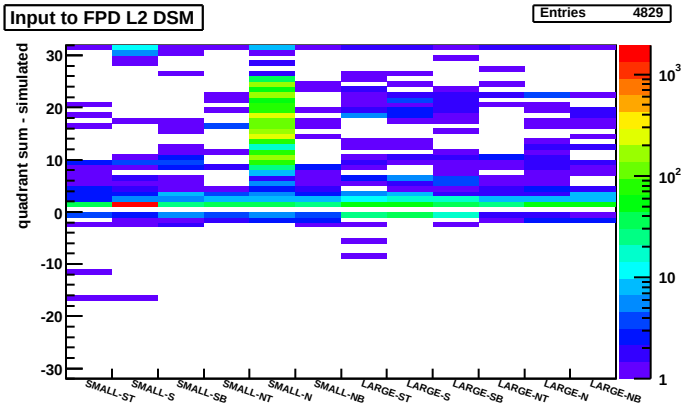

Input to FMS L1 DSM
Entries 3e+07
sumA
DSM board

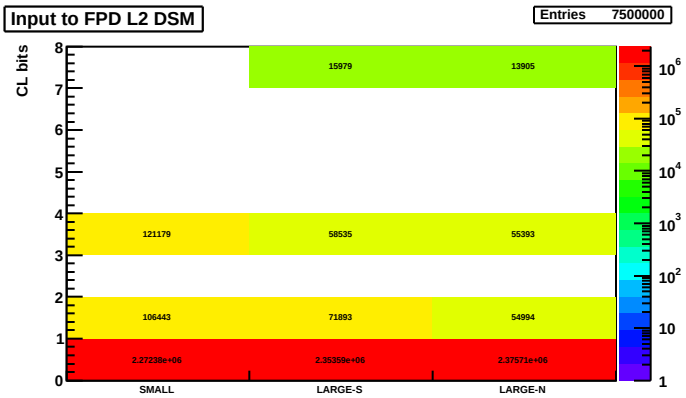

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

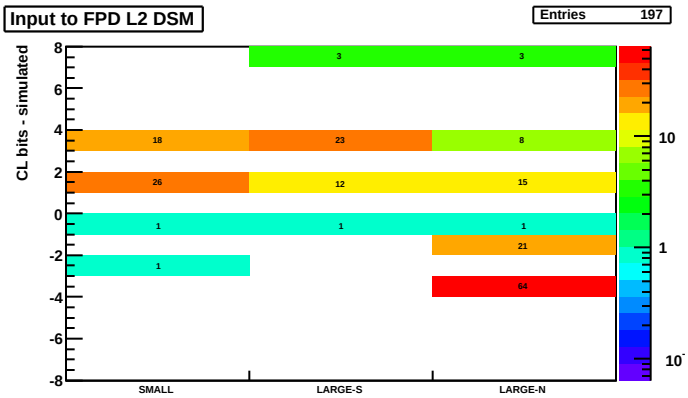

Input to FMS L1 DSM
Entries 3e+07
FMS L1 HT bits
DSM board

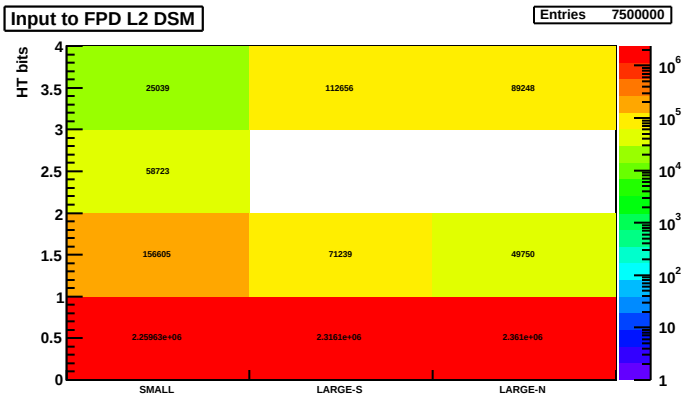

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

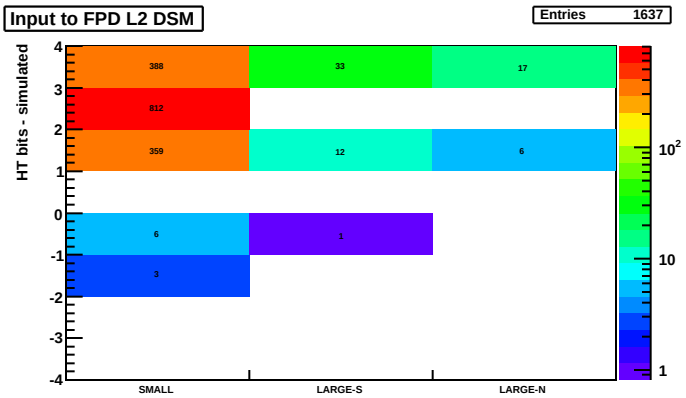

Input to FPD L2 DSM
Entries 3e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

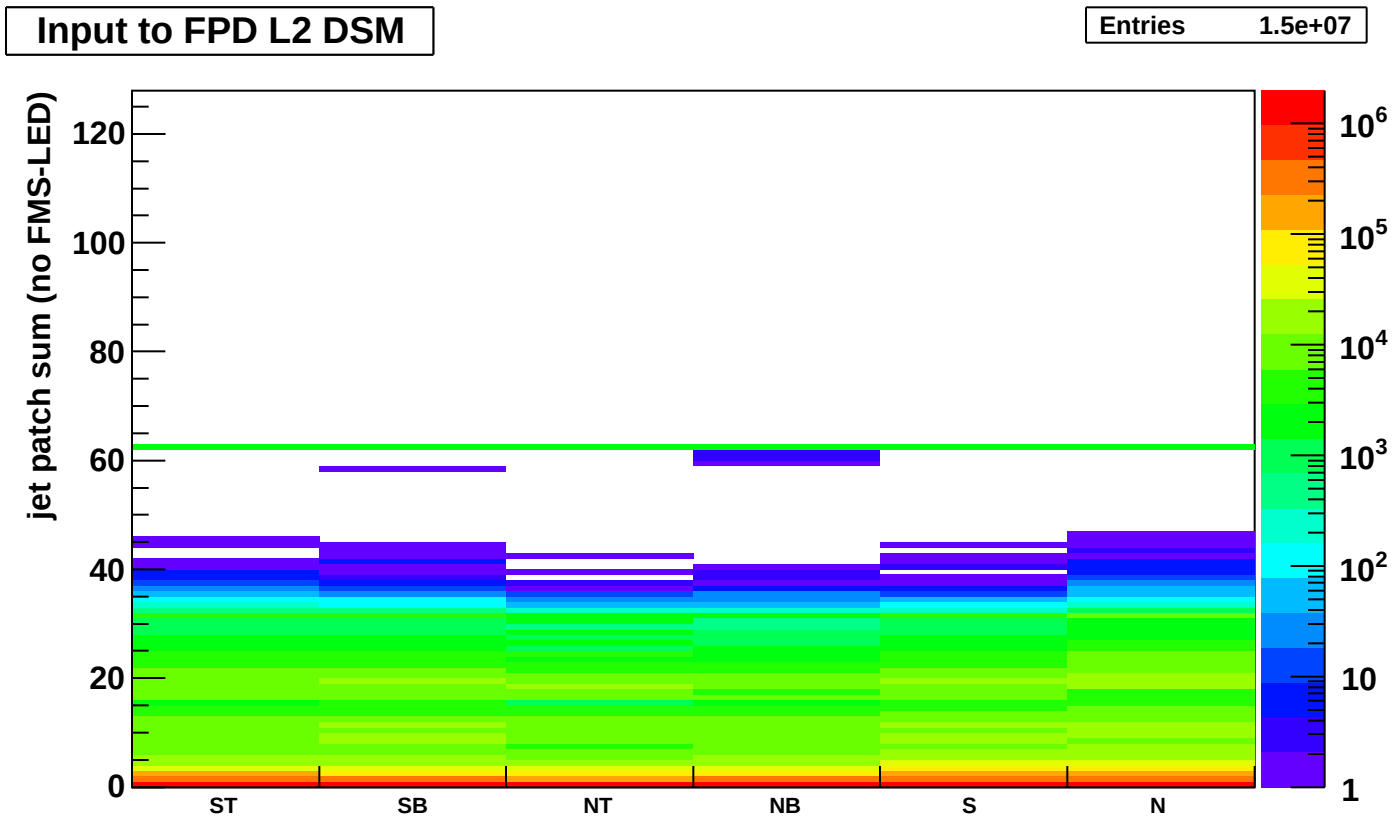

Input to FPD L2 DSM
Entries 4829
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

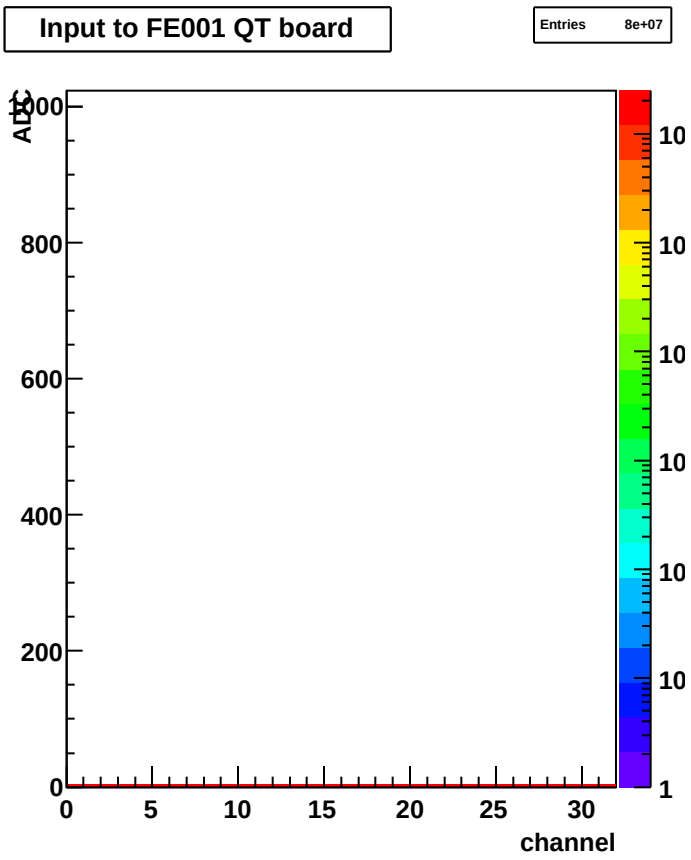

Input to FPD L2 DSM
Entries 7500000
CL bits
15979
13905
121179
58535
55393
106443
71893
54994
2.27238e+06
2.35359e+06
2.37571e+06
SMALL
LARGE-S
LARGE-N

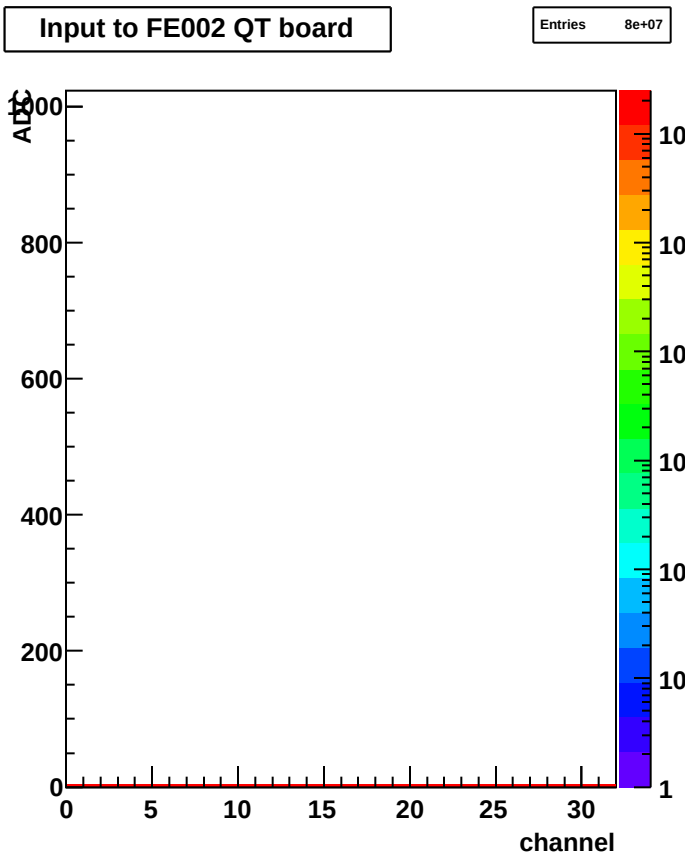

Input to FPD L2 DSM
Entries 197
CL bits - simulated
3
3
18
23
8
26
12
15
1
1
1
21
1
64
SMALL
LARGE-S
LARGE-N

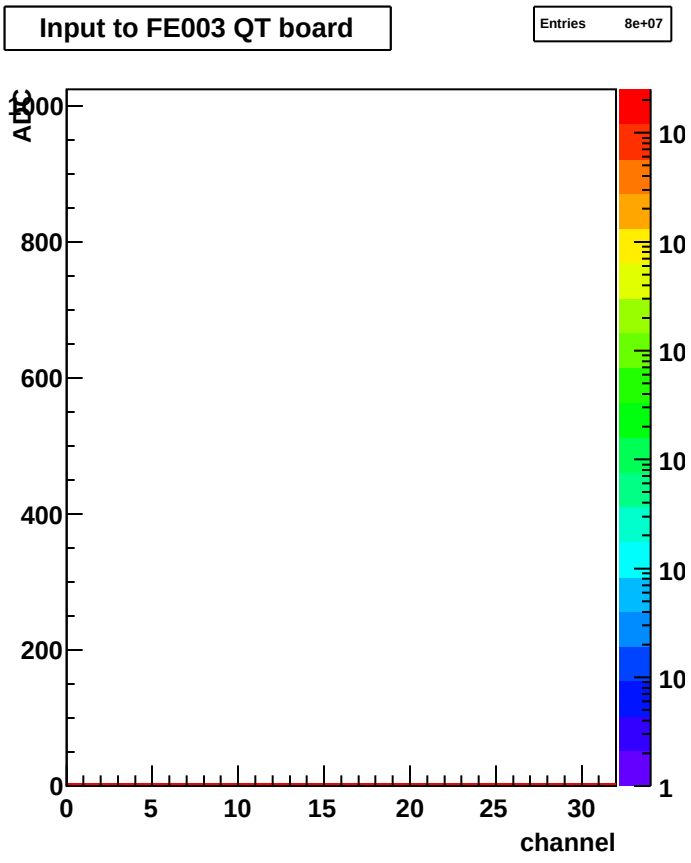

Input to FPD L2 DSM
Entries 7500000
HT bits
25039
112656
89248
58723
156605
71239
49750
2.25963e+06
2.3161e+06
2.361e+06
SMALL
LARGE-S
LARGE-N

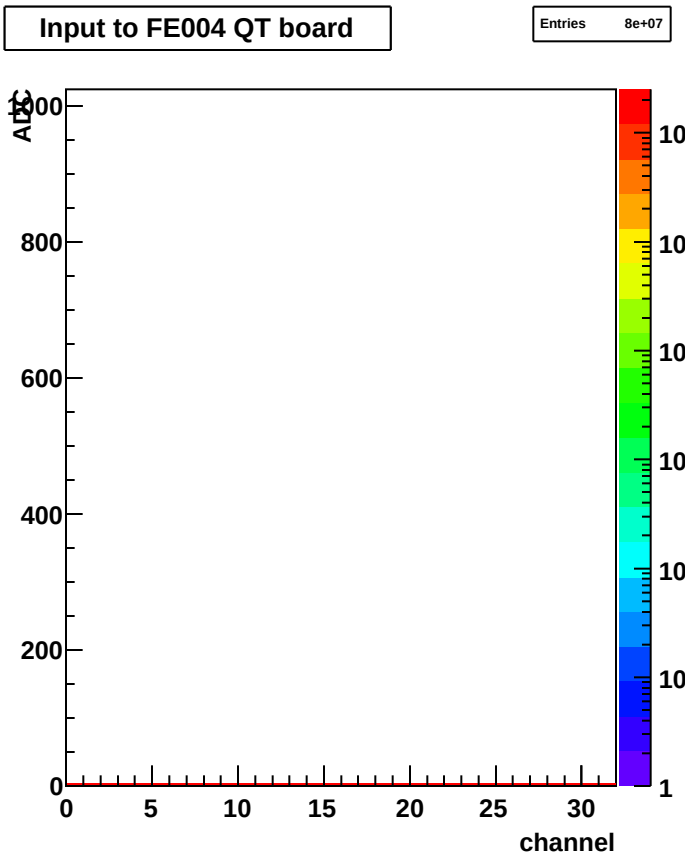

Input to FPD L2 DSM
Entries 1637
HT bits - simulated
388
33
17
812
359
12
6
6
1
3
SMALL
LARGE-S
LARGE-N

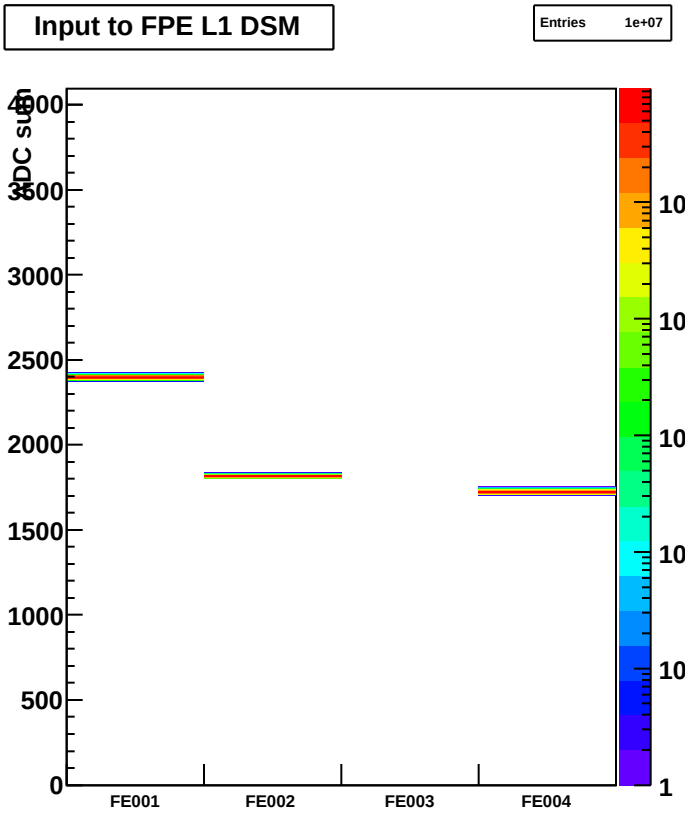

Input to FPD L2 DSM
Entries 1.5e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

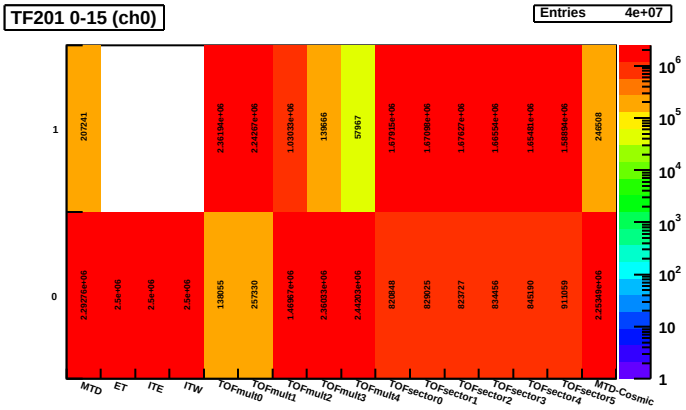

Input to FE001 QT board
Entries 8e+07
ADC
channel

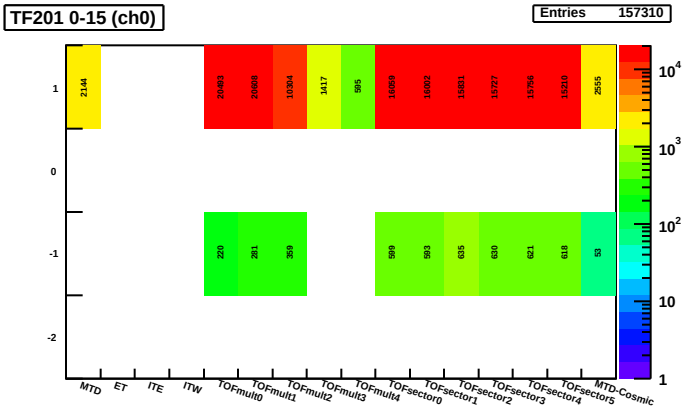

Input to FE002 QT board
Entries 8e+07
ADC
channel

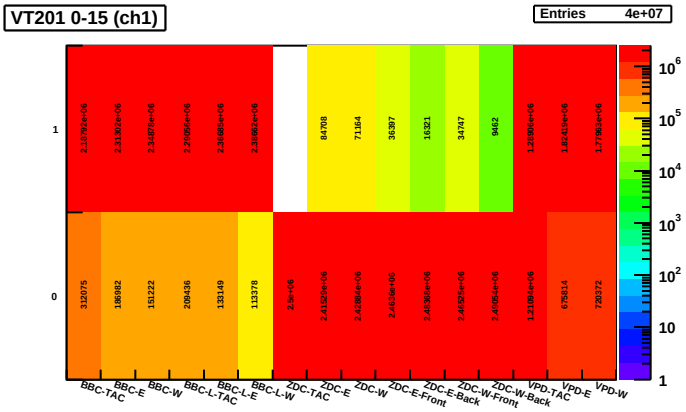

Input to FE003 QT board
Entries 8e+07
ADC
channel

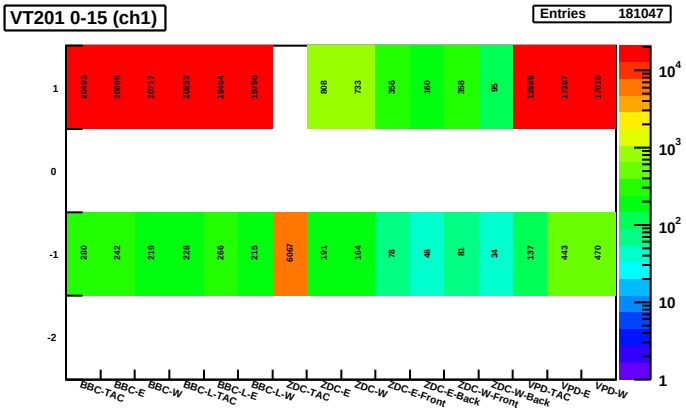

Input to FE004 QT board
Entries 8e+07
ADC
channel

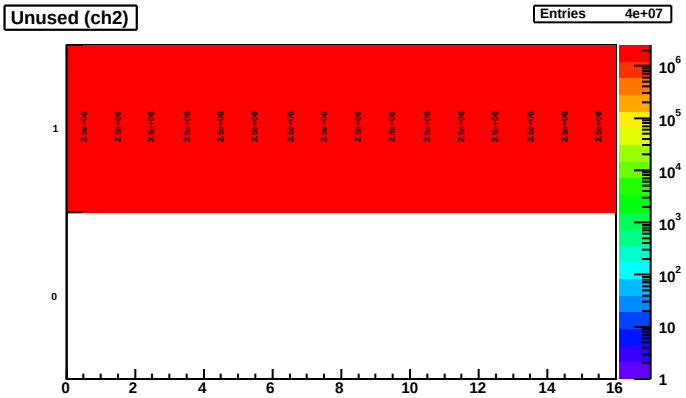

Input to FPE L1 DSM
Entries 1e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

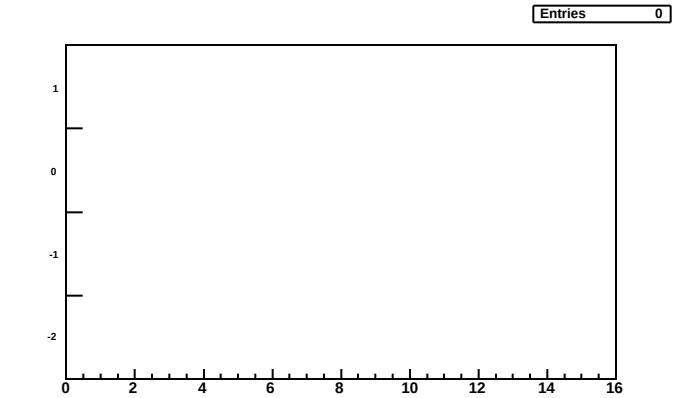

TF201 0-15 (ch0)
Entries 4e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

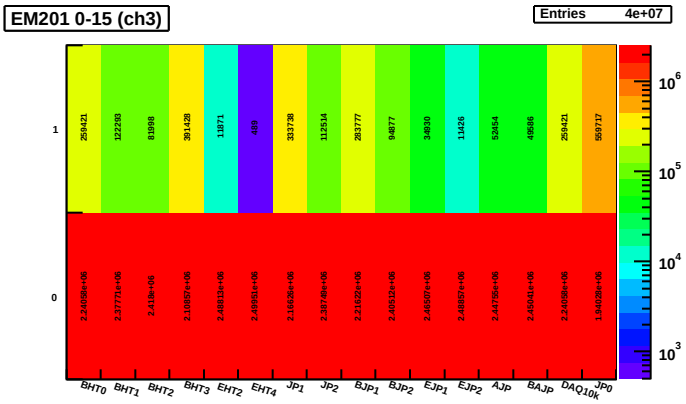

TF201 0-15 (ch0)
Entries 157310
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 181047
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 4e+07


Entries 0


EM201 0-15 (ch3)
Entries 4e+07
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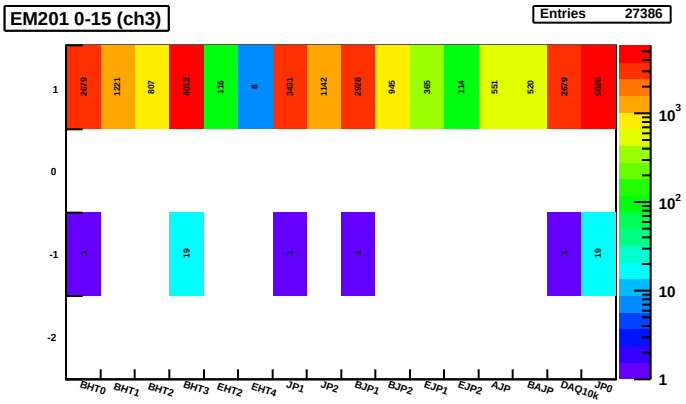
EM201 0-15 (ch3)
Entries 27386
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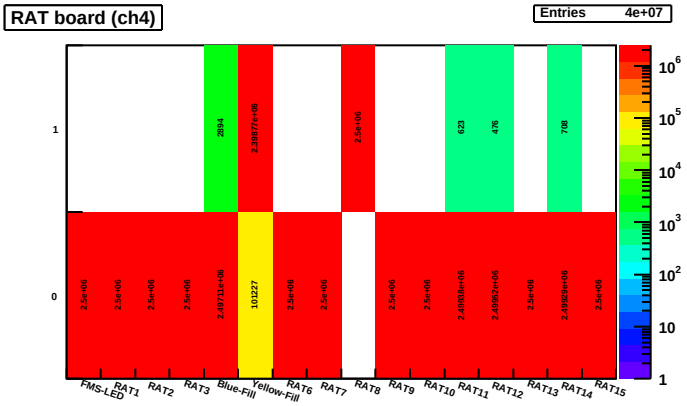
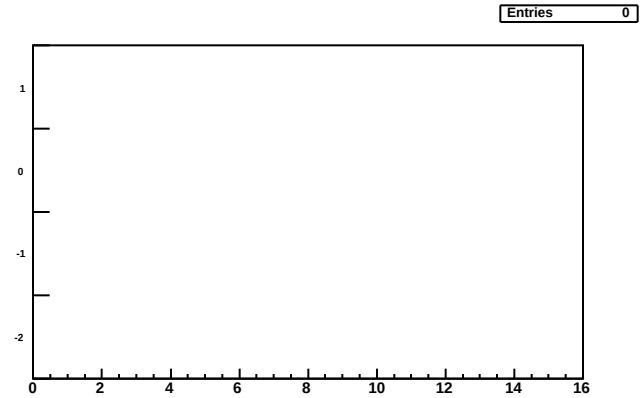
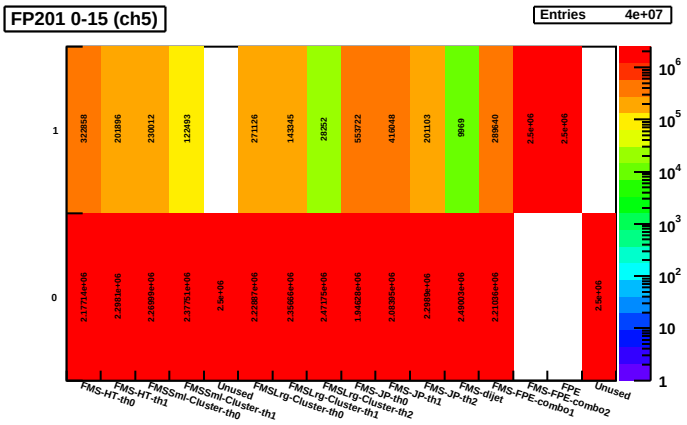
RAT board (ch4)





FP201 0-15 (ch5)



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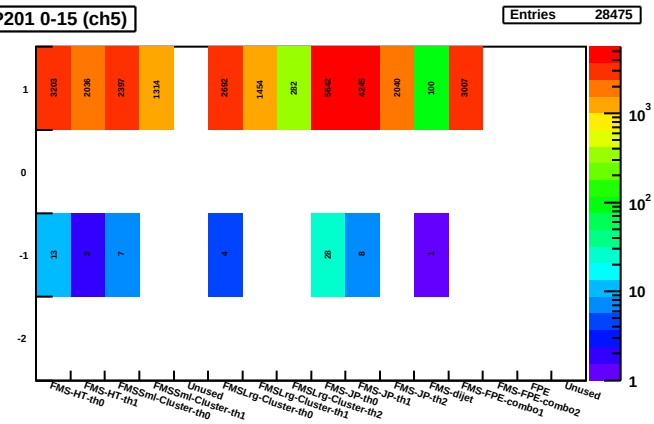
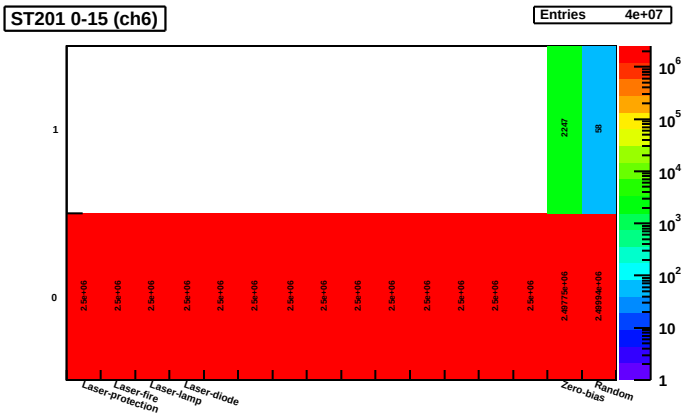
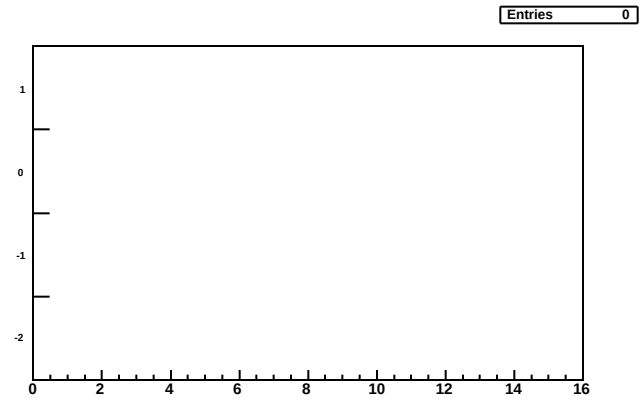
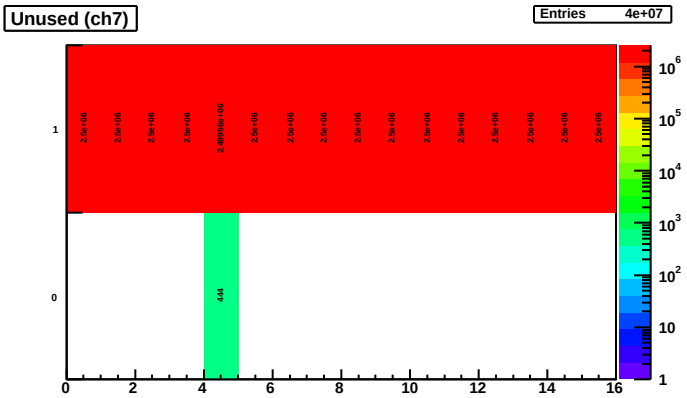
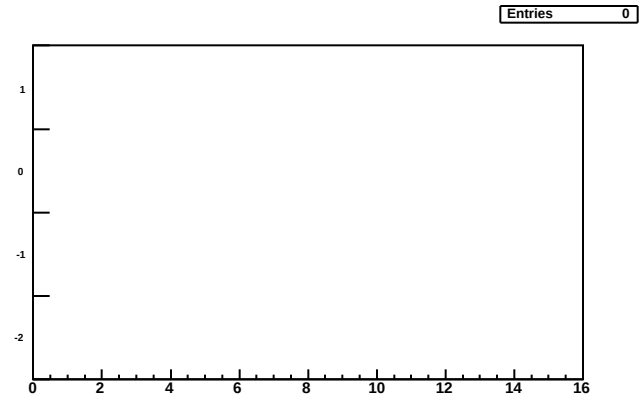


ST201 0-15 (ch6)





Unused (ch7)