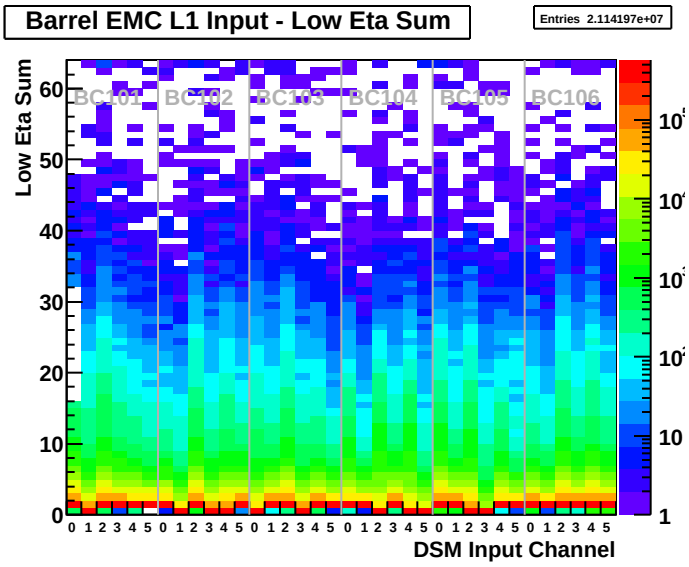
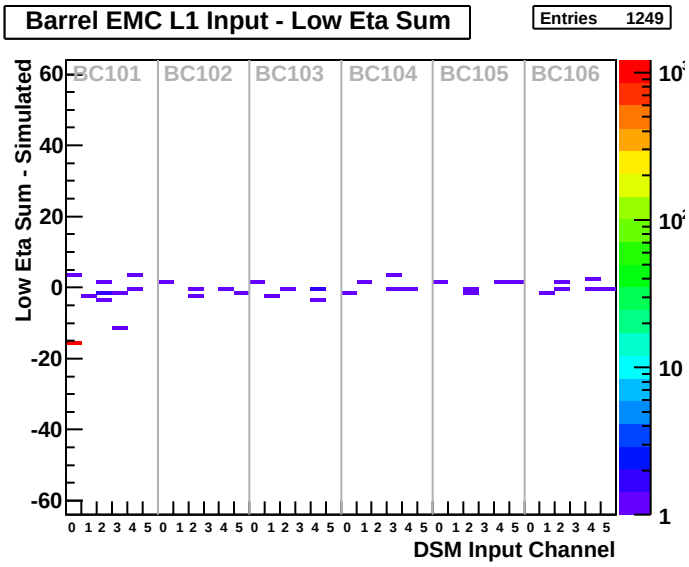
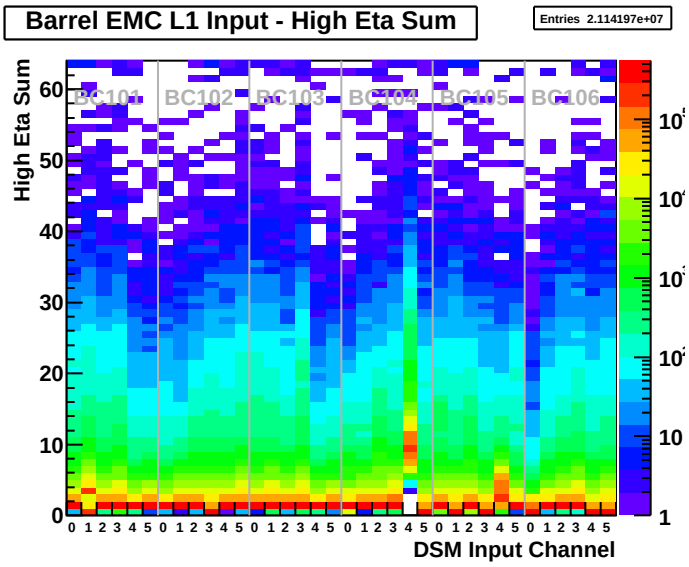
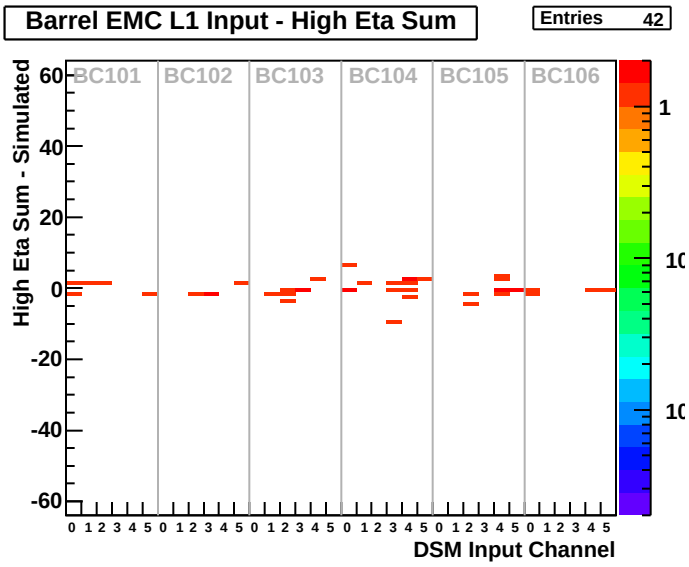
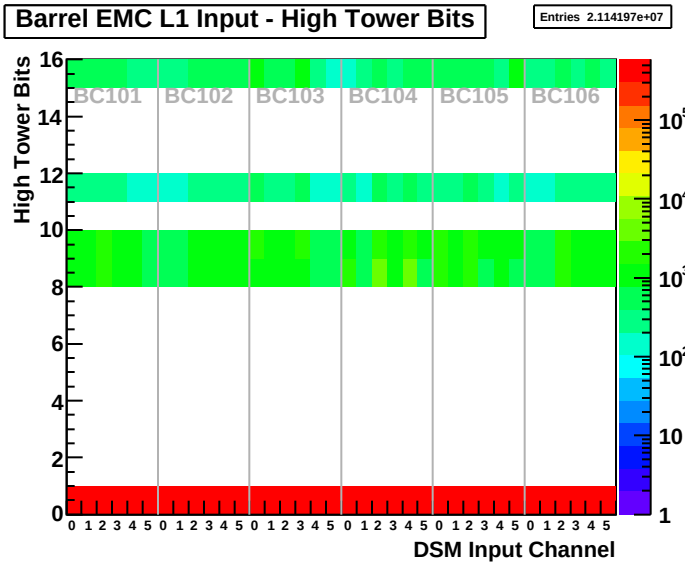
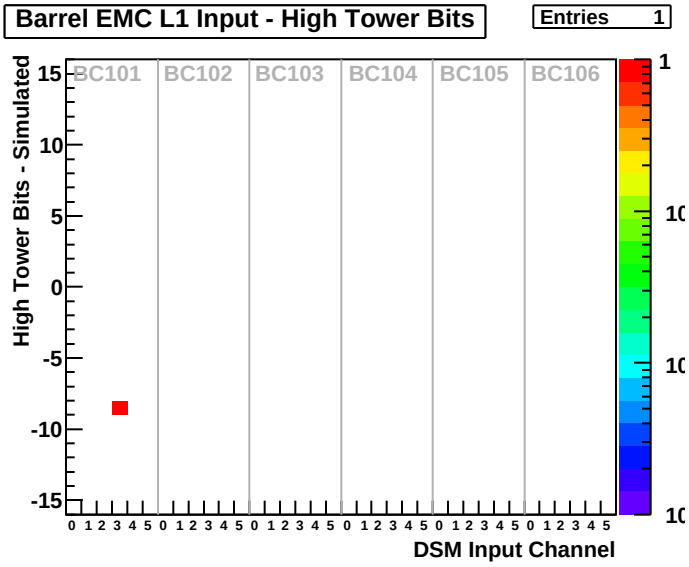

Barrel EMC L1 Input - Low Eta Sum
Entries 2.114197e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

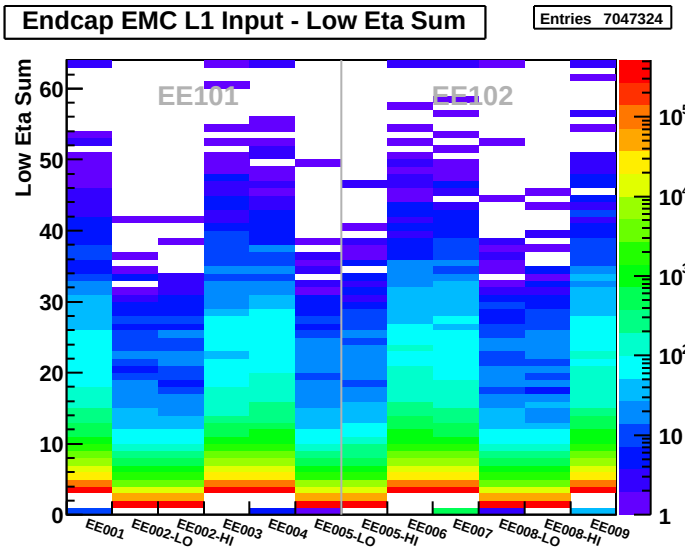

Barrel EMC L1 Input - Low Eta Sum
Entries 1249
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

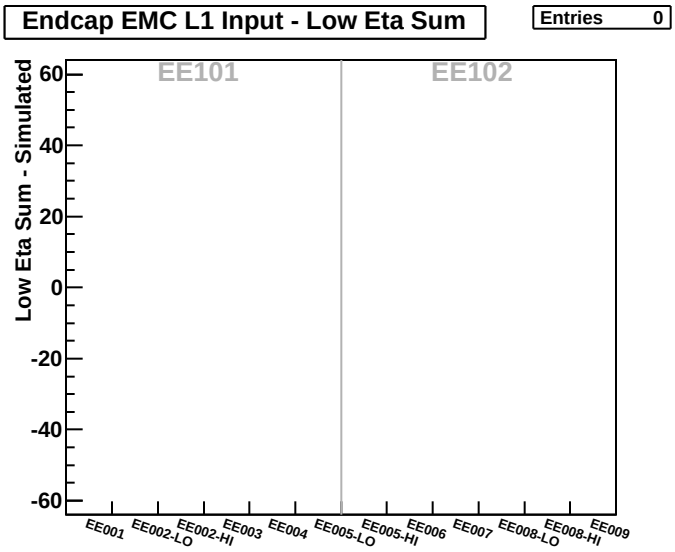

Barrel EMC L1 Input - High Eta Sum
Entries 2.114197e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

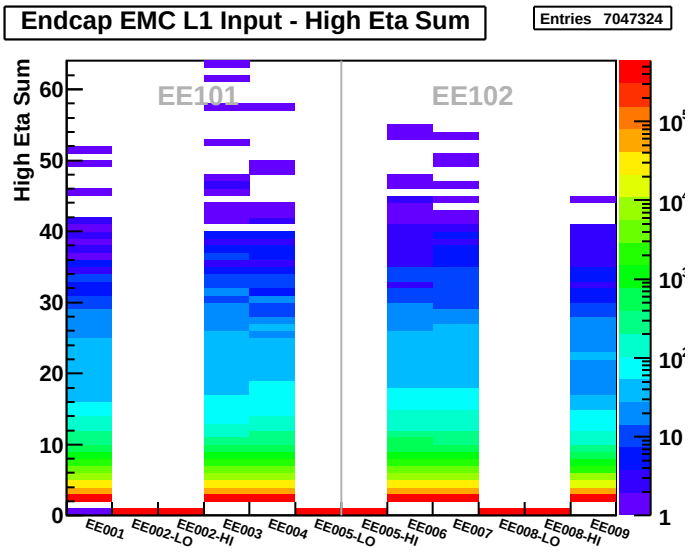

Barrel EMC L1 Input - High Eta Sum
Entries 42
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

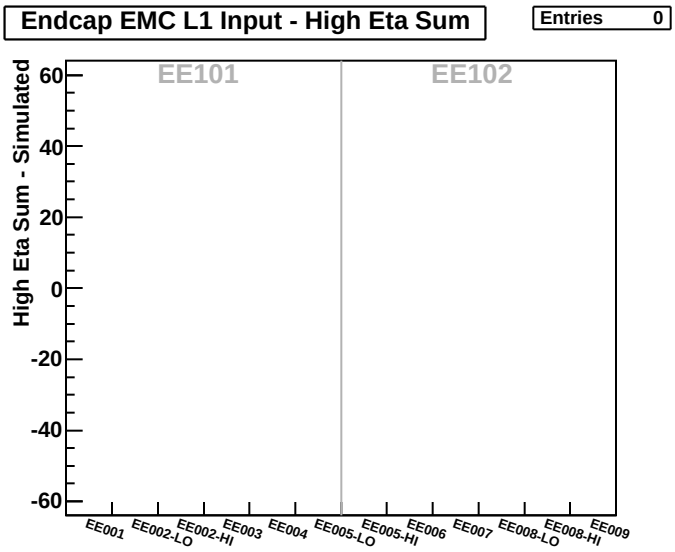

Barrel EMC L1 Input - High Tower Bits
Entries 2.114197e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

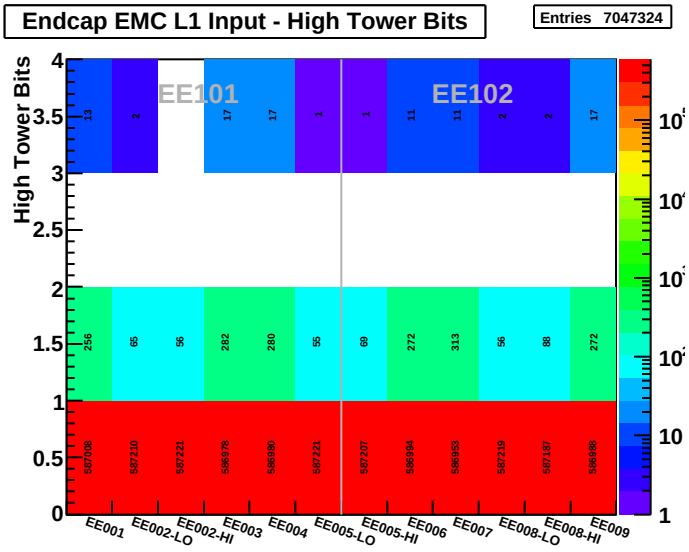

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

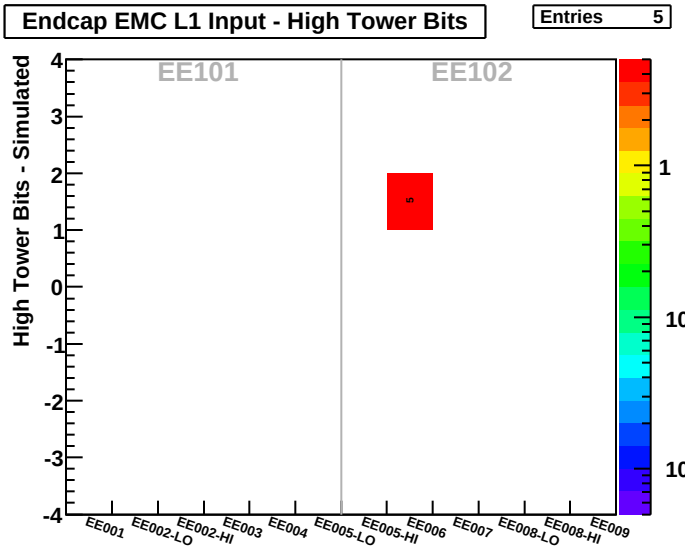

Endcap EMC L1 Input - Low Eta Sum
Entries 7047324
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

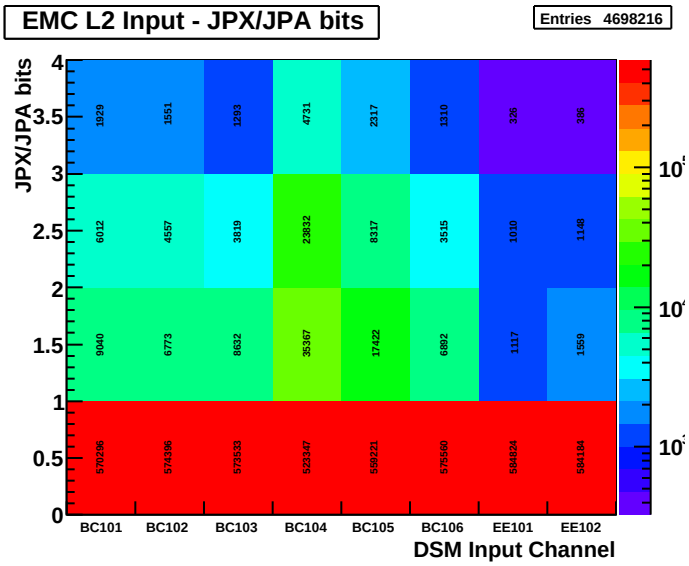

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

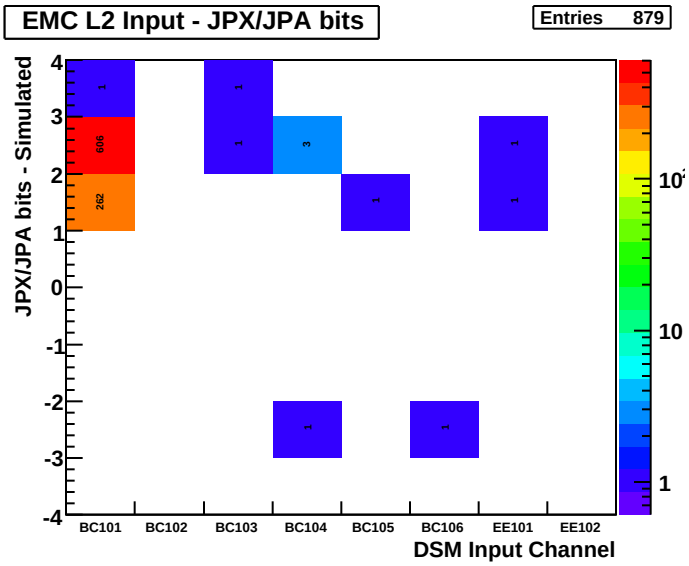

Endcap EMC L1 Input - High Eta Sum
Entries 7047324
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

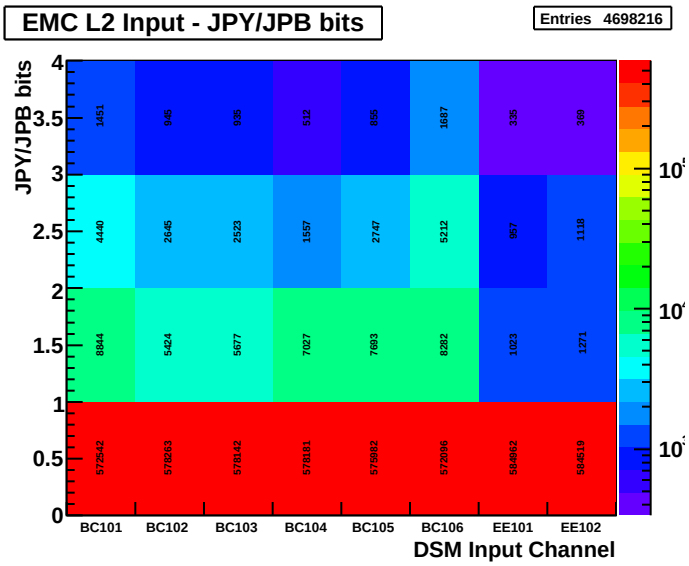

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

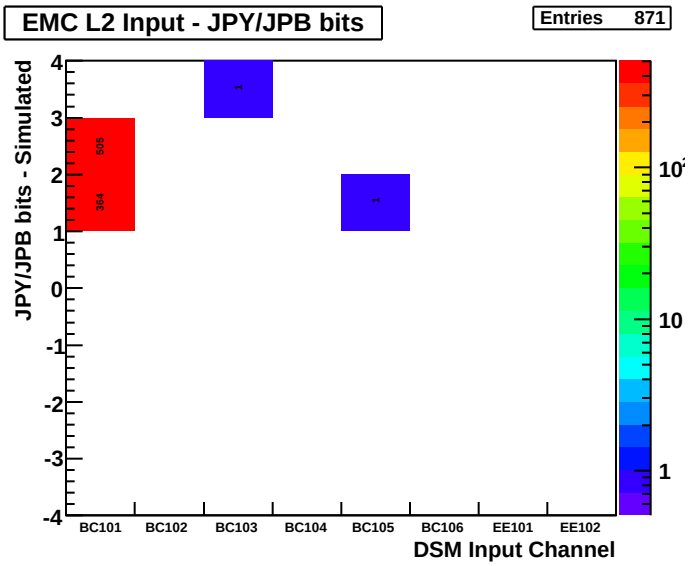

Endcap EMC L1 Input - High Tower Bits
Entries 7047324
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

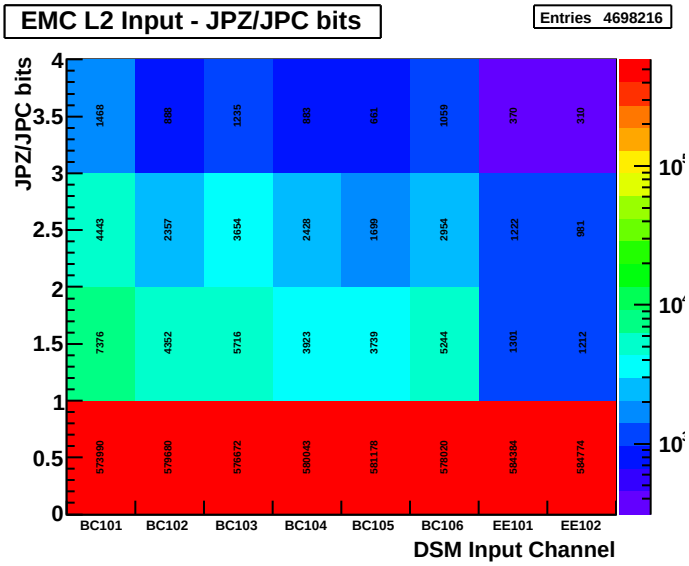

Endcap EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

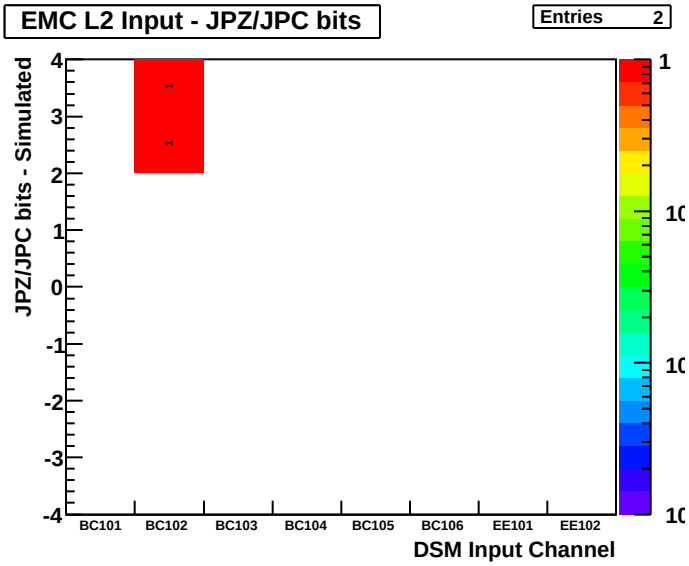

EMC L2 Input - JPX/JPA bits
Entries 4698216
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1929 1551 1293 4731 2317 1310 326 386
6012 4557 3819 23832 8317 3515 1010 1148
9040 6773 8632 35367 17422 6892 1117 1559
570296 574386 572533 523347 59221 575560 584824 584184

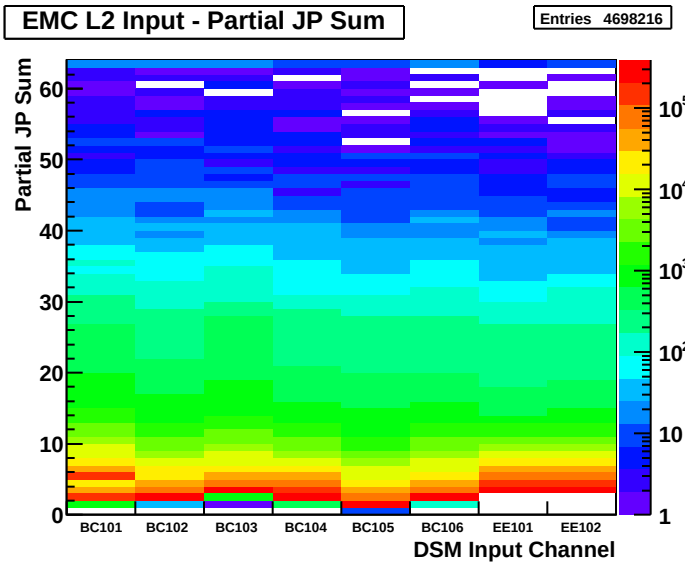

EMC L2 Input - JPX/JPA bits
Entries 879
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1 1 1 1
606 3
262

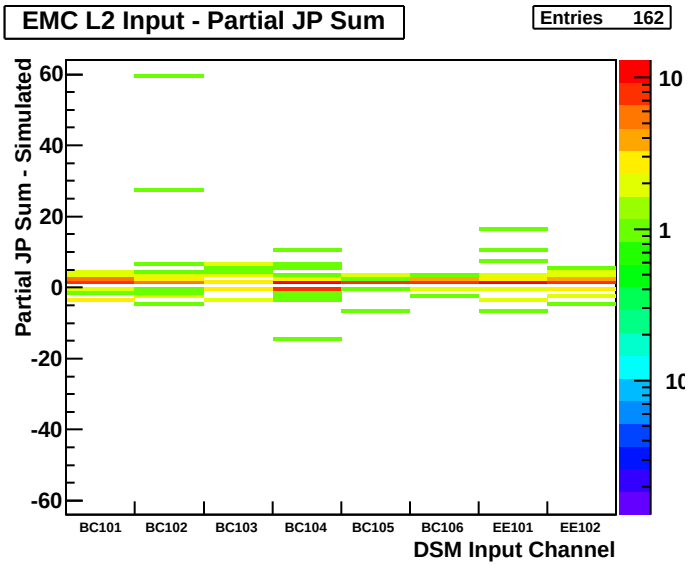

EMC L2 Input - JPY/JPB bits
Entries 4698216
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1451 845 935 512 855 1687 335 389
4440 2645 2523 1557 2747 5212 957 1118
8844 5424 5677 7027 7693 8282 1023 1271
572542 578263 578142 578181 575982 572096 584862 584519

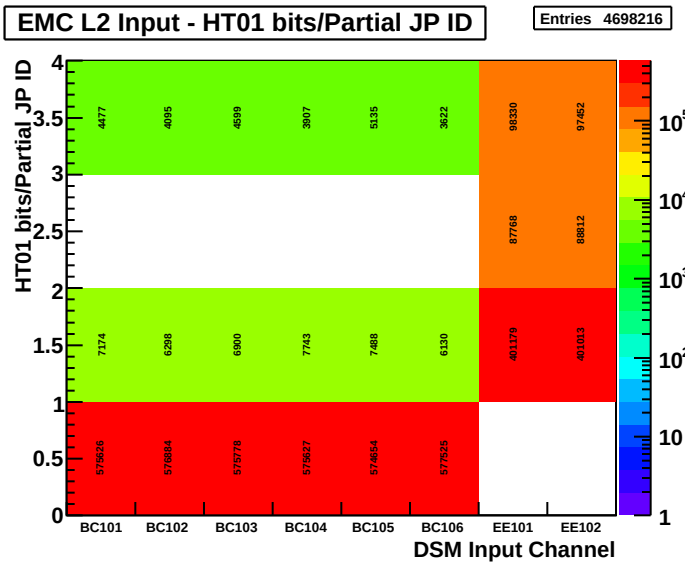

EMC L2 Input - JPY/JPB bits
Entries 871
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
505 1
364

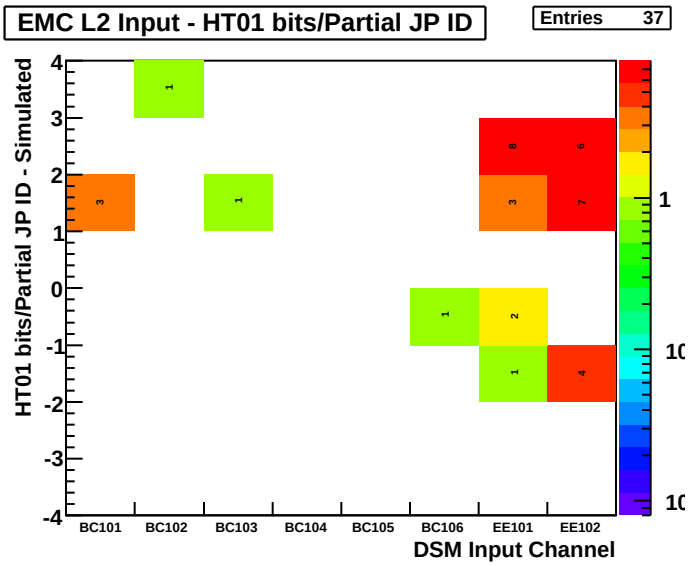

EMC L2 Input - JPZ/JPC bits
Entries 4698216
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1468 888 1235 883 661 1059 370 310
4443 2357 3654 2428 1699 2954 1222 981
7376 4352 5716 3923 3739 5244 1301 1212
573990 579580 576572 580043 581178 578020 584384 584774

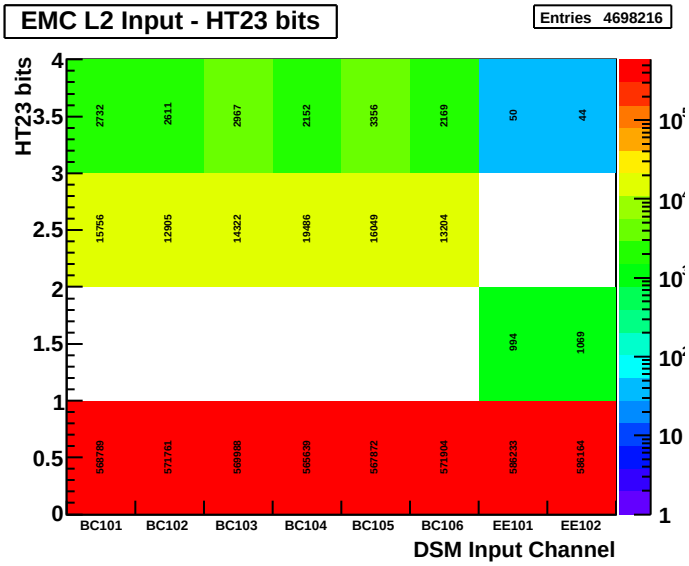

EMC L2 Input - JPZ/JPC bits
Entries 2
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1

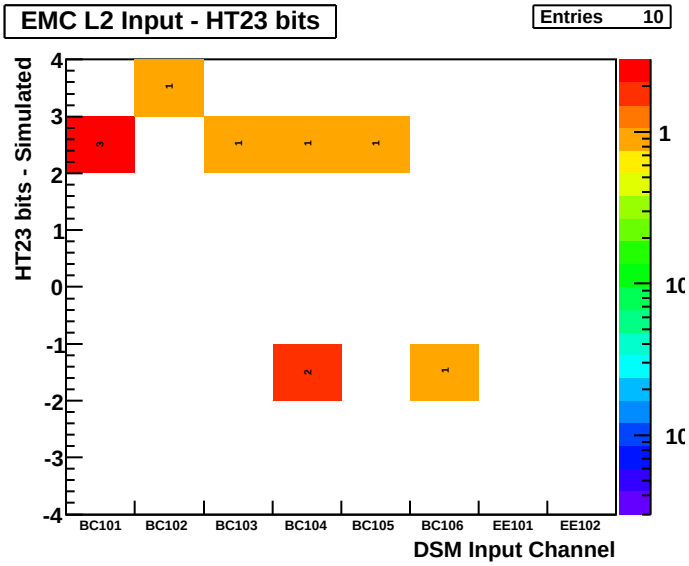

EMC L2 Input - Partial JP Sum
Entries 4698216
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

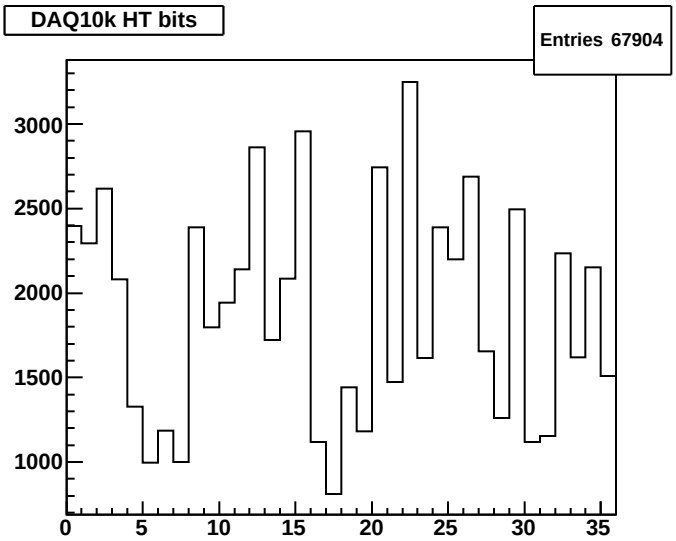

EMC L2 Input - Partial JP Sum
Entries 162
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

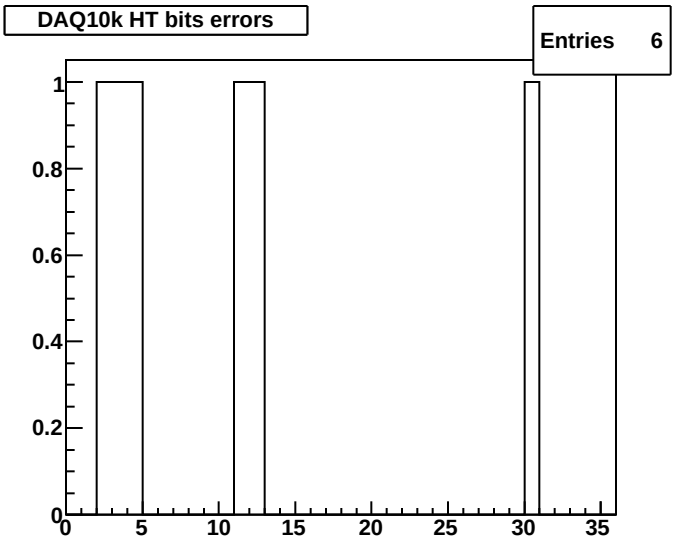

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4698216
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

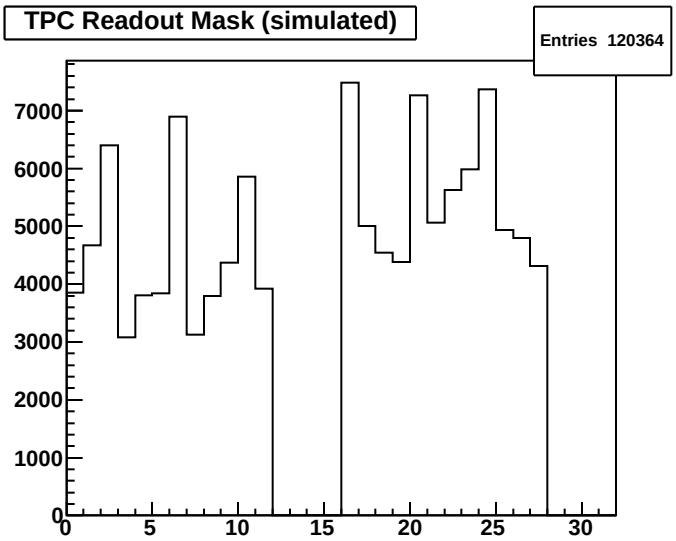

EMC L2 Input - HT01 bits/Partial JP ID
Entries 37
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

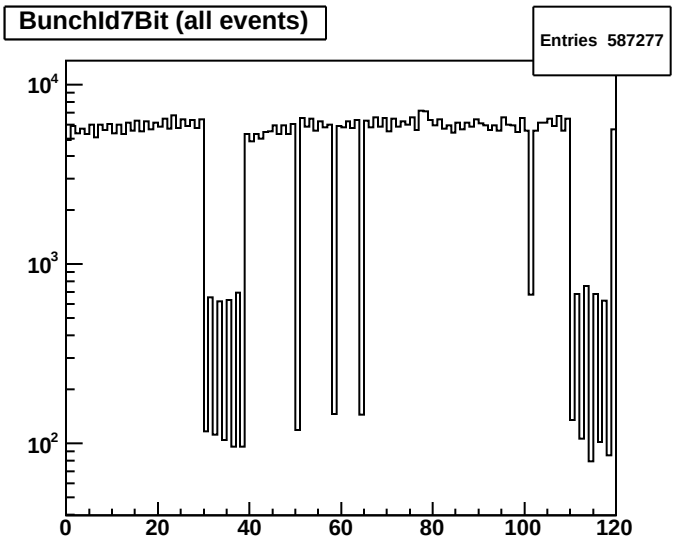

EMC L2 Input - HT23 bits
Entries 4698216
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

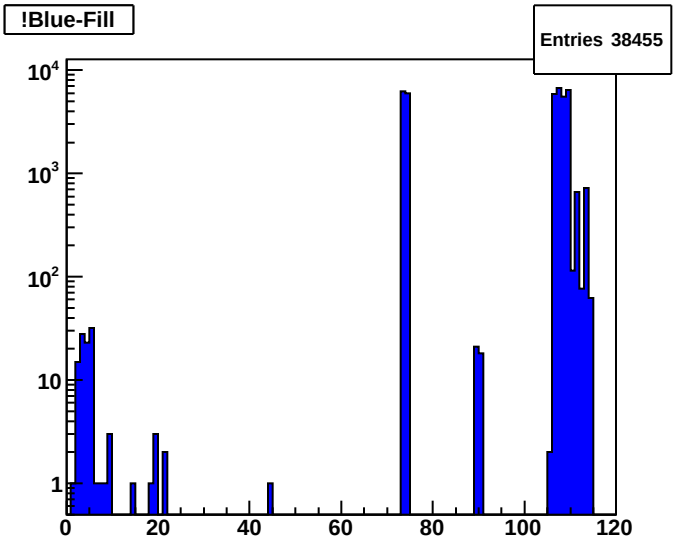

EMC L2 Input - HT23 bits
Entries 10
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

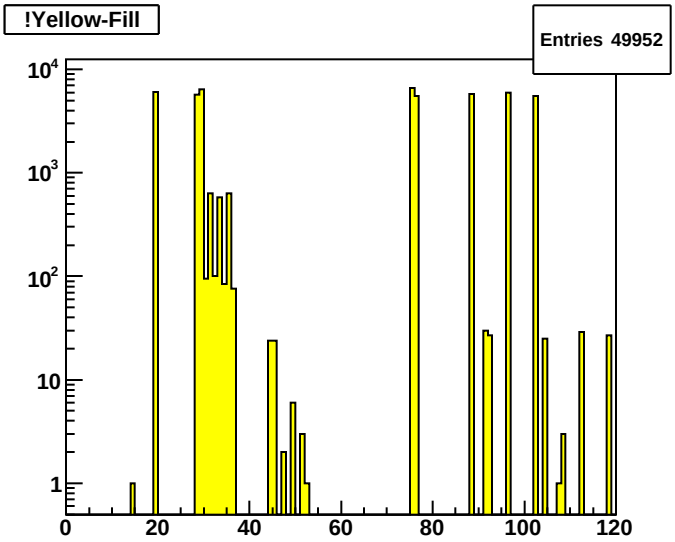

DAQ10k HT bits
Entries 67904

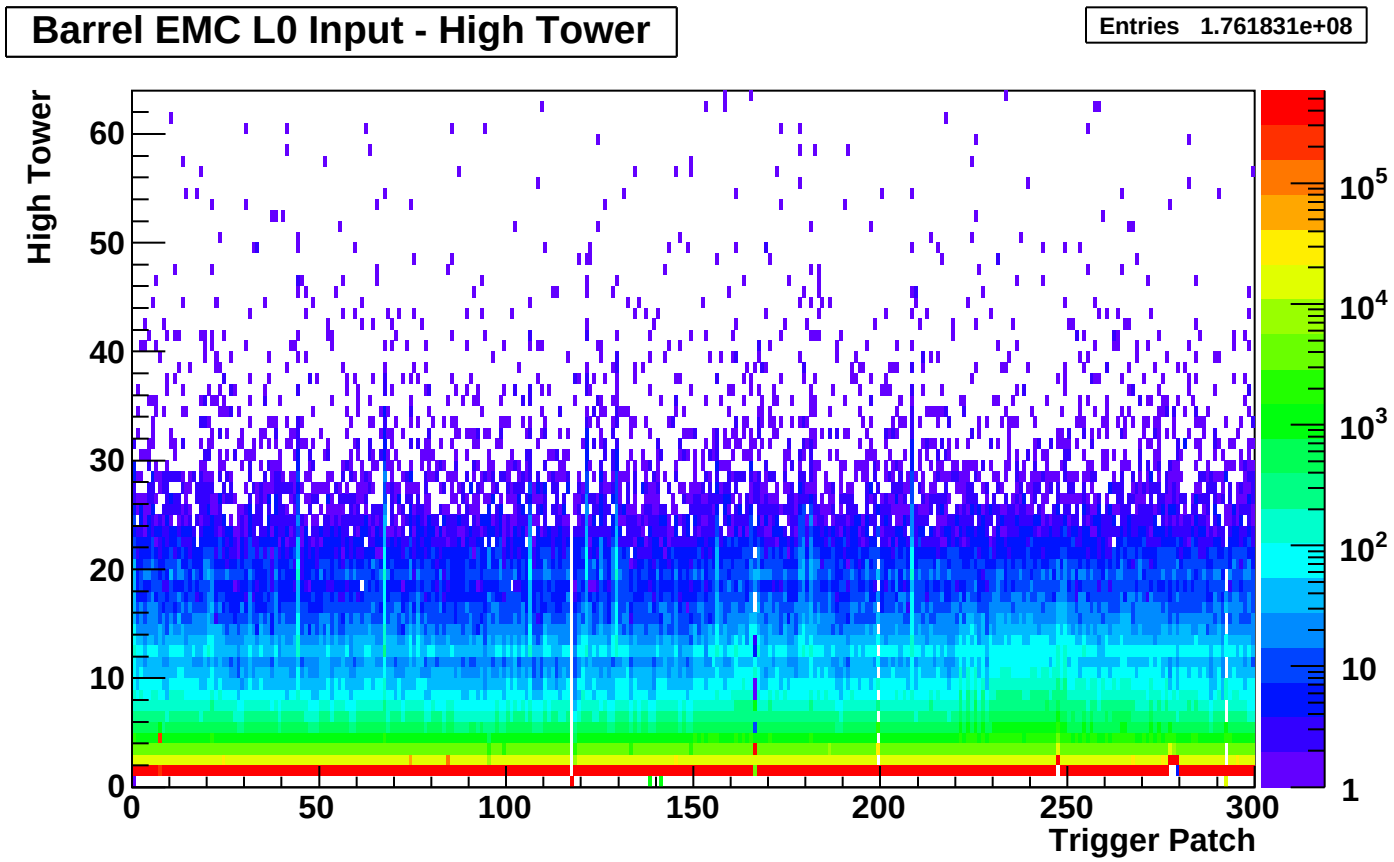

DAQ10k HT bits errors
Entries 6

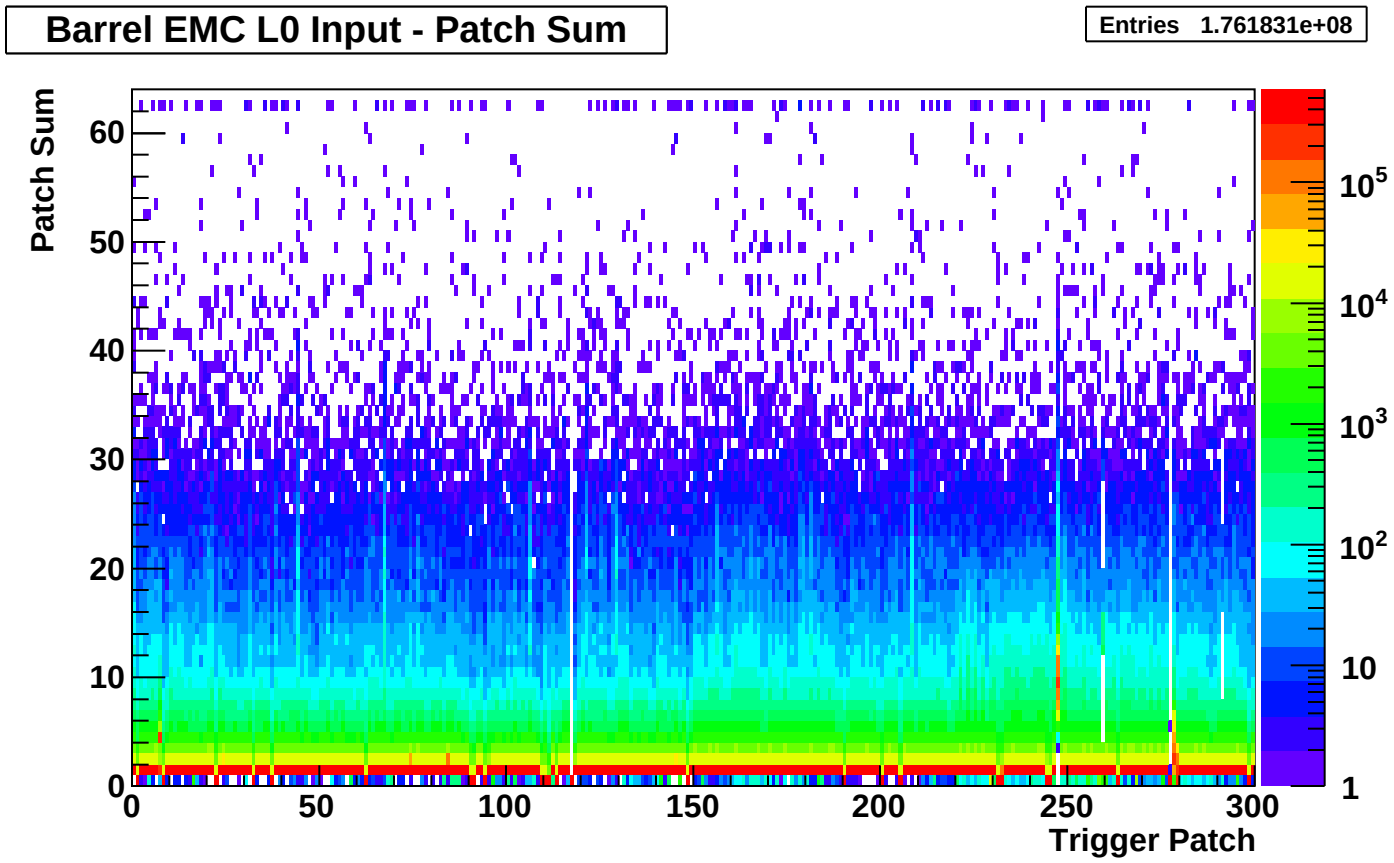

TPC Readout Mask (simulated)
Entries 120364

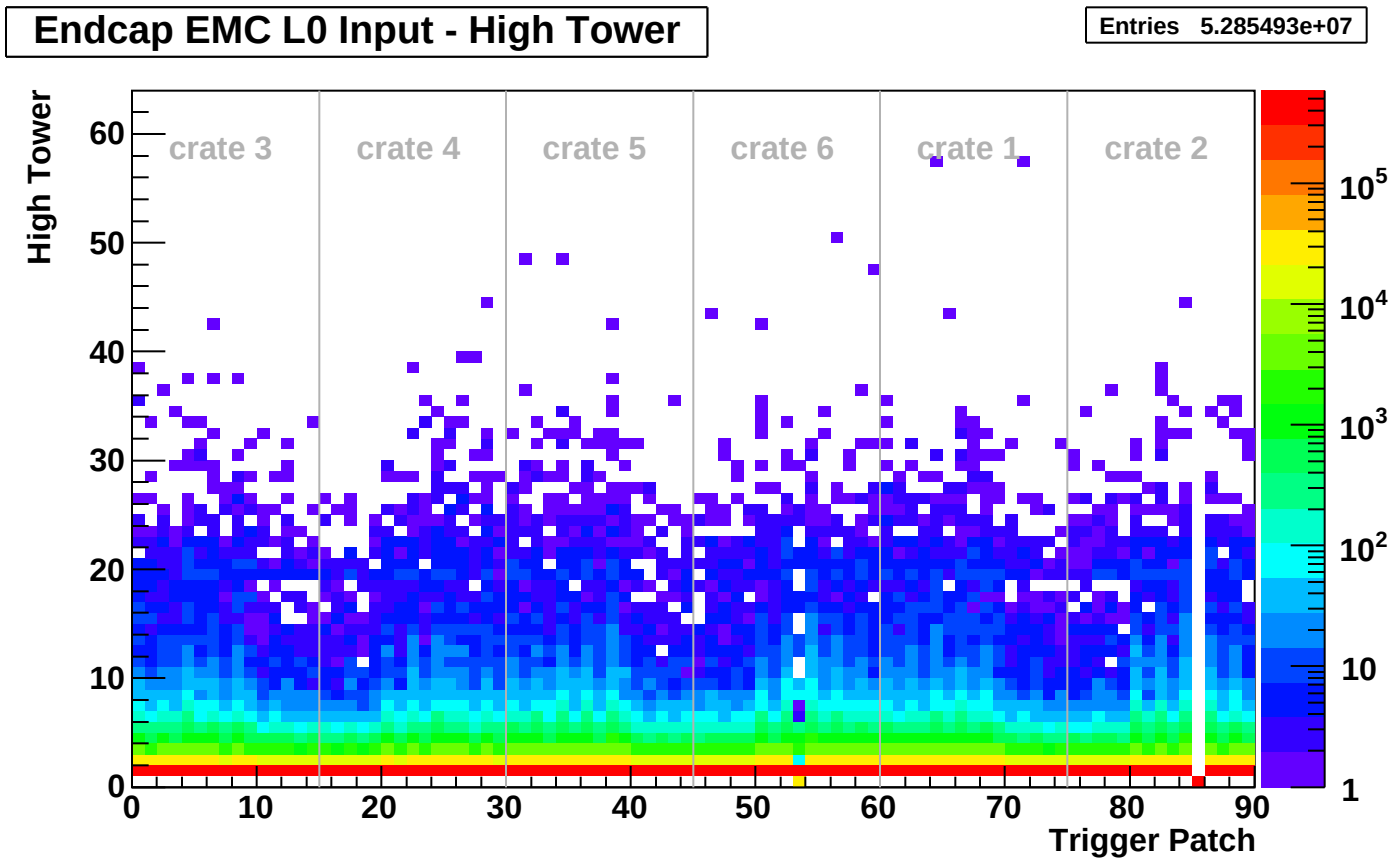

BunchId7Bit (all events)
Entries 587277

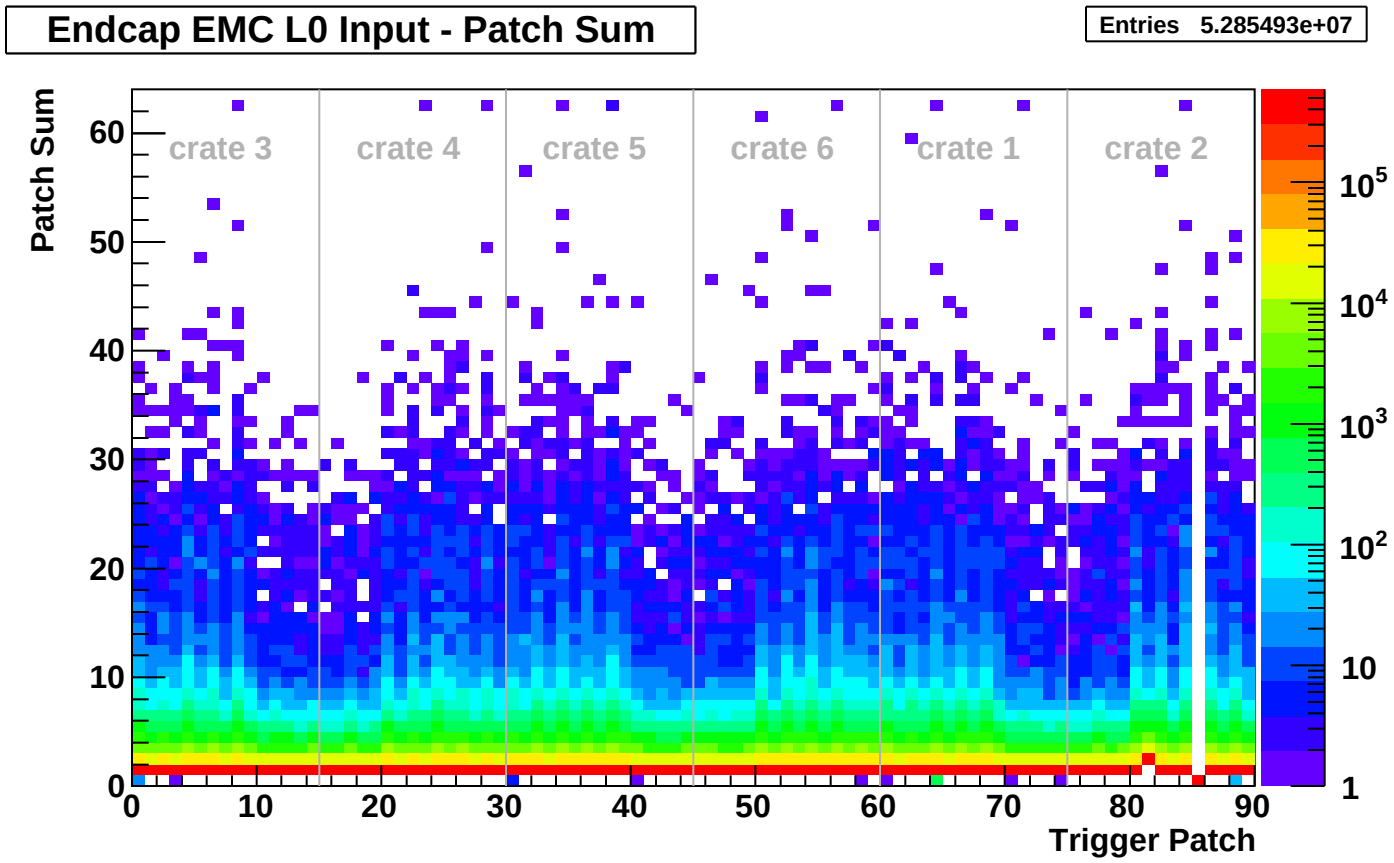

!Blue-Fill
Entries 38455

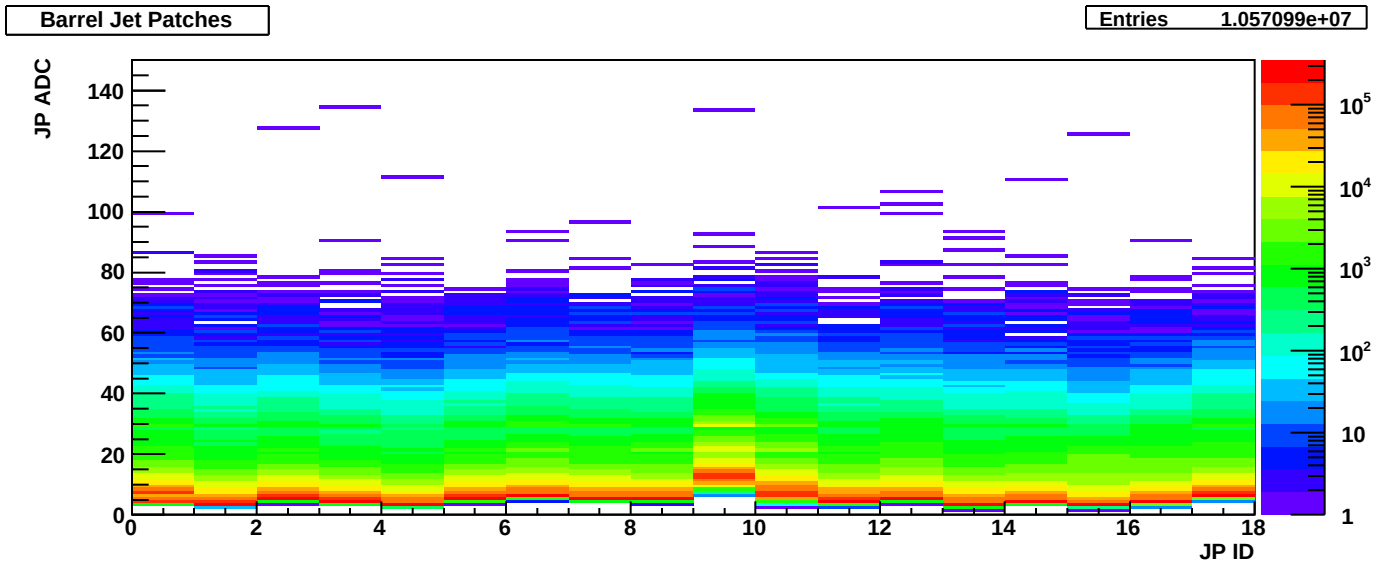

!Yellow-Fill
Entries 49952

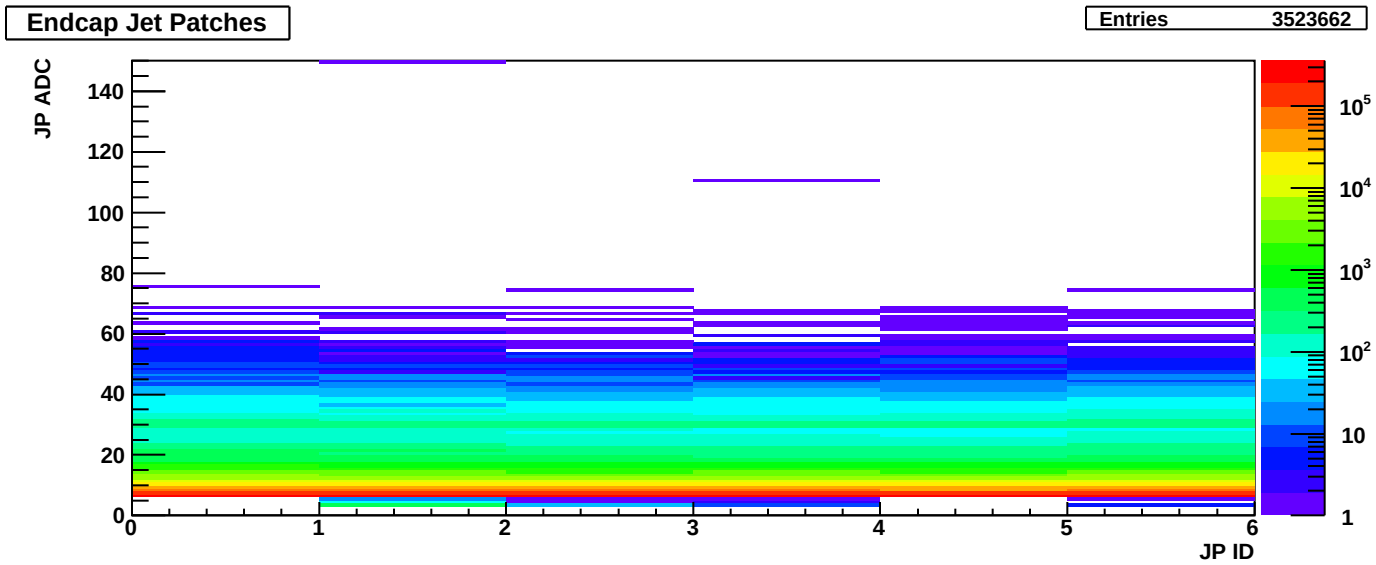

Barrel EMC L0 Input - High Tower
Entries 1.761831e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

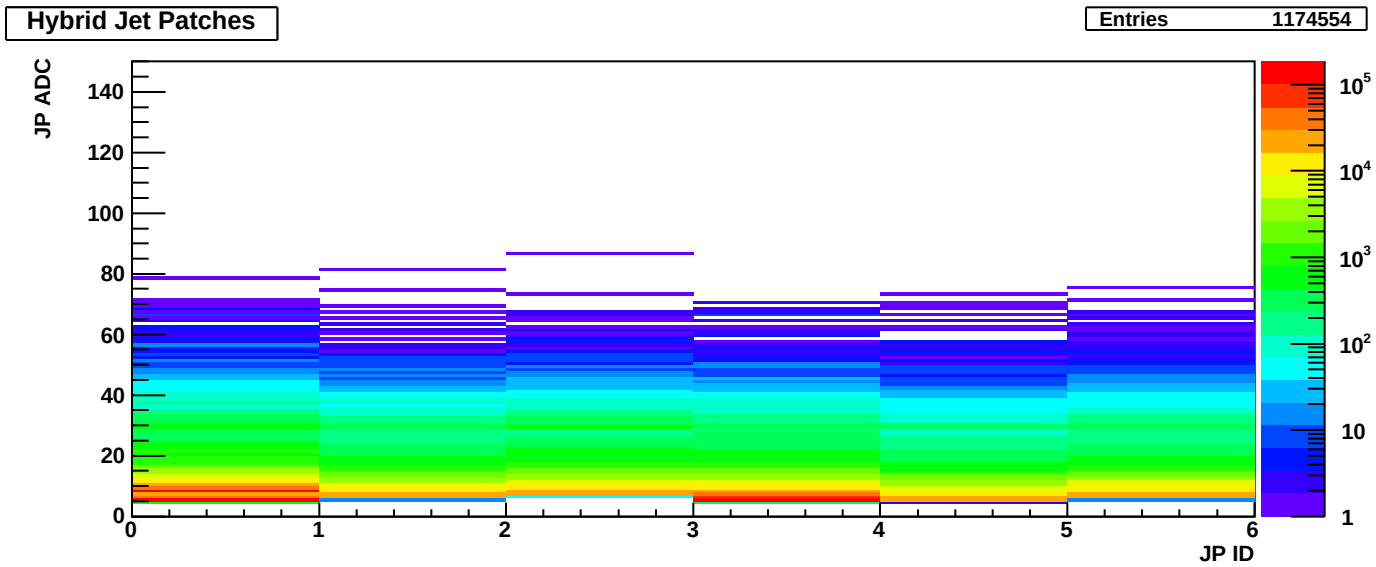

Barrel EMC L0 Input - Patch Sum
Entries 1.761831e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

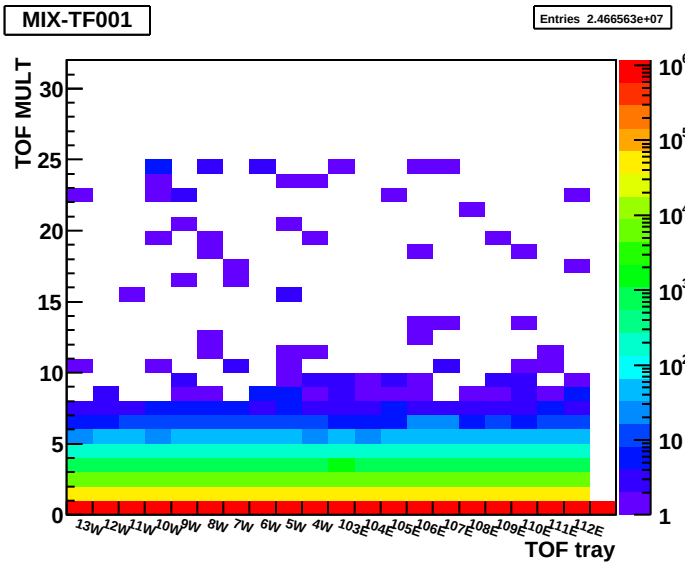

Endcap EMC L0 Input - High Tower
Entries 5.285493e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

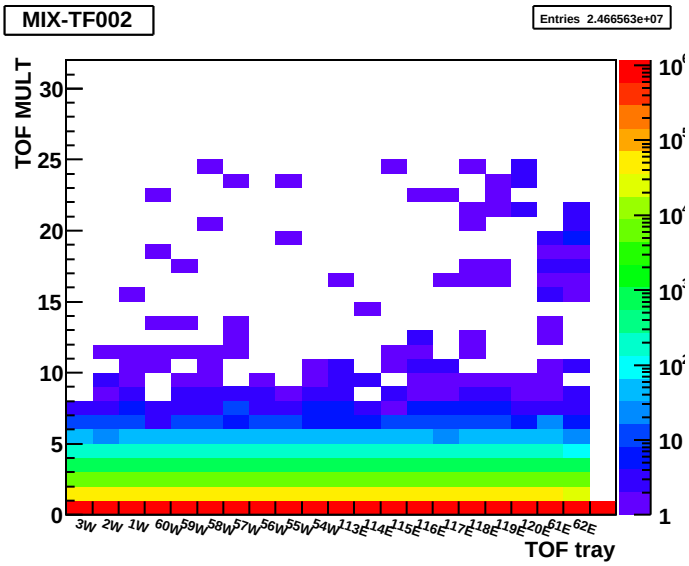

Endcap EMC L0 Input - Patch Sum
Entries 5.285493e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

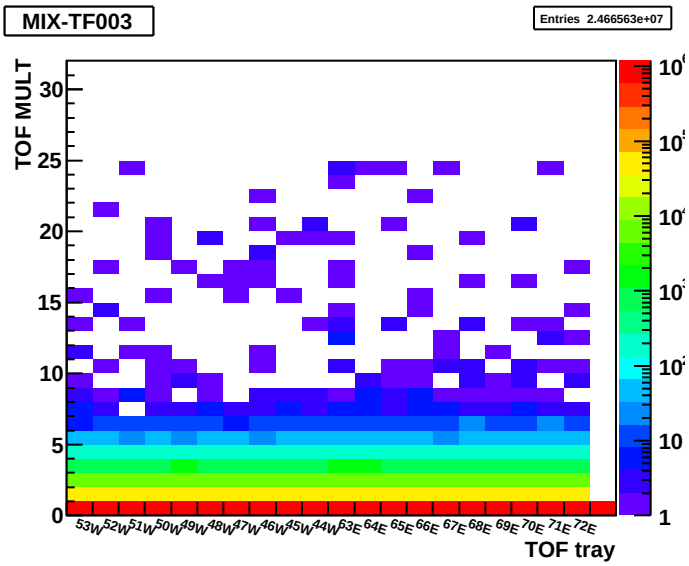

Barrel Jet Patches
Entries 1.057099e+07
JP ADC
JP ID

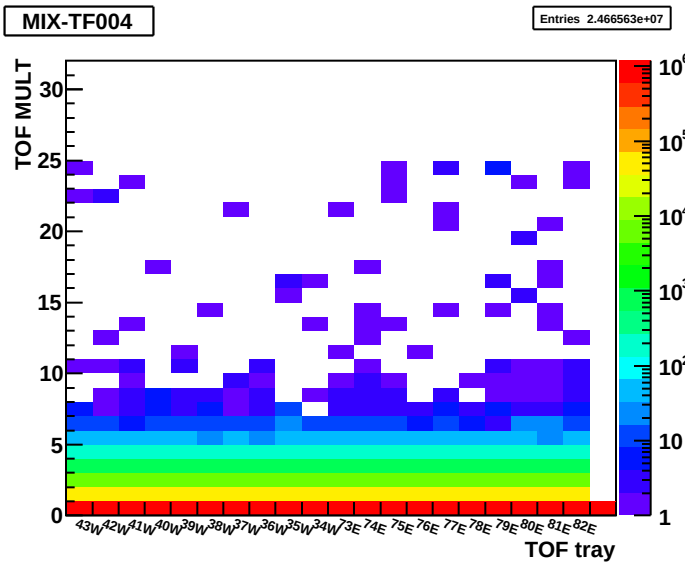

Endcap Jet Patches
Entries 3523662
JP ADC
JP ID

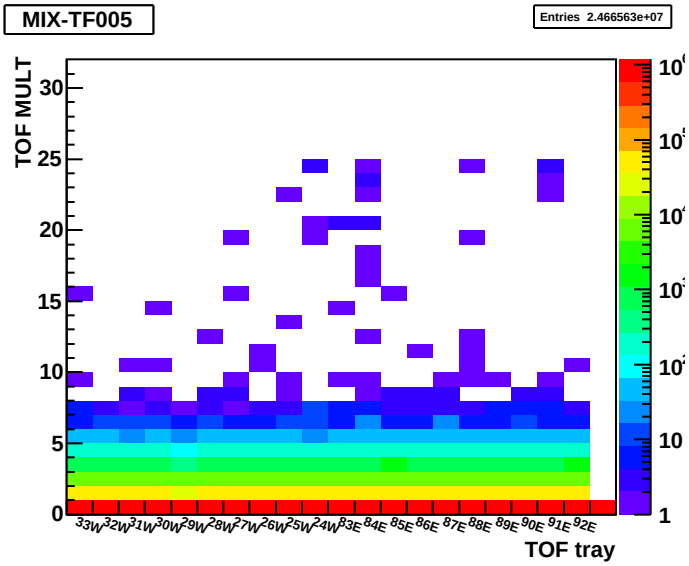

Hybrid Jet Patches
Entries 1174554
JP ADC
JP ID

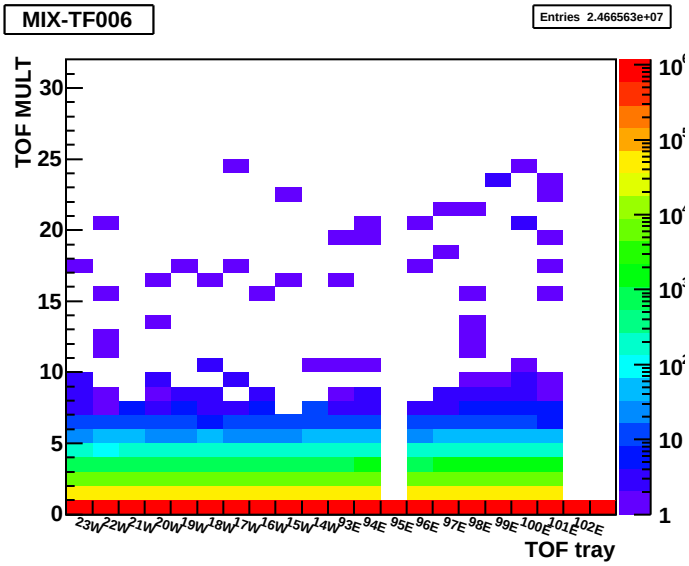

MIX-TF001
Entries 2.466563e+07
TOF MULT
TOF tray

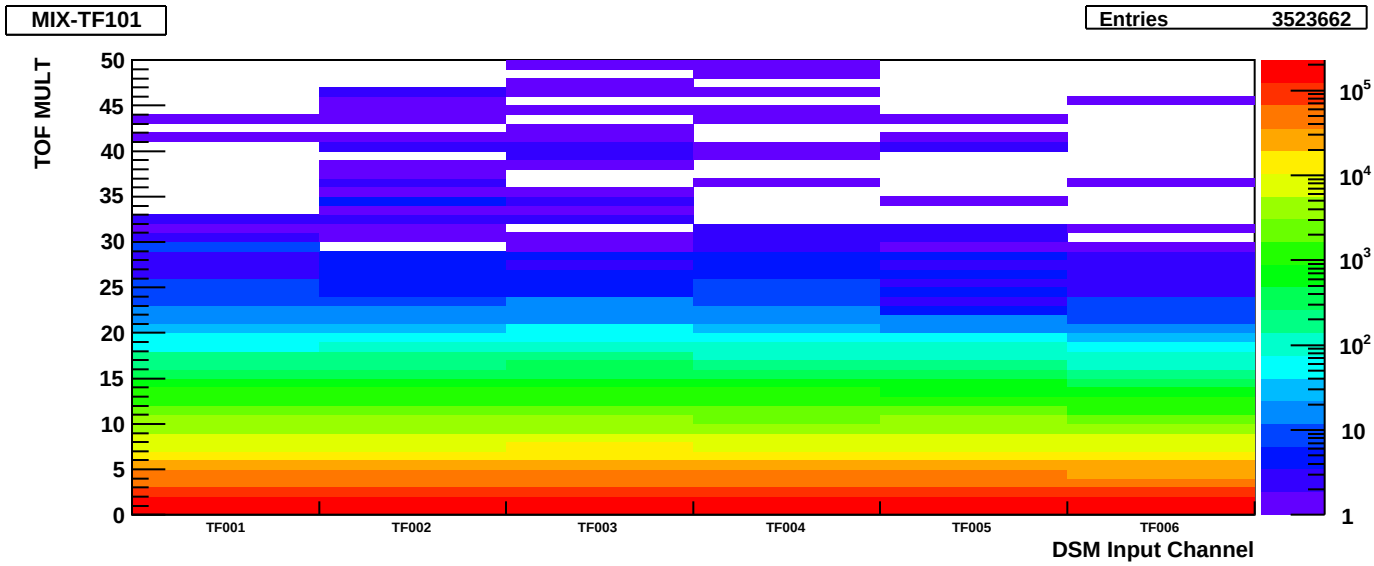

MIX-TF002
Entries 2.466563e+07
TOF MULT
TOF tray

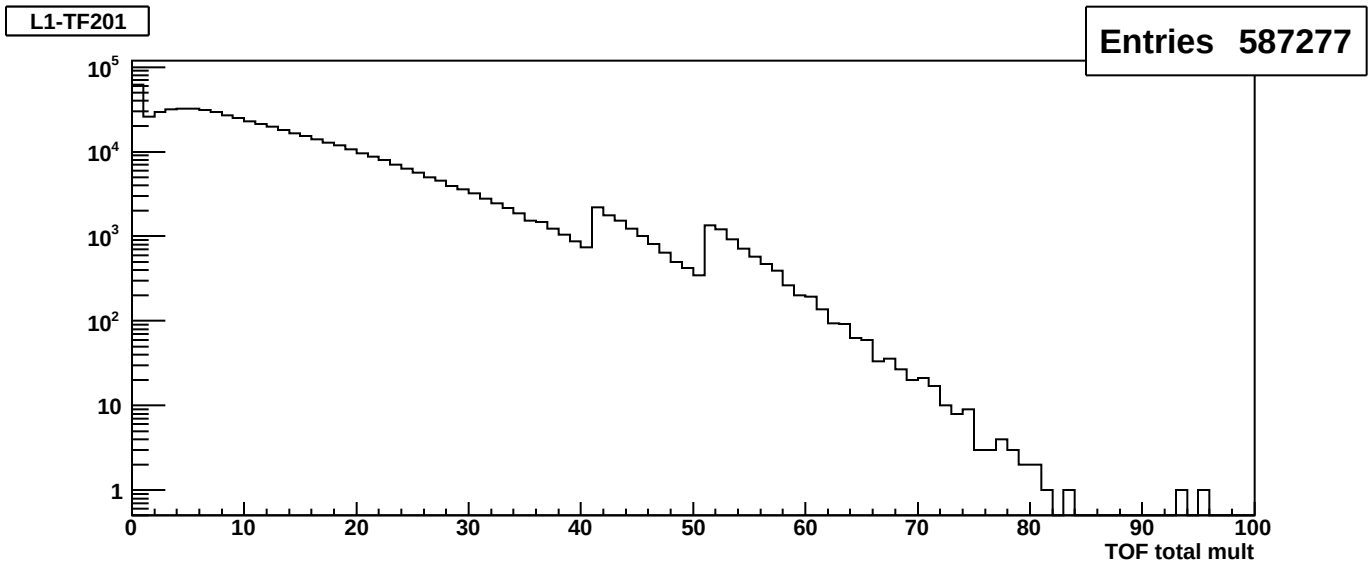

MIX-TF003
Entries 2.466563e+07
TOF MULT
TOF tray

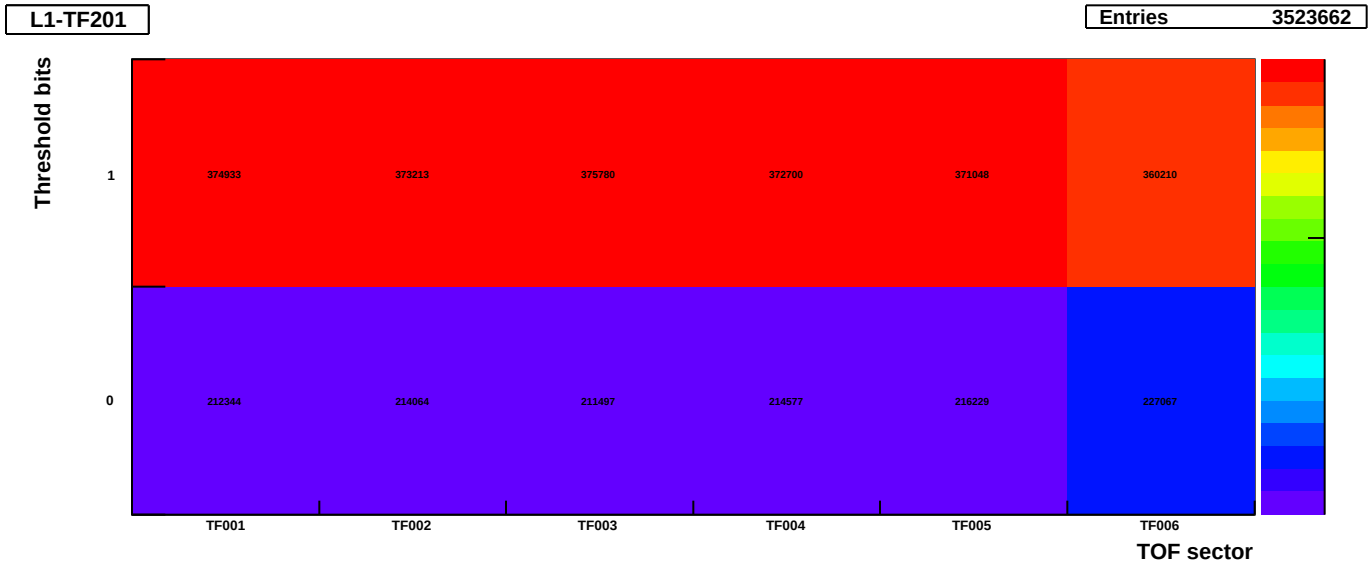

MIX-TF004
Entries 2.466563e+07
TOF MULT
TOF tray

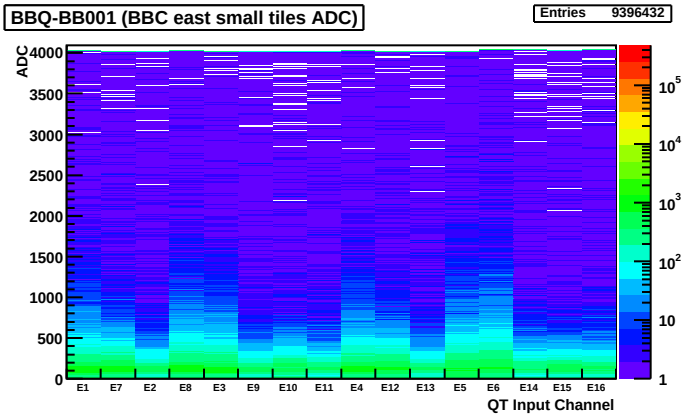

MIX-TF005
Entries 2.466563e+07
TOF MULT
TOF tray

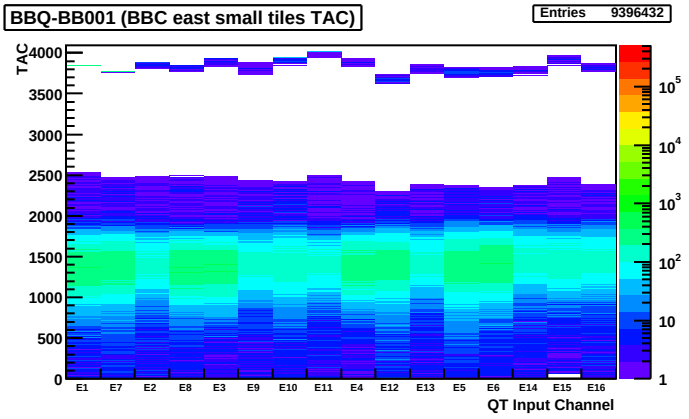

MIX-TF006
Entries 2.466563e+07
TOF MULT
TOF tray

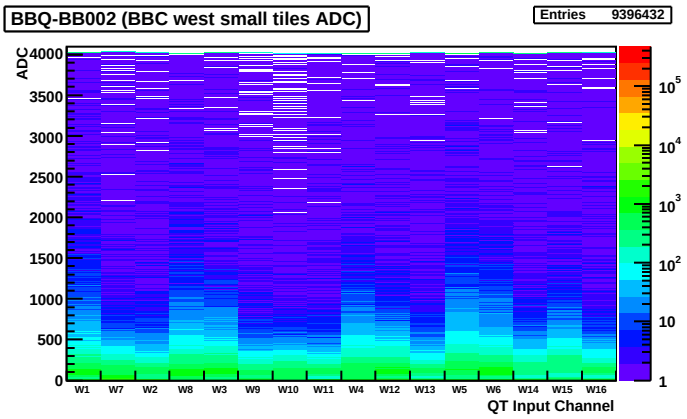

MIX-TF101
Entries 3523662
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

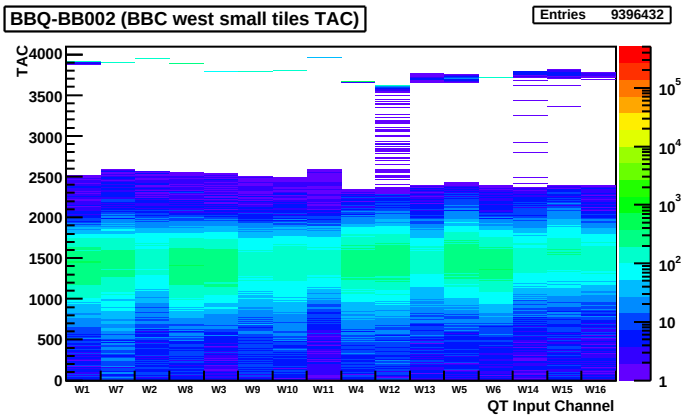

L1-TF201
Entries 587277
TOF total mult

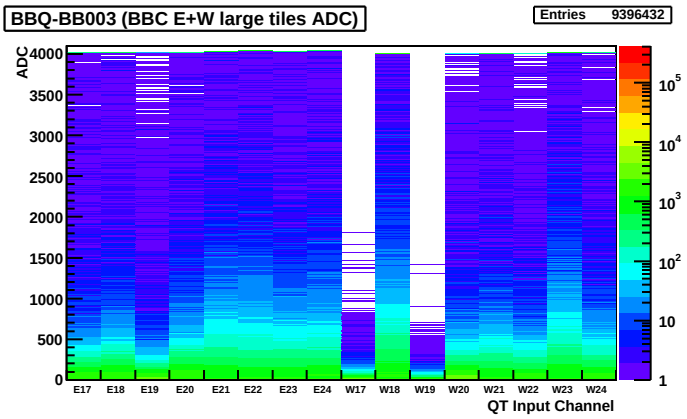

L1-TF201
Entries 3523662
Threshold bits
374933
373213
375780
372700
371048
360210
212344
214064
211497
214577
216229
227067
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

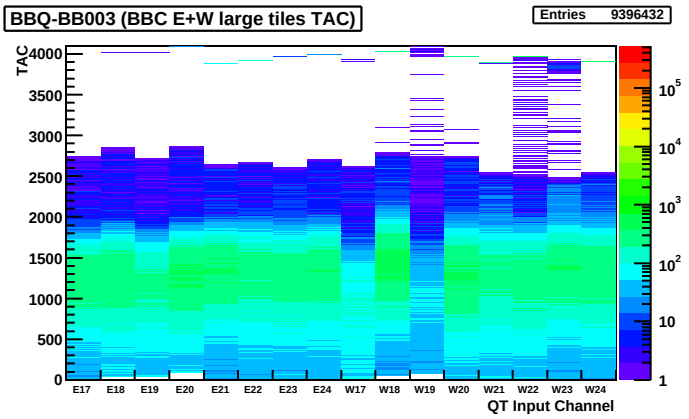

BBQ-BB001 (BBC east small tiles ADC)
Entries 9396432
ADC
QT Input Channel

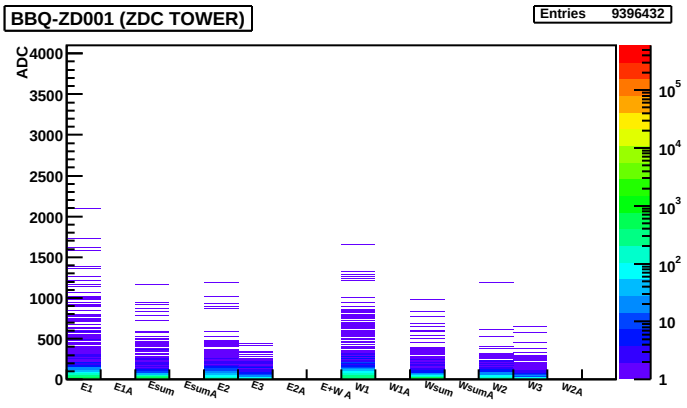

BBQ-BB001 (BBC east small tiles TAC)
Entries 9396432
TAC
QT Input Channel

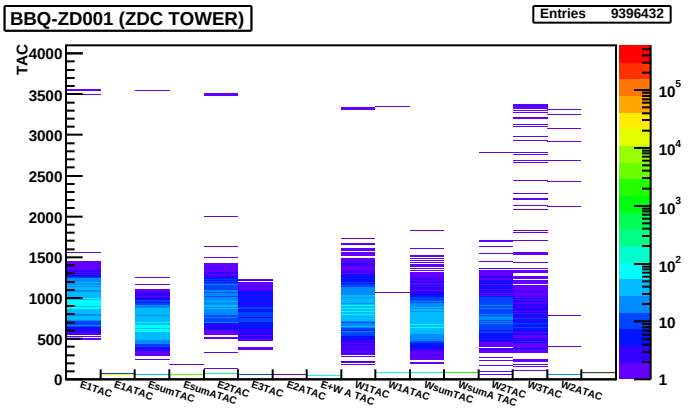

BBQ-BB002 (BBC west small tiles ADC)
Entries 9396432
ADC
QT Input Channel

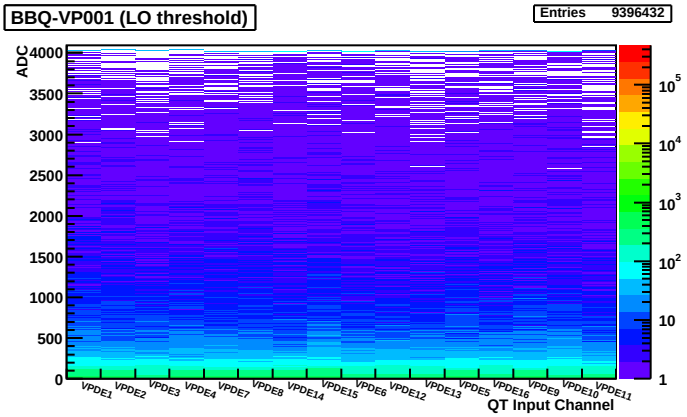

BBQ-BB002 (BBC west small tiles TAC)
Entries 9396432
TAC
QT Input Channel

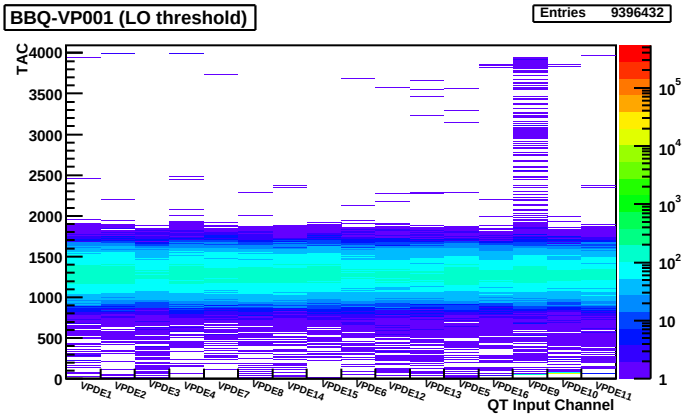

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9396432
ADC
QT Input Channel

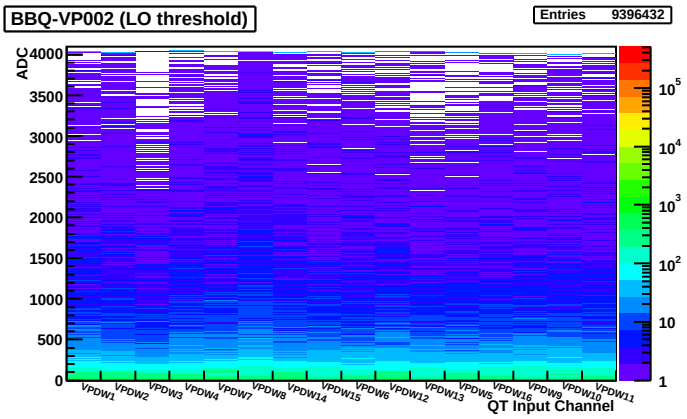

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9396432
TAC
QT Input Channel

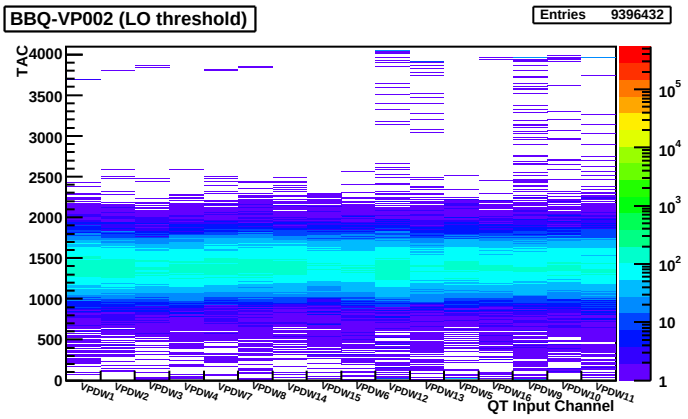

BBQ-ZD001 (ZDC TOWER)
Entries 9396432
ADC

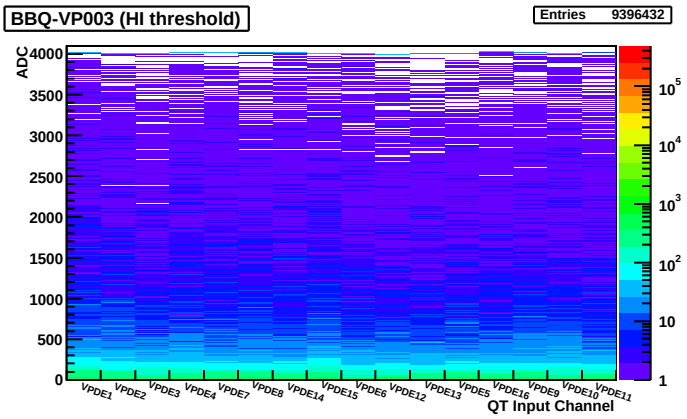

BBQ-ZD001 (ZDC TOWER)
Entries 9396432
TAC

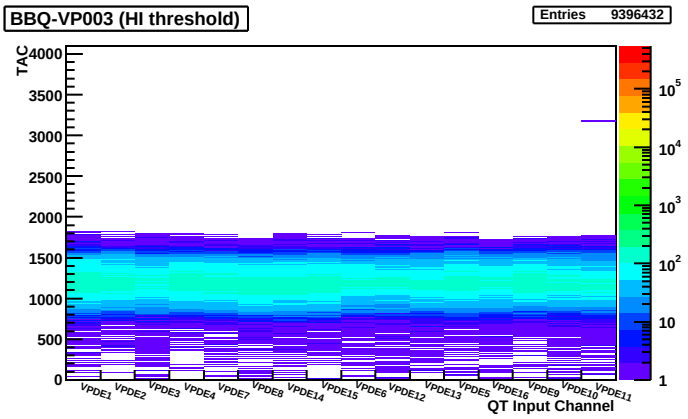

BBQ-VP001 (LO threshold)
Entries 9396432
ADC
QT Input Channel

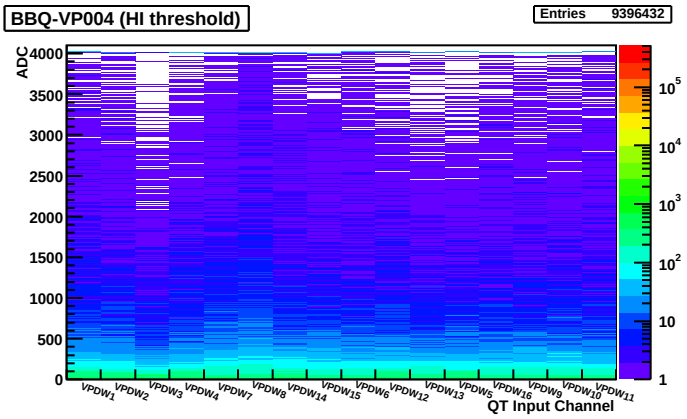

BBQ-VP001 (LO threshold)
Entries 9396432
TAC
QT Input Channel

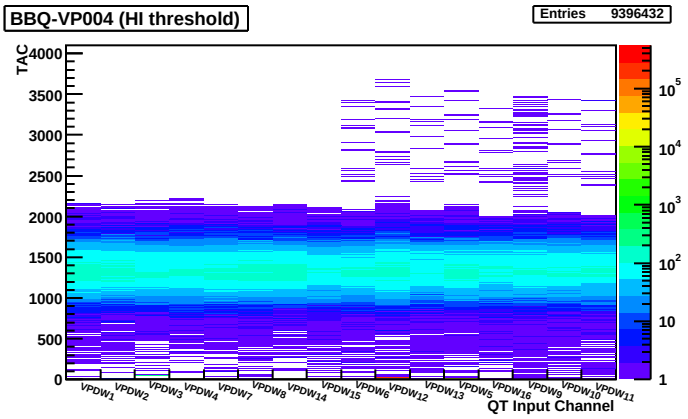

BBQ-VP002 (LO threshold)
Entries 9396432
ADC
QT Input Channel

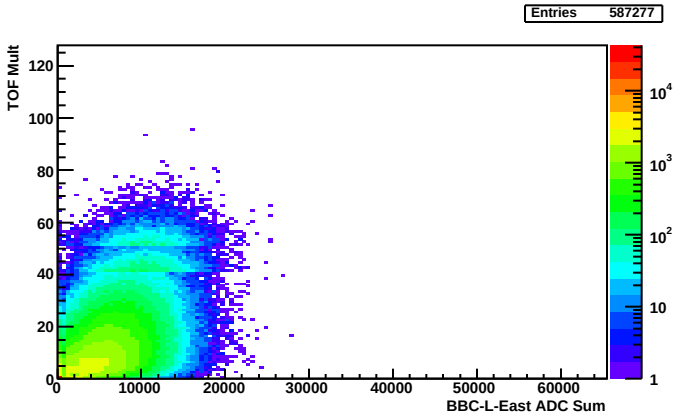

BBQ-VP002 (LO threshold)
Entries 9396432
TAC
QT Input Channel

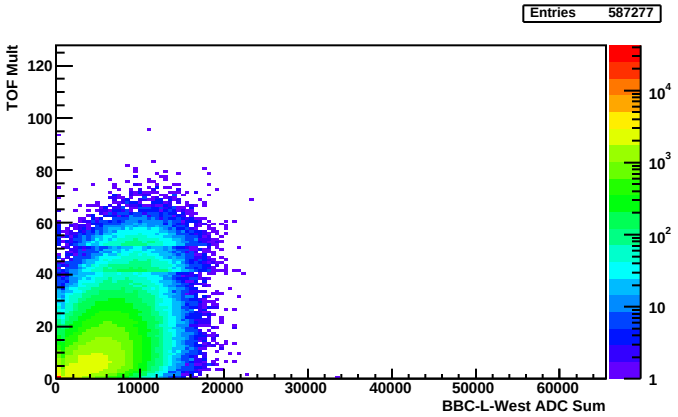

BBQ-VP003 (HI threshold)
Entries 9396432
ADC
QT Input Channel

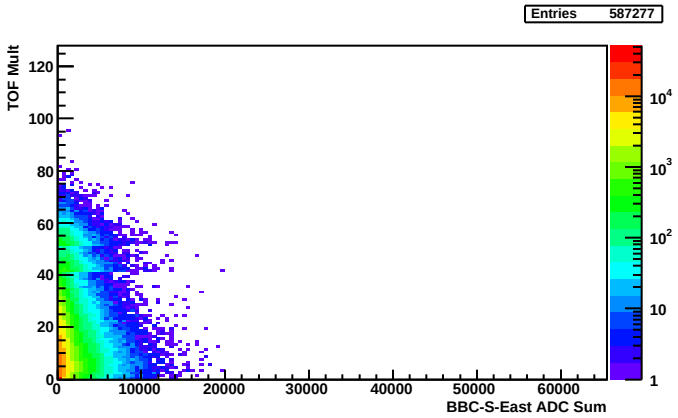

BBQ-VP003 (HI threshold)
Entries 9396432
TAC
QT Input Channel

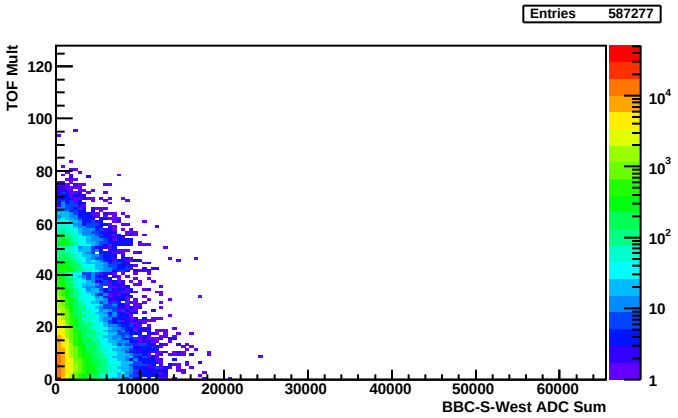

BBQ-VP004 (HI threshold)
Entries 9396432
ADC
QT Input Channel

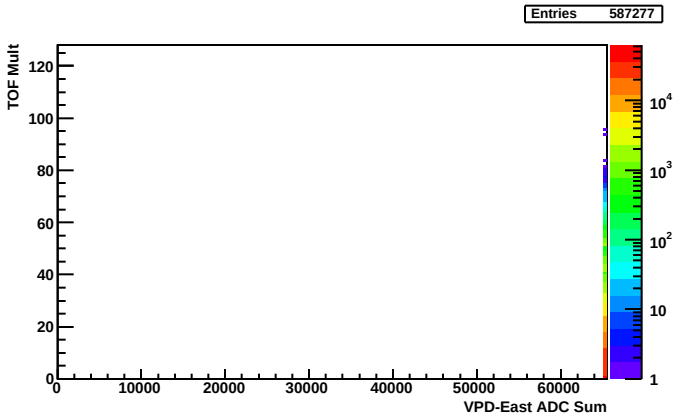

BBQ-VP004 (HI threshold)
Entries 9396432
TAC
QT Input Channel

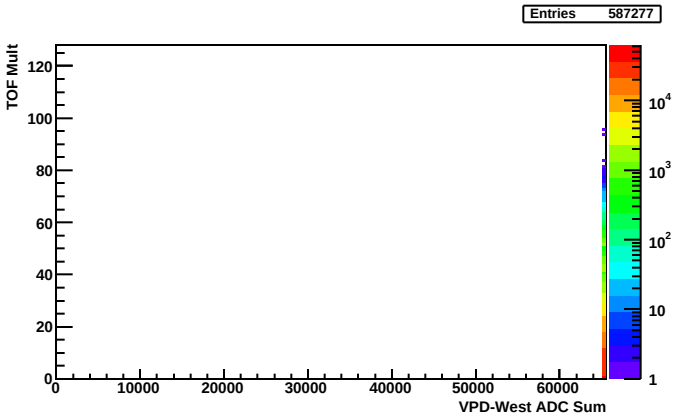

Entries 587277
TOF Mult
BBC-L-East ADC Sum

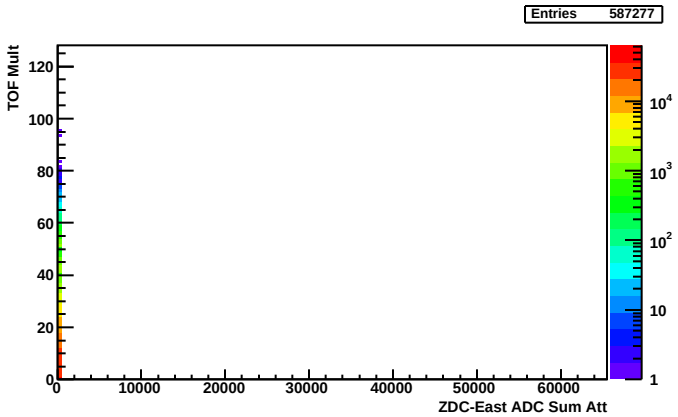

Entries 587277
TOF Mult
BBC-L-West ADC Sum

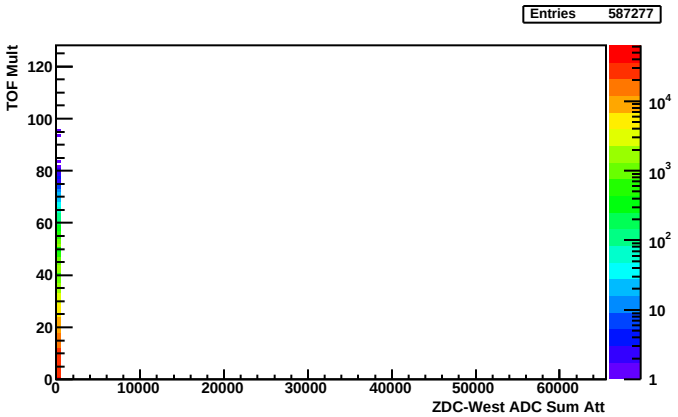

Entries 587277
TOF Mult
BBC-S-East ADC Sum

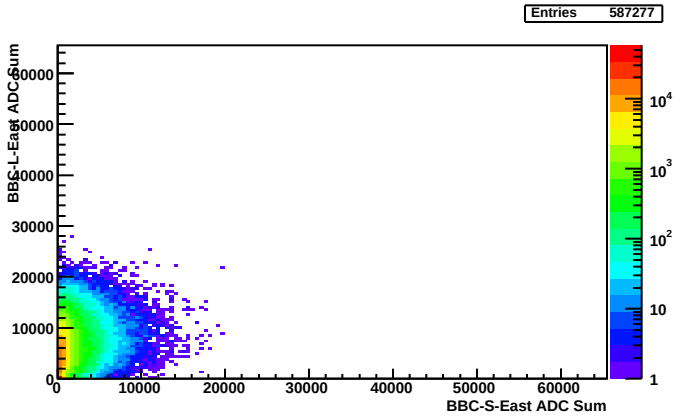

Entries 587277
TOF Mult
BBC-S-West ADC Sum

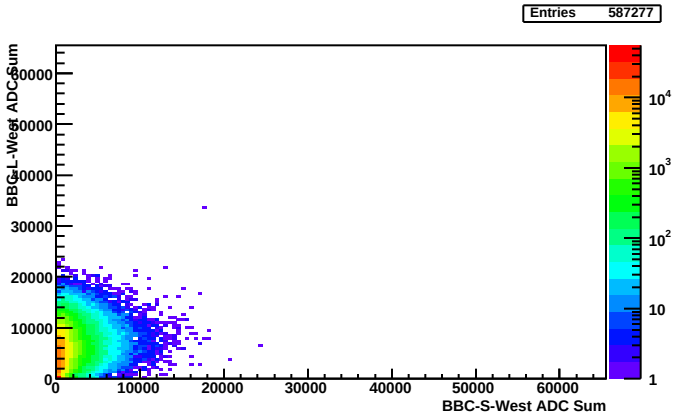

Entries 587277
TOF Mult
VPD-East ADC Sum

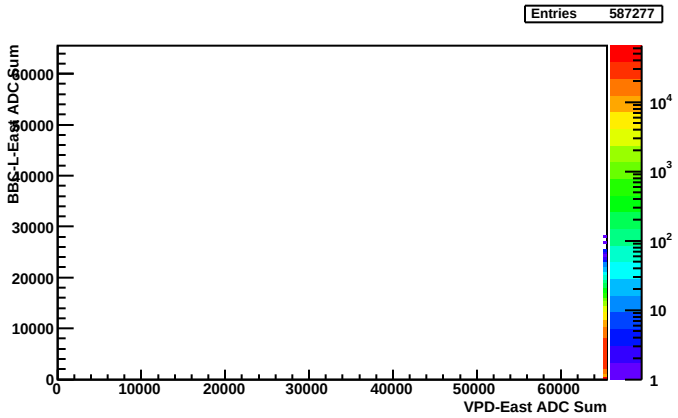

Entries 587277
TOF Mult
VPD-West ADC Sum

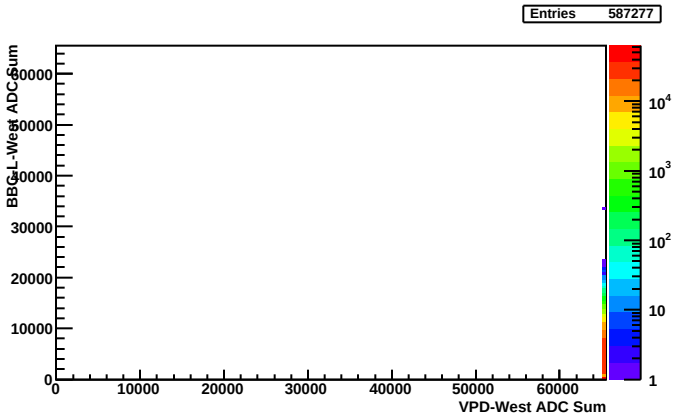

Entries 587277
TOF Mult
ZDC-East ADC Sum Att

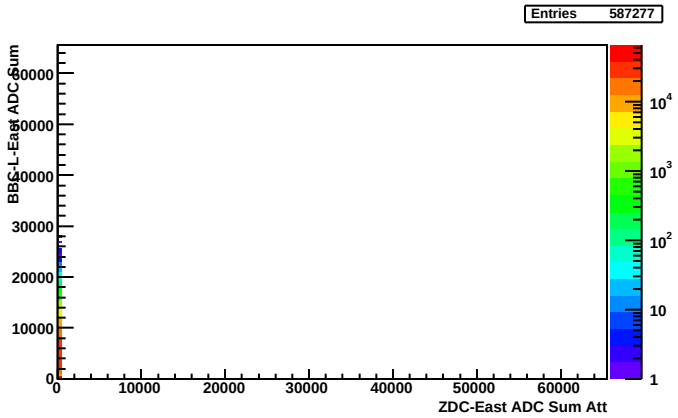

Entries 587277
TOF Mult
ZDC-West ADC Sum Att

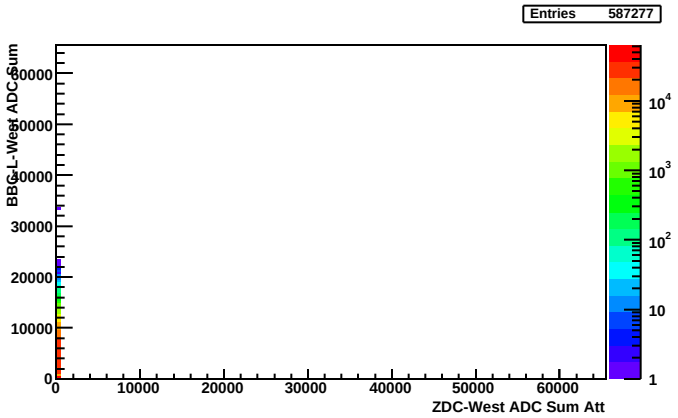

Entries 587277
BBC-L-East ADC Sum
BBC-S-East ADC Sum

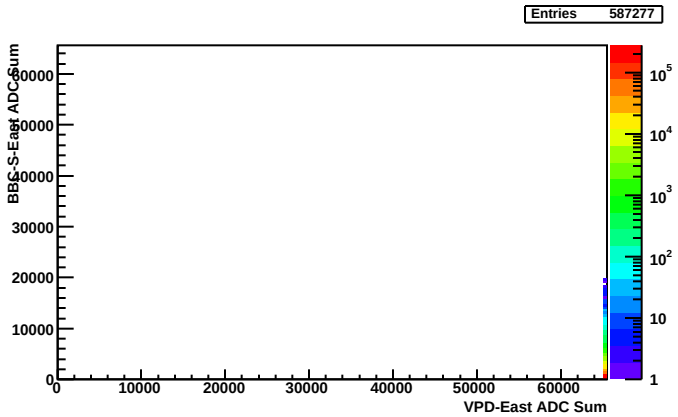

Entries 587277
BBC-L-West ADC Sum
BBC-S-West ADC Sum

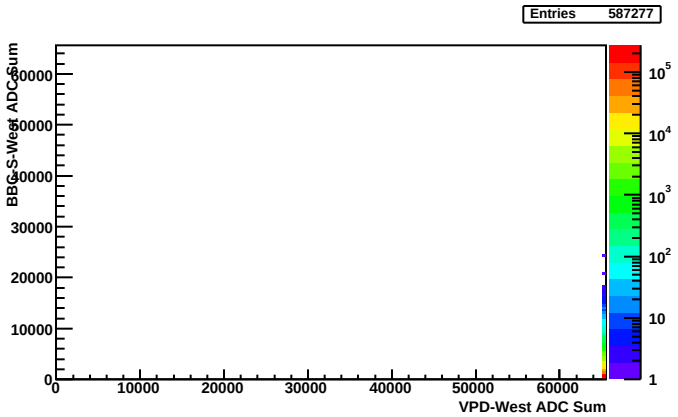

Entries 587277
BBC-L-East ADC Sum
VPD-East ADC Sum

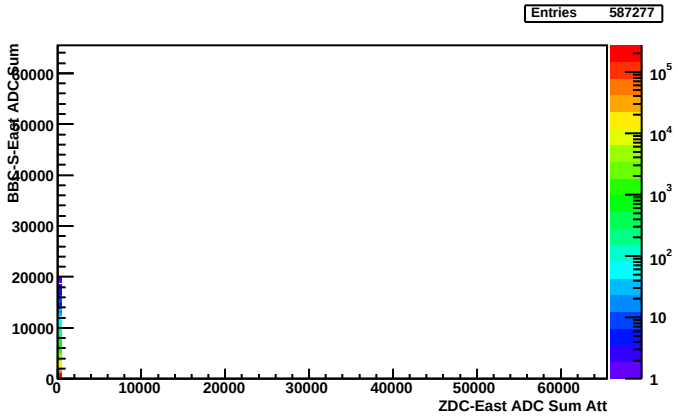

Entries 587277
BBC-L-West ADC Sum
VPD-West ADC Sum

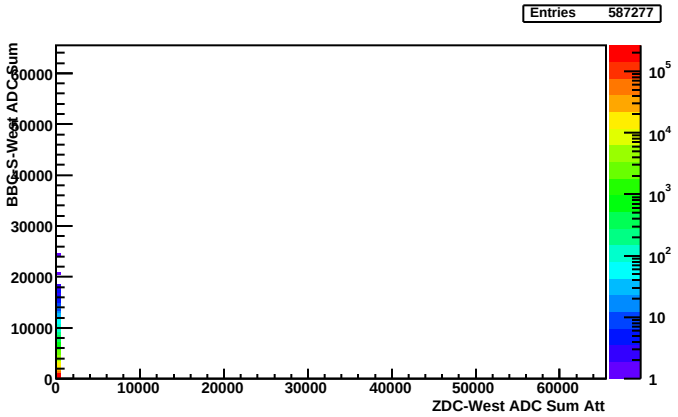

Entries 587277
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

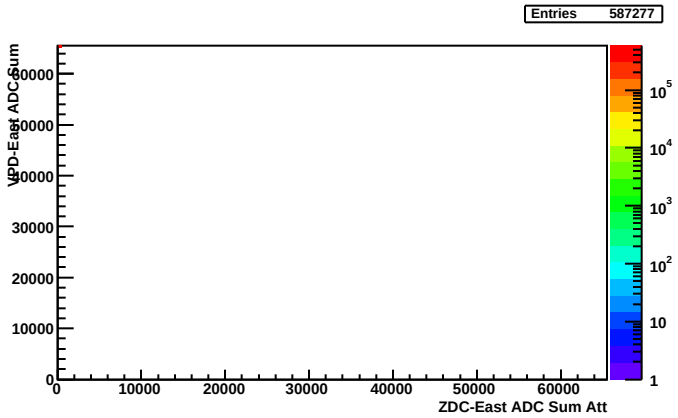

Entries 587277
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

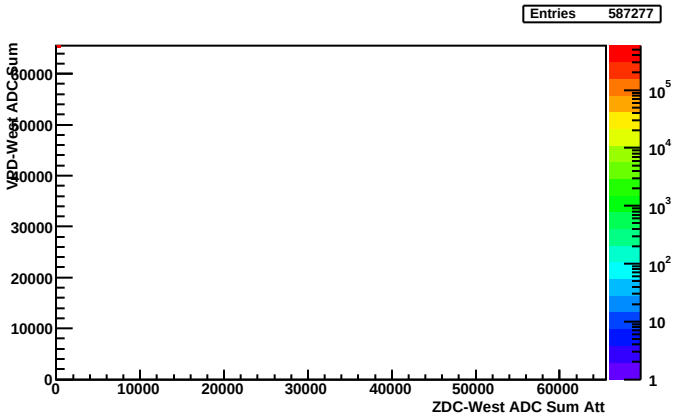

Entries 587277
BBC-S-East ADC Sum
VPD-East ADC Sum

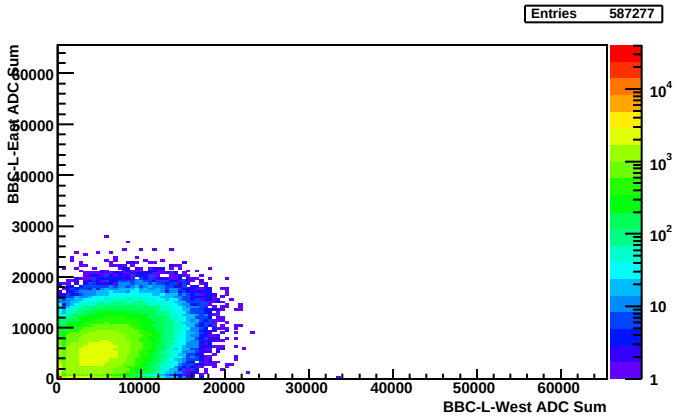

Entries 587277
BBC-S-West ADC Sum
VPD-West ADC Sum

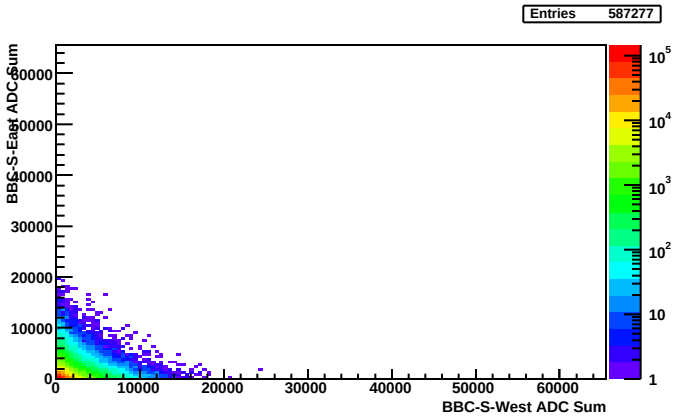

Entries 587277
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

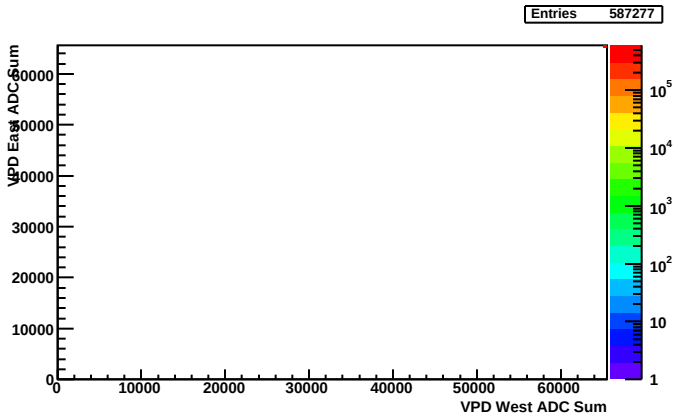

Entries 587277
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

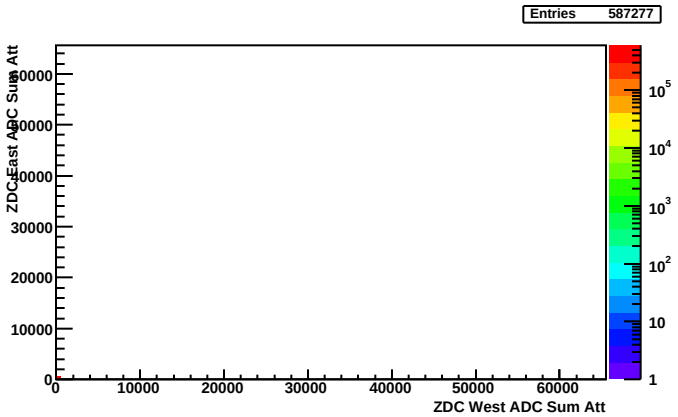

Entries 587277
VPD-East ADC Sum
ZDC-East ADC Sum Att

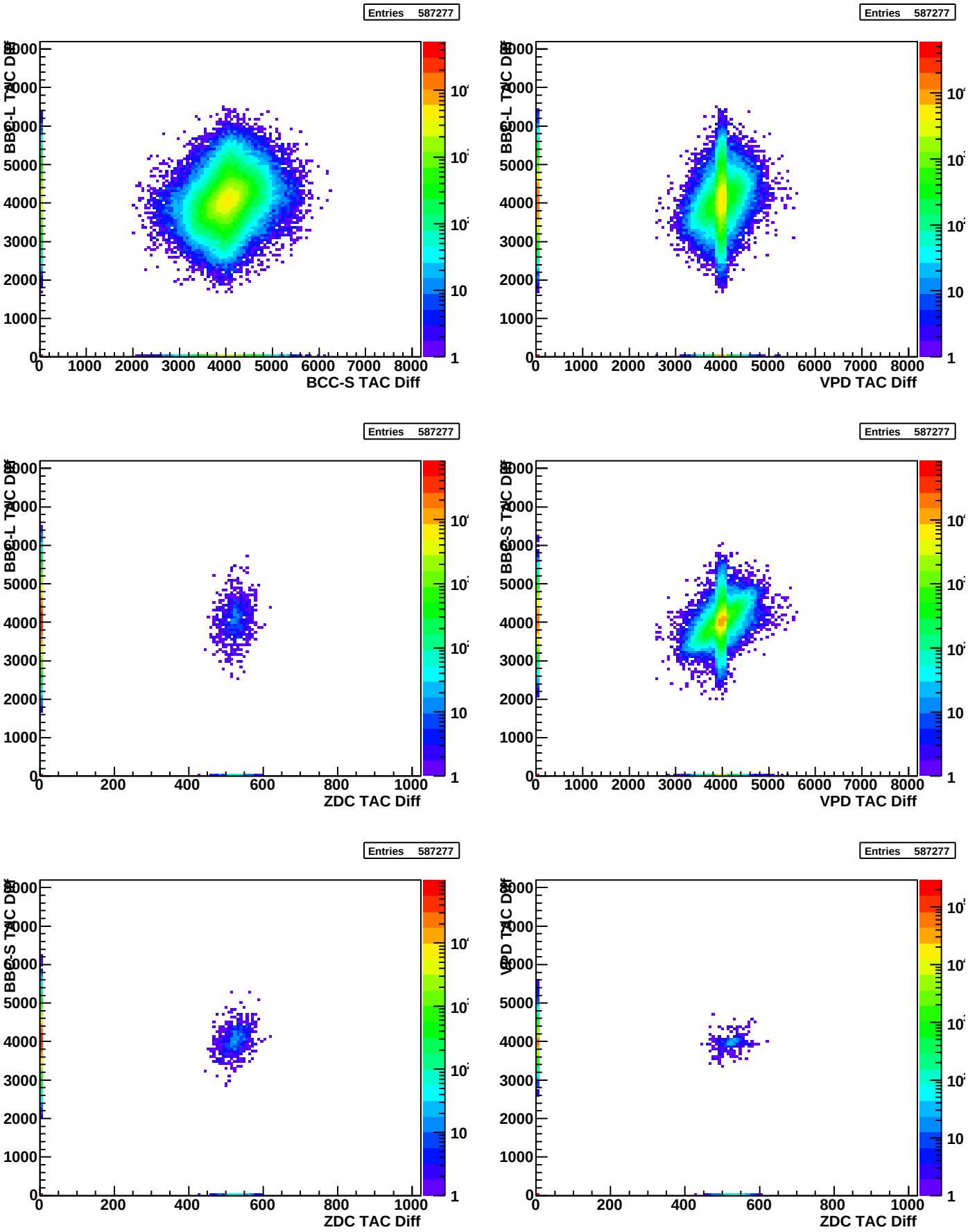

Entries 587277
VPD-West ADC Sum
ZDC-West ADC Sum Att

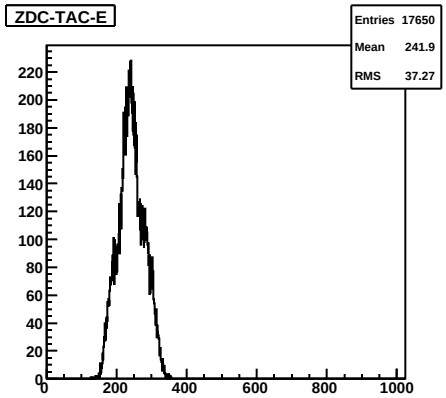

Entries 587277
BBC-L-East ADC Sum
BBC-L-West ADC Sum

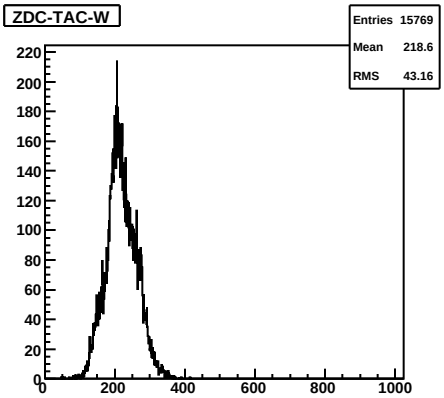

Entries 587277
BBC-S-East ADC Sum
BBC-S-West ADC Sum

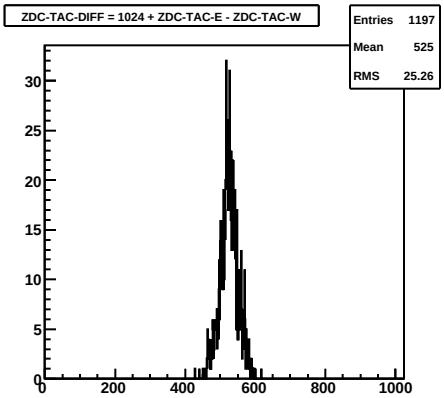

Entries 587277
VPD East ADC Sum
VPD West ADC Sum

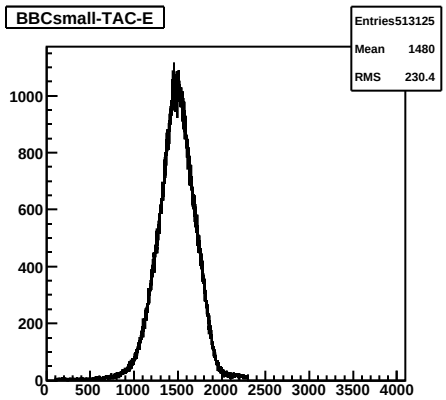

Entries 587277
ZDC East ADC Sum Att
ZDC West ADC Sum Att

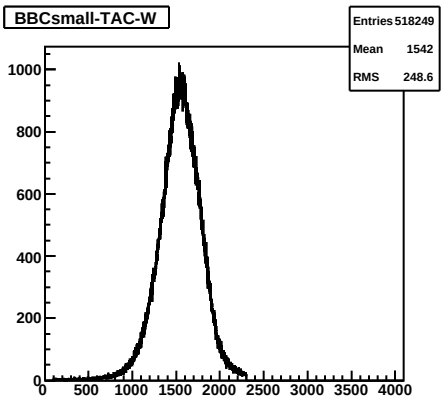

Entries 587277
BBC-L TAC Diff
BCC-S TAC Diff
Entries 587277
BBC-L TAC Diff
VPD TAC Diff
Entries 587277
BBC-L TAC Diff
ZDC TAC Diff
Entries 587277
BBC-S TAC Diff
VPD TAC Diff
Entries 587277
BBC-S TAC Diff
ZDC TAC Diff
Entries 587277
VPD TAC Diff
ZDC TAC Diff

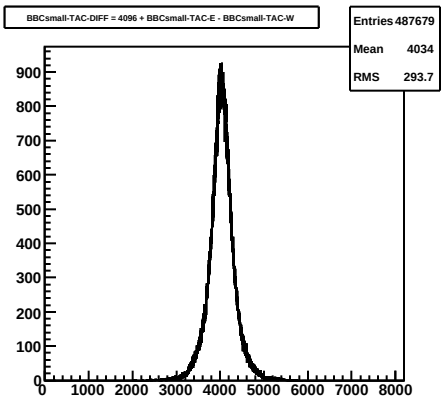

ZDC-TAC-E
Entries 17650
Mean 241.9
RMS 37.27

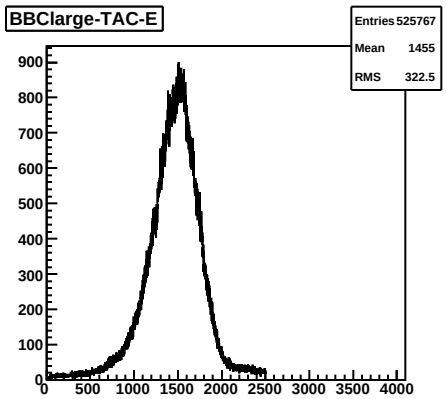

ZDC-TAC-W
Entries 15769
Mean 218.6
RMS 43.16

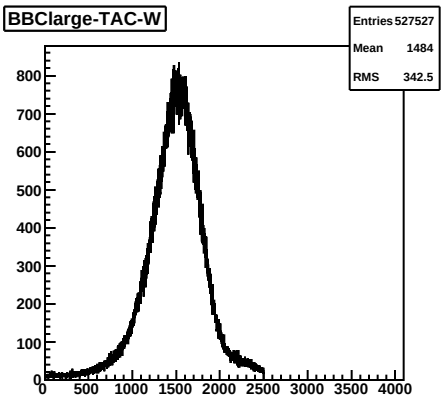

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1197
Mean 525
RMS 25.26

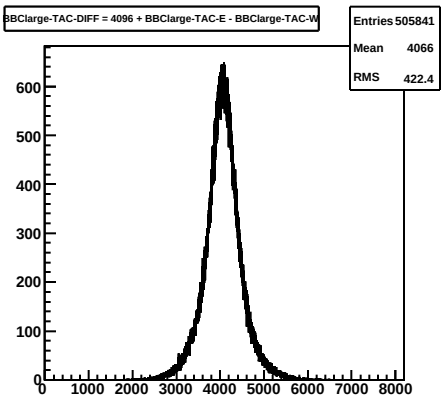

BBCsmall-TAC-E
Entries 513125
Mean 1480
RMS 230.4

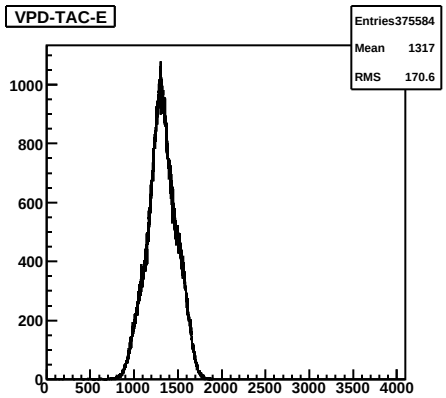

BBCsmall-TAC-W
Entries 518249
Mean 1542
RMS 248.6

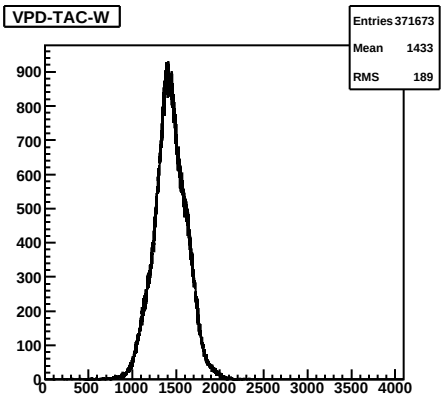

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 487679
Mean 4034
RMS 293.7

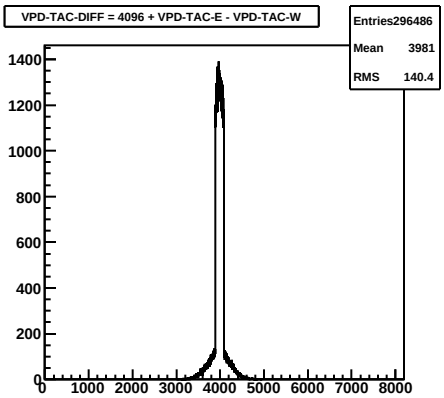

BBClarge-TAC-E
Entries 525767
Mean 1455
RMS 322.5

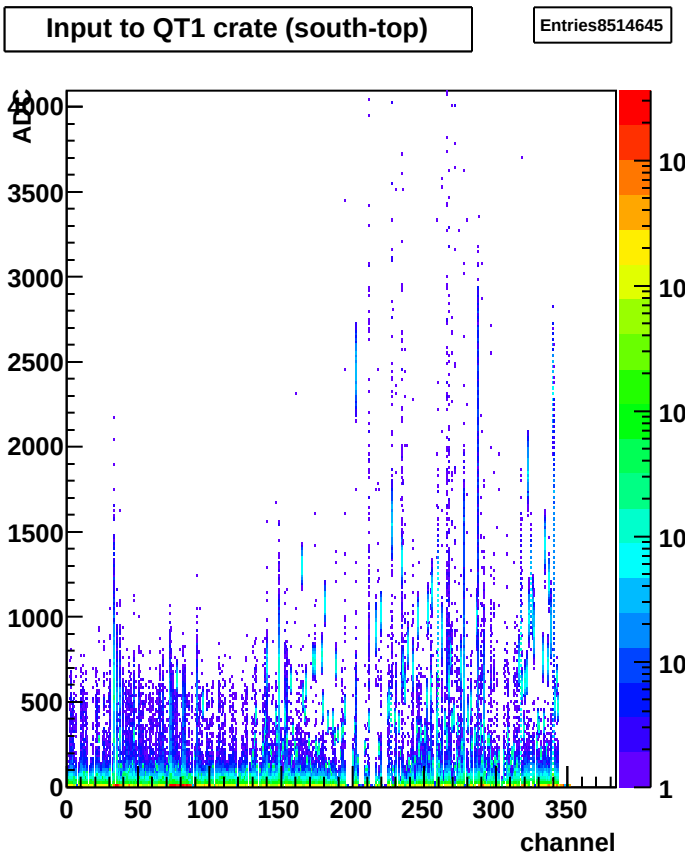

BBClarge-TAC-W
Entries 527527
Mean 1484
RMS 342.5

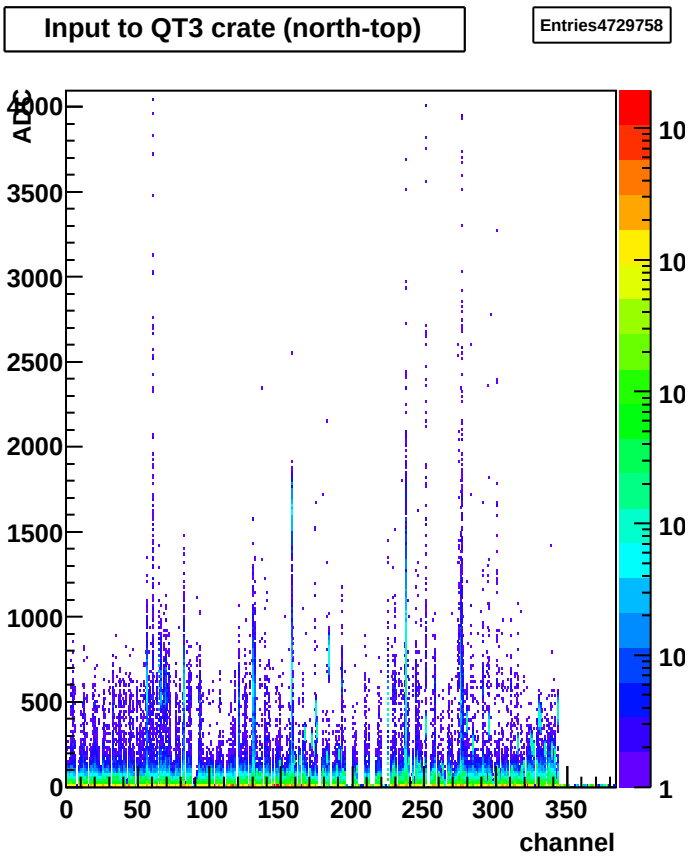

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 505841
Mean 4066
RMS 422.4

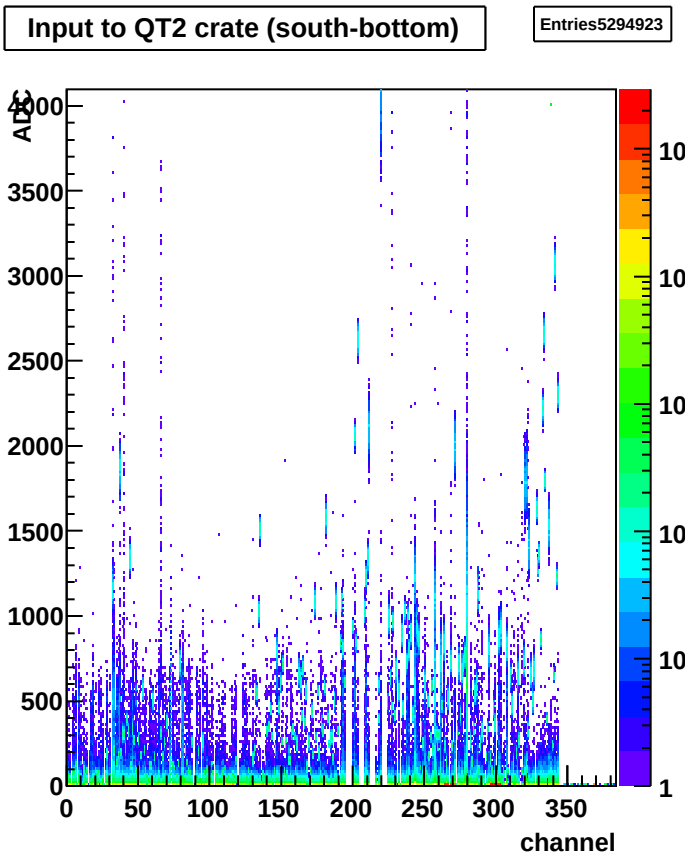

VPD-TAC-E
Entries 375584
Mean 1317
RMS 170.6

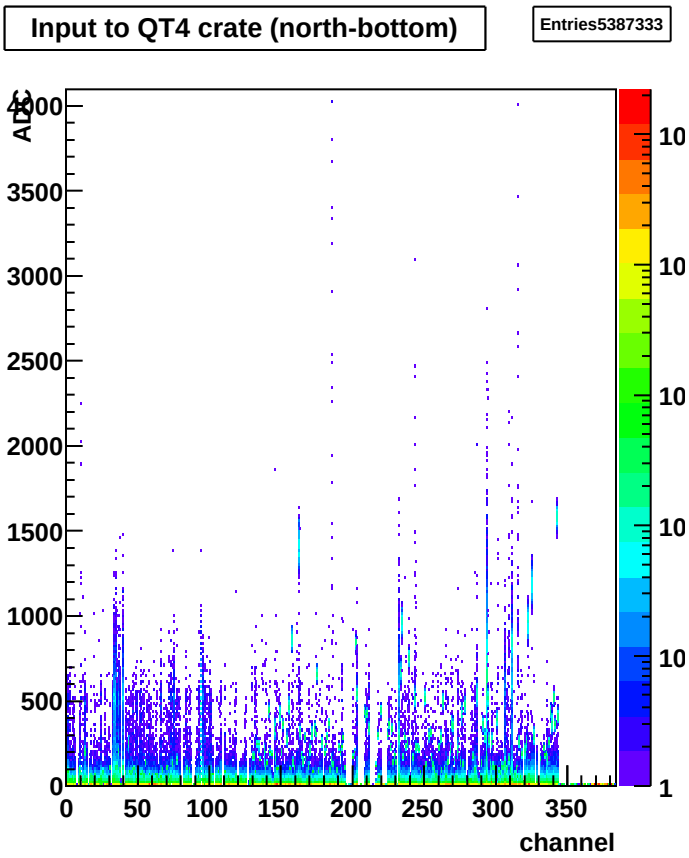

VPD-TAC-W
Entries 371673
Mean 1433
RMS 189

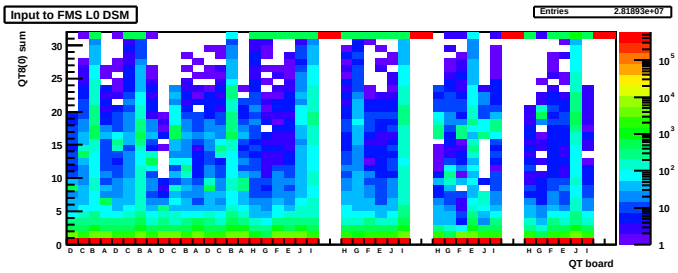

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 296486
Mean 3981
RMS 140.4

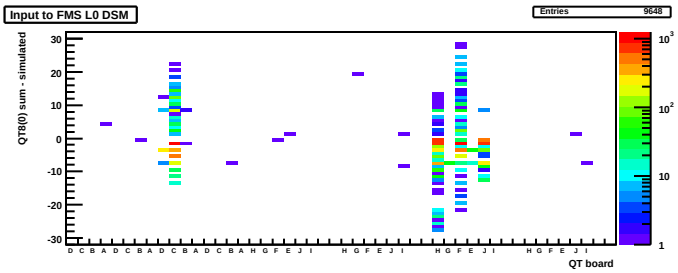

Input to QT1 crate (south-top)
Entries8514645
ADC
channel

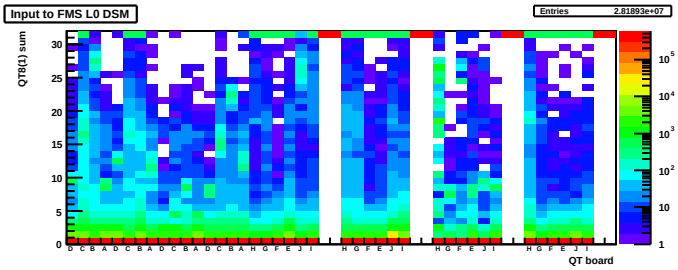

Input to QT3 crate (north-top)
Entries4729758
ADC
channel

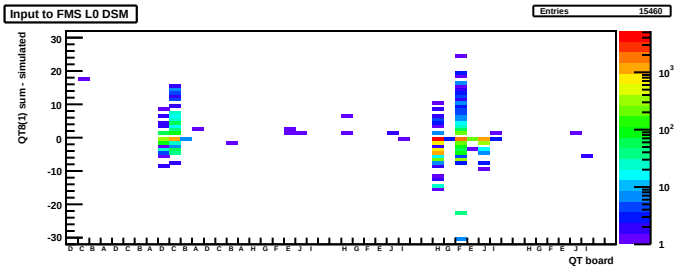

Input to QT2 crate (south-bottom)
Entries5294923
ADC
channel

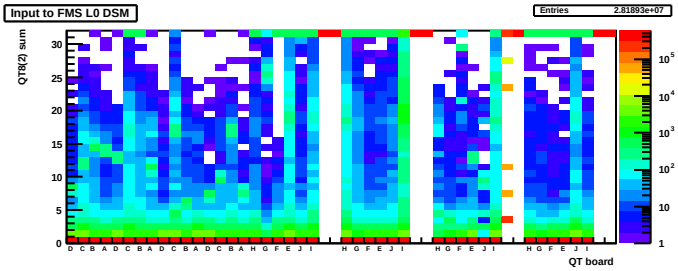

Input to QT4 crate (north-bottom)
Entries5387333
ADC
channel

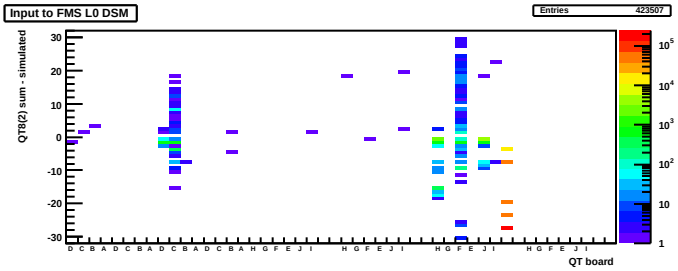

Input to FMS L0 DSM
Entries 2.81893e+07
QT8(0) sum
QT board

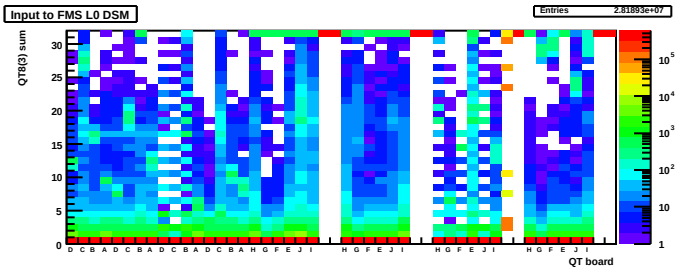

Input to FMS L0 DSM
Entries 9648
QT8(0) sum - simulated
QT board

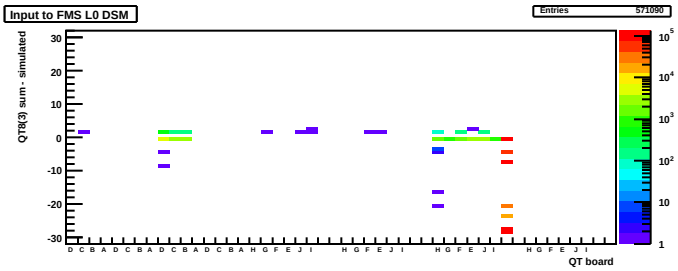

Input to FMS L0 DSM
Entries 2.81893e+07
QT8(1) sum
QT board

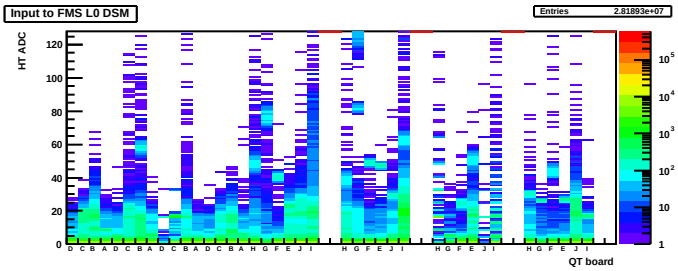

Input to FMS L0 DSM
Entries 15460
QT8(1) sum - simulated
QT board

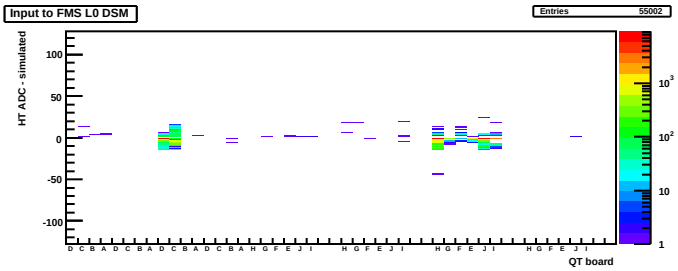

Input to FMS L0 DSM
Entries 2.81893e+07
QT8(2) sum
QT board

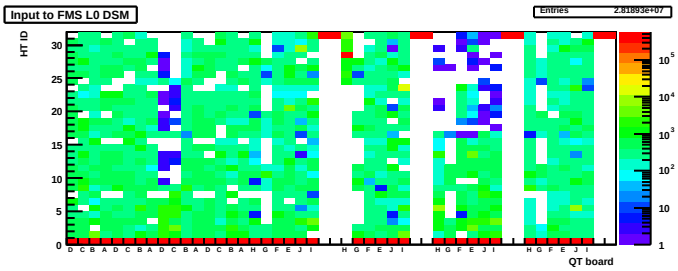

Input to FMS L0 DSM
Entries 423507
QT8(2) sum - simulated
QT board

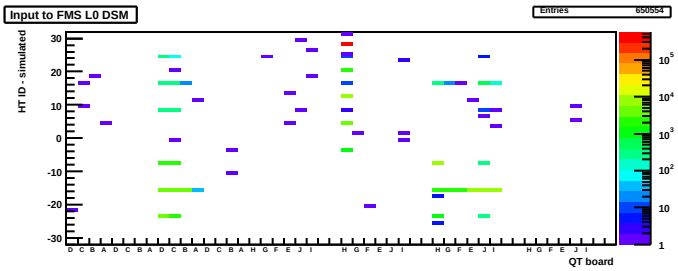

Input to FMS L0 DSM
Entries 2.81893e+07
QT8(3) sum
QT board

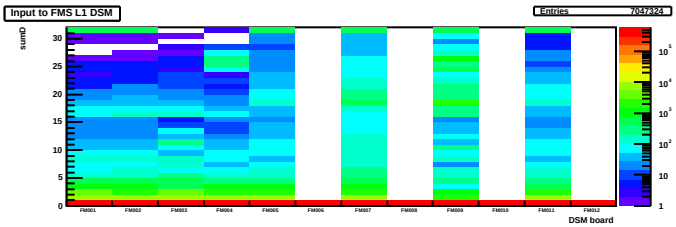

Input to FMS L0 DSM
Entries 571090
QT8(3) sum - simulated
QT board

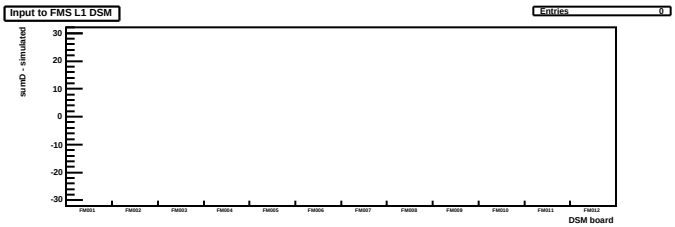

Input to FMS L0 DSM
Entries 2.81893e+07
HT ADC
QT board

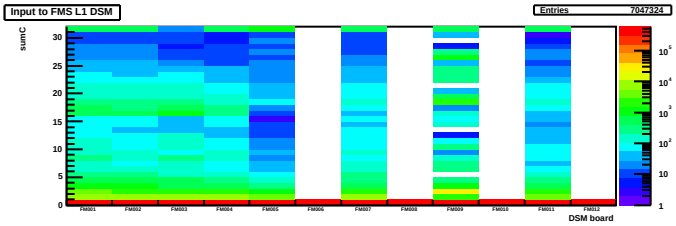

Input to FMS L0 DSM
Entries 55002
HT ADC - simulated
QT board

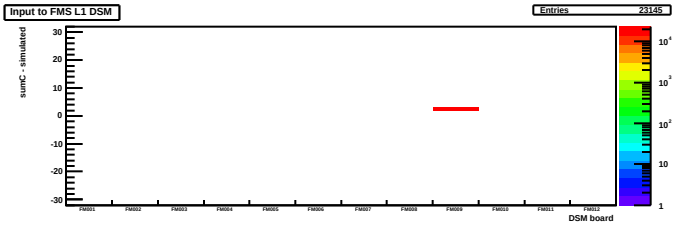

Input to FMS L0 DSM
Entries 2.81893e+07
HT ID
QT board

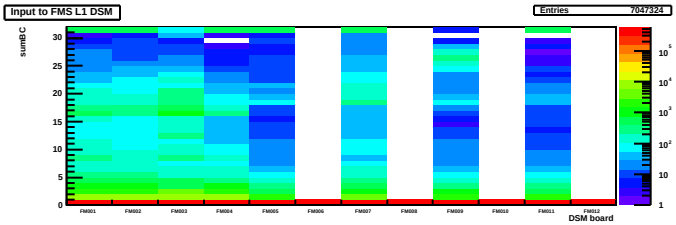

Input to FMS L0 DSM
Entries 650554
HT ID - simulated
QT board

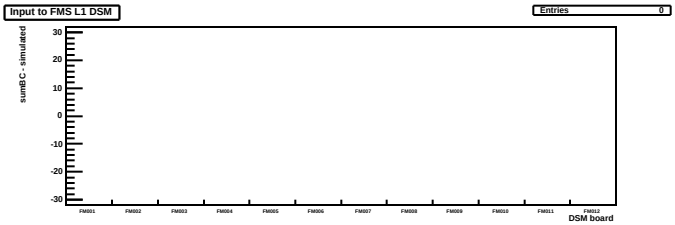

Input to FMS L1 DSM
Entries 7047324
sumD
DSM board

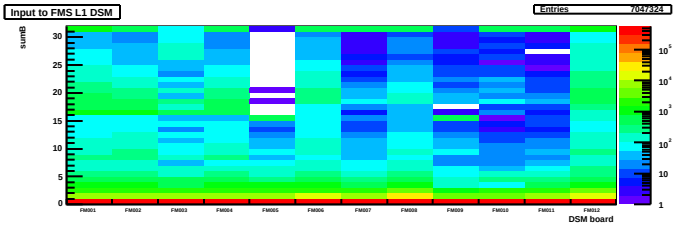

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

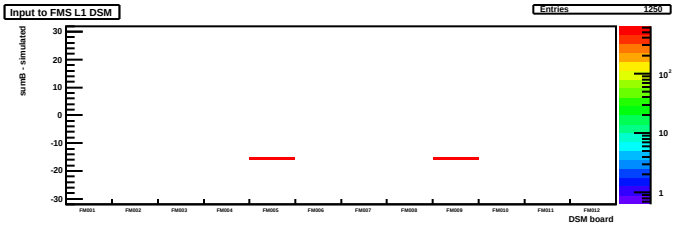

Input to FMS L1 DSM
Entries 7047324
sumC
DSM board

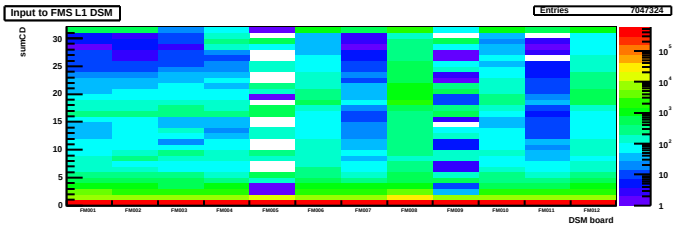

Input to FMS L1 DSM
Entries 23145
sumC - simulated
DSM board

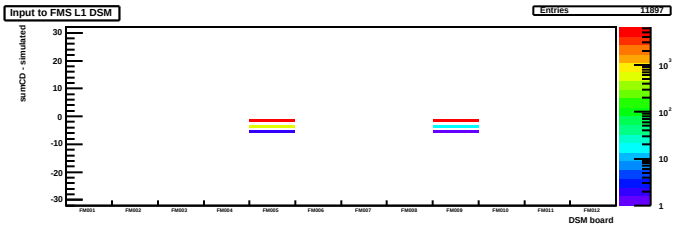

Input to FMS L1 DSM
Entries 7047324
sumBC
DSM board

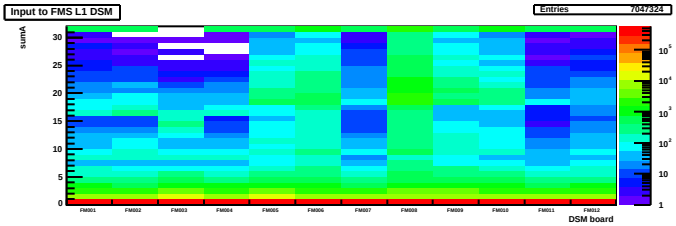

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

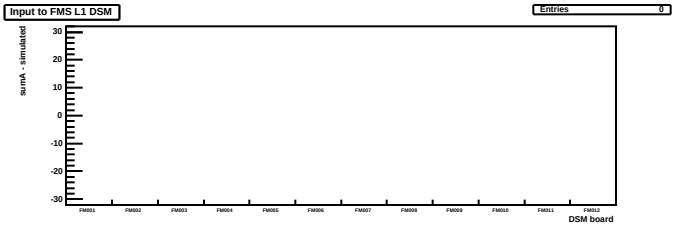

Input to FMS L1 DSM
Entries 7047324
sumB
DSM board

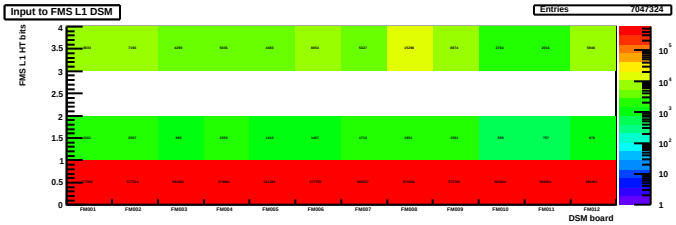

Input to FMS L1 DSM
Entries 1250
sumB - simulated
DSM board

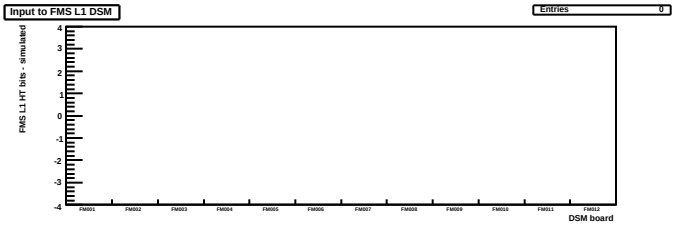

Input to FMS L1 DSM
Entries 7047324
sumCD
DSM board

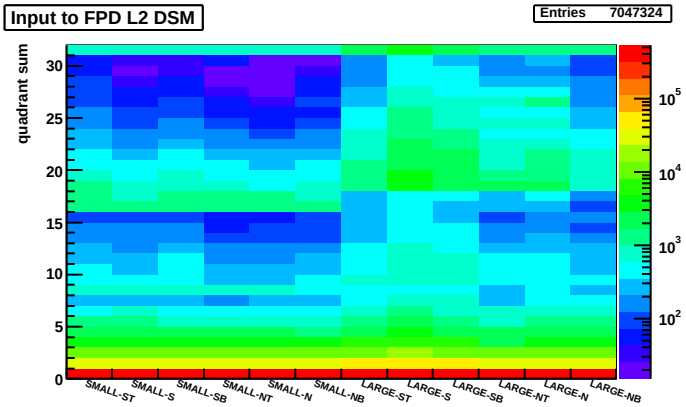

Input to FMS L1 DSM
Entries 11897
sumCD - simulated
DSM board

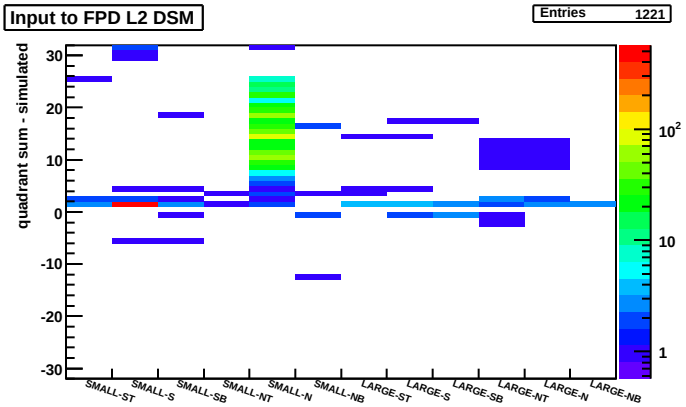

Input to FMS L1 DSM
Entries 7047324
sumA
DSM board

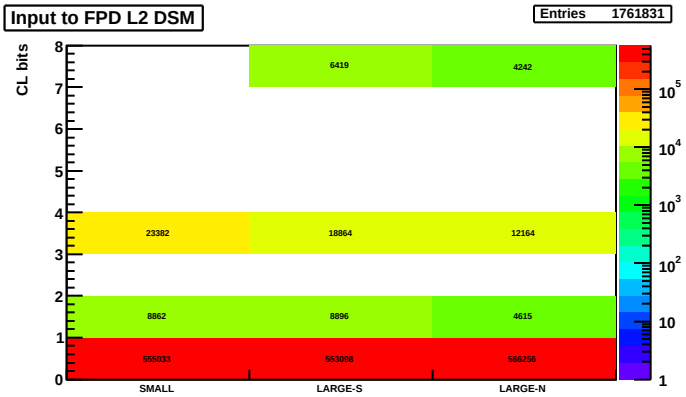

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

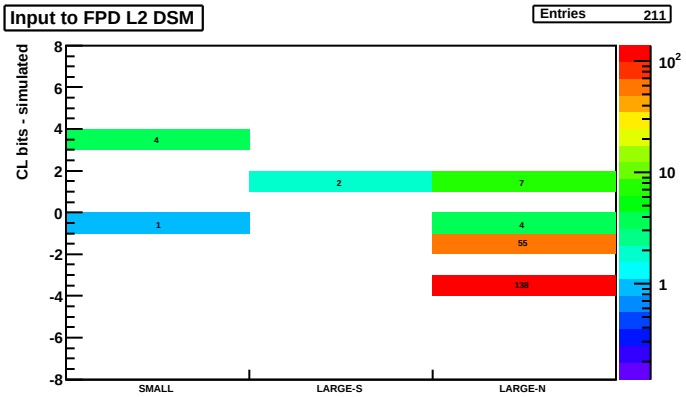

Input to FMS L1 DSM
Entries 7047324
FMS L1 HT bits
DSM board

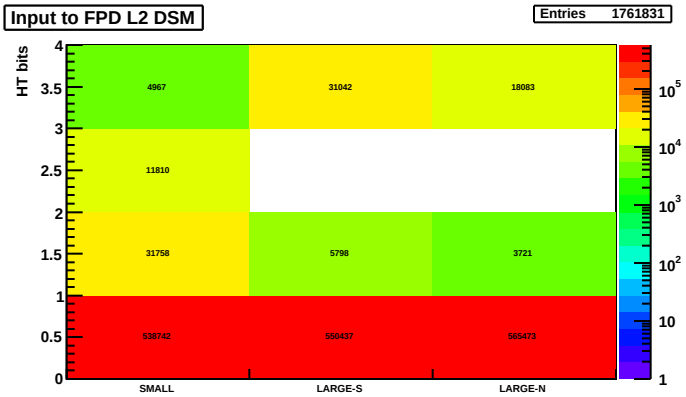

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

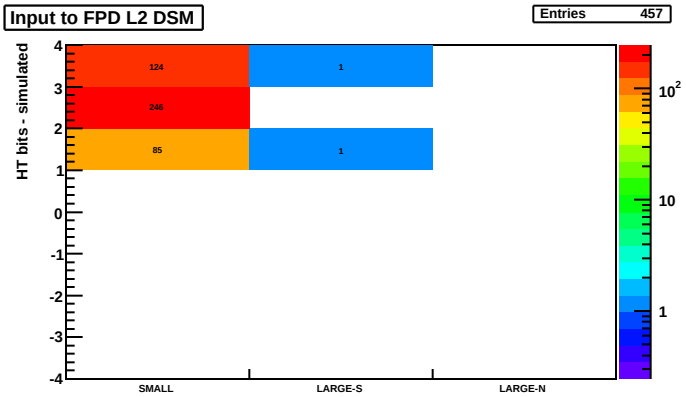

Input to FPD L2 DSM
Entries 7047324
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

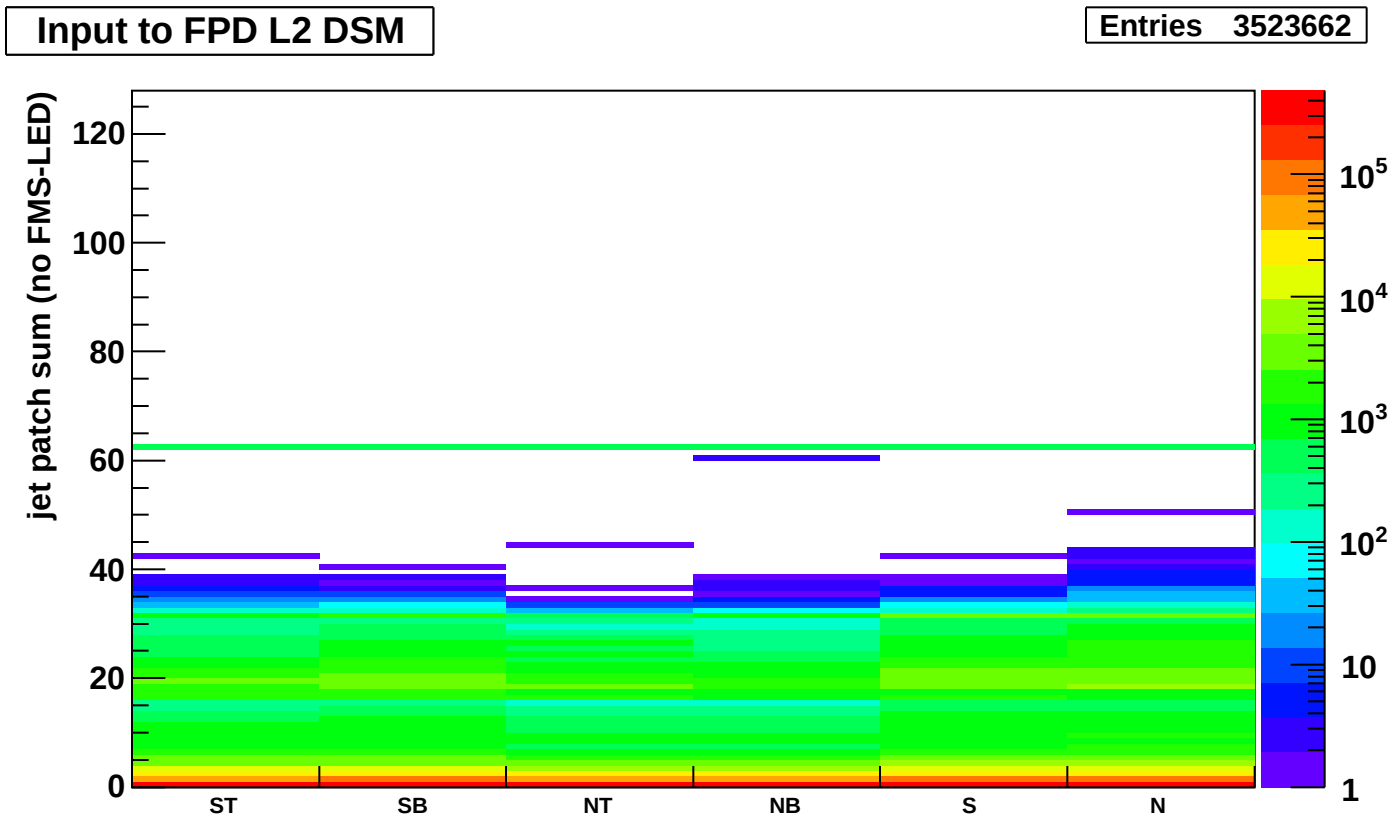

Input to FPD L2 DSM
Entries 1221
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

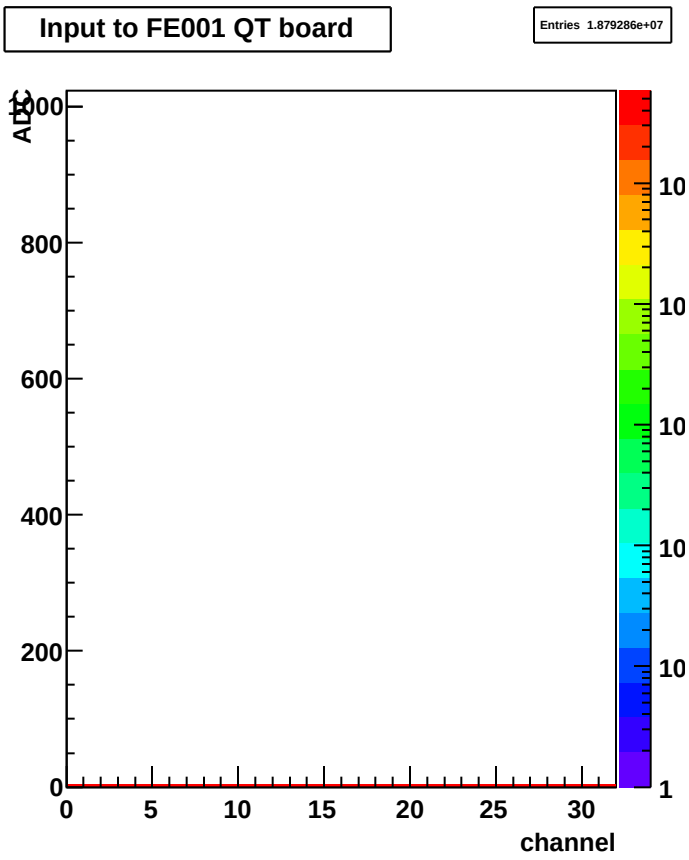

Input to FPD L2 DSM
Entries 1761831
CL bits
6419
4242
23382
18864
12164
8862
8896
4615
555033
553098
566256
SMALL
LARGE-S
LARGE-N

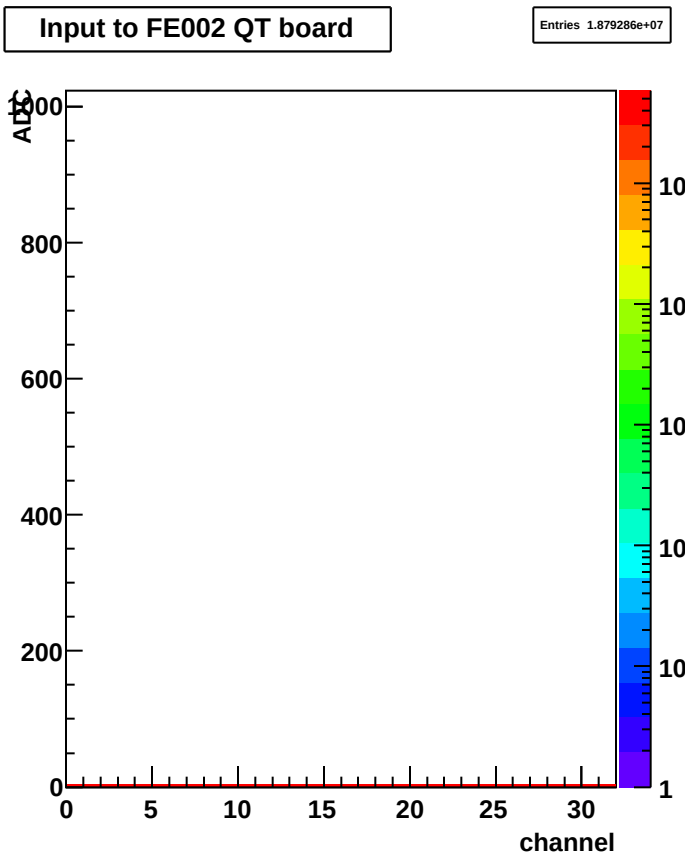

Input to FPD L2 DSM
Entries 211
CL bits - simulated
4
2
7
1
4
55
138
SMALL
LARGE-S
LARGE-N

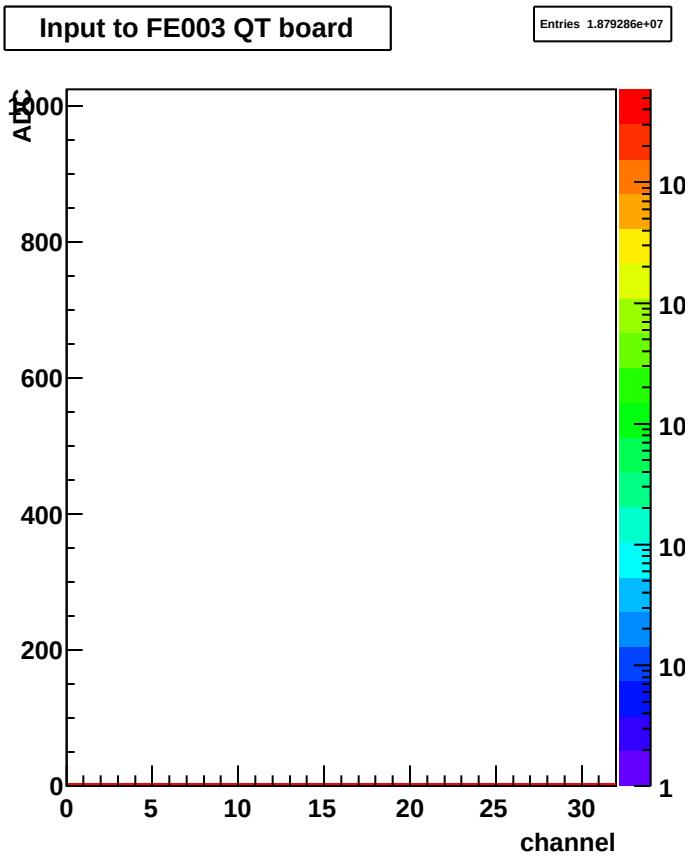

Input to FPD L2 DSM
Entries 1761831
HT bits
4967
31042
18083
11810
31758
5798
3721
538742
550437
565473
SMALL
LARGE-S
LARGE-N

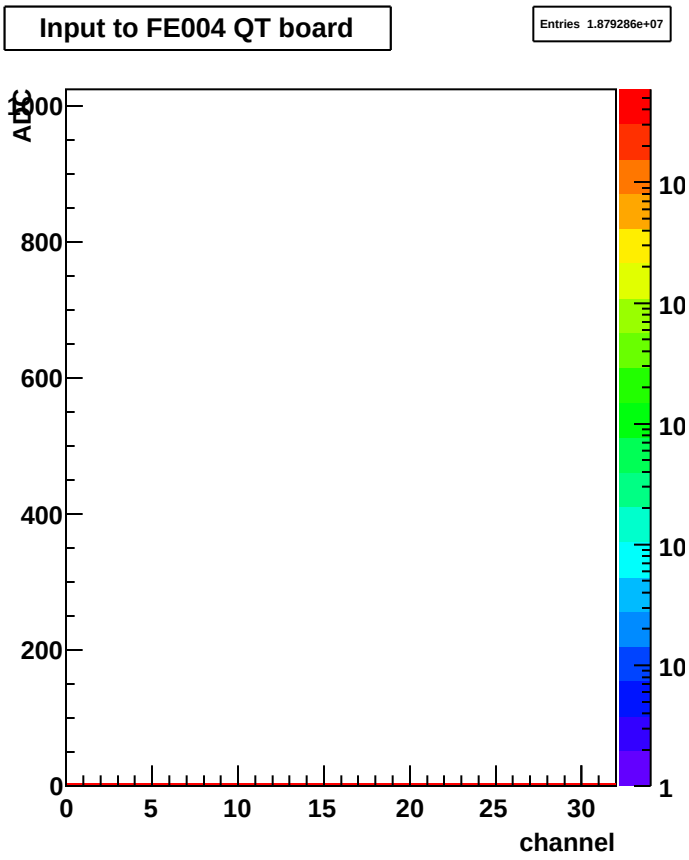

Input to FPD L2 DSM
Entries 457
HT bits - simulated
124
1
246
85
1
SMALL
LARGE-S
LARGE-N

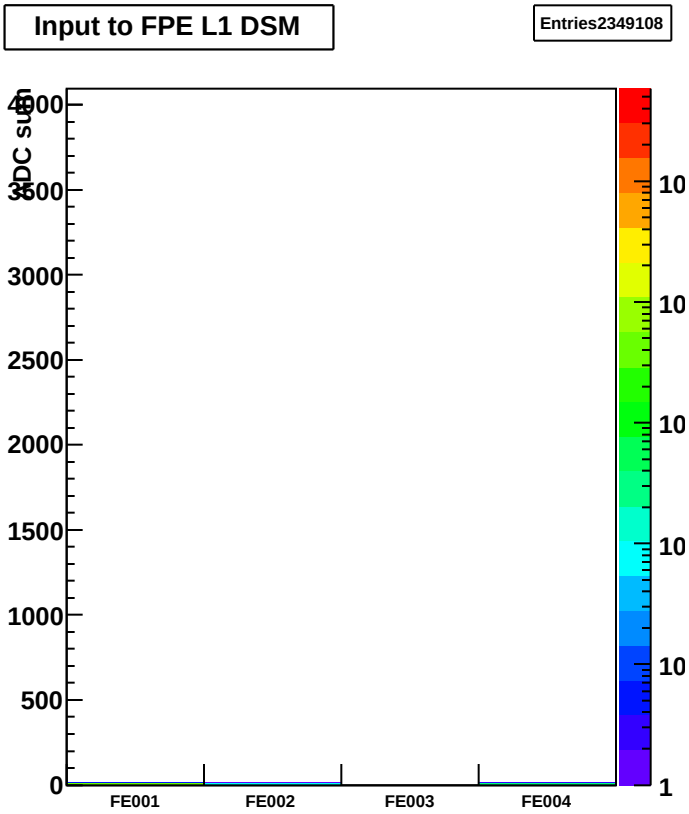

Input to FPD L2 DSM
Entries 3523662
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

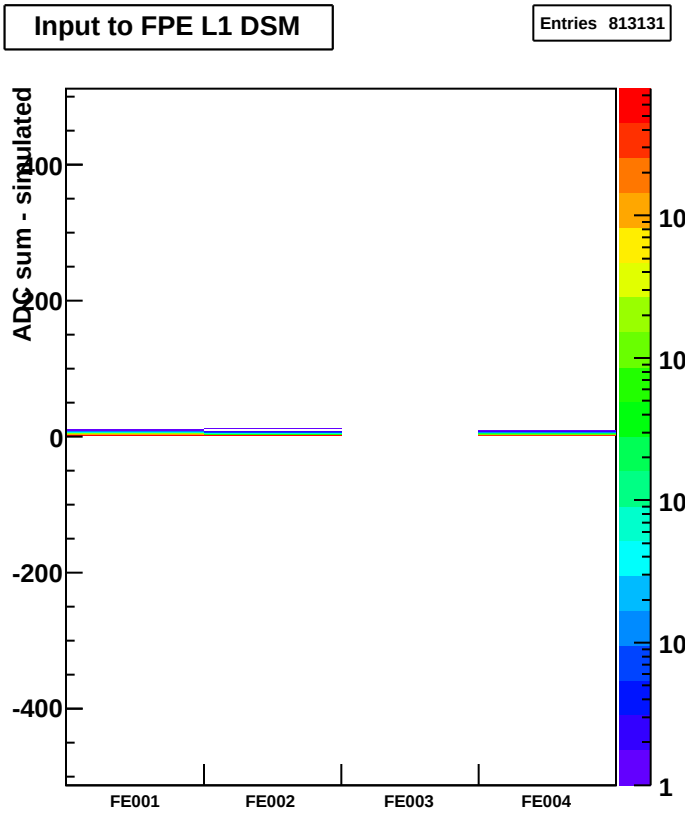

Input to FE001 QT board
Entries 1.879286e+07
ADC
channel


Input to FE002 QT board
Entries 1.879286e+07
ADC
channel

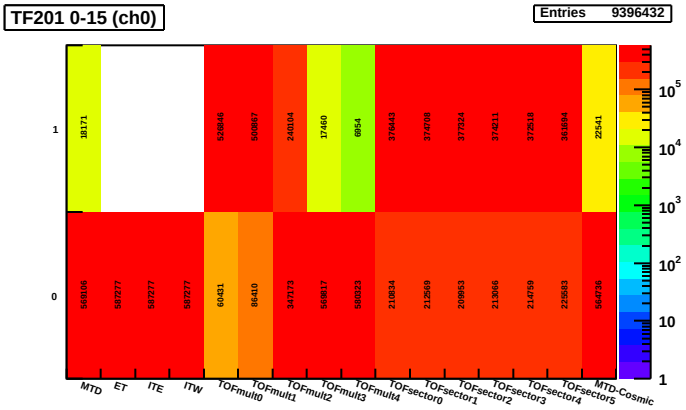

Input to FE003 QT board
Entries 1.879286e+07
ADC
channel


Input to FE004 QT board
Entries 1.879286e+07
ADC
channel

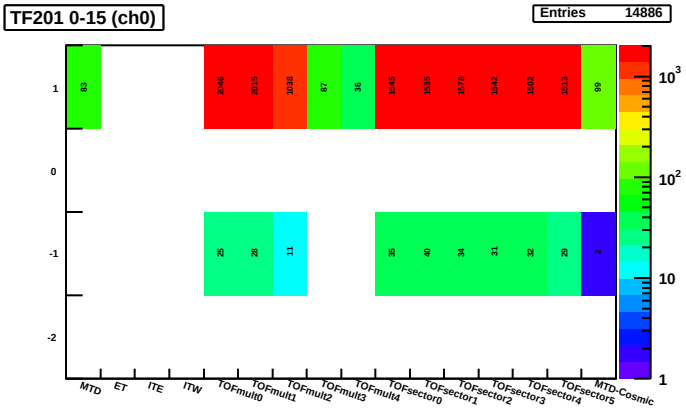

Input to FPE L1 DSM
Entries2349108
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 813131
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

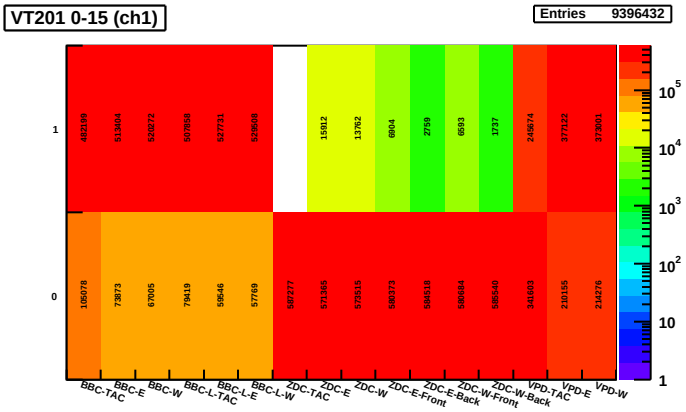
TF201 0-15 (ch0)



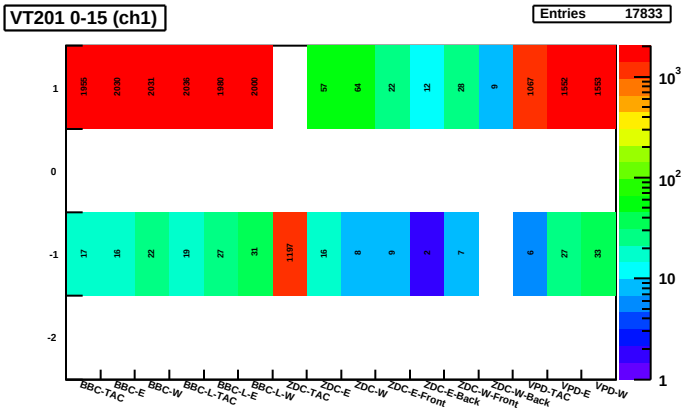
TF201 0-15 (ch0)



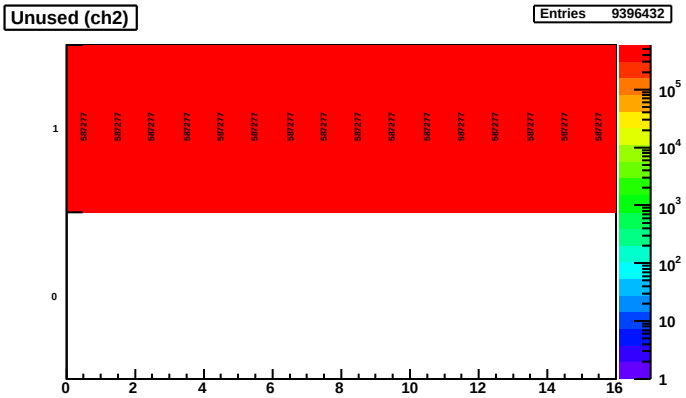
VT201 0-15 (ch1)

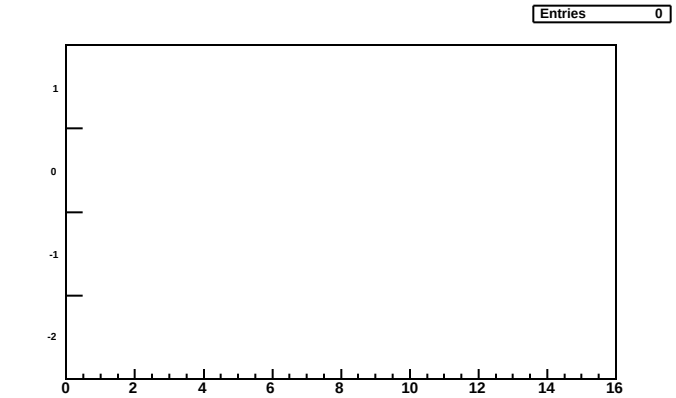


VT201 0-15 (ch1)

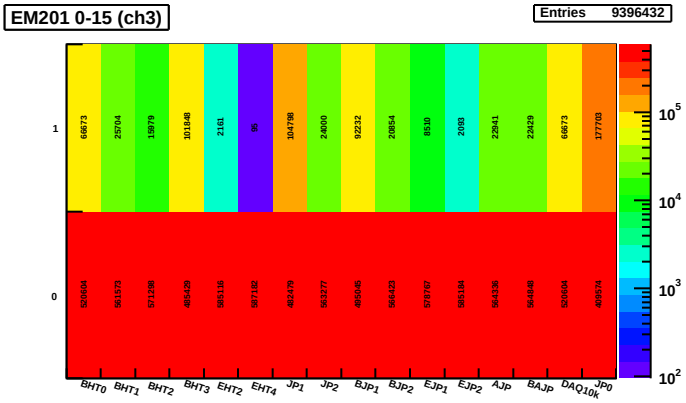


Unused (ch2)

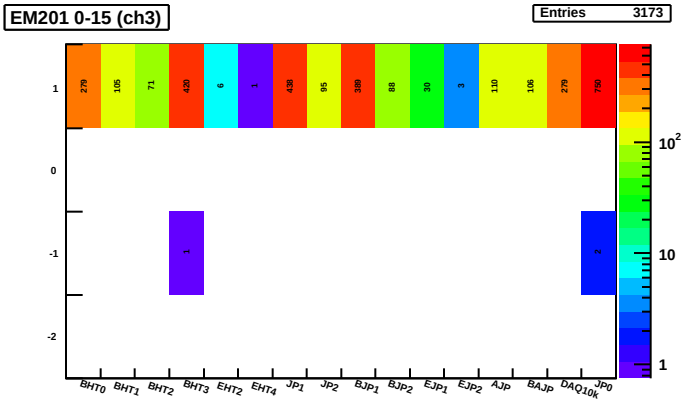




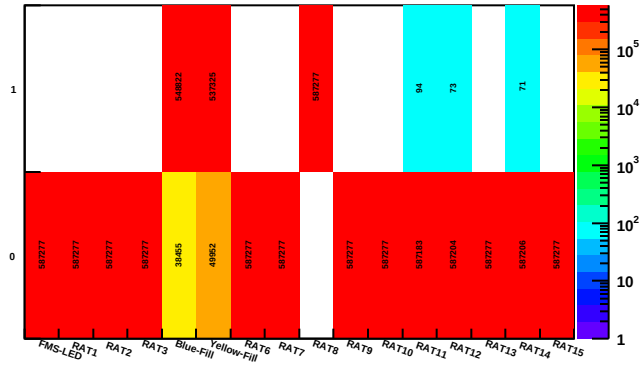
EM201 0-15 (ch3)

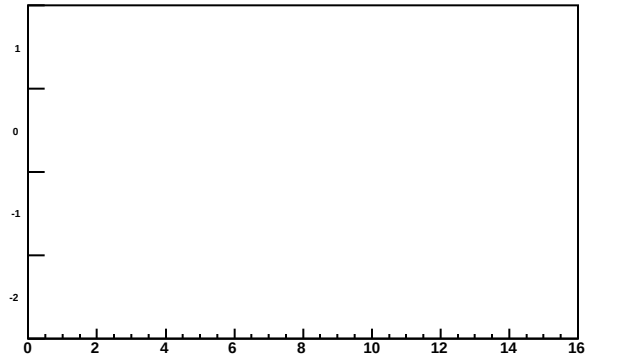


EM201 0-15 (ch3)

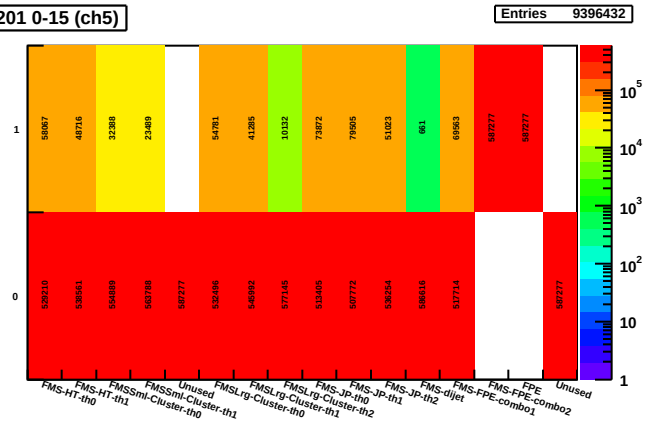


RAT board (ch4)

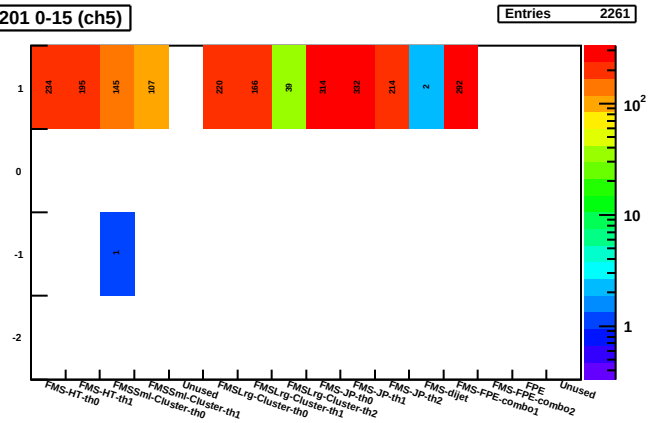




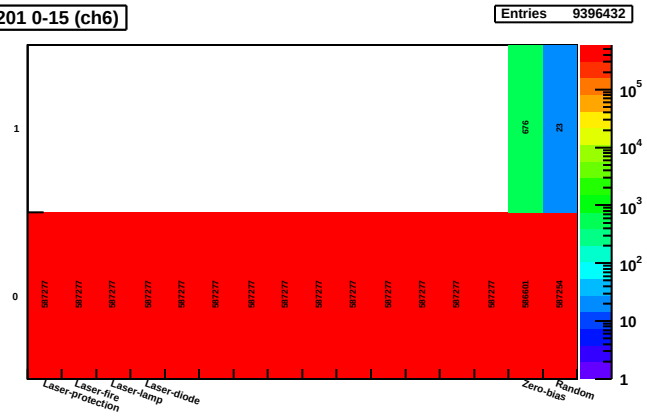
FP201 0-15 (ch5)

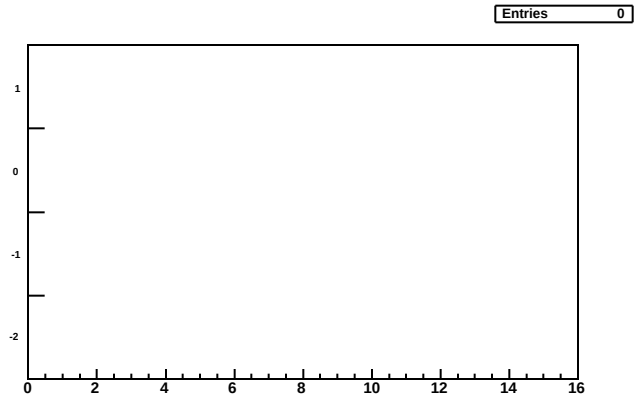
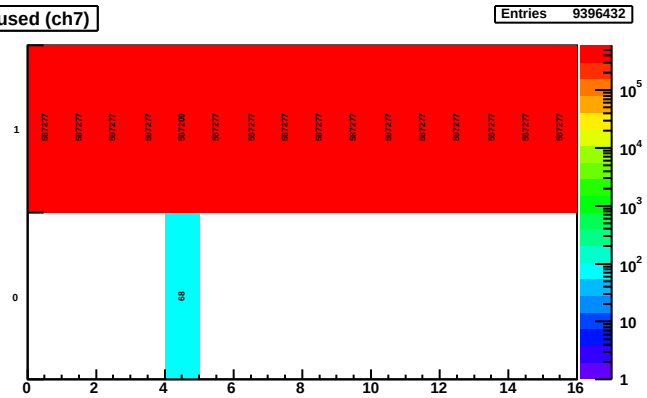
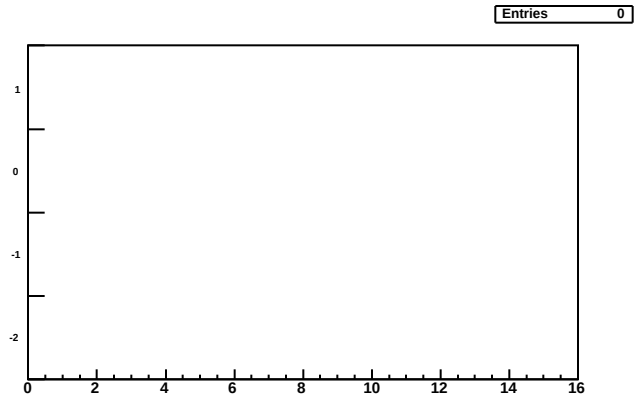


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)