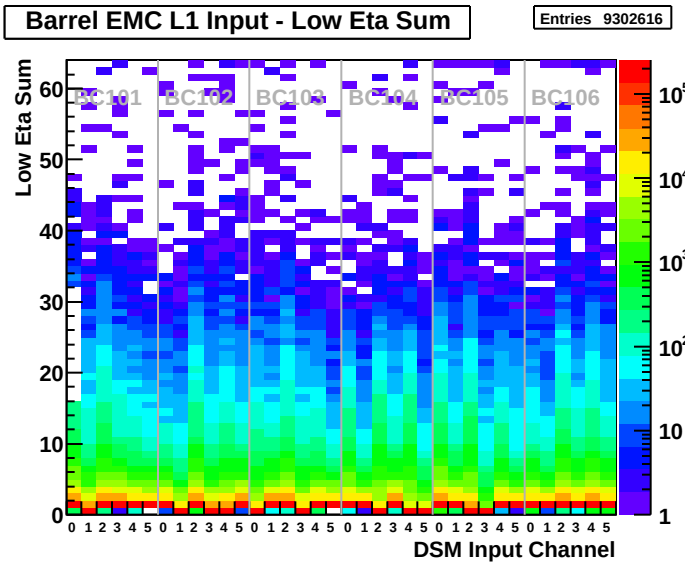
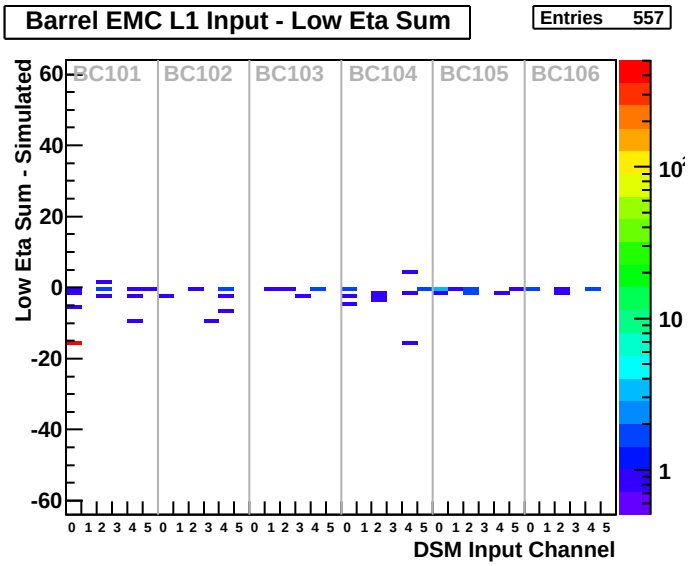
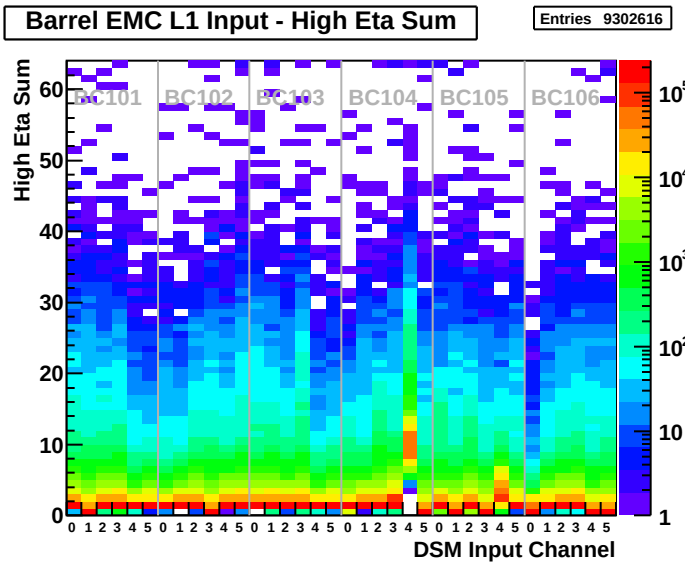
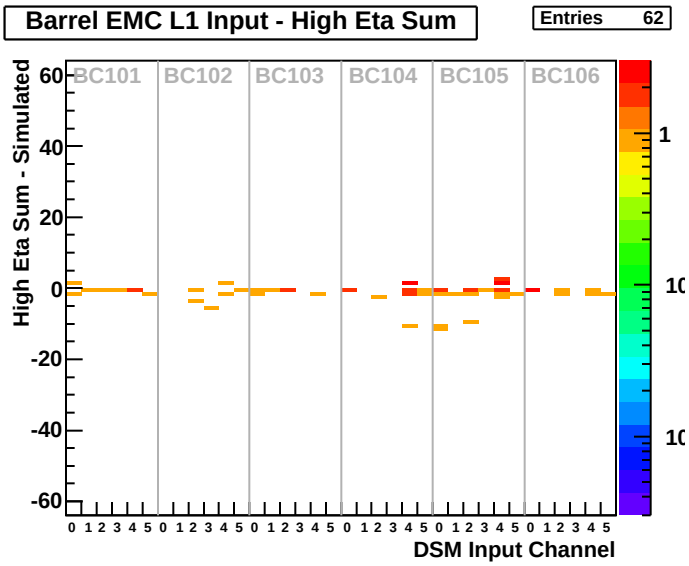
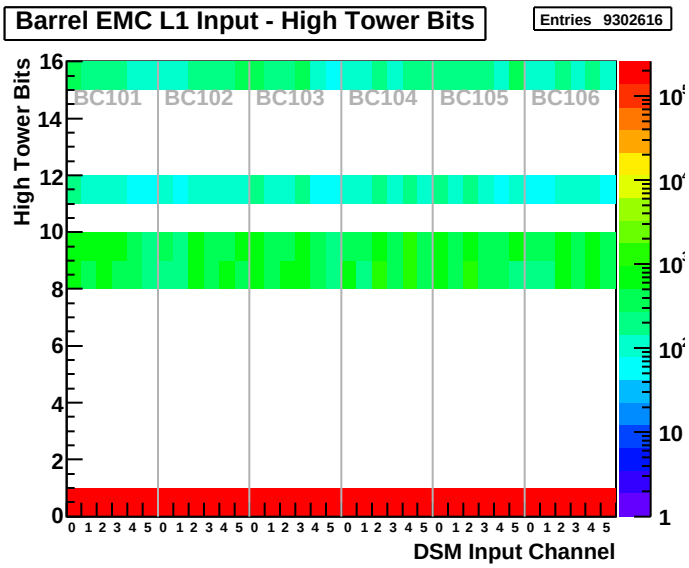
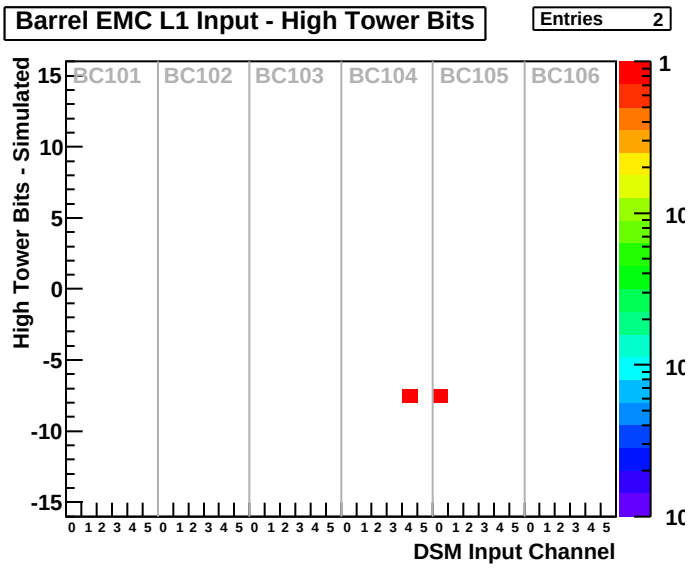

Barrel EMC L1 Input - Low Eta Sum
Entries 9302616
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

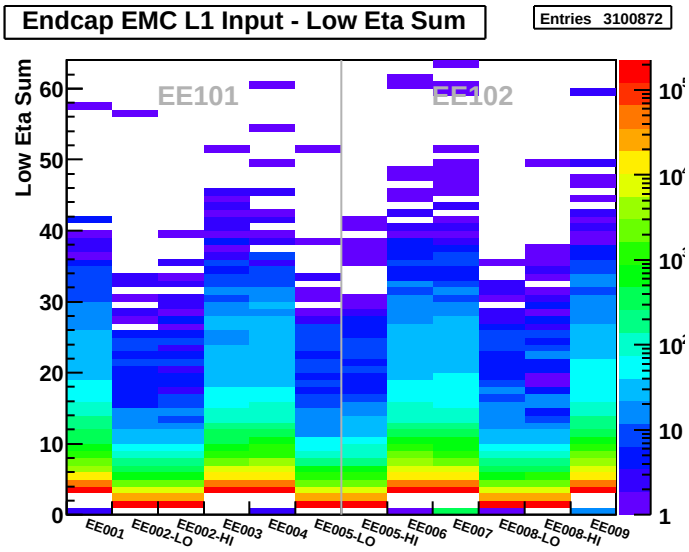

Barrel EMC L1 Input - Low Eta Sum
Entries 557
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

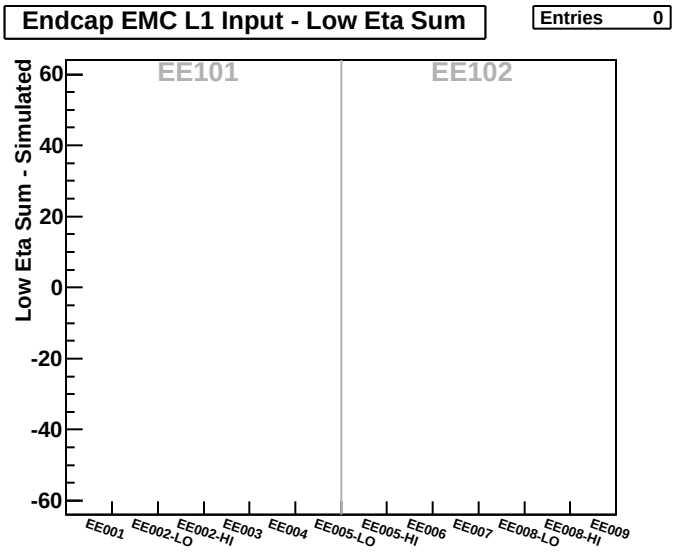

Barrel EMC L1 Input - High Eta Sum
Entries 9302616
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

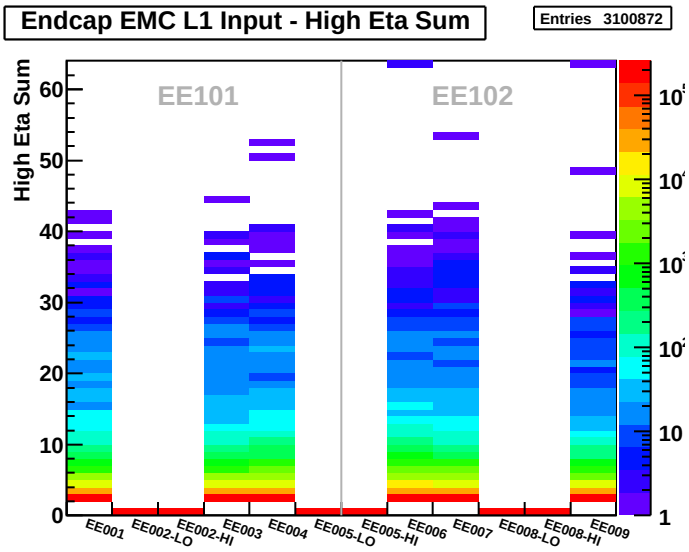

Barrel EMC L1 Input - High Eta Sum
Entries 62
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

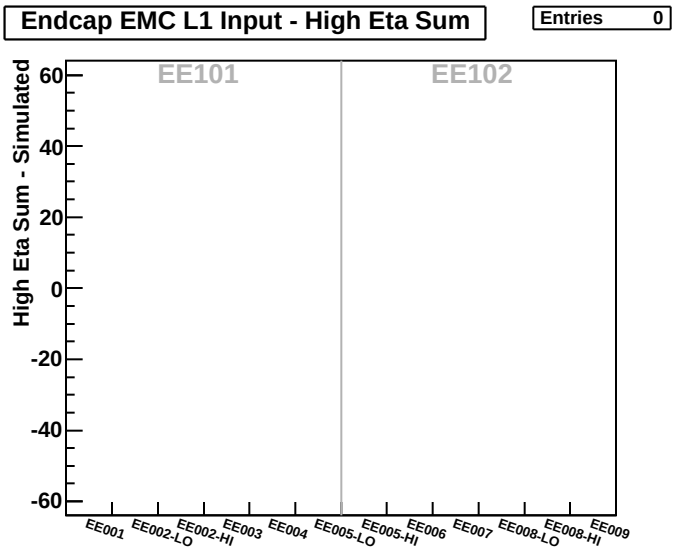

Barrel EMC L1 Input - High Tower Bits
Entries 9302616
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

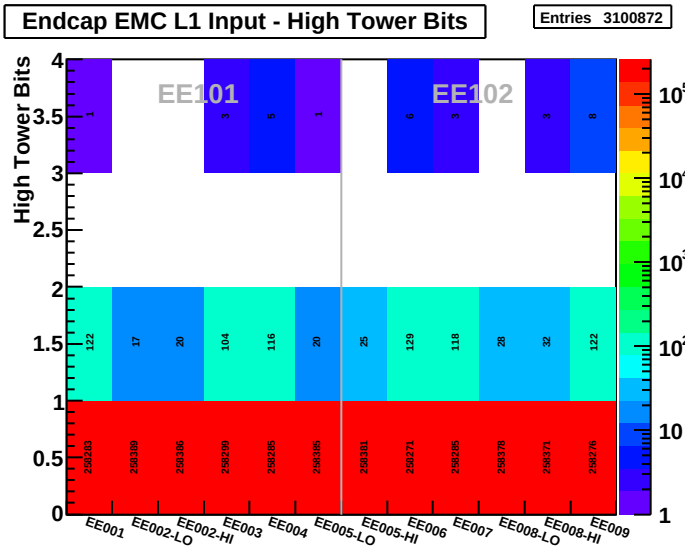

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

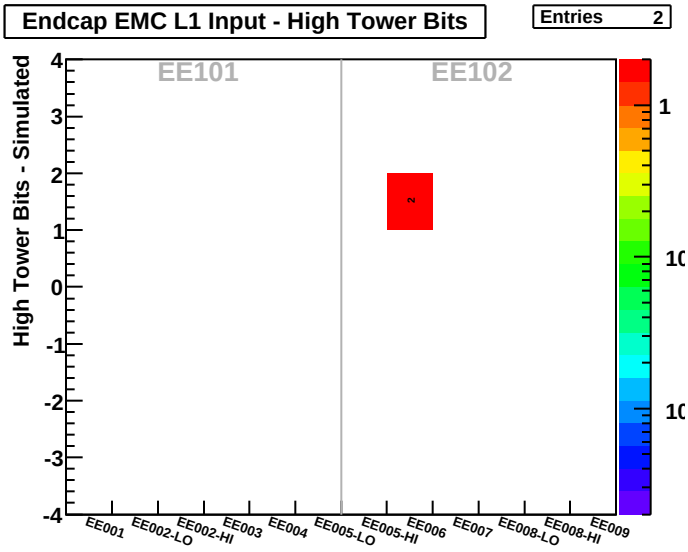

Endcap EMC L1 Input - Low Eta Sum
Entries 3100872
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

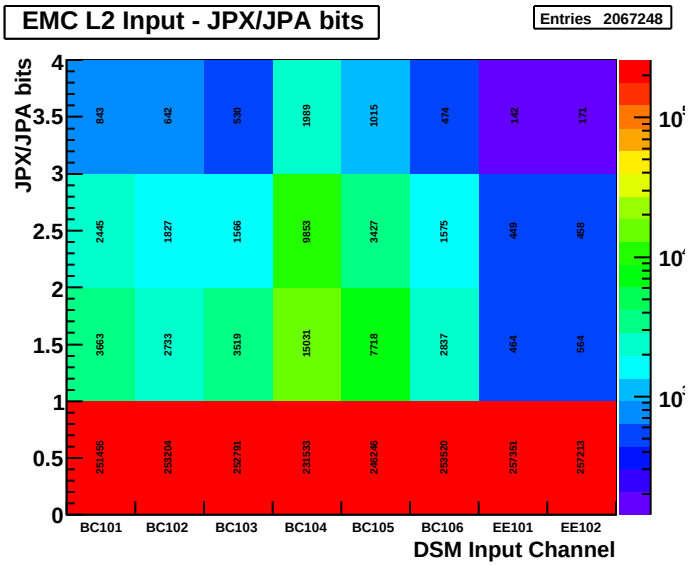

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

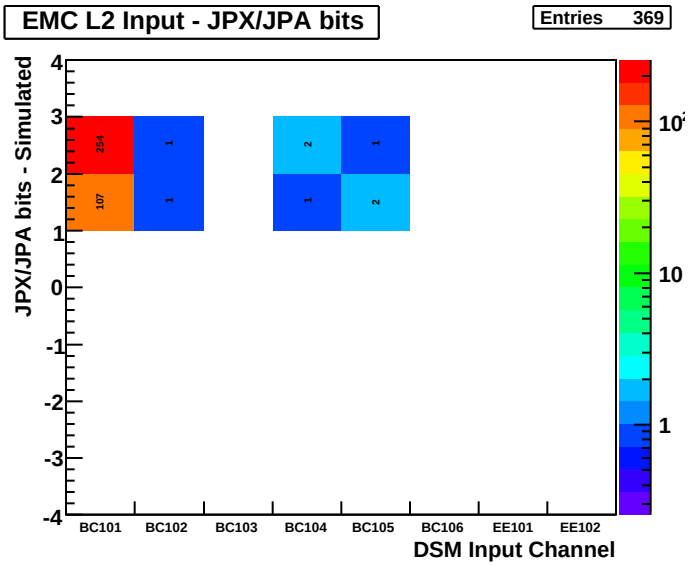

Endcap EMC L1 Input - High Eta Sum
Entries 3100872
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

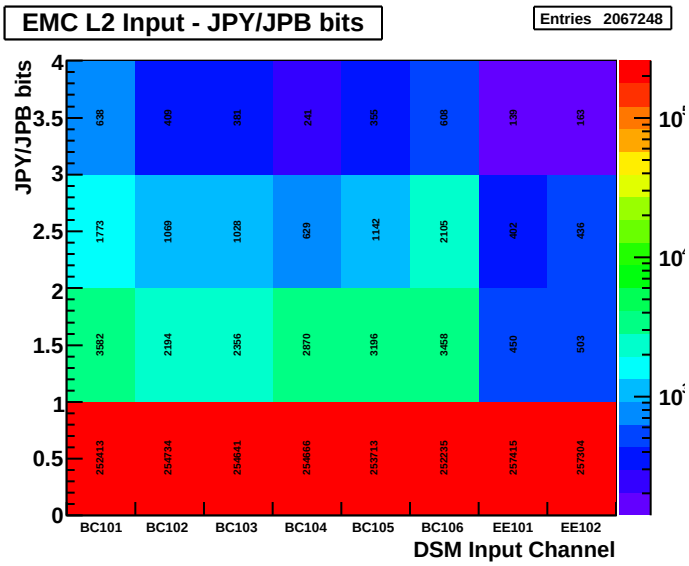

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

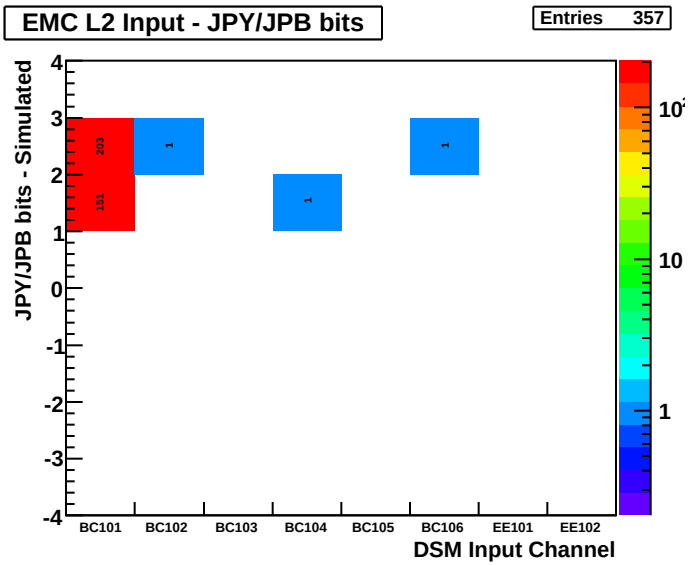

Endcap EMC L1 Input - High Tower Bits
Entries 3100872
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

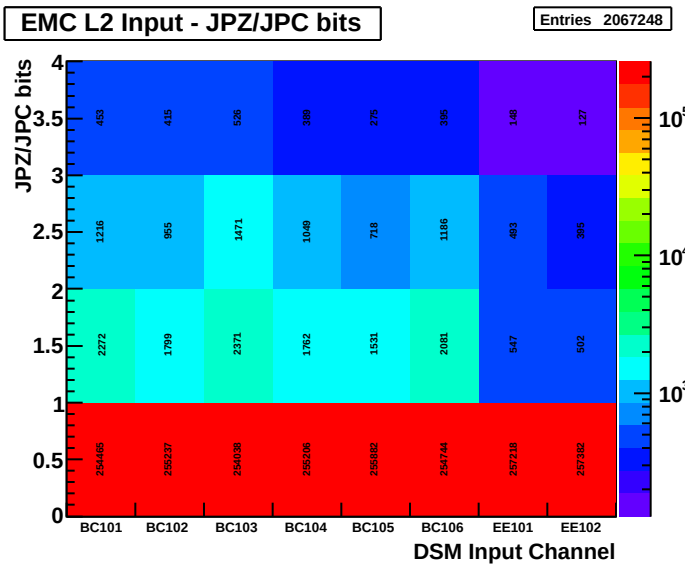

Endcap EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

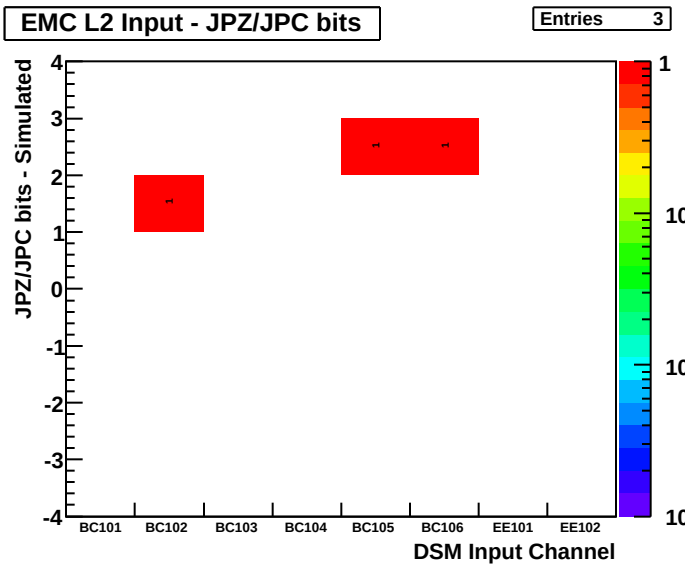

EMC L2 Input - JPX/JPA bits
Entries 2067248
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
843 642 530 1989 1015 474 142 171
2445 1827 1566 9853 3427 1575 449 468
3663 2733 3519 15031 7718 2837 464 564
251455 253204 252791 231533 246246 253520 257351 257213

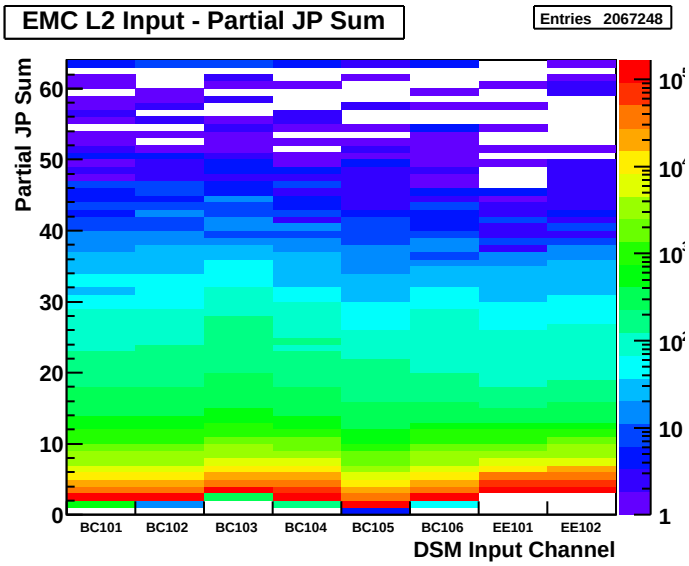

EMC L2 Input - JPX/JPA bits
Entries 369
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
254 1 2 1
107 1 1 2

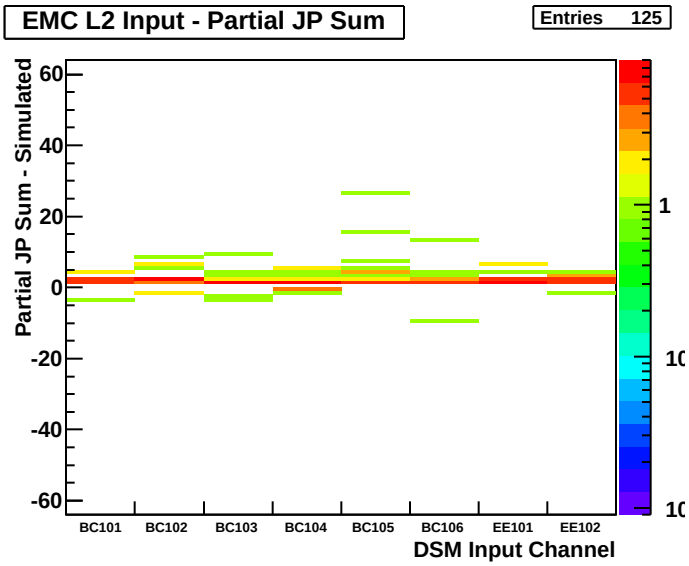

EMC L2 Input - JPY/JPB bits
Entries 2067248
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
638 409 381 241 355 608 139 163
1773 1069 1028 629 1142 2105 402 436
3882 2194 2356 2970 3196 3458 450 503
252413 254734 254641 254666 253713 252235 257415 257304

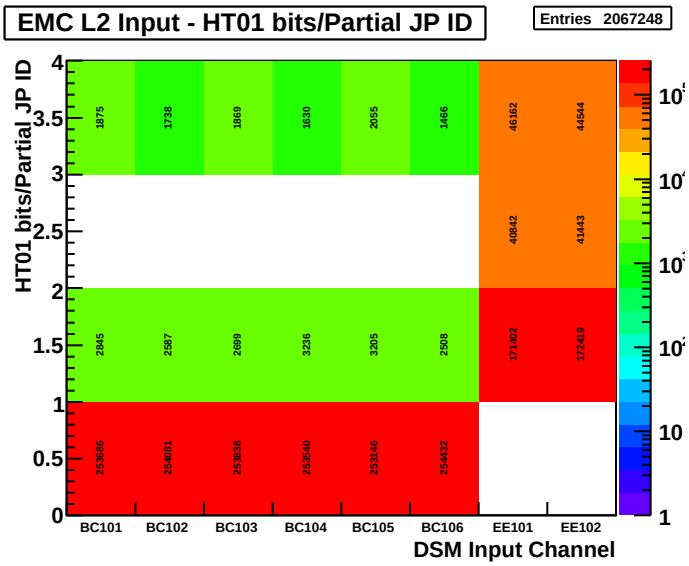

EMC L2 Input - JPY/JPB bits
Entries 357
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
203 1 1
151 1

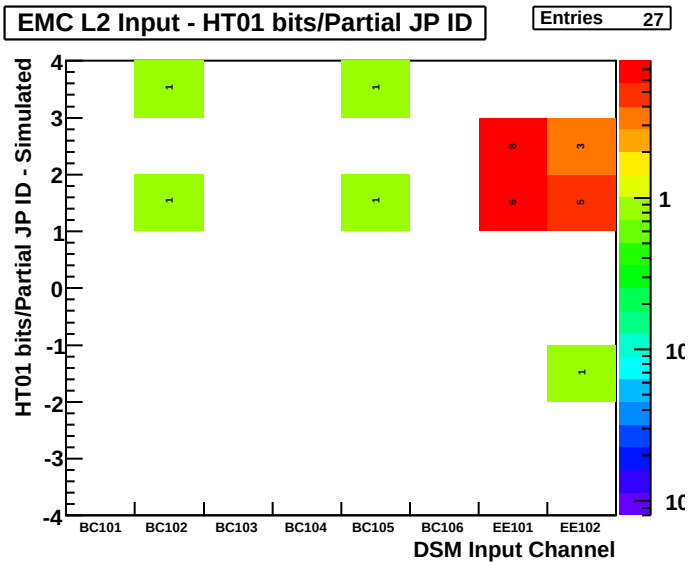

EMC L2 Input - JPZ/JPC bits
Entries 2067248
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
453 415 526 389 275 395 148 127
1216 955 1471 1049 718 1186 493 395
2272 1799 2371 1762 1531 2081 547 502
254465 255237 254038 255206 255882 254744 257218 257382

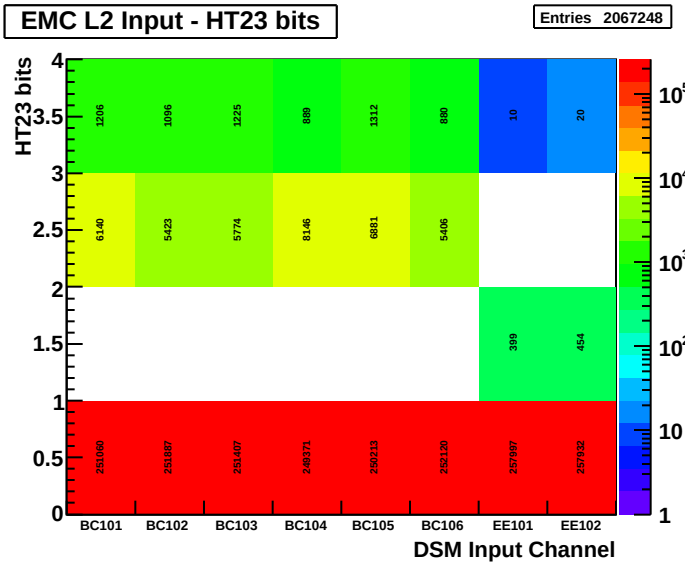

EMC L2 Input - JPZ/JPC bits
Entries 3
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1 1

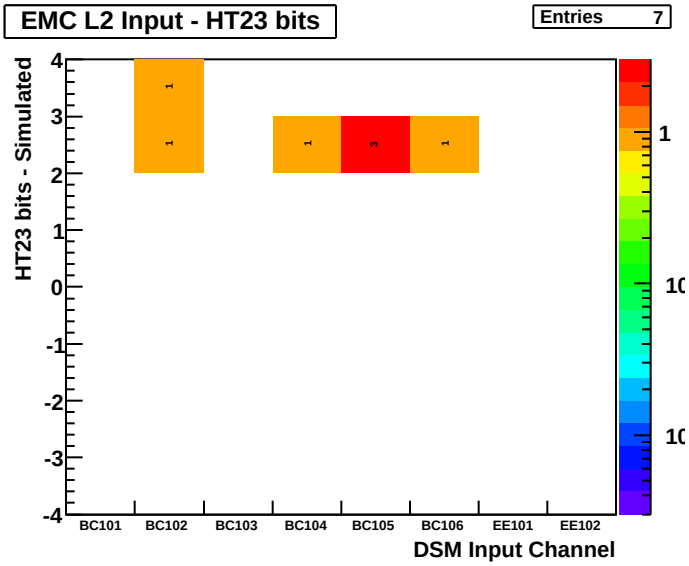

EMC L2 Input - Partial JP Sum
Entries 2067248
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

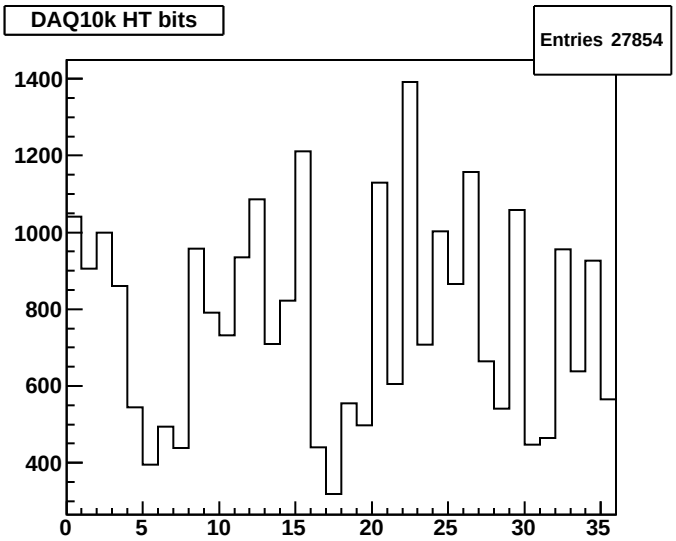

EMC L2 Input - Partial JP Sum
Entries 125
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

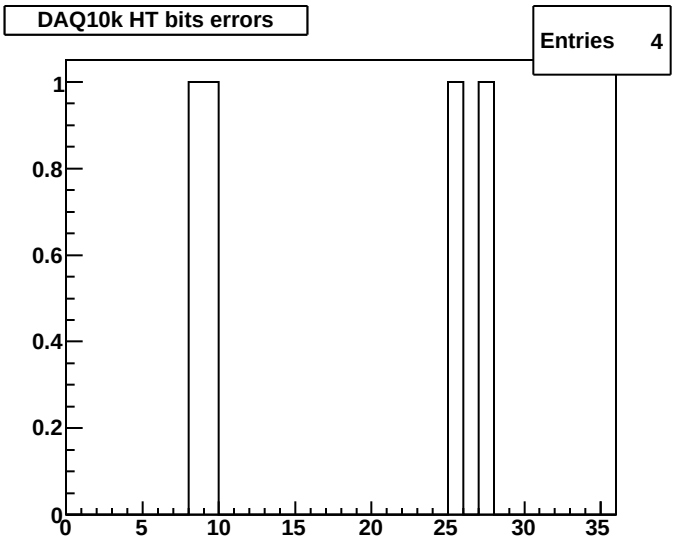

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2067248
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

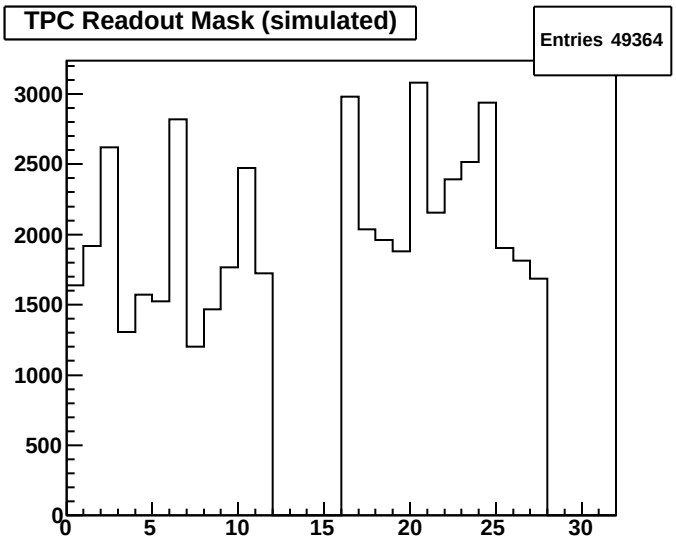

EMC L2 Input - HT01 bits/Partial JP ID
Entries 27
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

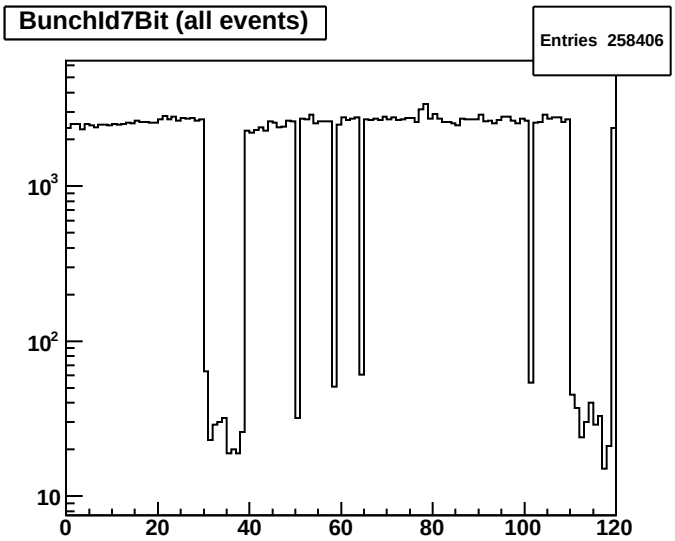

EMC L2 Input - HT23 bits
Entries 2067248
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

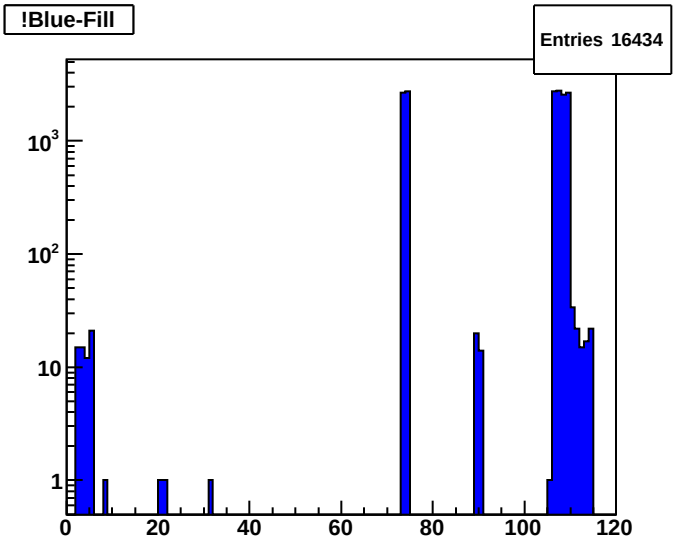

EMC L2 Input - HT23 bits
Entries 7
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

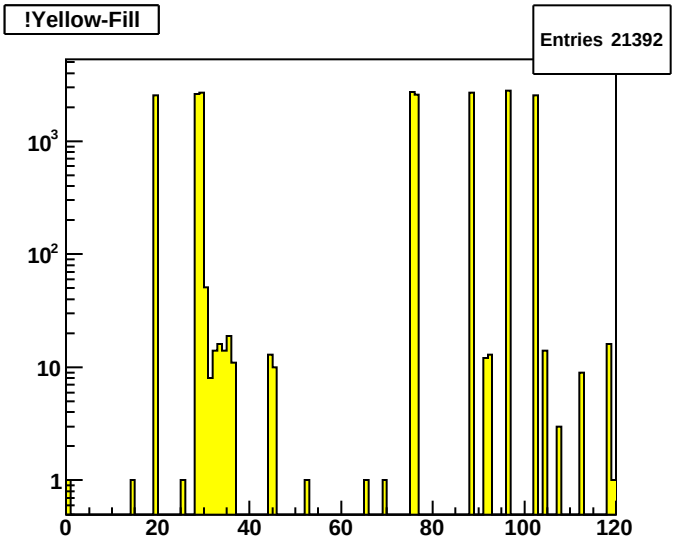

DAQ10k HT bits
Entries 27854

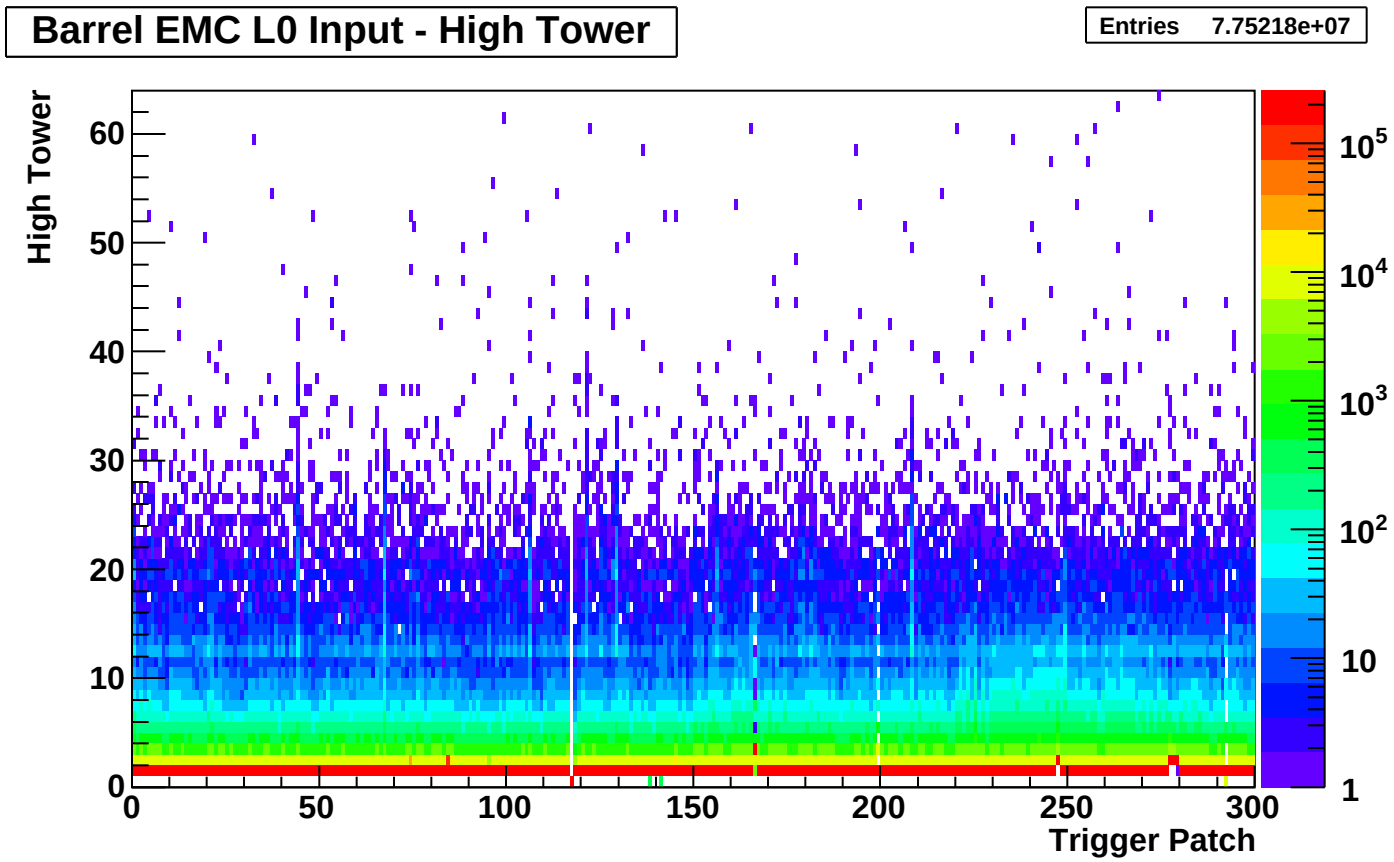

DAQ10k HT bits errors
Entries 4

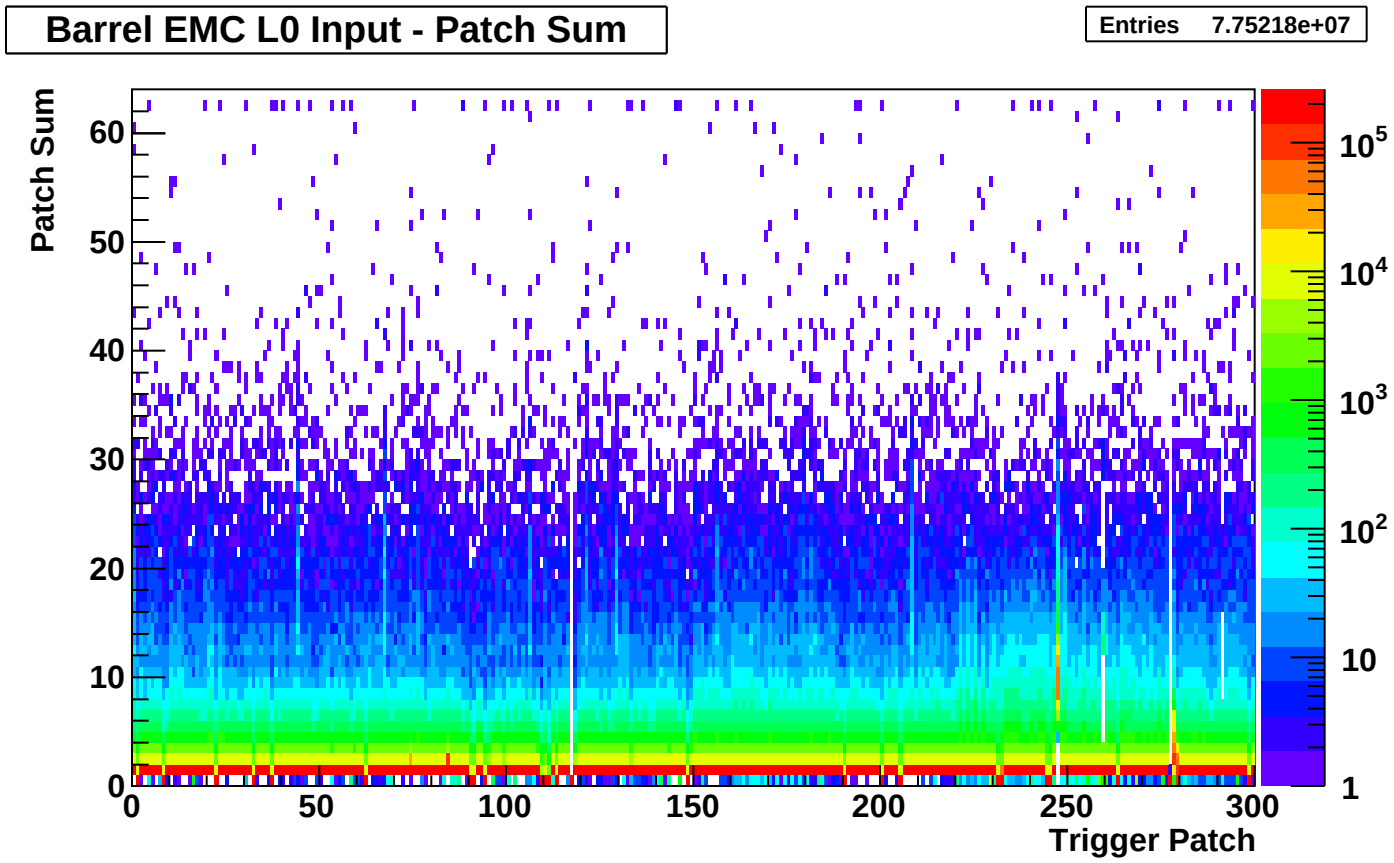

TPC Readout Mask (simulated)
Entries 49364

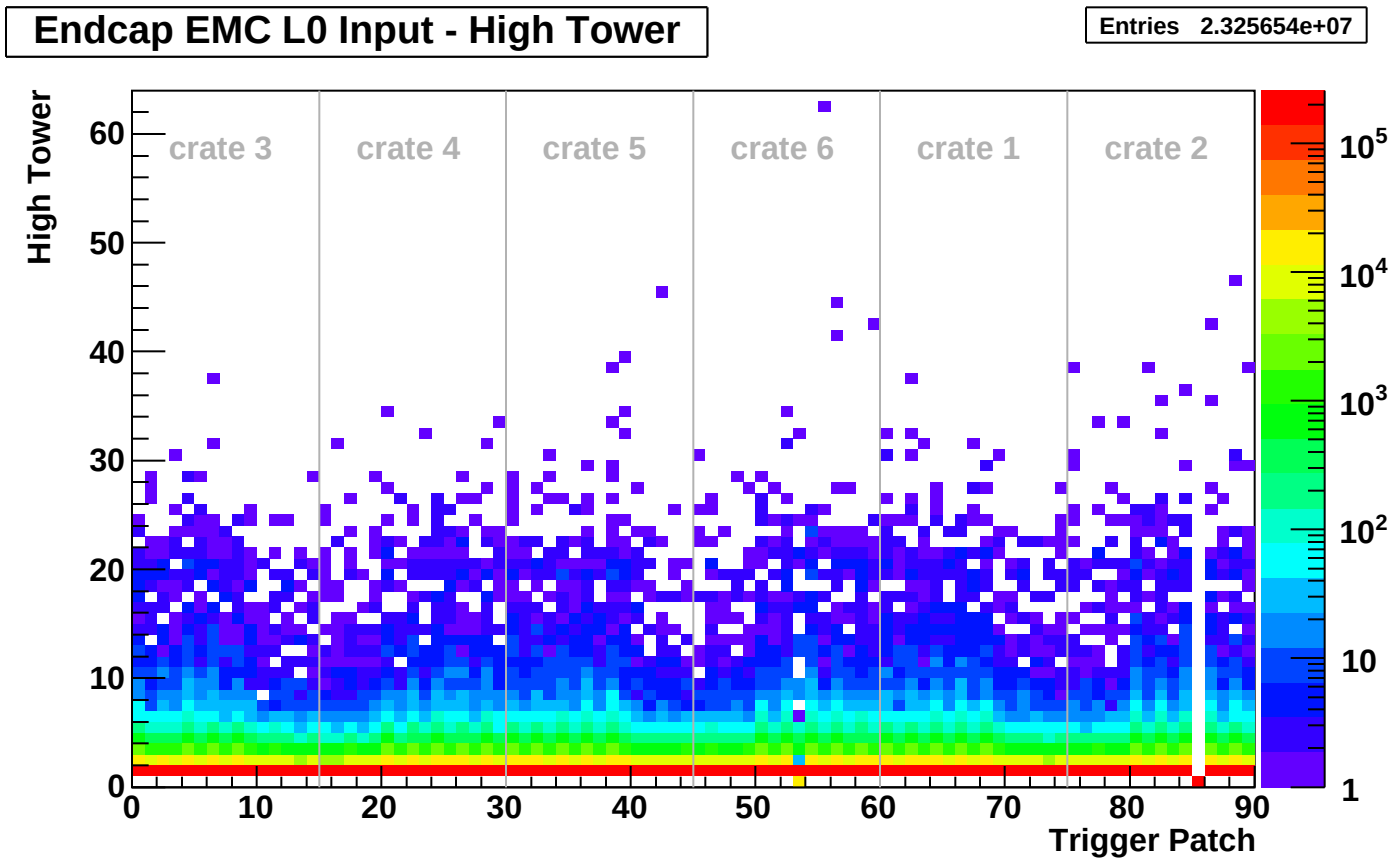

BunchId7Bit (all events)
Entries 258406

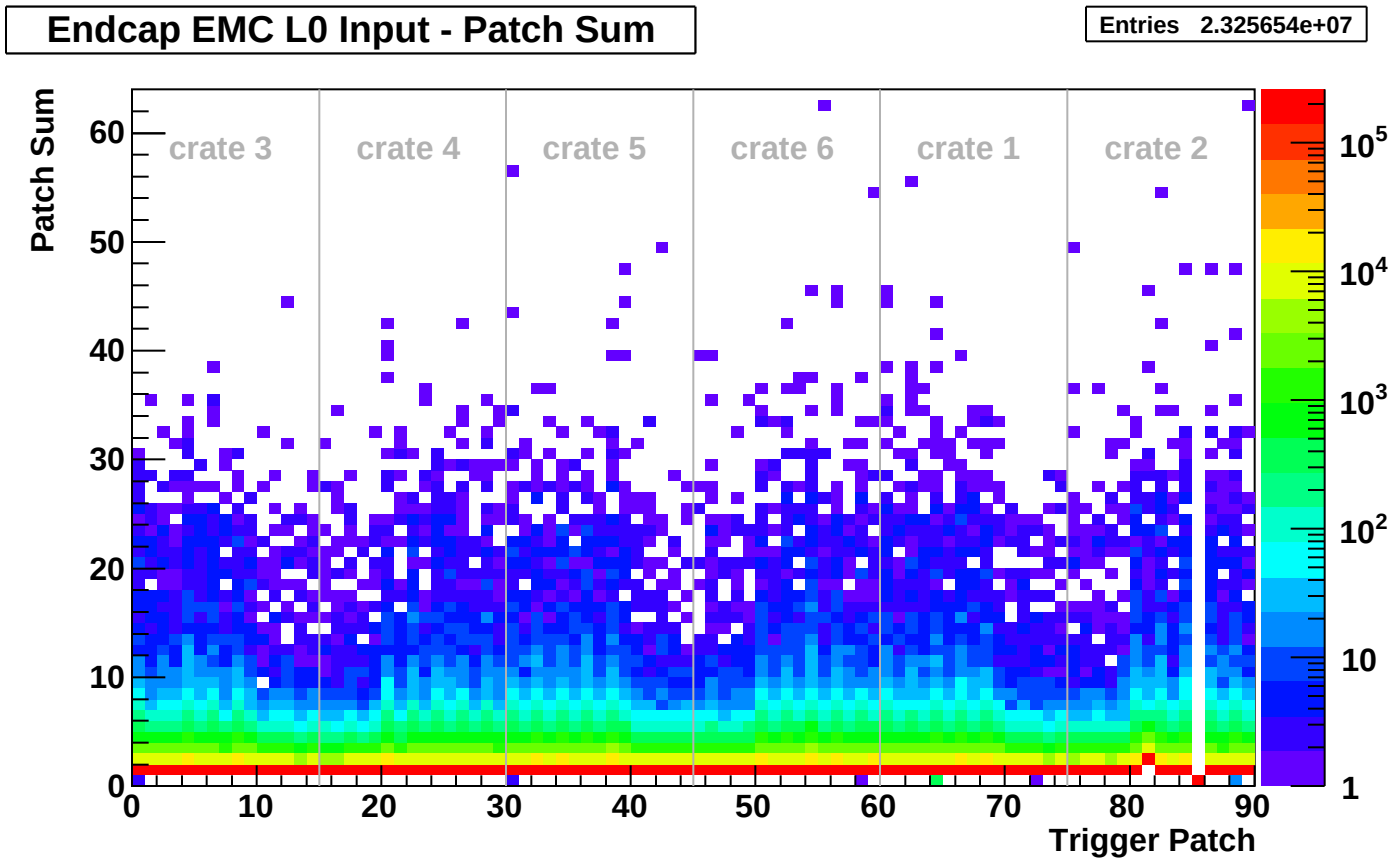

!Blue-Fill
Entries 16434

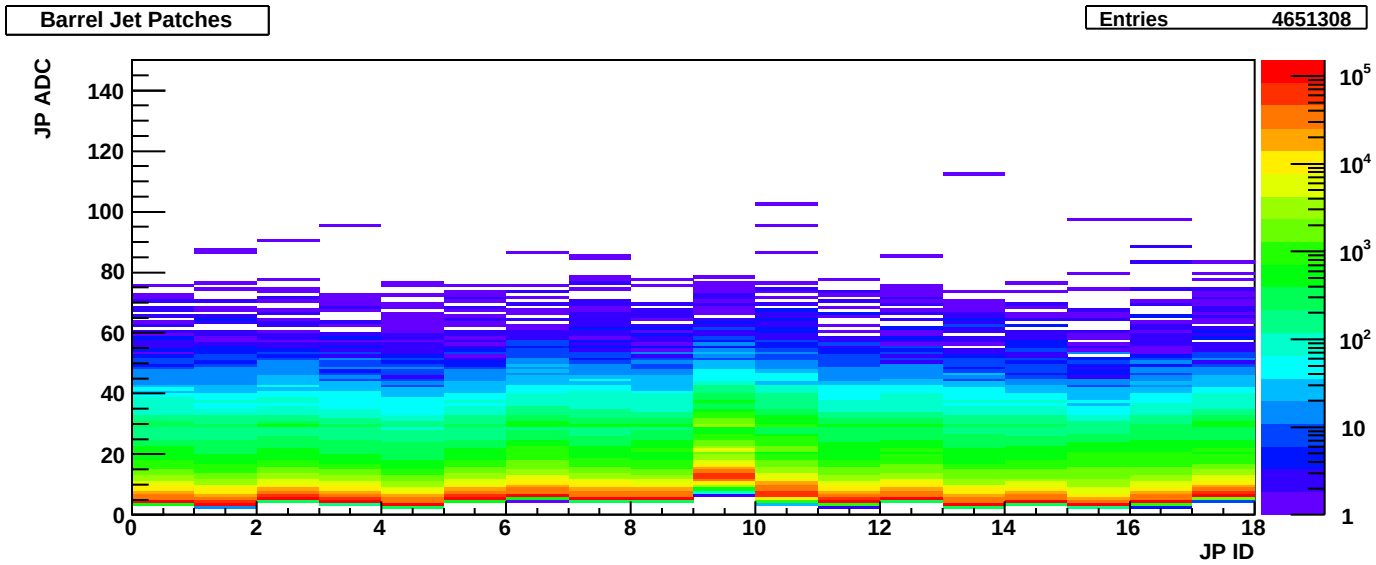

!Yellow-Fill
Entries 21392

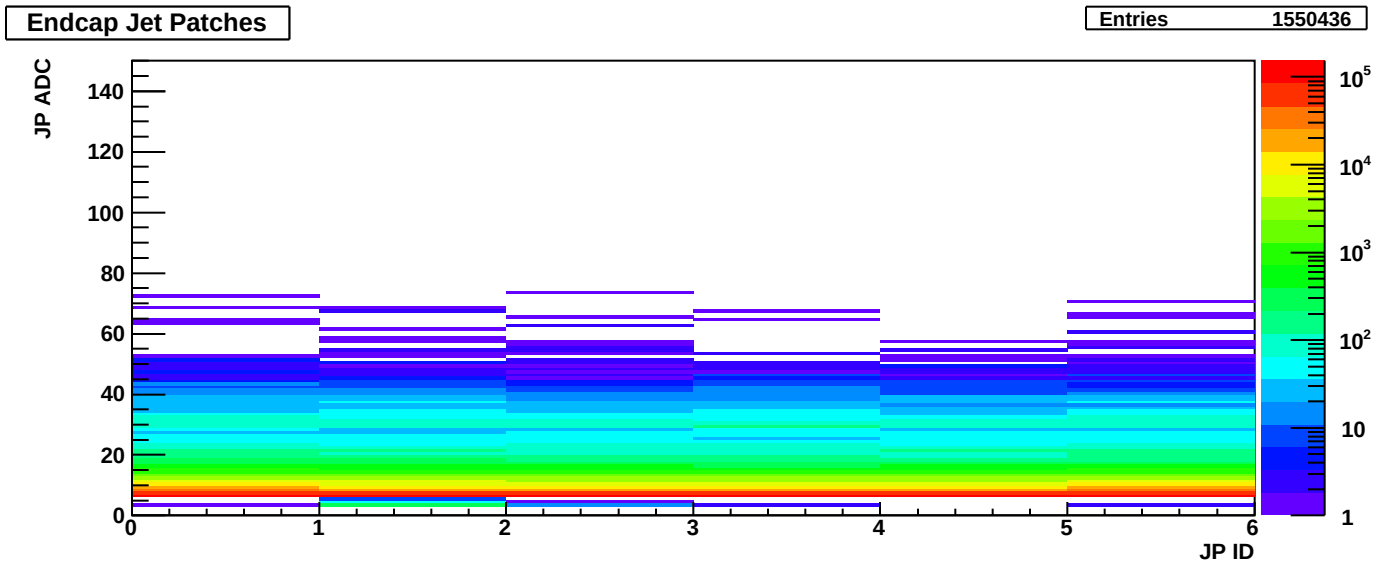

Barrel EMC L0 Input - High Tower
Entries 7.75218e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

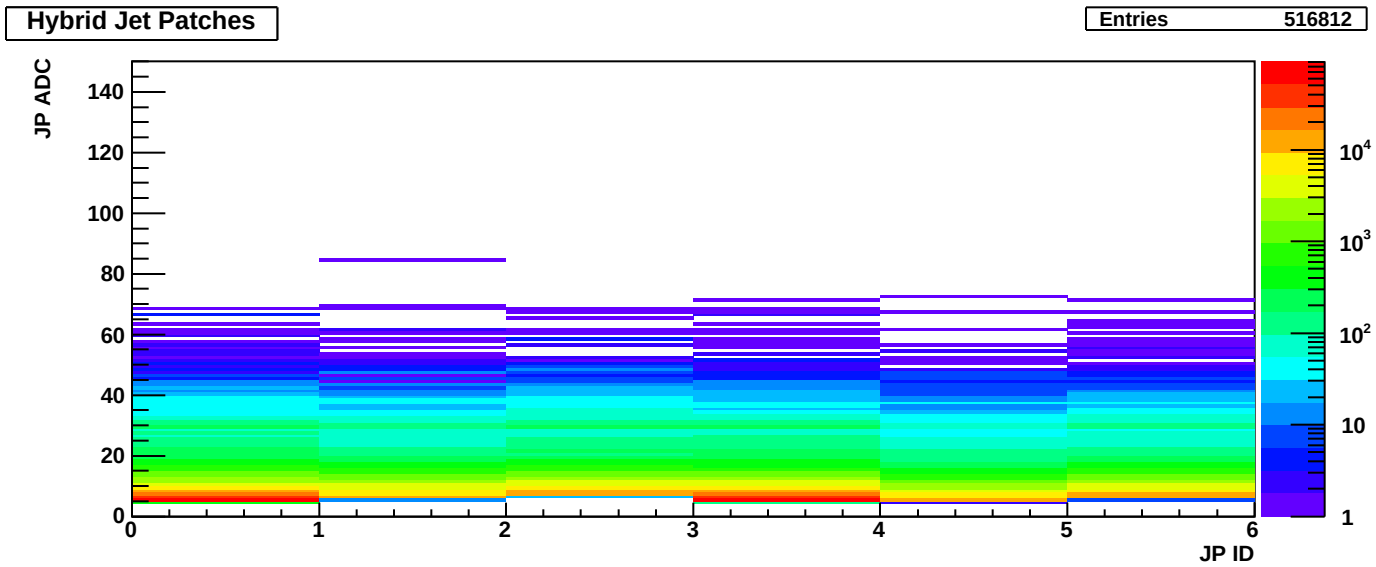

Barrel EMC L0 Input - Patch Sum
Entries 7.75218e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

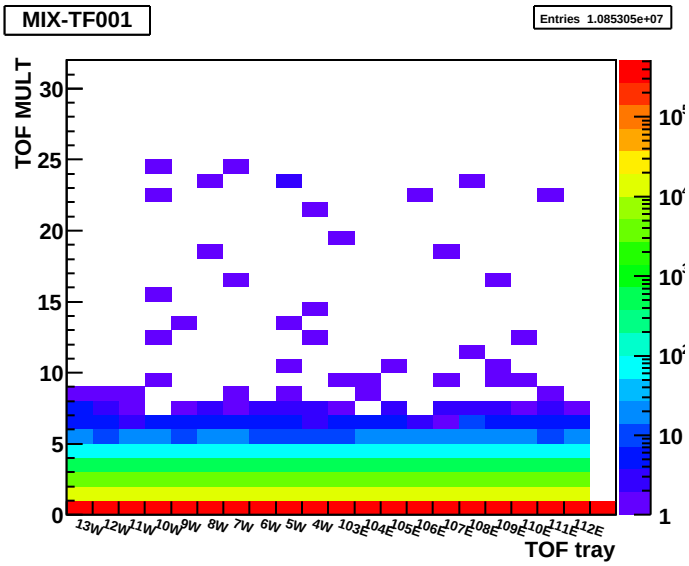

Endcap EMC L0 Input - High Tower
Entries 2.325654e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

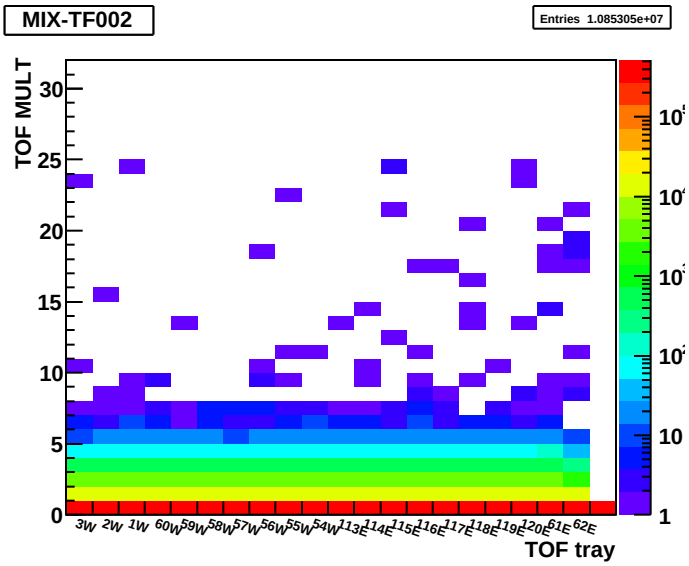

Endcap EMC L0 Input - Patch Sum
Entries 2.325654e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

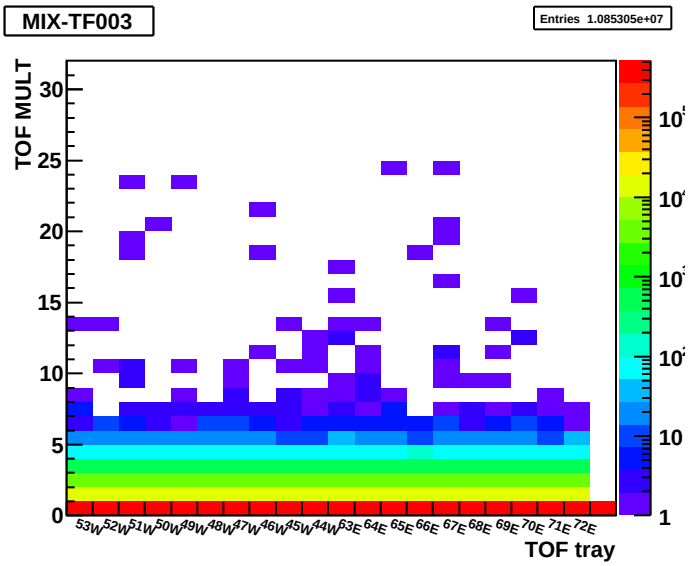

Barrel Jet Patches
Entries 4651308
JP ADC
JP ID

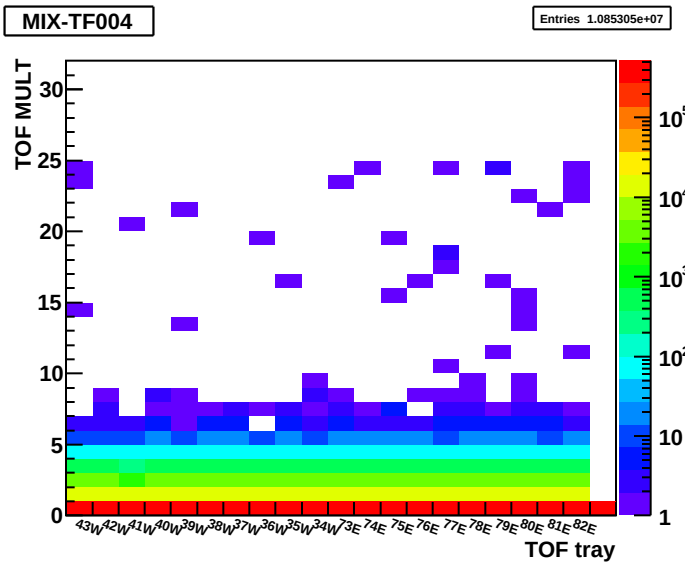

Endcap Jet Patches
Entries 1550436
JP ADC
JP ID

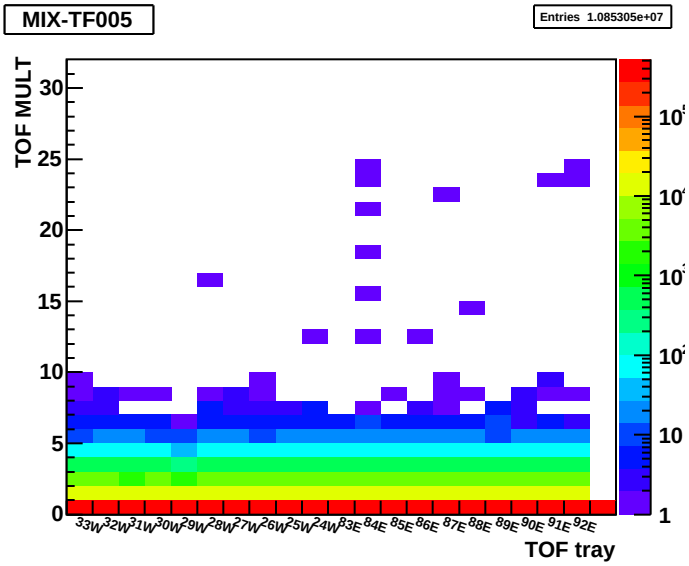

Hybrid Jet Patches
Entries 516812
JP ADC
JP ID

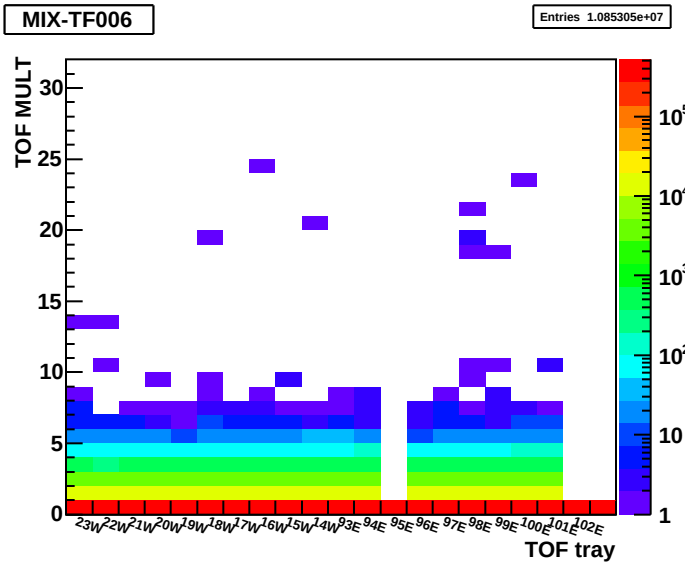

MIX-TF001
Entries 1.085305e+07
TOF MULT
TOF tray

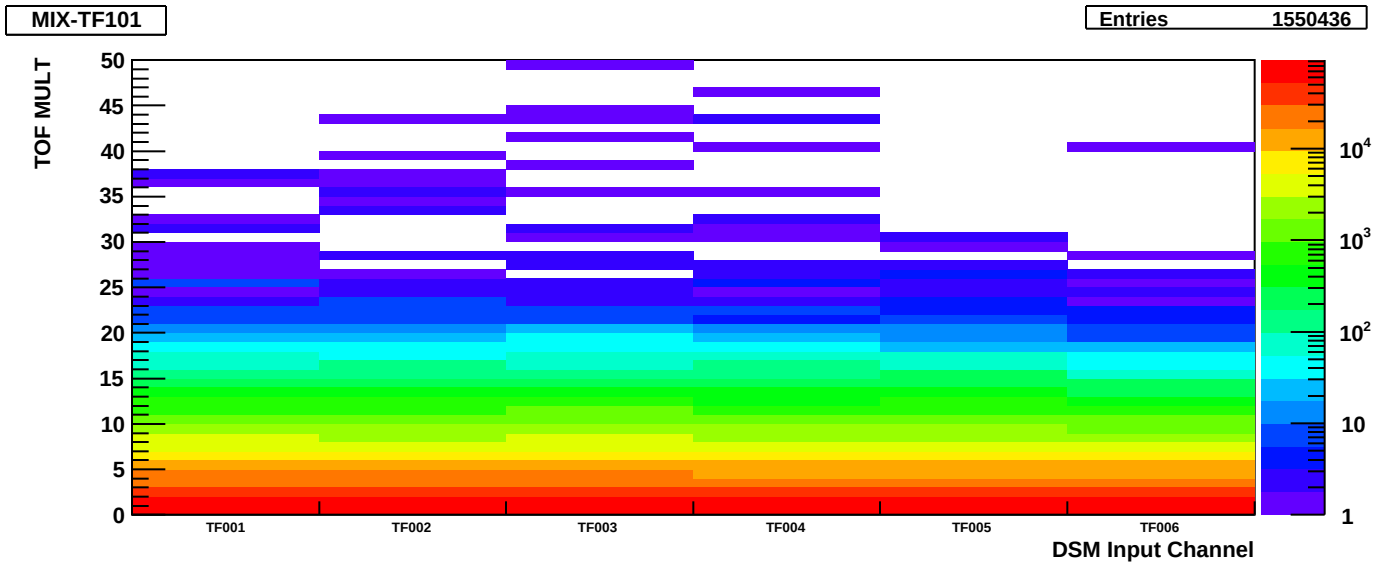

MIX-TF002
Entries 1.085305e+07
TOF MULT
TOF tray

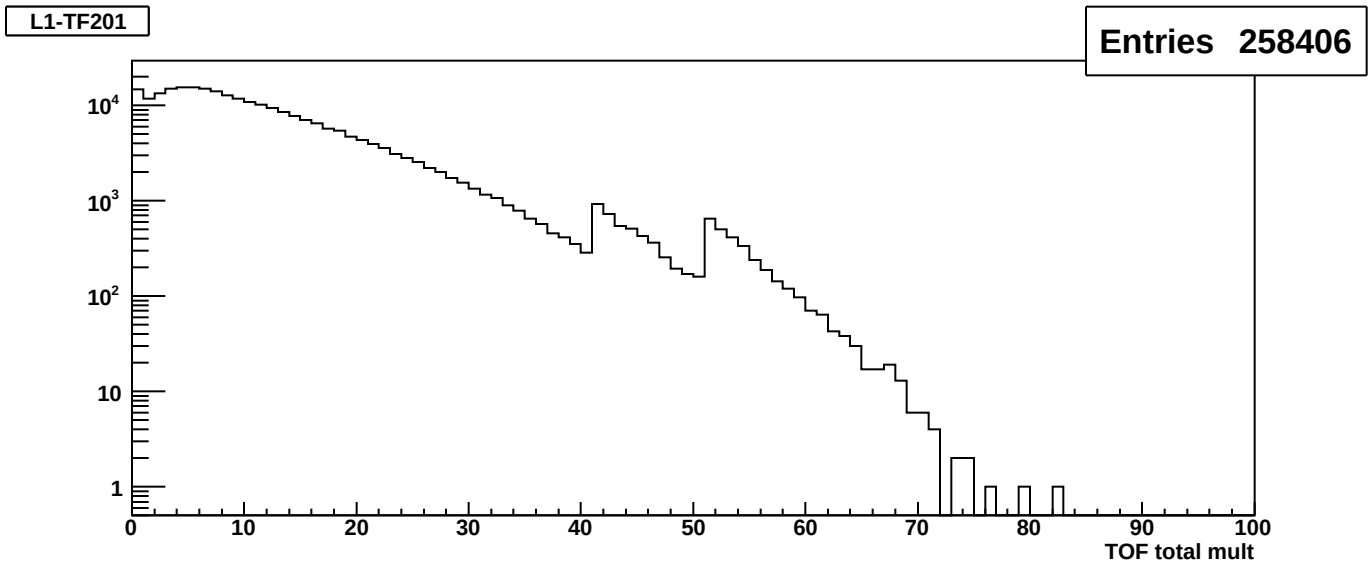

MIX-TF003
Entries 1.085305e+07
TOF MULT
TOF tray

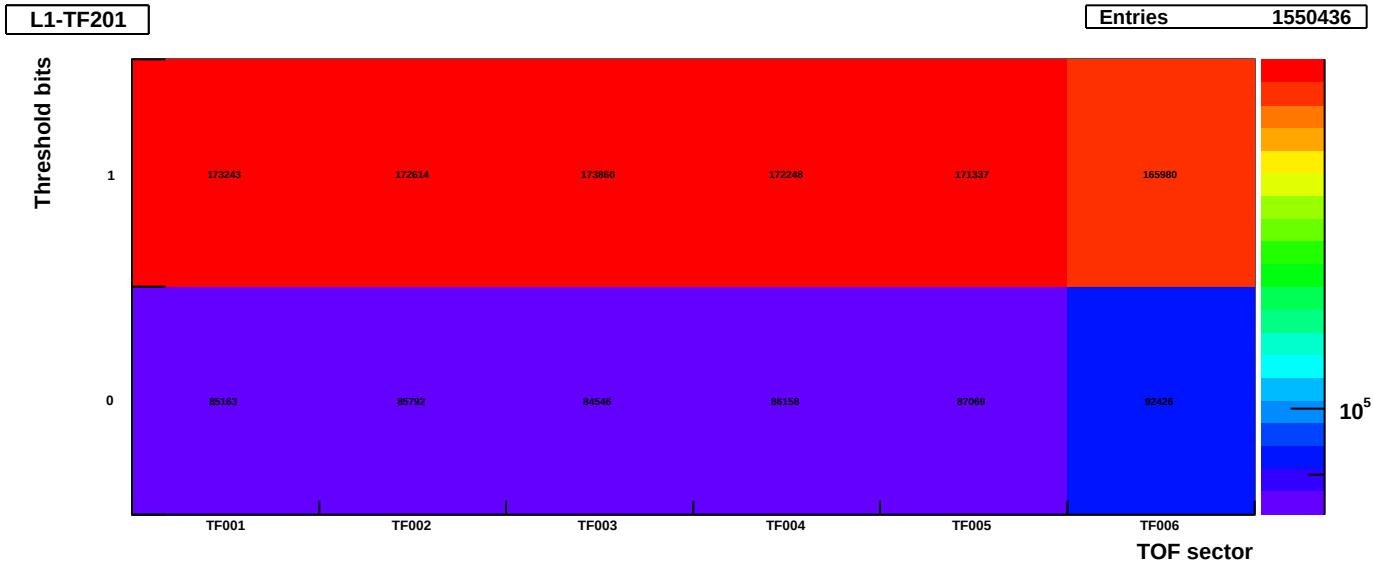

MIX-TF004
Entries 1.085305e+07
TOF MULT
TOF tray

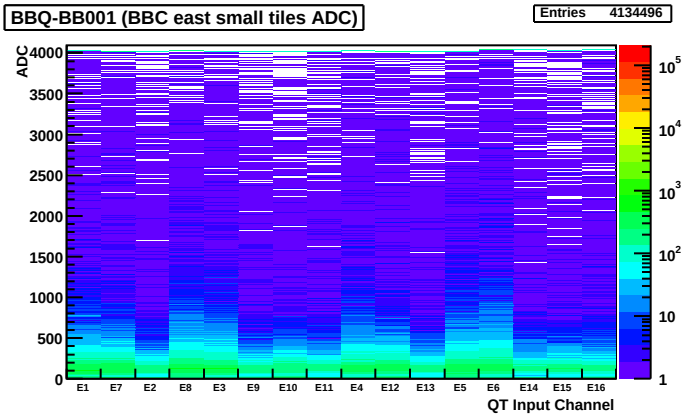

MIX-TF005
Entries 1.085305e+07
TOF MULT
TOF tray

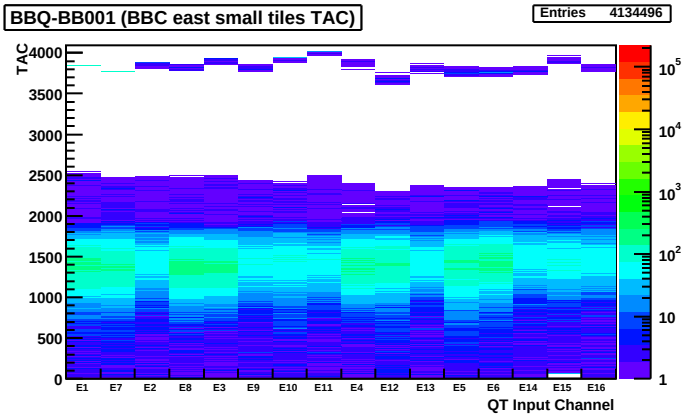

MIX-TF006
Entries 1.085305e+07
TOF MULT
TOF tray

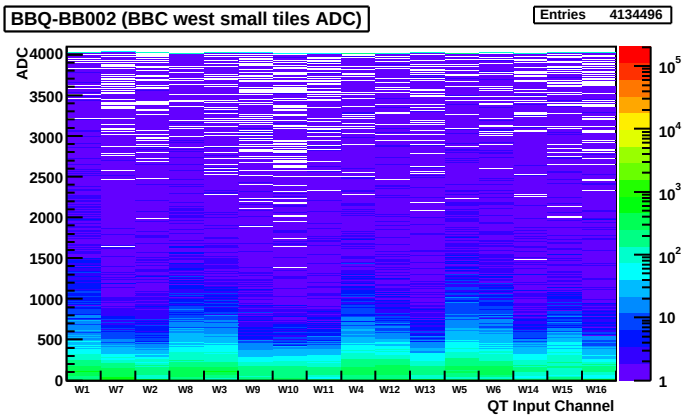

MIX-TF101
Entries 1550436
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

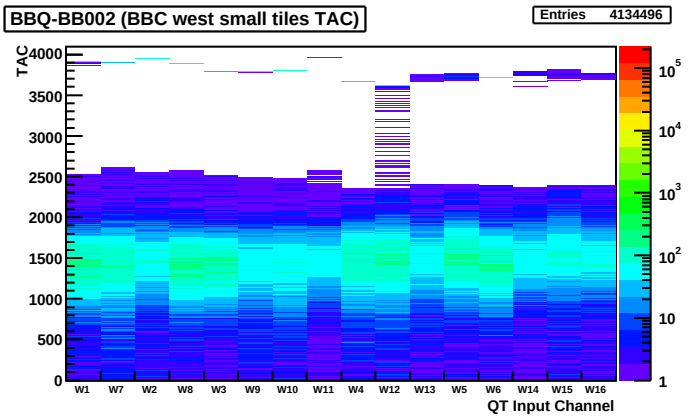

L1-TF201
Entries 258406
TOF total mult

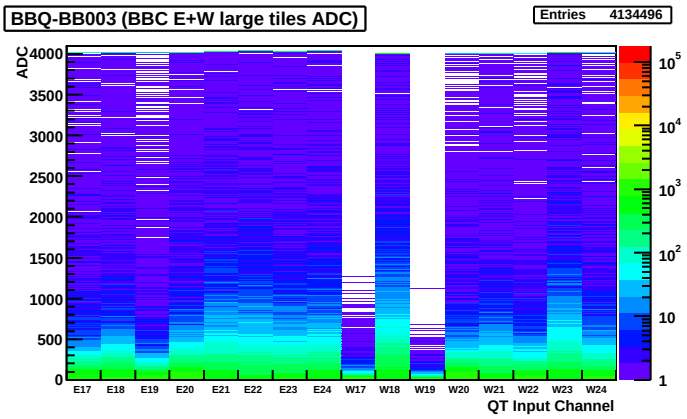

L1-TF201
Entries 1550436
Threshold bits
173243
172614
173860
172248
171337
165980
85163
85792
84546
86158
87069
92426
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

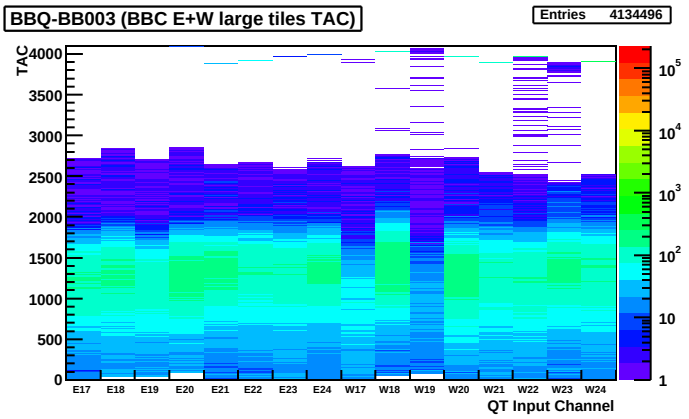

BBQ-BB001 (BBC east small tiles ADC)
Entries 4134496
ADC
QT Input Channel

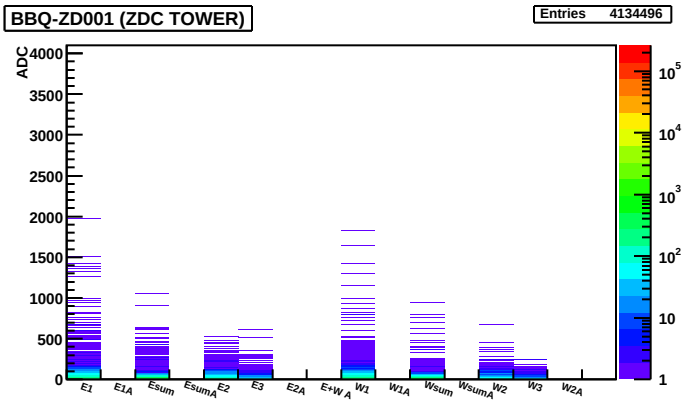

BBQ-BB001 (BBC east small tiles TAC)
Entries 4134496
TAC
QT Input Channel

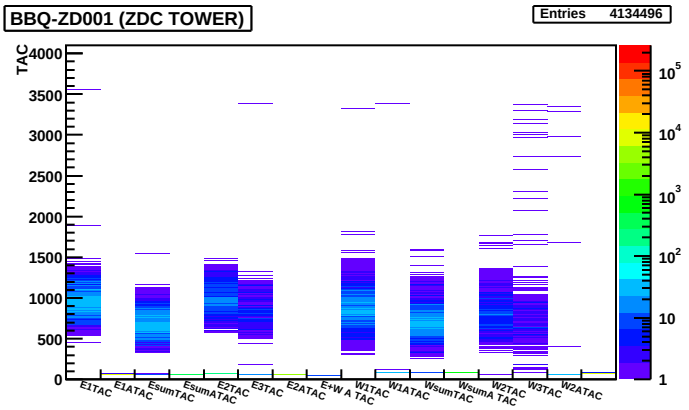

BBQ-BB002 (BBC west small tiles ADC)
Entries 4134496
ADC
QT Input Channel

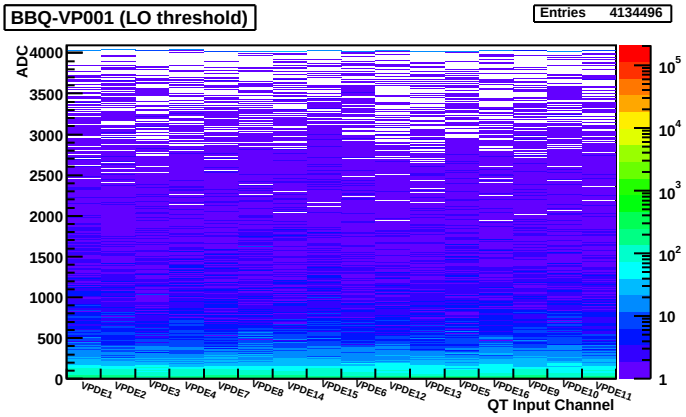

BBQ-BB002 (BBC west small tiles TAC)
Entries 4134496
TAC
QT Input Channel

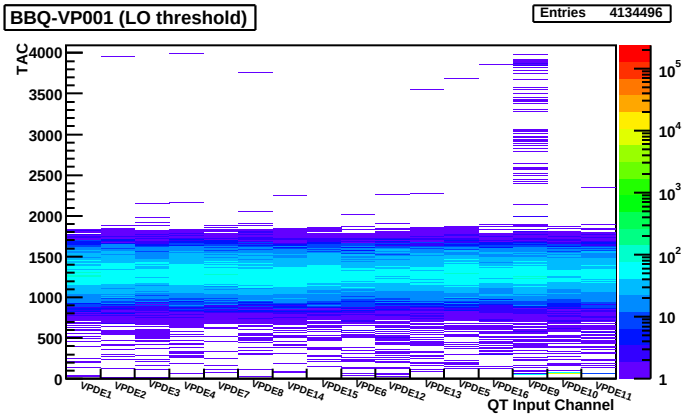

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4134496
ADC
QT Input Channel

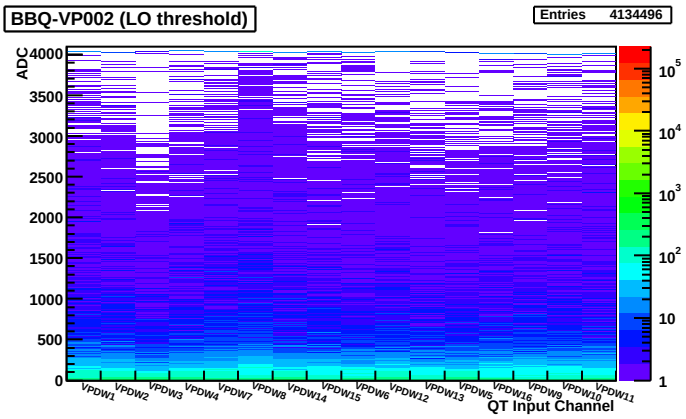

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4134496
TAC
QT Input Channel

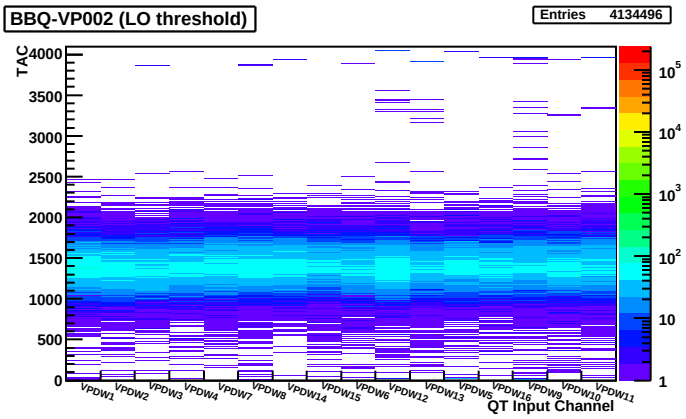

BBQ-ZD001 (ZDC TOWER)
Entries 4134496
ADC

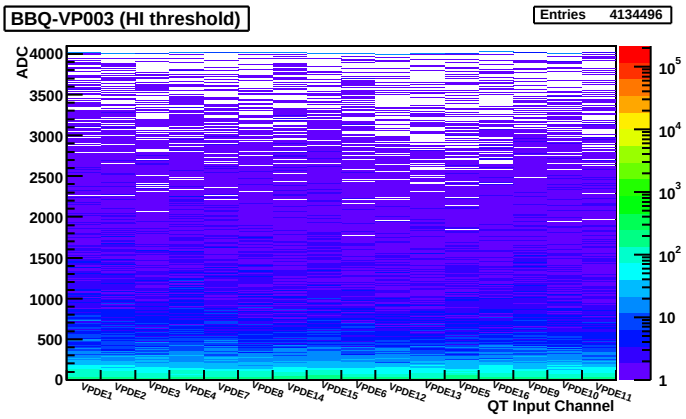

BBQ-ZD001 (ZDC TOWER)
Entries 4134496
TAC

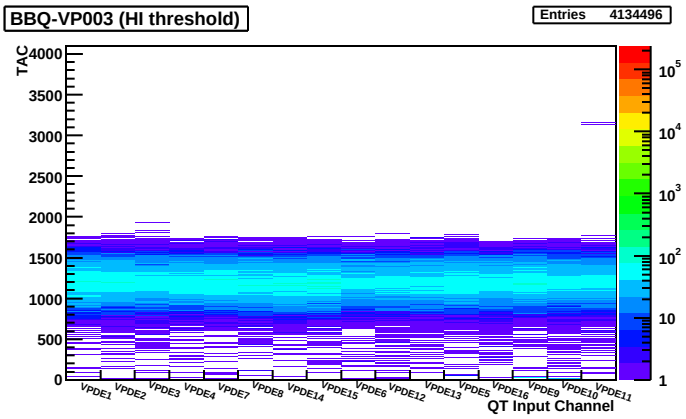

BBQ-VP001 (LO threshold)
Entries 4134496
ADC
QT Input Channel

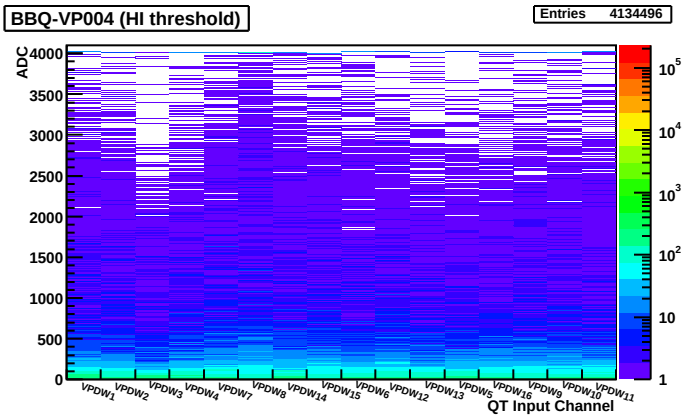

BBQ-VP001 (LO threshold)
Entries 4134496
TAC
QT Input Channel

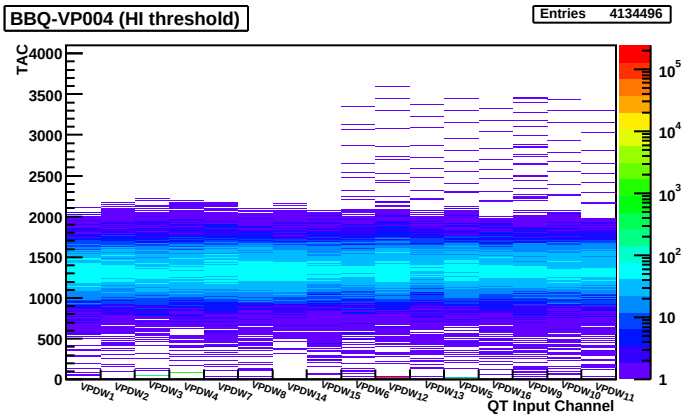

BBQ-VP002 (LO threshold)
Entries 4134496
ADC
QT Input Channel

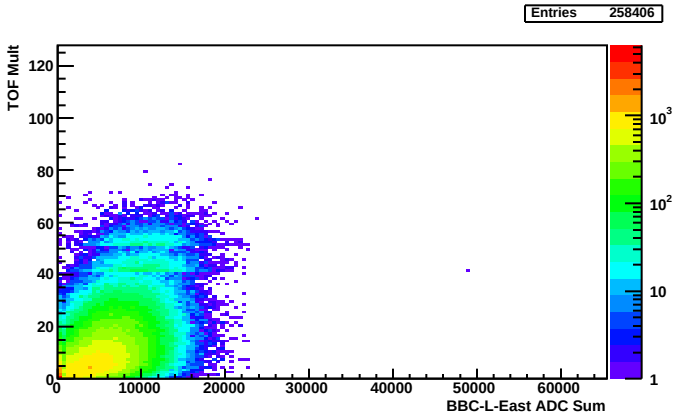

BBQ-VP002 (LO threshold)
Entries 4134496
TAC
QT Input Channel

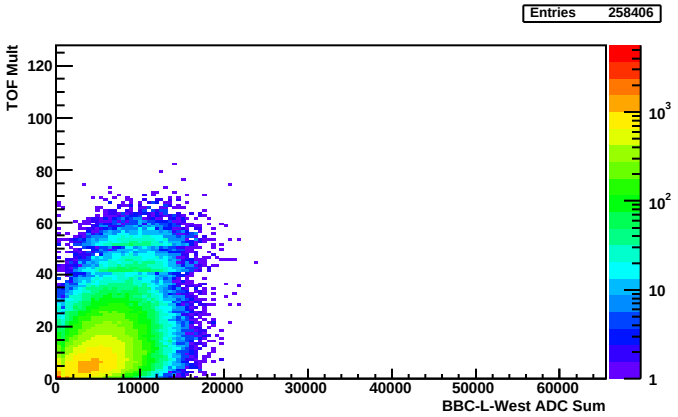

BBQ-VP003 (HI threshold)
Entries 4134496
ADC
QT Input Channel

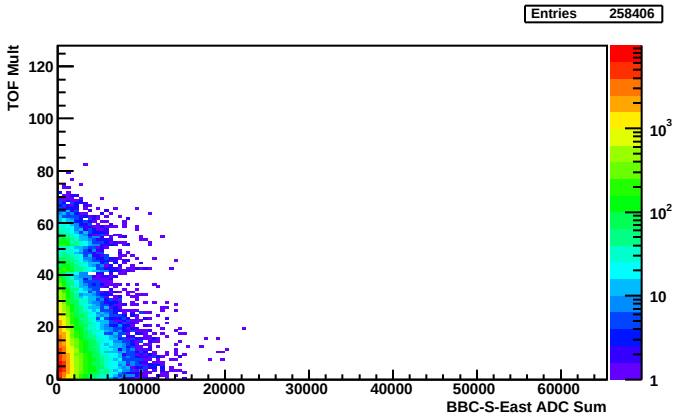

BBQ-VP003 (HI threshold)
Entries 4134496
TAC
QT Input Channel

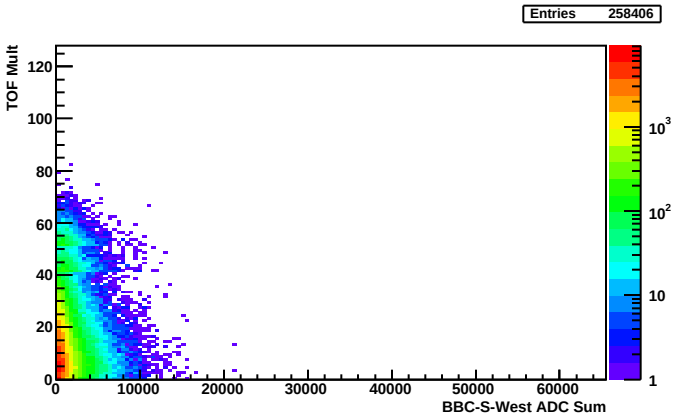

BBQ-VP004 (HI threshold)
Entries 4134496
ADC
QT Input Channel

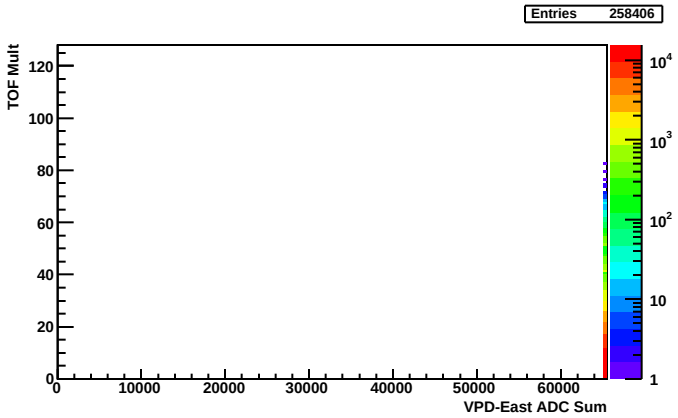

BBQ-VP004 (HI threshold)
Entries 4134496
TAC
QT Input Channel

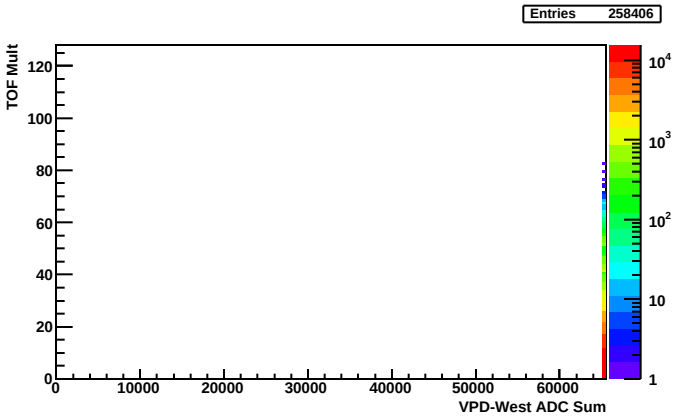

Entries 258406
TOF Mult
BBC-L-East ADC Sum

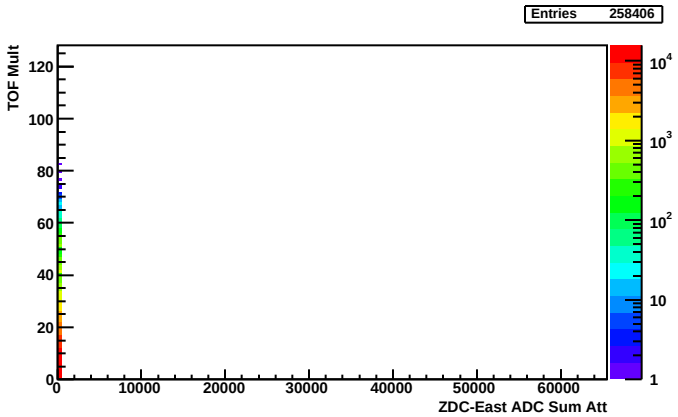

Entries 258406
TOF Mult
BBC-L-West ADC Sum

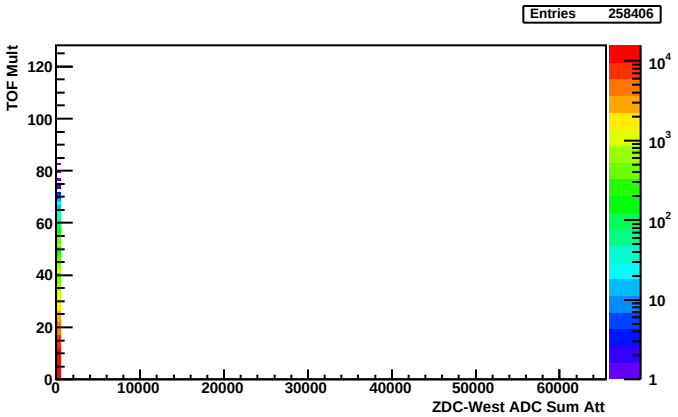

Entries 258406
TOF Mult
BBC-S-East ADC Sum

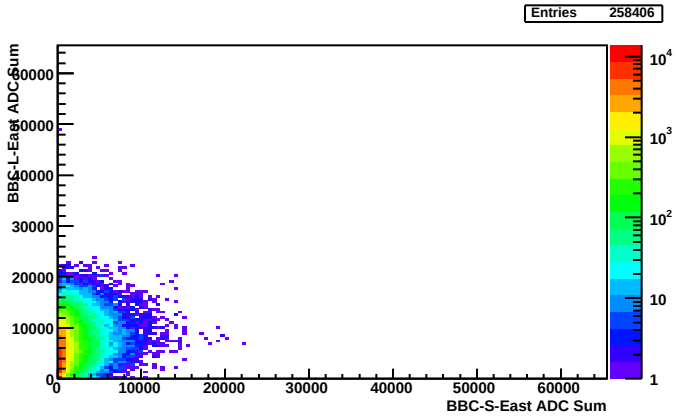

Entries 258406
TOF Mult
BBC-S-West ADC Sum

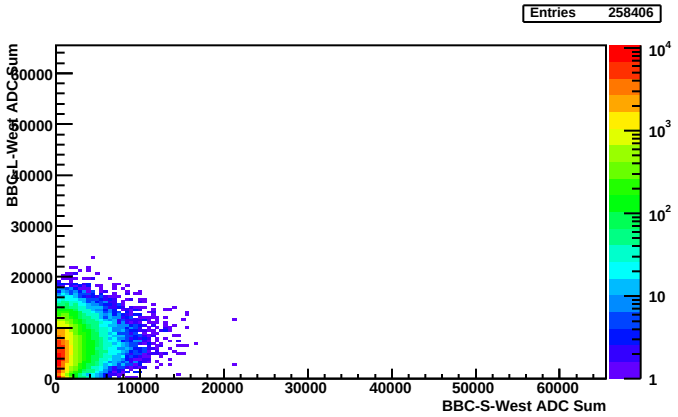

Entries 258406
TOF Mult
VPD-East ADC Sum

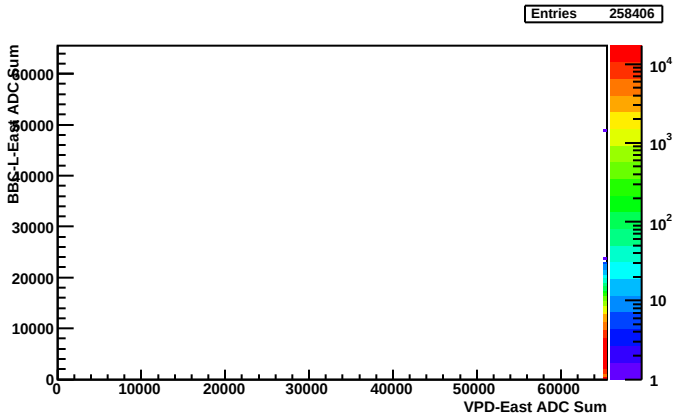

Entries 258406
TOF Mult
VPD-West ADC Sum

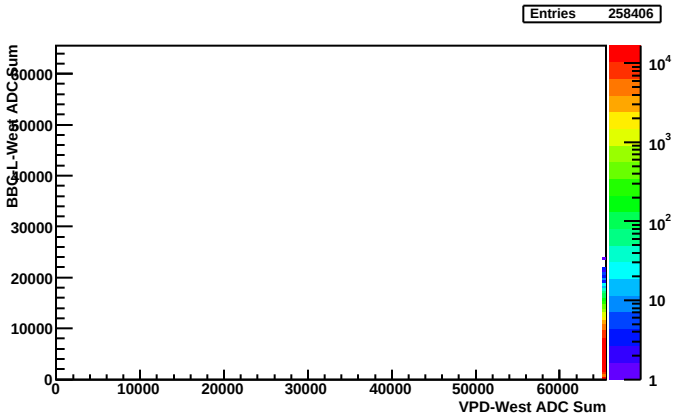

Entries 258406
TOF Mult
ZDC-East ADC Sum Att


Entries 258406
TOF Mult
ZDC-West ADC Sum Att


Entries 258406
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 258406
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 258406
BBC-L-East ADC Sum
VPD-East ADC Sum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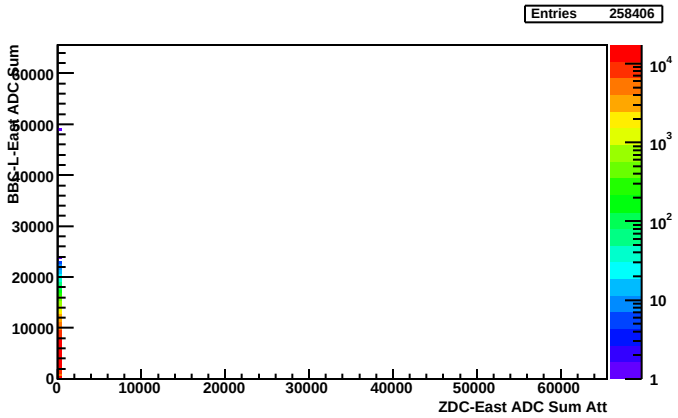
BBC-L-West ADC Sum
VPD-West ADC Sum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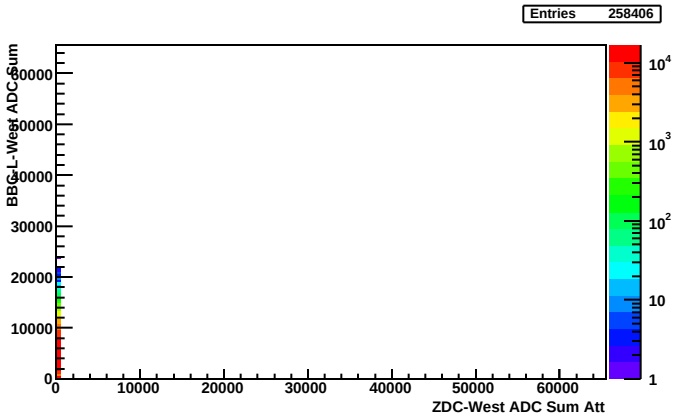
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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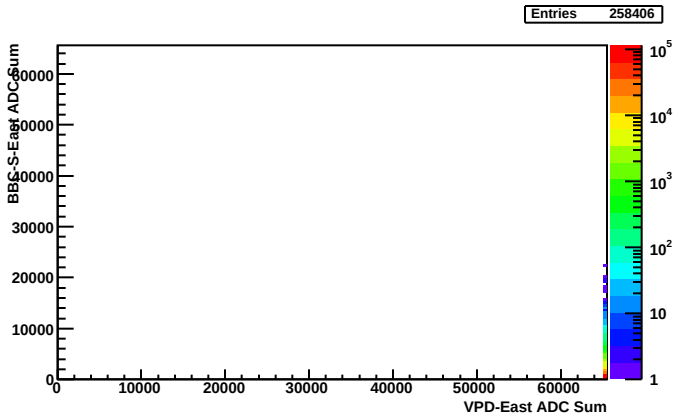
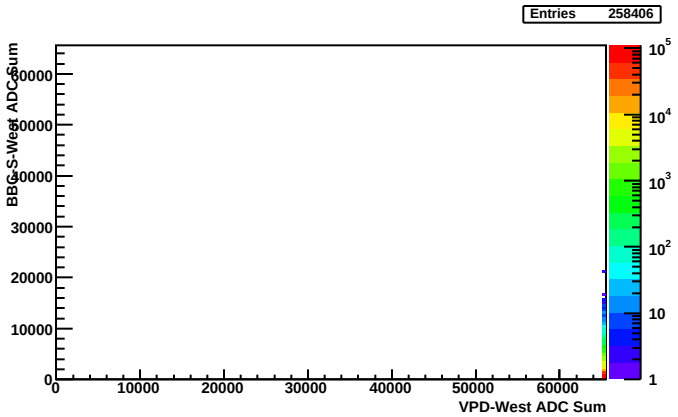
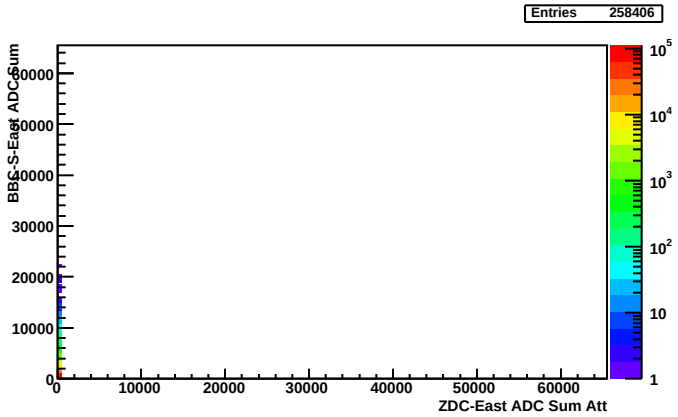
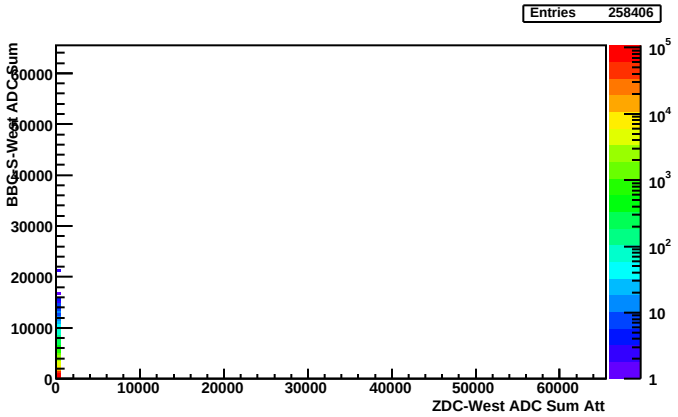
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 258406
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 258406
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 258406
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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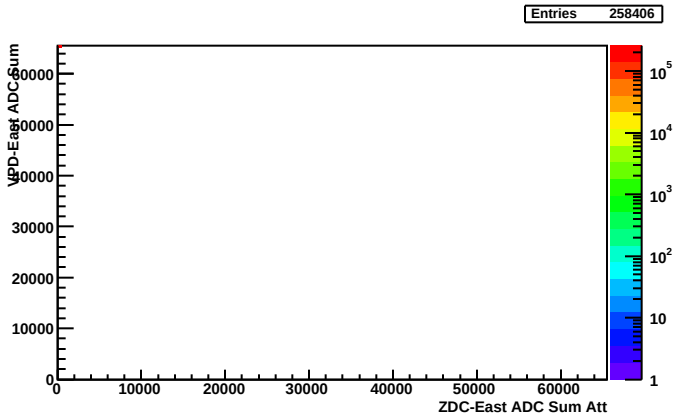
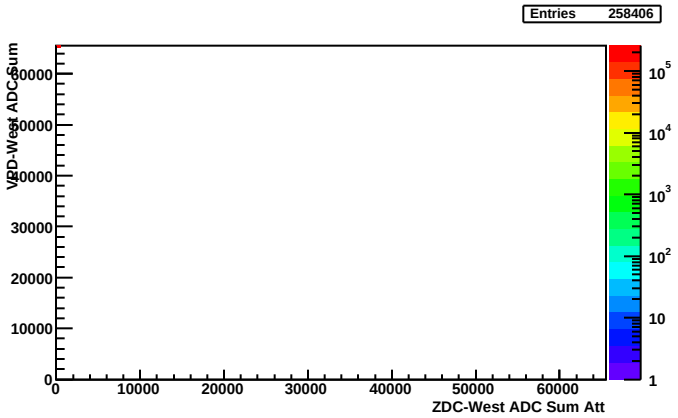
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 258406
VPD-East ADC Sum
ZDC-East ADC Sum Att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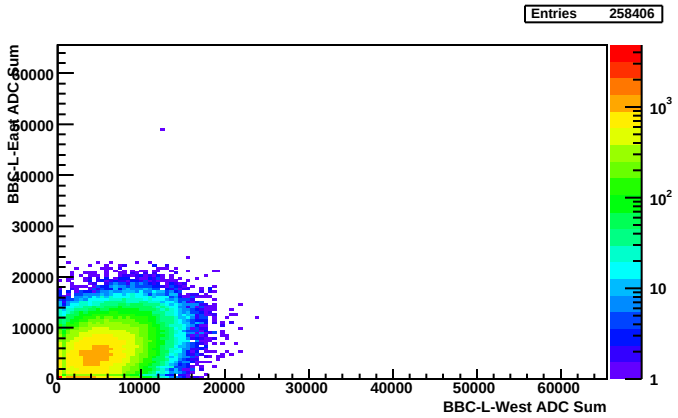
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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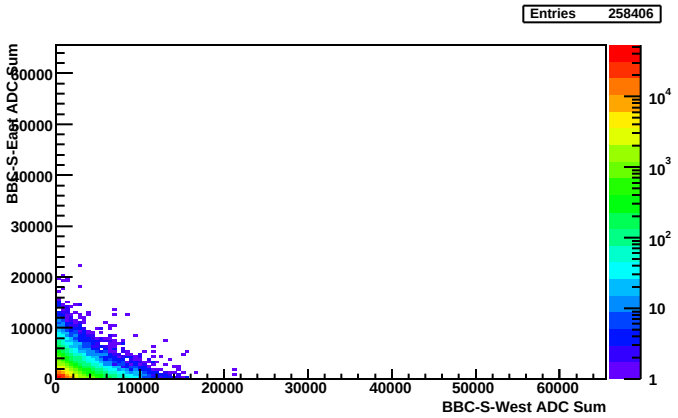
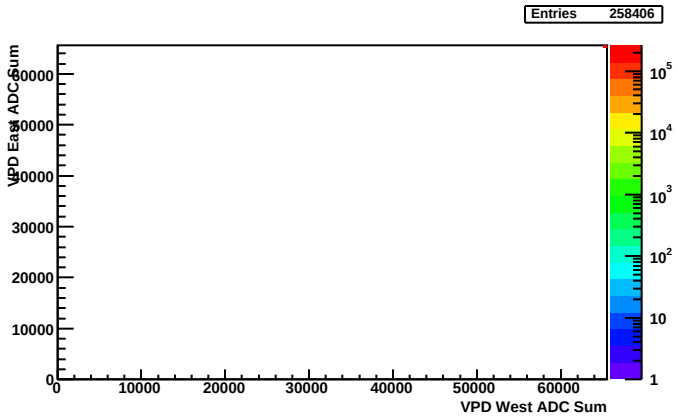
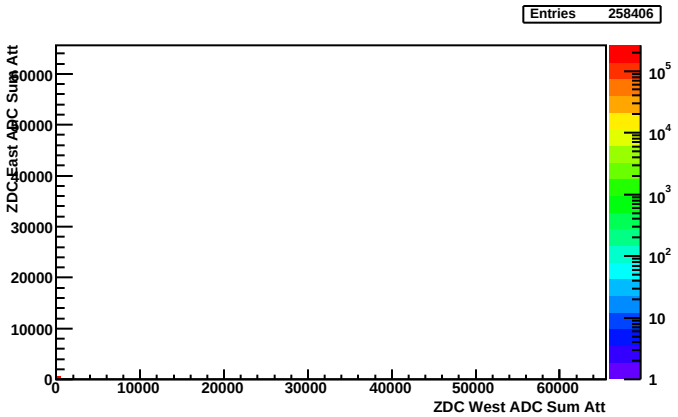
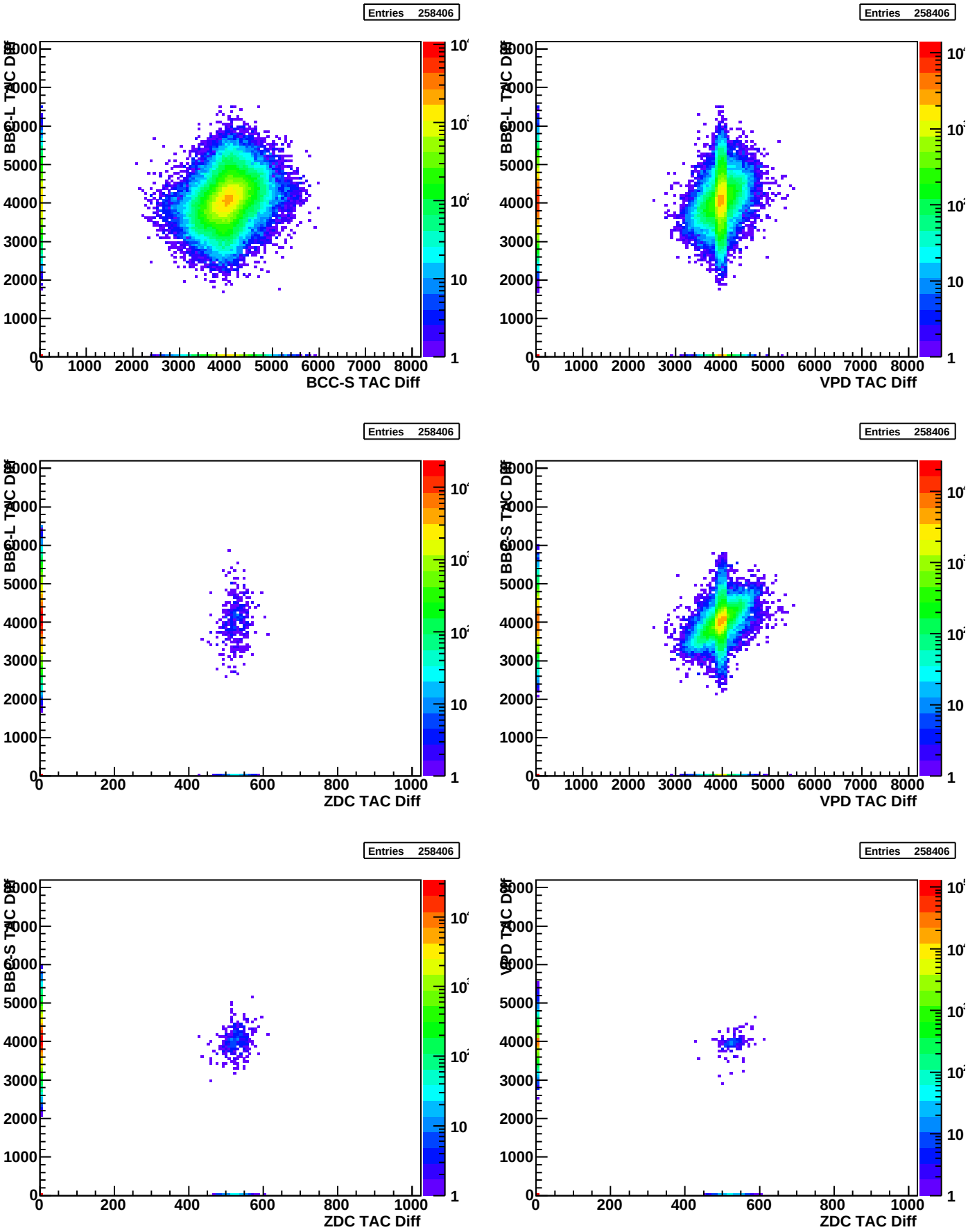
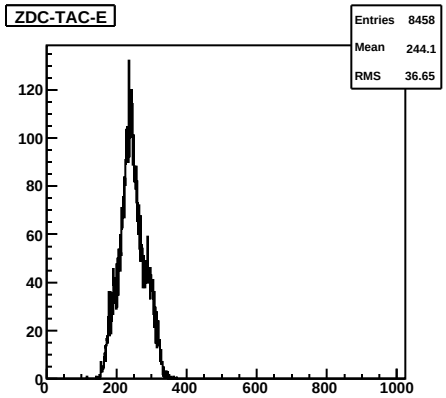
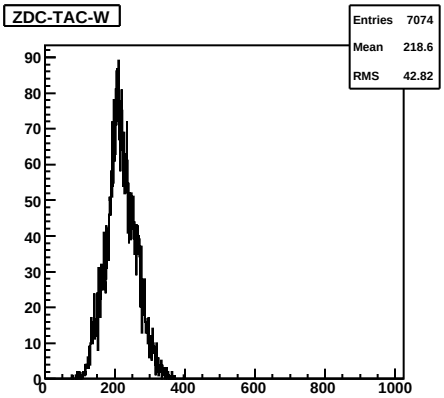

Entries 258406
BBC-S-East ADC Sum
BBC-S-West ADC Sum

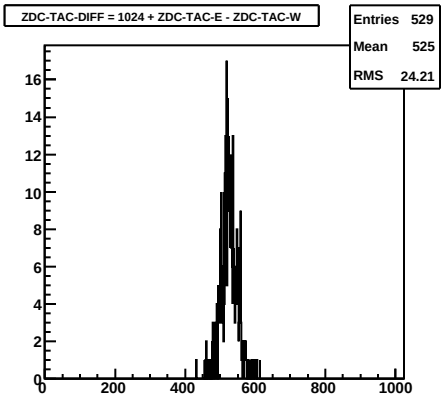

Entries 258406
VPD East ADC Sum
VPD West ADC Sum

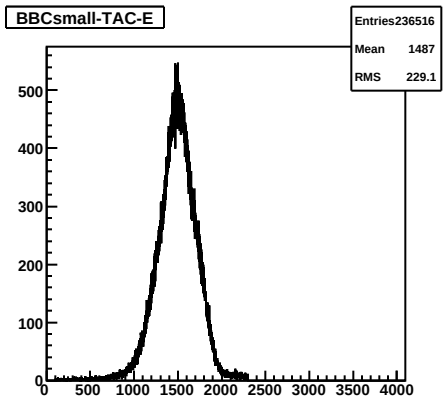

Entries 258406
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

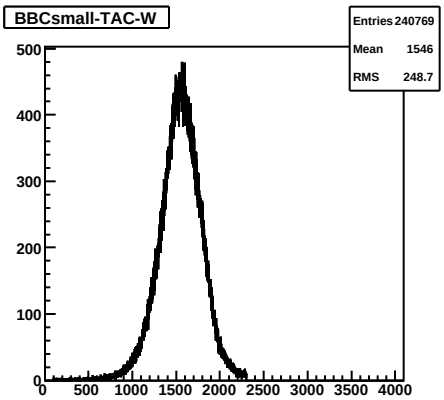

Entries 258406
BBC-L TAC Diff
BCC-S TAC Diff
Entries 258406
BBC-L TAC Diff
VPD TAC Diff
Entries 258406
BBC-L TAC Diff
ZDC TAC Diff
Entries 258406
BBC-S TAC Diff
VPD TAC Diff
Entries 258406
BBC-S TAC Diff
ZDC TAC Diff
Entries 258406
VPD TAC Diff
ZDC TAC Diff

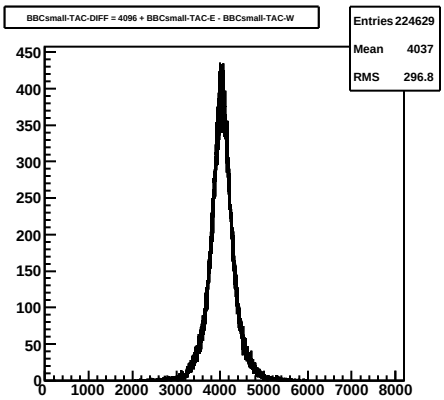

ZDC-TAC-E
Entries 8458
Mean 244.1
RMS 36.65

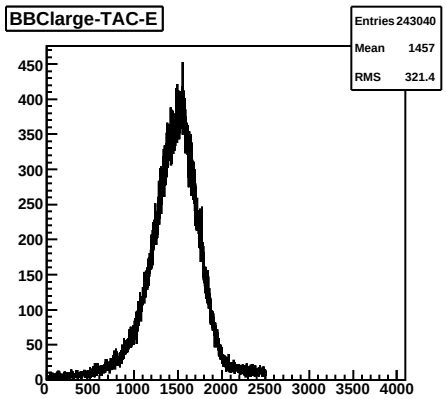

ZDC-TAC-W
Entries 7074
Mean 218.6
RMS 42.82

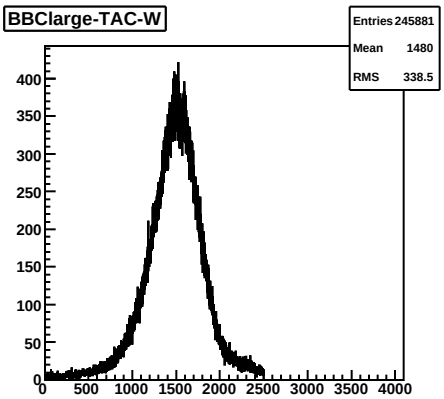

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 529
Mean 525
RMS 24.21

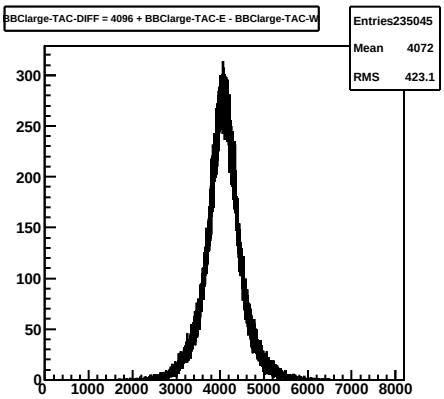

BBCsmall-TAC-E
Entries 236516
Mean 1487
RMS 229.1

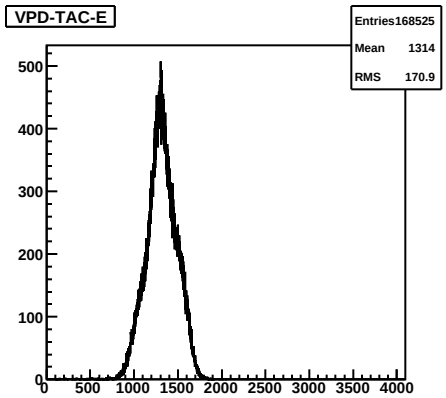

BBCsmall-TAC-W
Entries 240769
Mean 1546
RMS 248.7

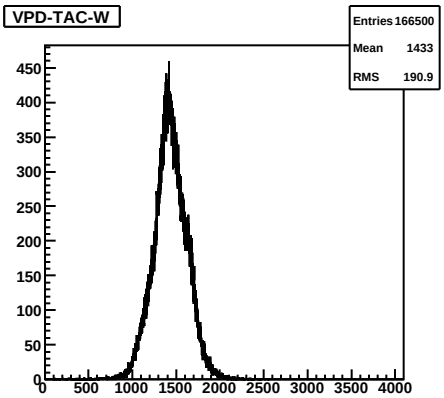

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 224629
Mean 4037
RMS 296.8

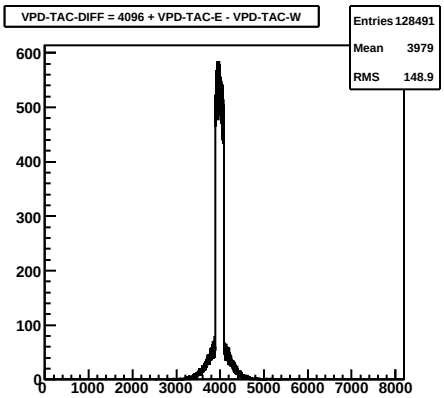

BBClarge-TAC-E
Entries 243040
Mean 1457
RMS 321.4

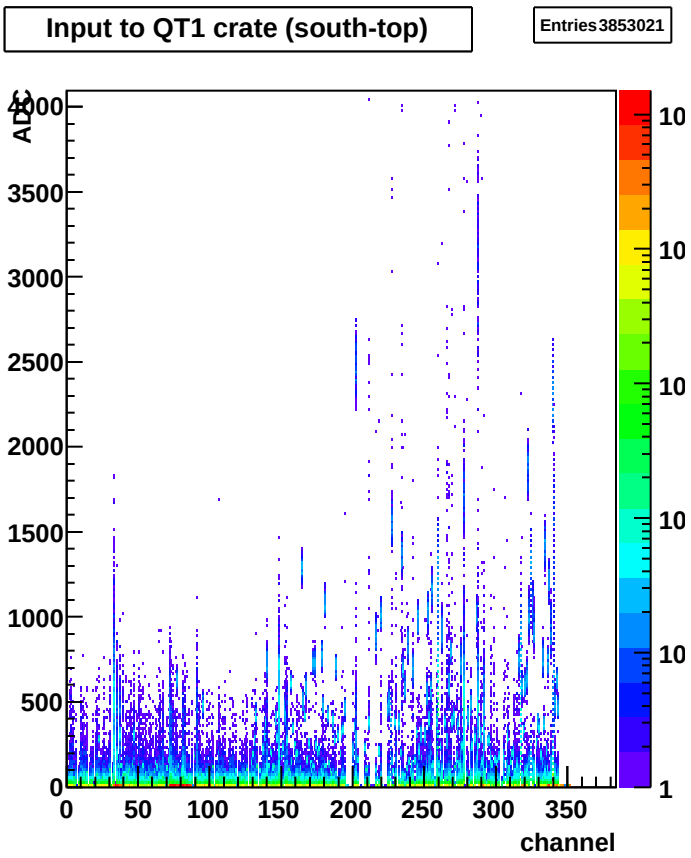

BBClarge-TAC-W
Entries 245881
Mean 1480
RMS 338.5

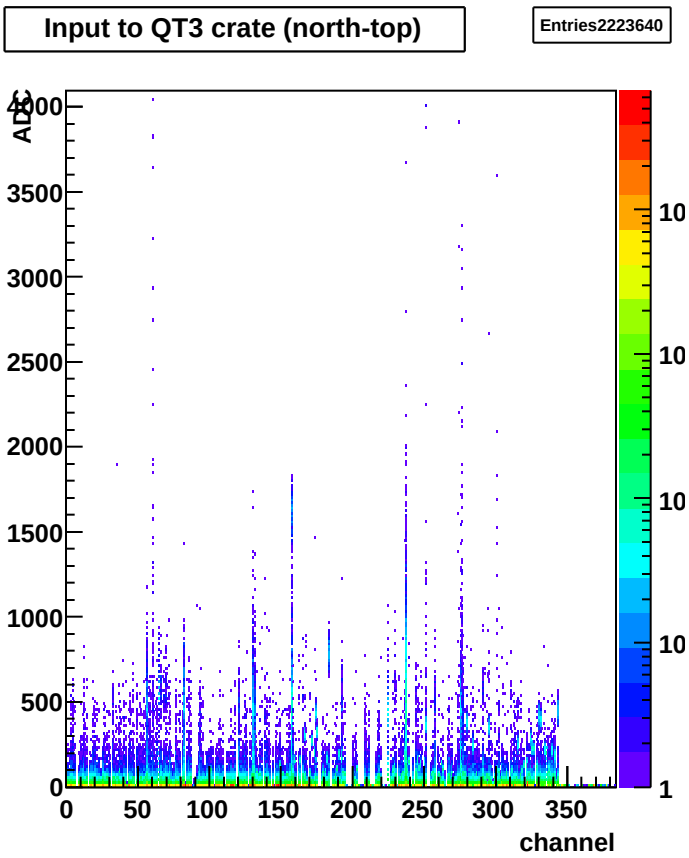

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 235045
Mean 4072
RMS 423.1

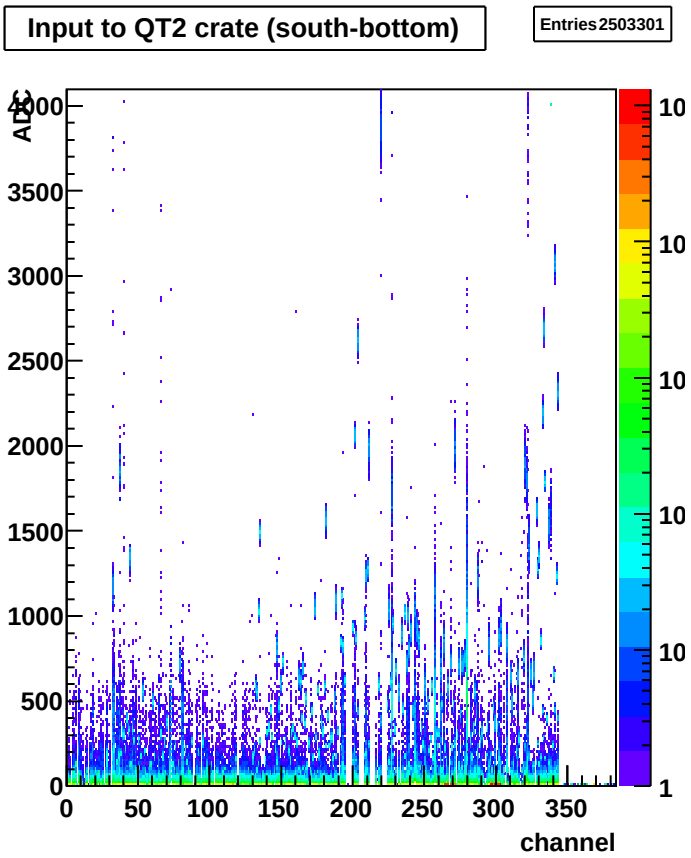

VPD-TAC-E
Entries 168525
Mean 1314
RMS 170.9

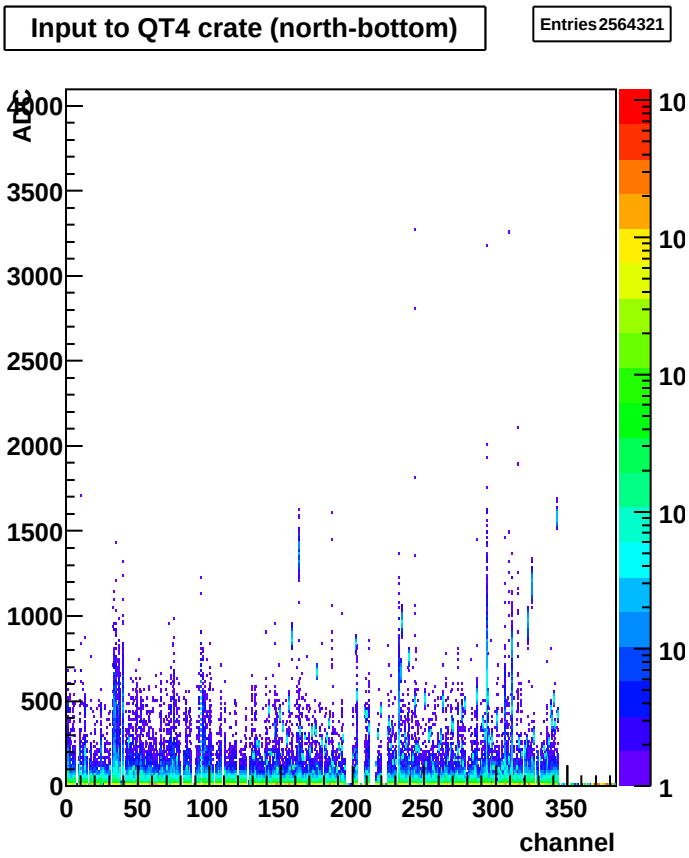

VPD-TAC-W
Entries 166500
Mean 1433
RMS 190.9

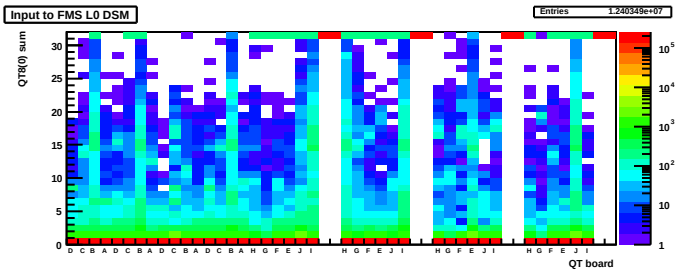

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 128491
Mean 3979
RMS 148.9

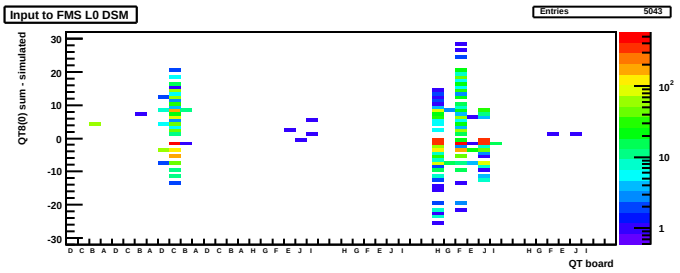

Input to QT1 crate (south-top)
Entries 3853021
ADC
channel

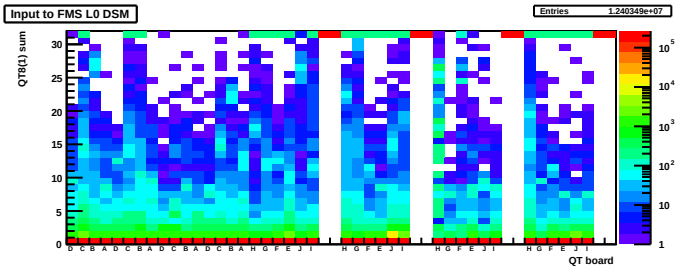

Input to QT3 crate (north-top)
Entries 2223640
ADC
channel

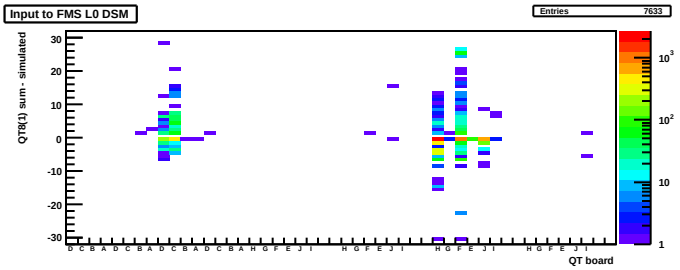

Input to QT2 crate (south-bottom)
Entries 2503301
ADC
channel

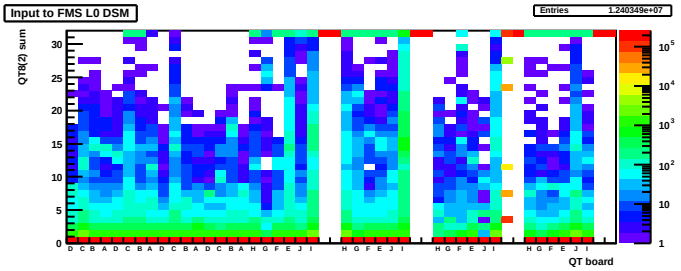

Input to QT4 crate (north-bottom)
Entries 2564321
ADC
channel

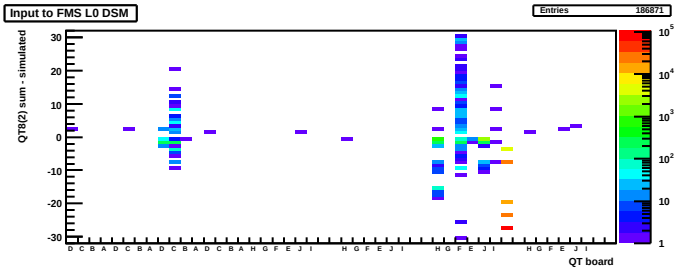

Input to FMS L0 DSM
Entries 1.240349e+07
QT8(0) sum
QT board

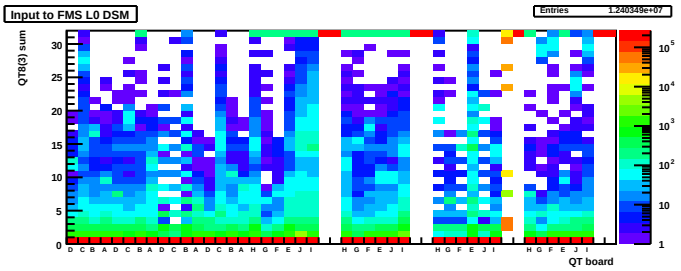

Input to FMS L0 DSM
Entries 5043
QT8(0) sum - simulated
QT board

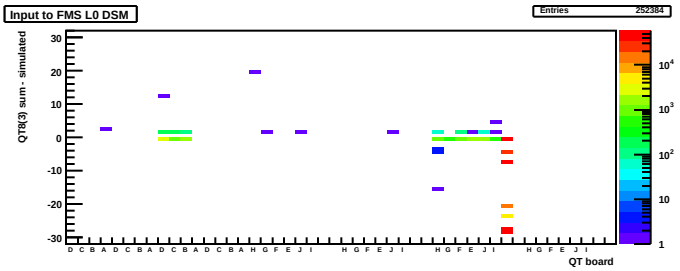

Input to FMS L0 DSM
Entries 1.240349e+07
QT8(1) sum
QT board

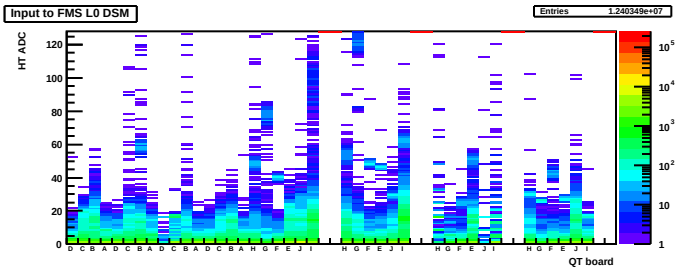

Input to FMS L0 DSM
Entries 7633
QT8(1) sum - simulated
QT board

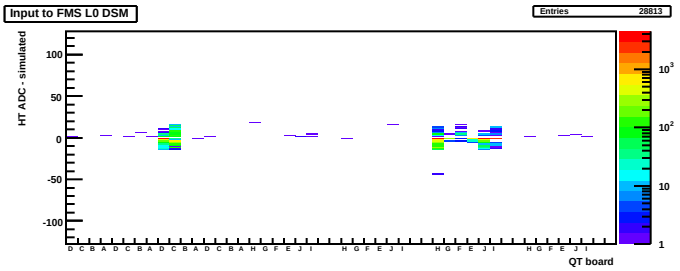

Input to FMS L0 DSM
Entries 1.240349e+07
QT8(2) sum
QT board

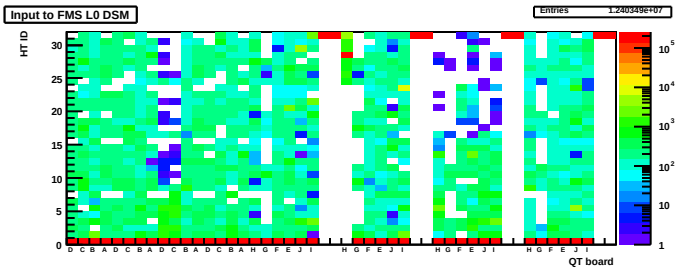

Input to FMS L0 DSM
Entries 186871
QT8(2) sum - simulated
QT board

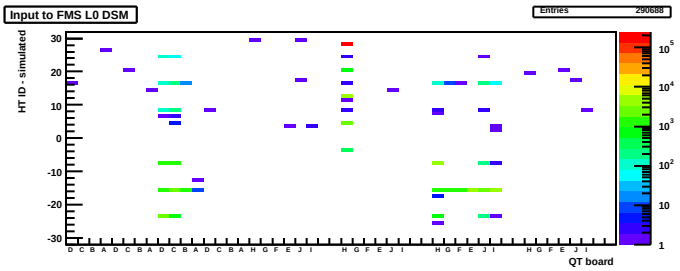

Input to FMS L0 DSM
Entries 1.240349e+07
QT8(3) sum
QT board

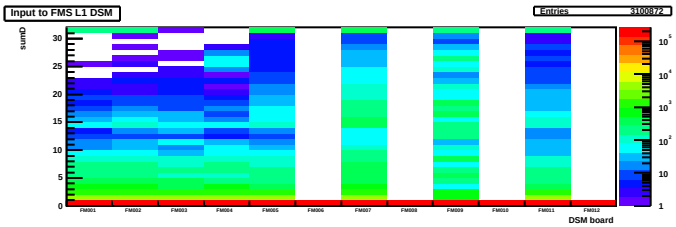

Input to FMS L0 DSM
Entries 252384
QT8(3) sum - simulated
QT board

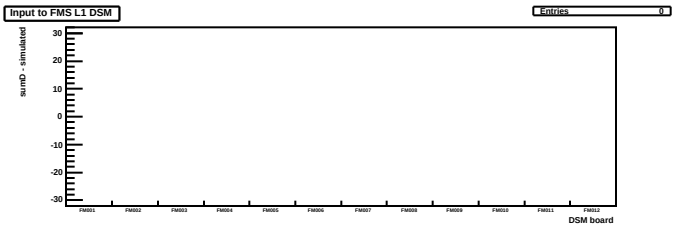

Input to FMS L0 DSM
Entries 1.240349e+07
HT ADC
QT board

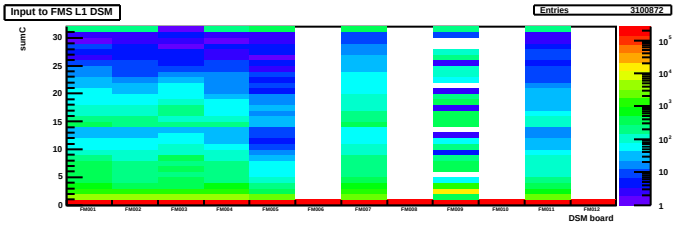

Input to FMS L0 DSM
Entries 28813
HT ADC - simulated
QT board

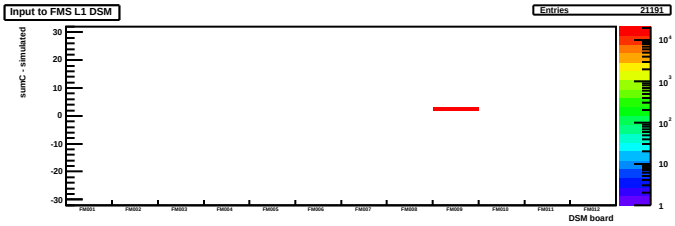

Input to FMS L0 DSM
Entries 1.240349e+07
HT ID
QT board

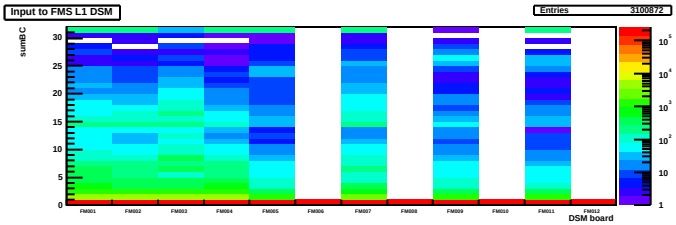

Input to FMS L0 DSM
Entries 290688
HT ID - simulated
QT board

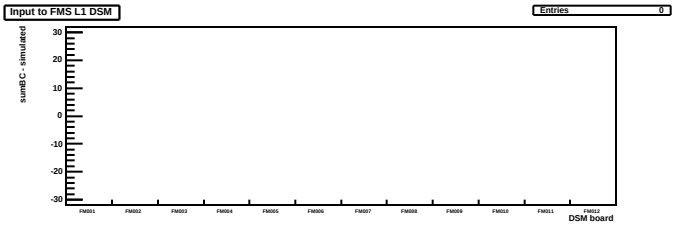

Input to FMS L1 DSM
Entries 3100872
sumD
DSM board

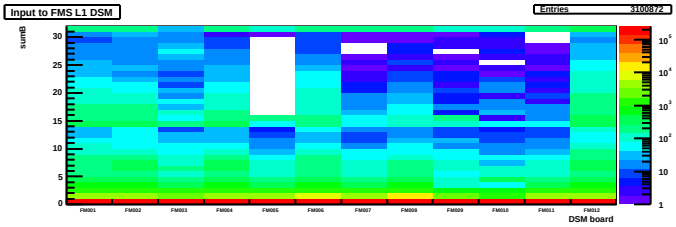

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

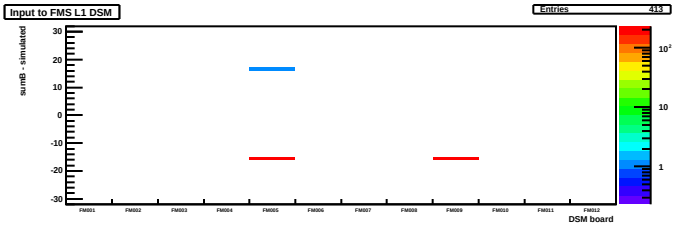

Input to FMS L1 DSM
Entries 3100872
sumC
DSM board

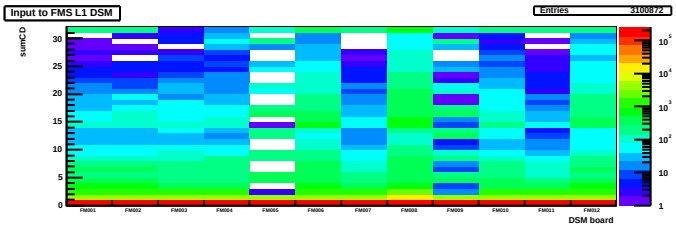

Input to FMS L1 DSM
Entries 21191
sumC - simulated
DSM board

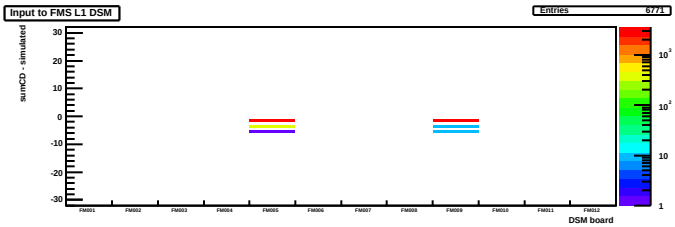

Input to FMS L1 DSM
Entries 3100872
sumBC
DSM board

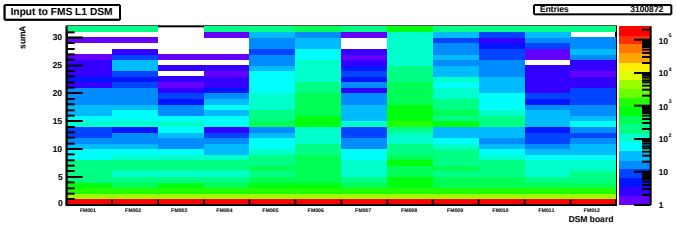

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

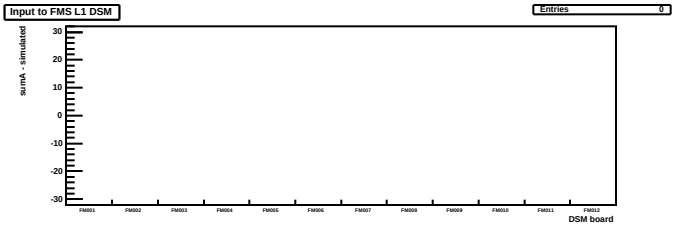

Input to FMS L1 DSM
Entries 3100872
sumB
DSM board

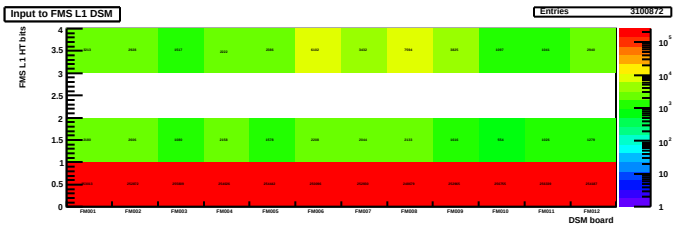

Input to FMS L1 DSM
Entries 413
sumB - simulated
DSM board

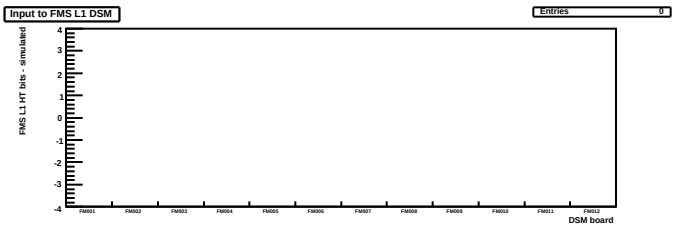

Input to FMS L1 DSM
Entries 3100872
sumCD
DSM board

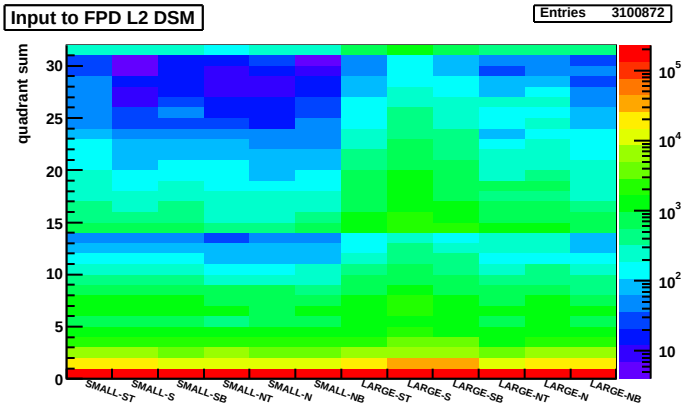

Input to FMS L1 DSM
Entries 6771
sumCD - simulated
DSM board

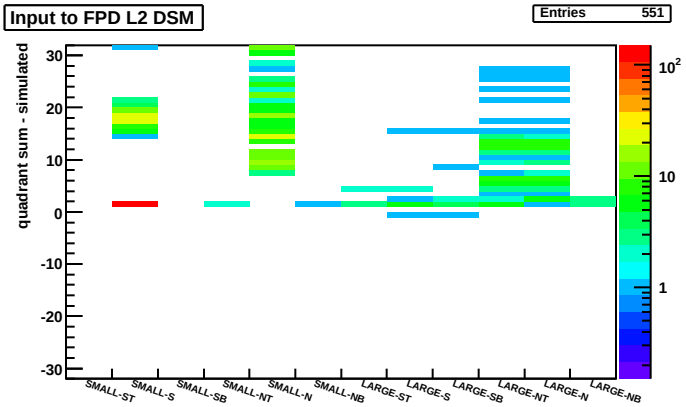

Input to FMS L1 DSM
Entries 3100872
sumA
DSM board

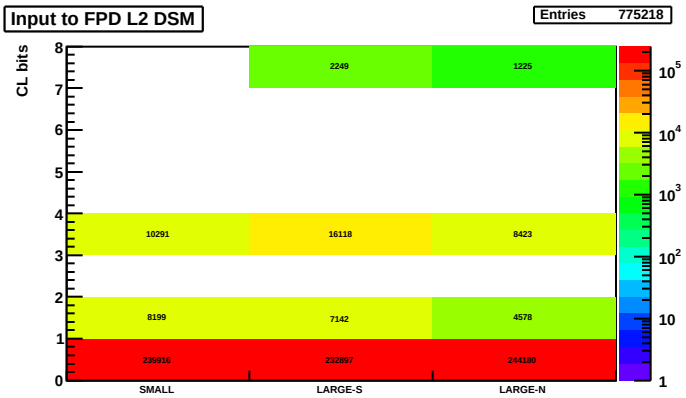

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

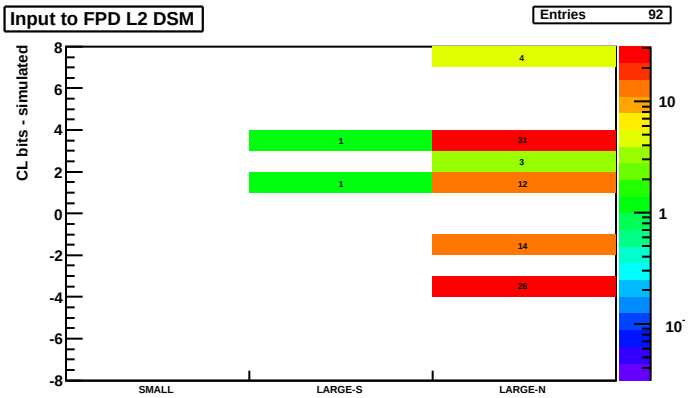

Input to FMS L1 DSM
Entries 3100872
FMS L1 HT bits
DSM board

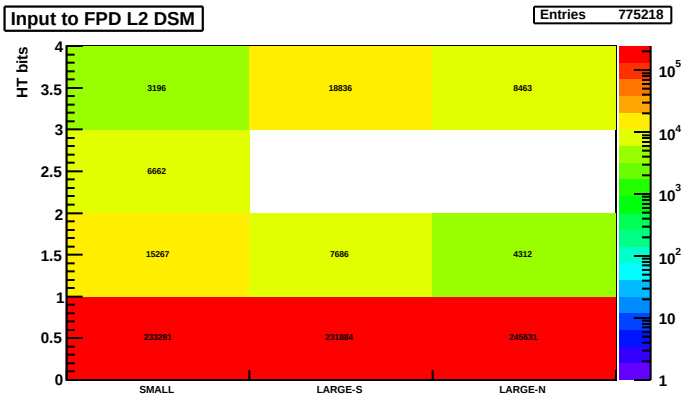

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

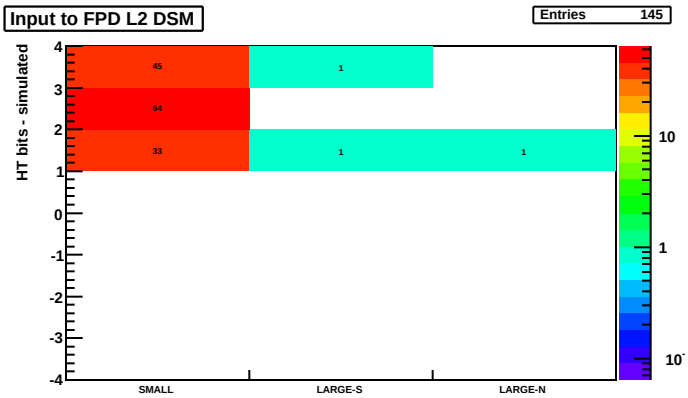

Input to FPD L2 DSM
Entries 3100872
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

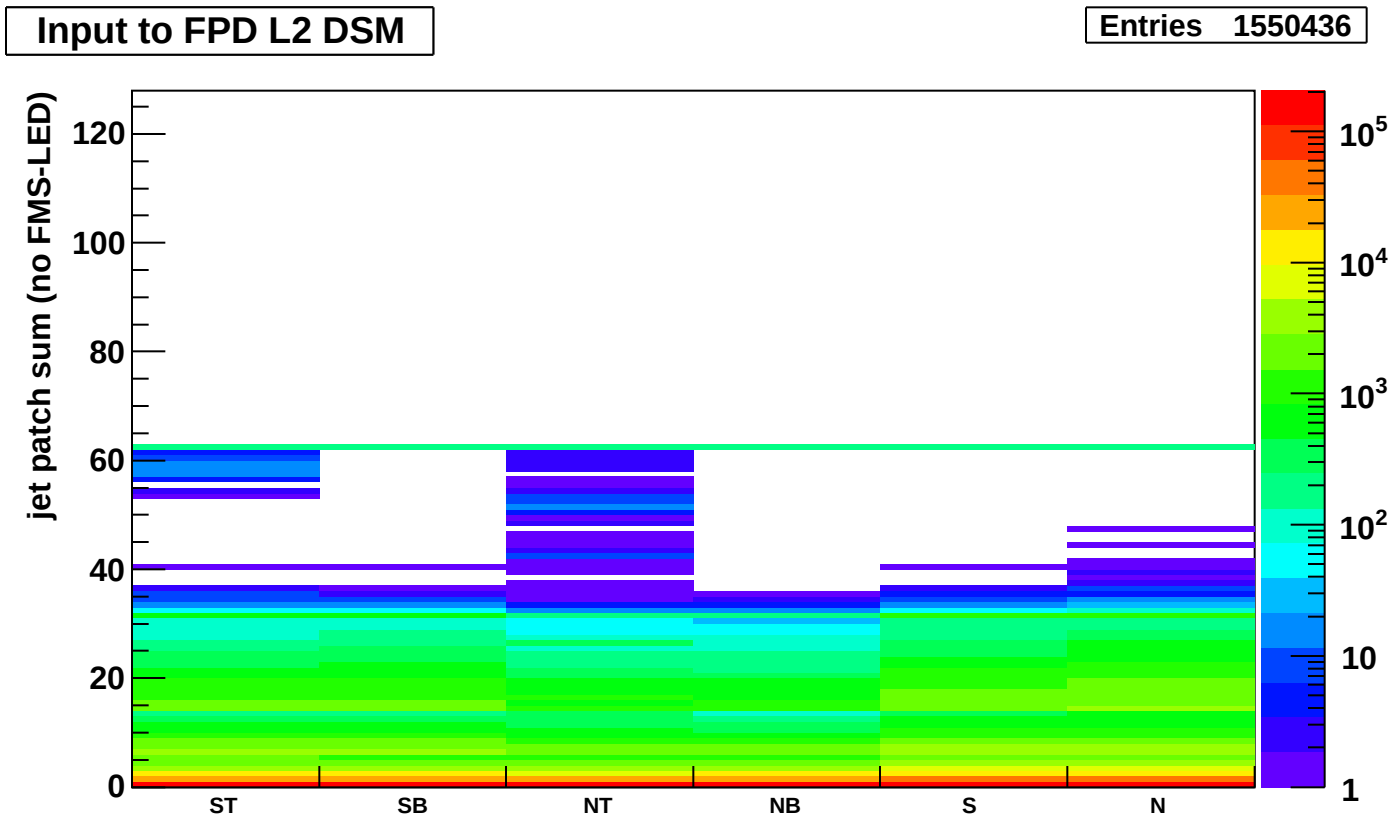

Input to FPD L2 DSM
Entries 551
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

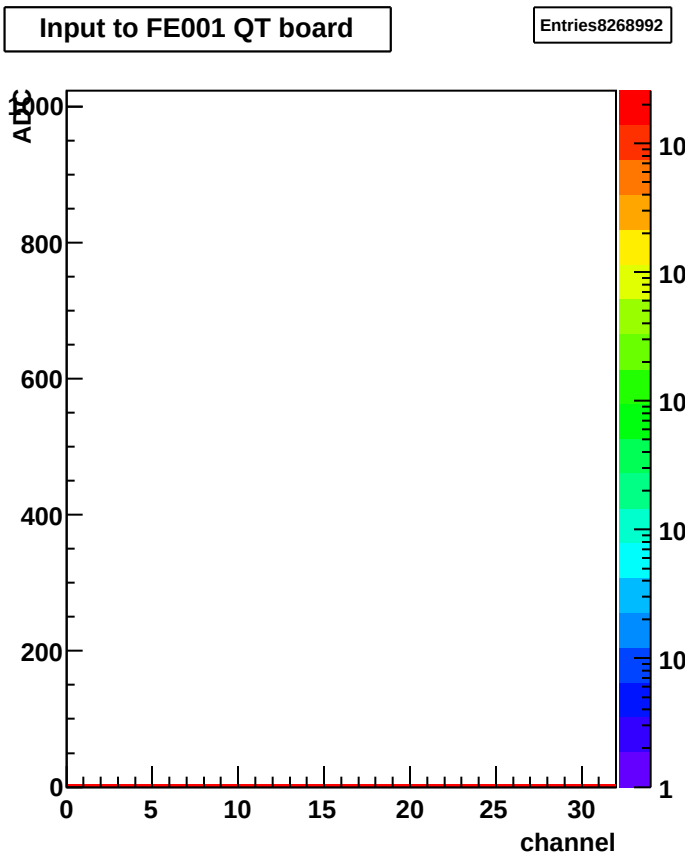

Input to FPD L2 DSM
Entries 775218
CL bits
2249
1225
10291
16118
8423
8199
7142
4578
239916
232897
244180
SMALL
LARGE-S
LARGE-N

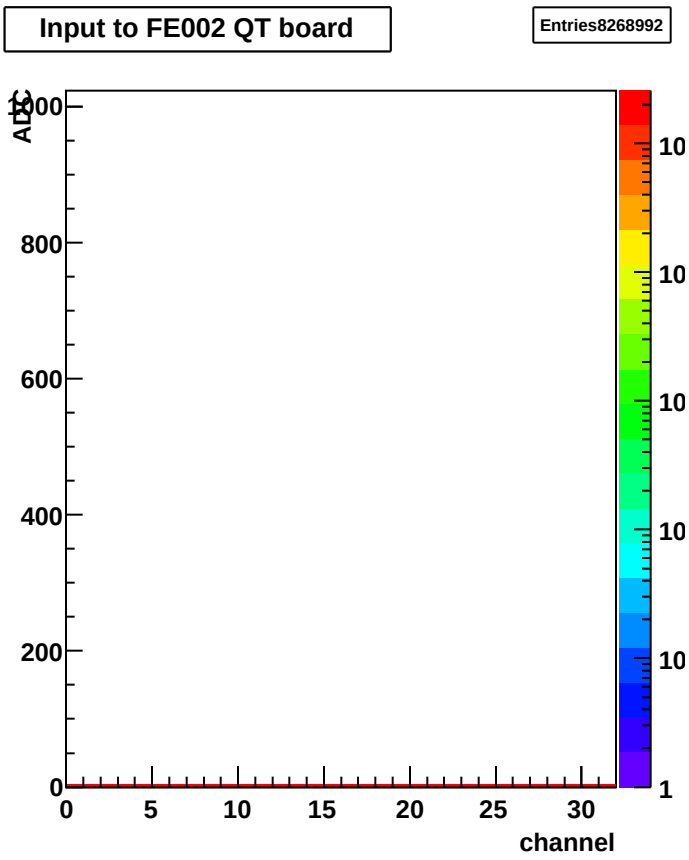

Input to FPD L2 DSM
Entries 92
CL bits - simulated
4
1
31
3
1
12
14
26
SMALL
LARGE-S
LARGE-N

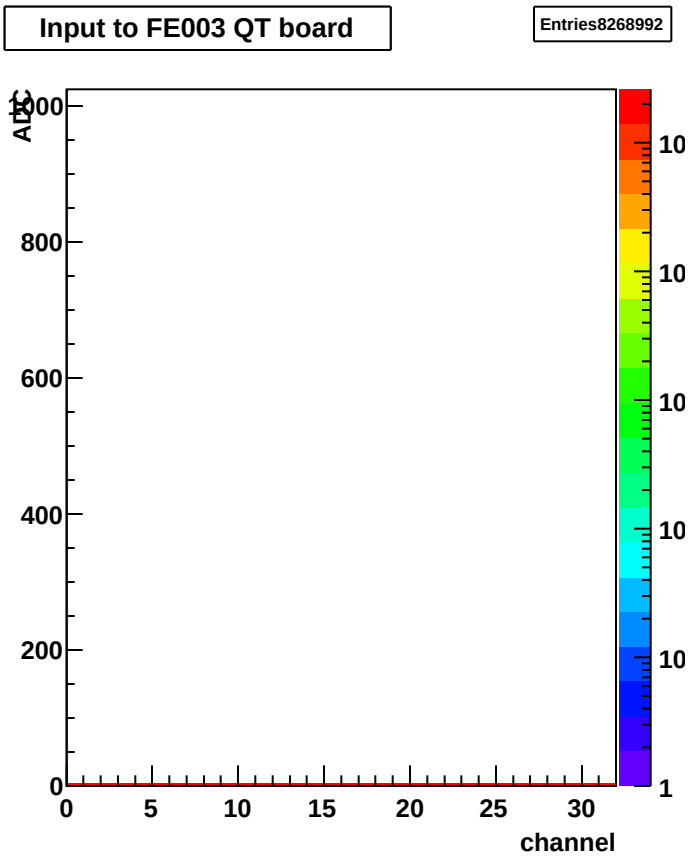

Input to FPD L2 DSM
Entries 775218
HT bits
3196
18836
8463
6662
15267
7686
4312
233281
231884
245631
SMALL
LARGE-S
LARGE-N

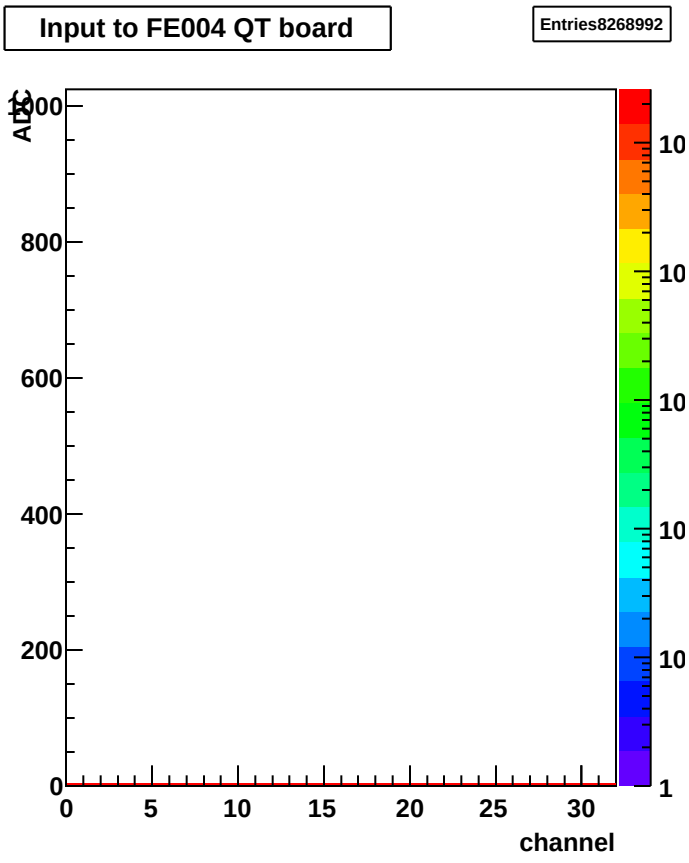

Input to FPD L2 DSM
Entries 145
HT bits - simulated
45
1
64
33
1
1
SMALL
LARGE-S
LARGE-N

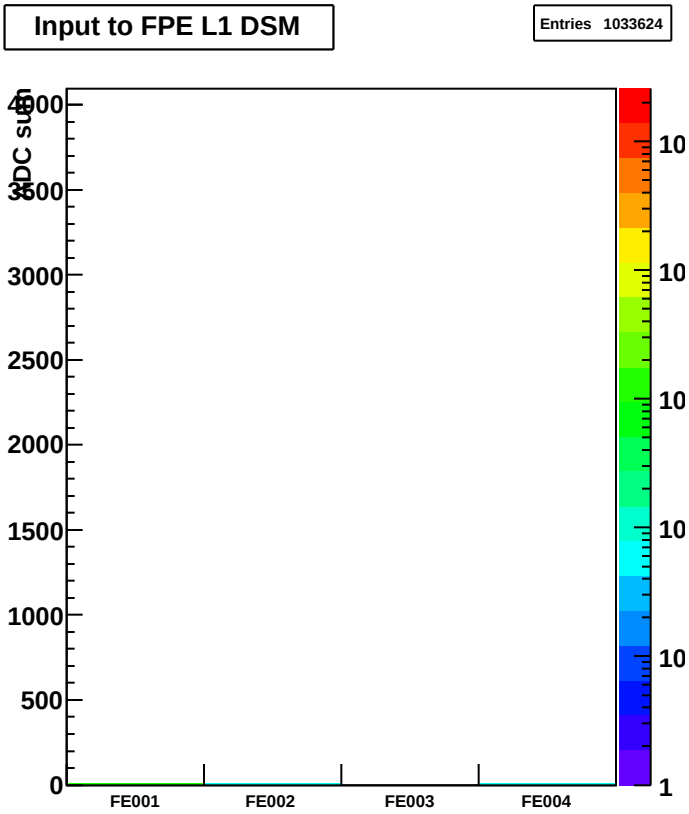

Input to FPD L2 DSM
Entries 1550436
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

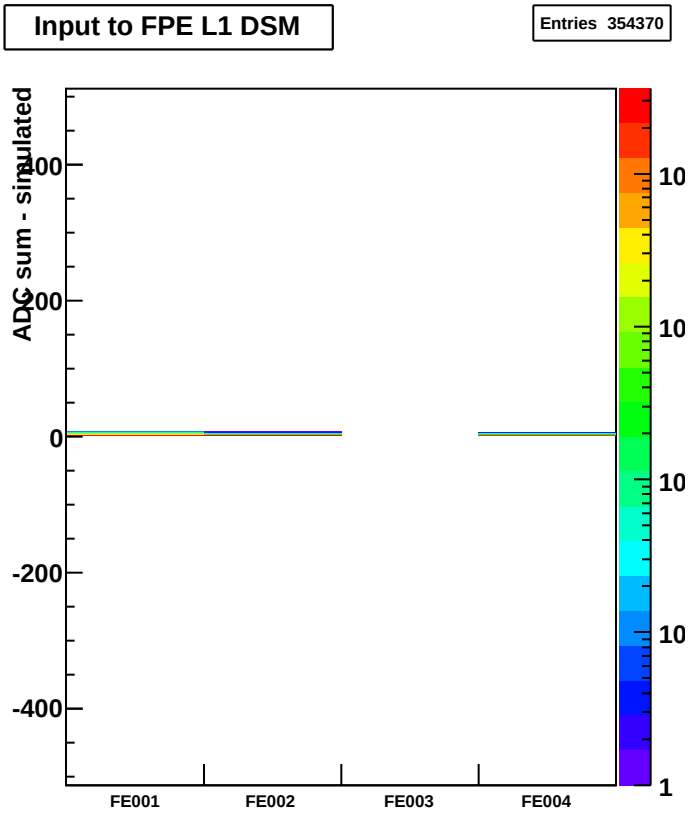

Input to FE001 QT board
Entries8268992
ADC
channel


Input to FE002 QT board
Entries8268992
ADC
channel

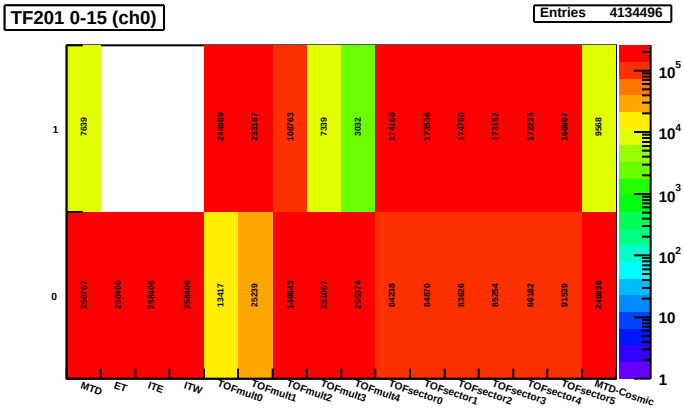

Input to FE003 QT board
Entries8268992
ADC
channel


Input to FE004 QT board
Entries8268992
ADC
channel

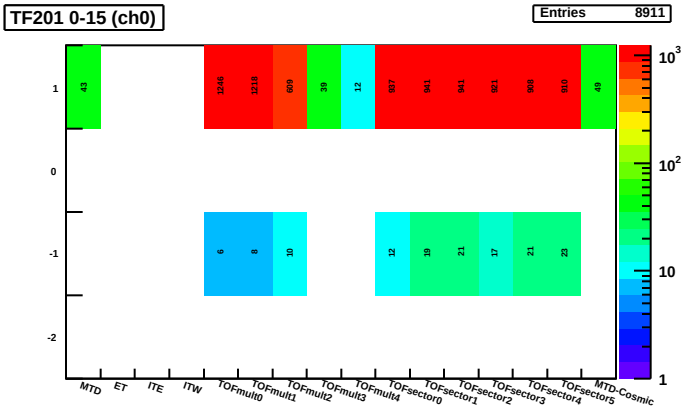

Input to FPE L1 DSM
Entries 1033624
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 354370
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

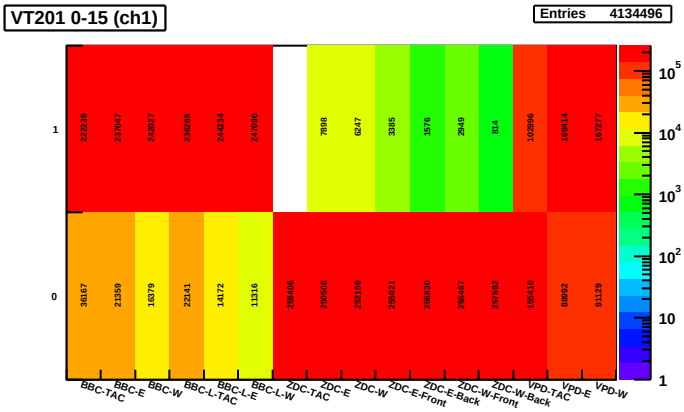
TF201 0-15 (ch0)



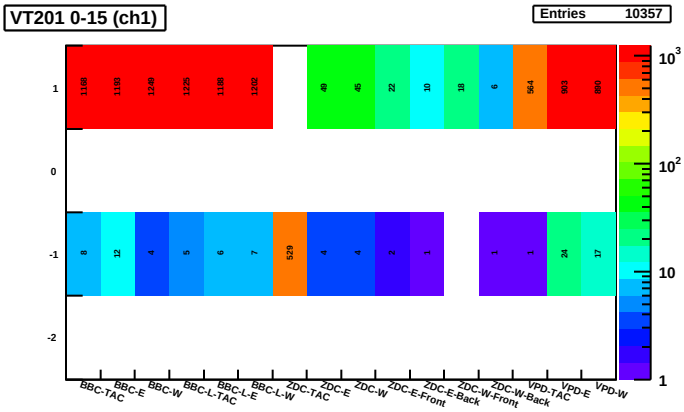
TF201 0-15 (ch0)



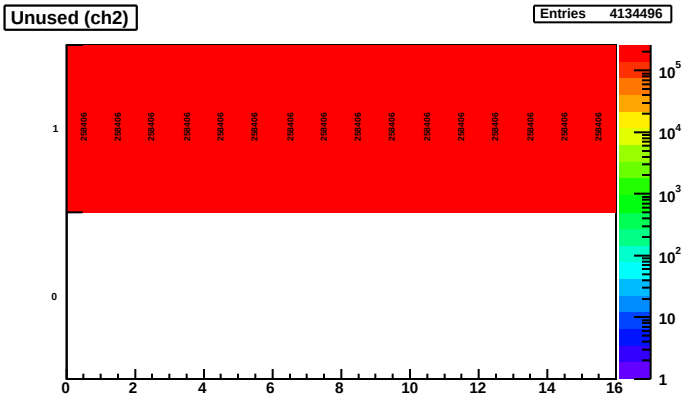
VT201 0-15 (ch1)

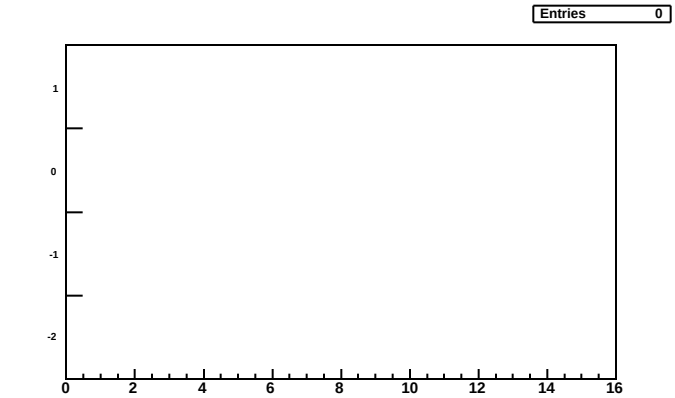


VT201 0-15 (ch1)

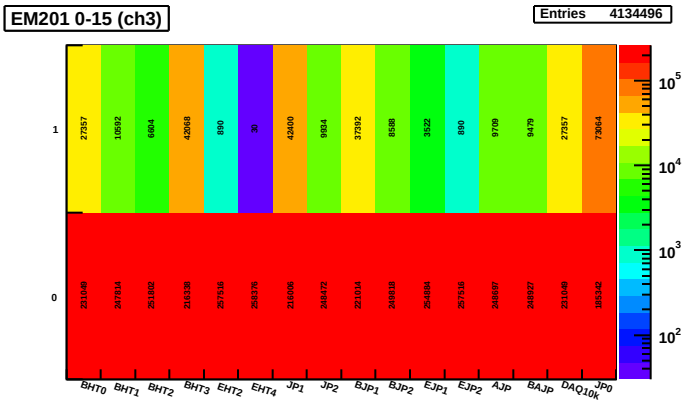


Unused (ch2)





EM201 0-15 (ch3)



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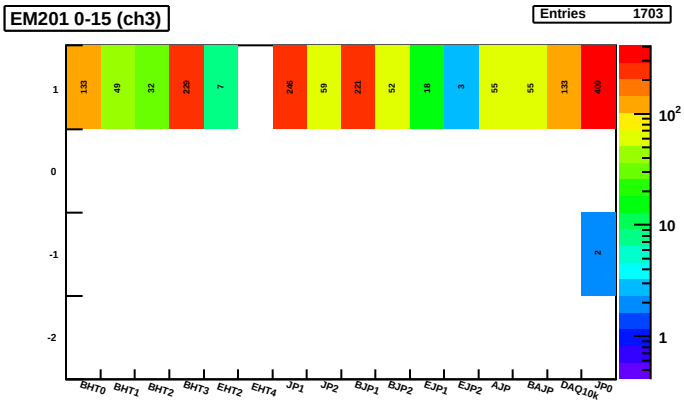
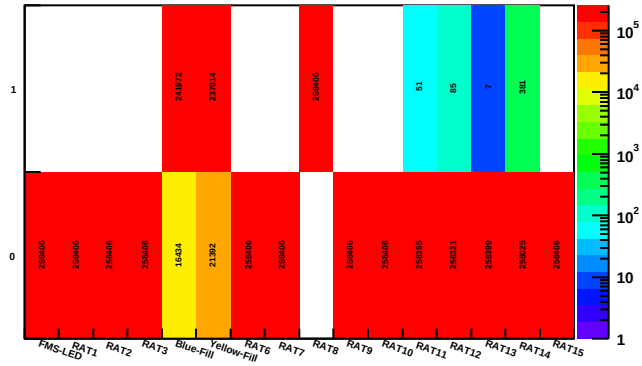
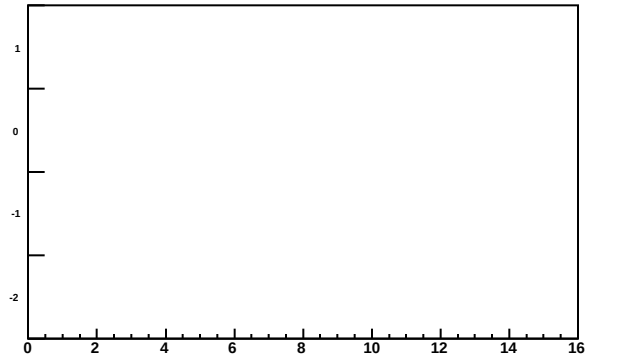
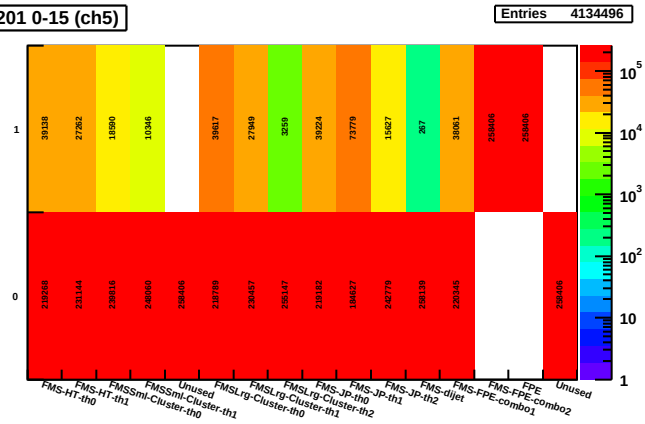


RAT board (ch4)





FP201 0-15 (ch5)



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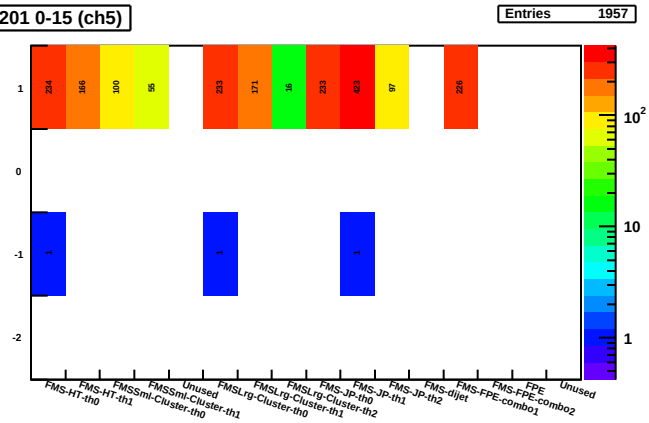

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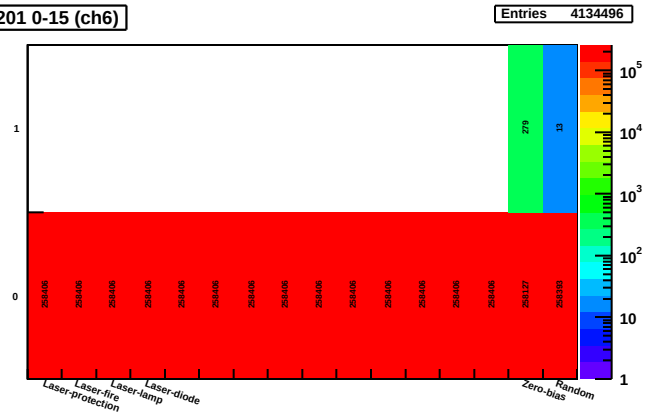

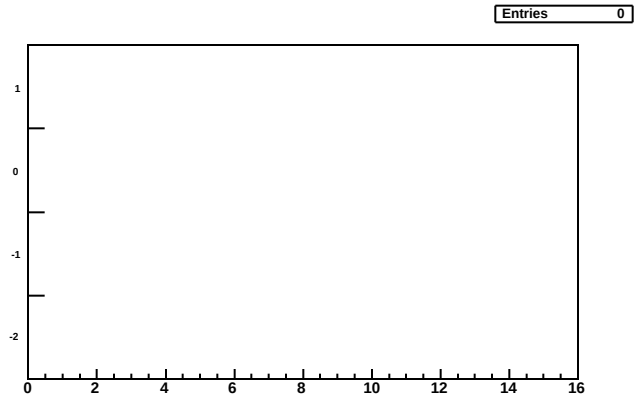
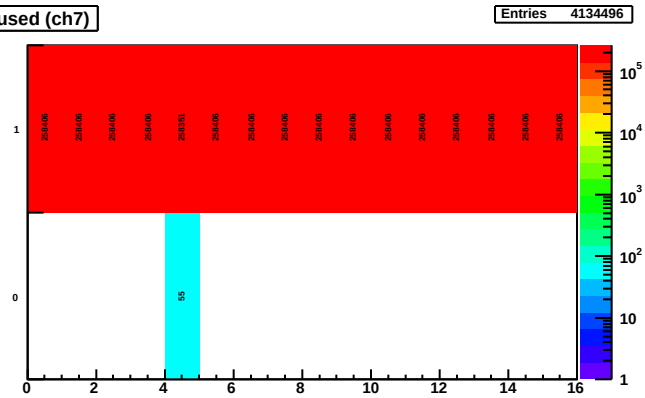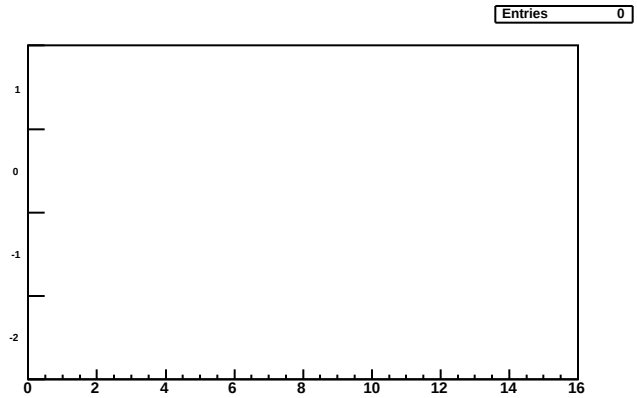
Unused (ch7)