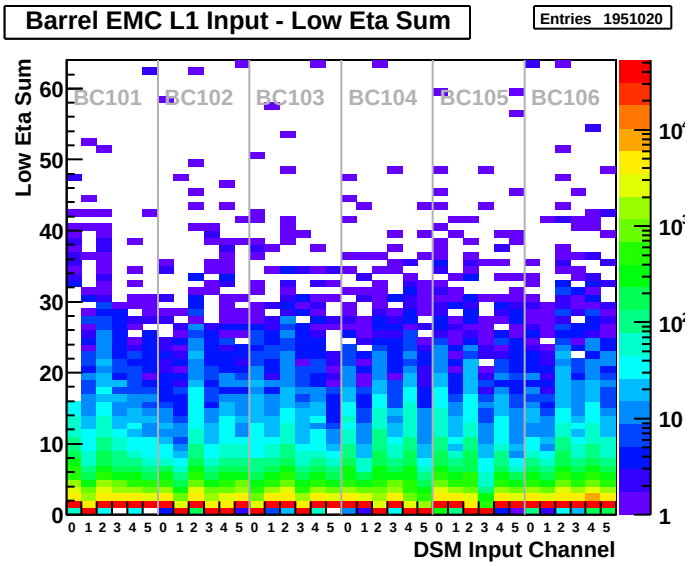
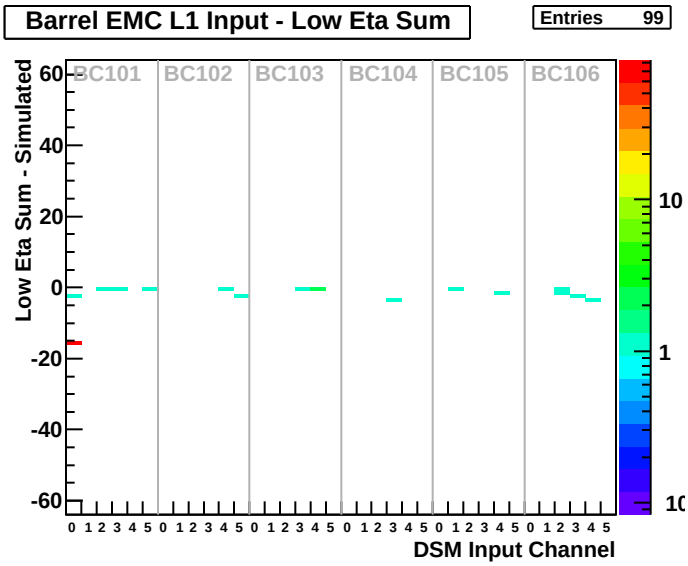
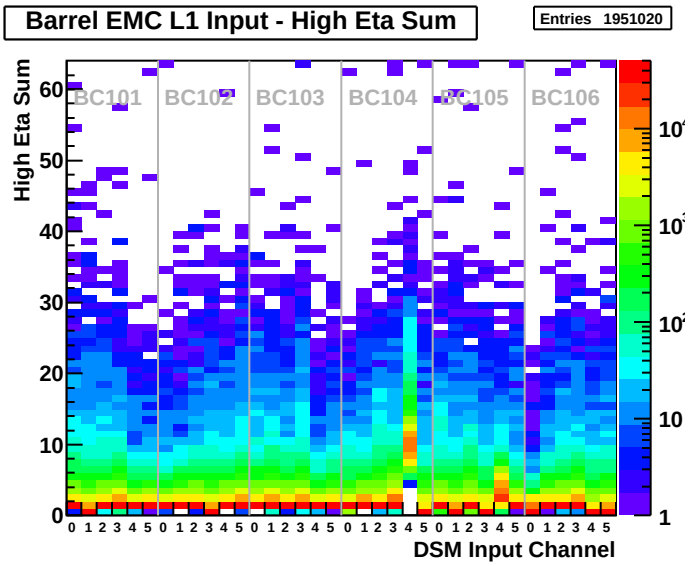
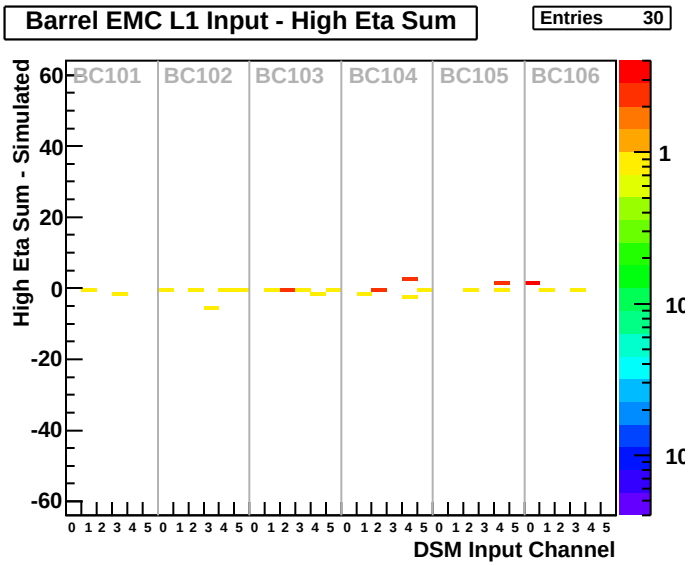
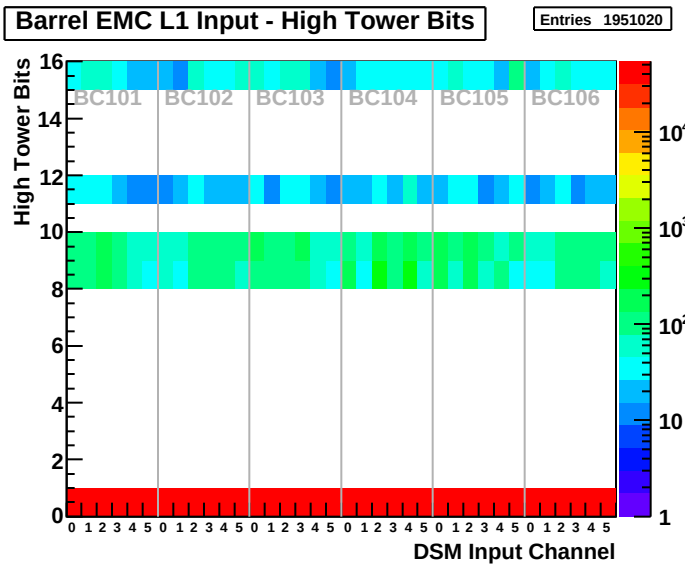
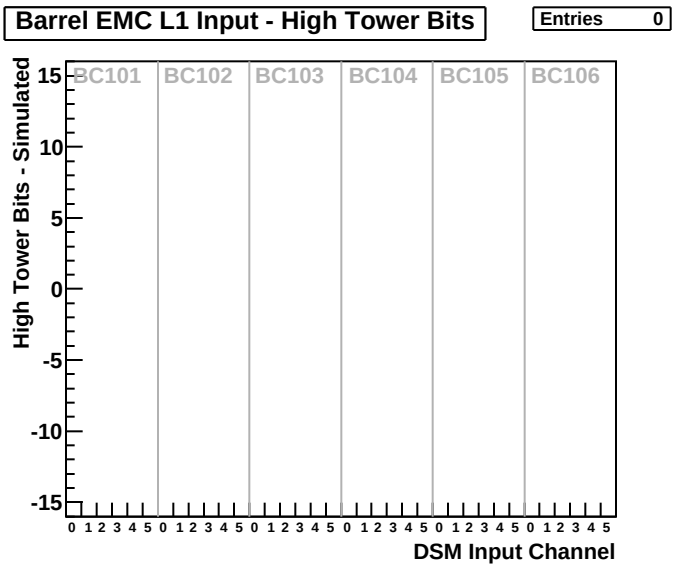

Barrel EMC L1 Input - Low Eta Sum
Entries 1951020
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

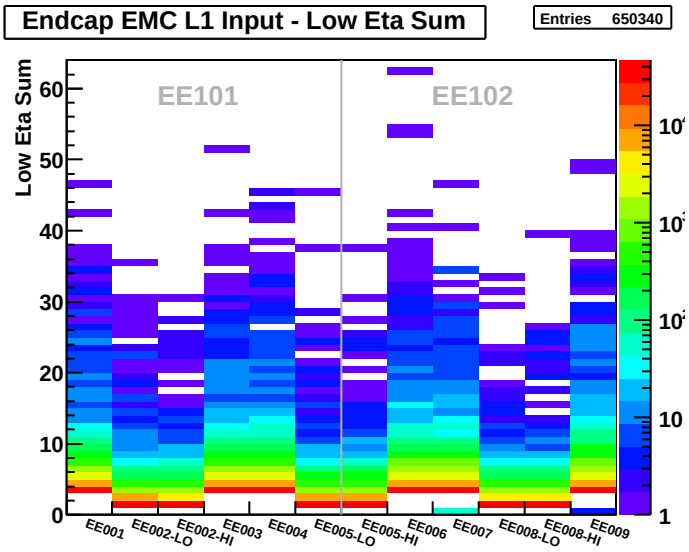

Barrel EMC L1 Input - Low Eta Sum
Entries 99
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

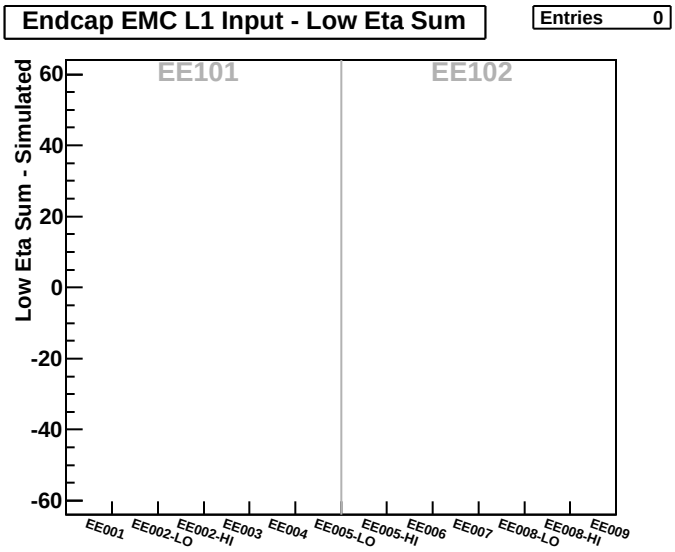

Barrel EMC L1 Input - High Eta Sum
Entries 1951020
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

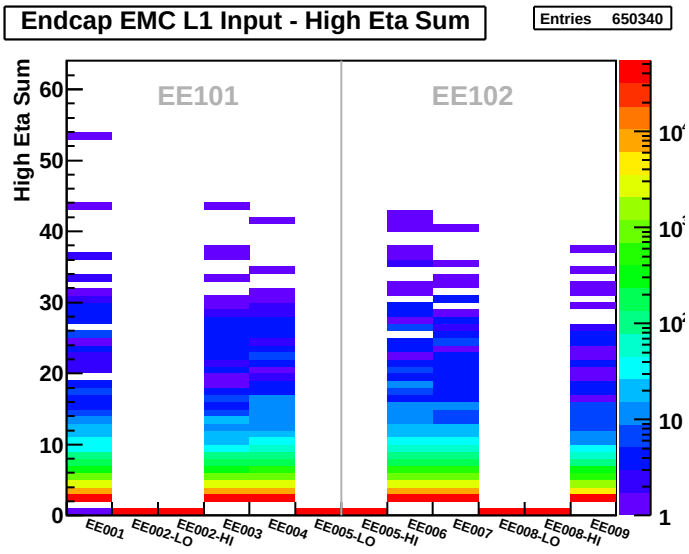

Barrel EMC L1 Input - High Eta Sum
Entries 30
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

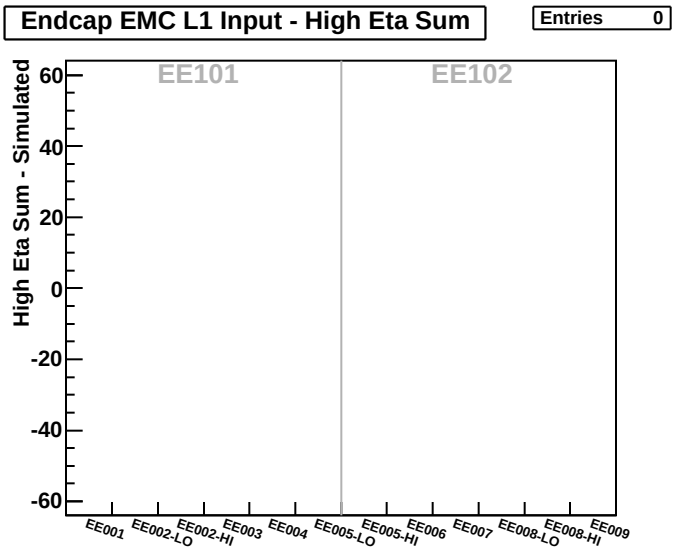

Barrel EMC L1 Input - High Tower Bits
Entries 1951020
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

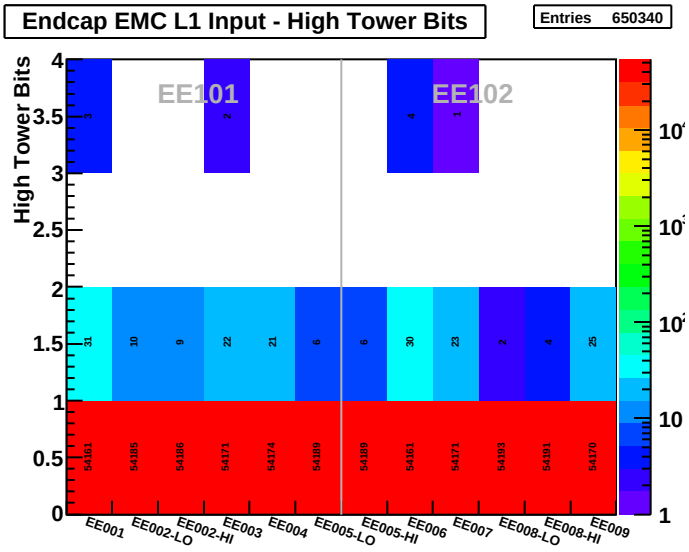

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

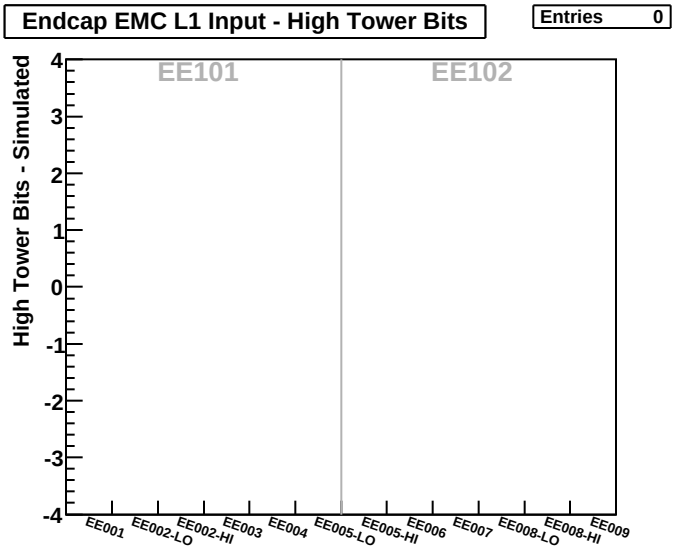

Endcap EMC L1 Input - Low Eta Sum
Entries 650340
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

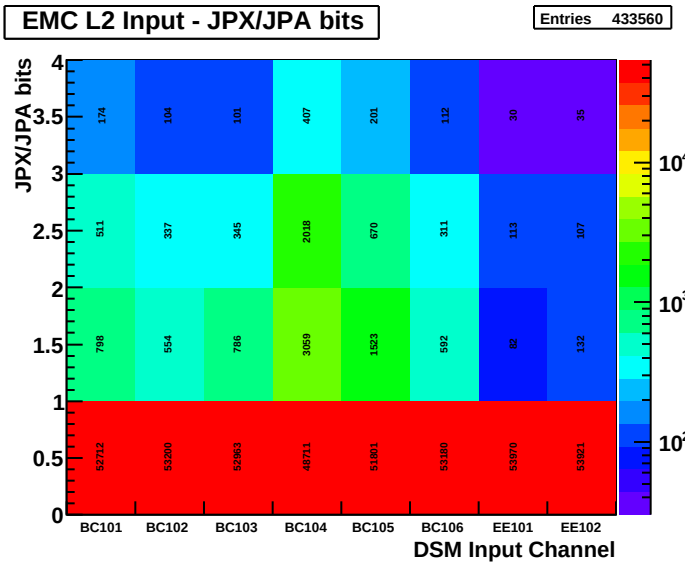

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

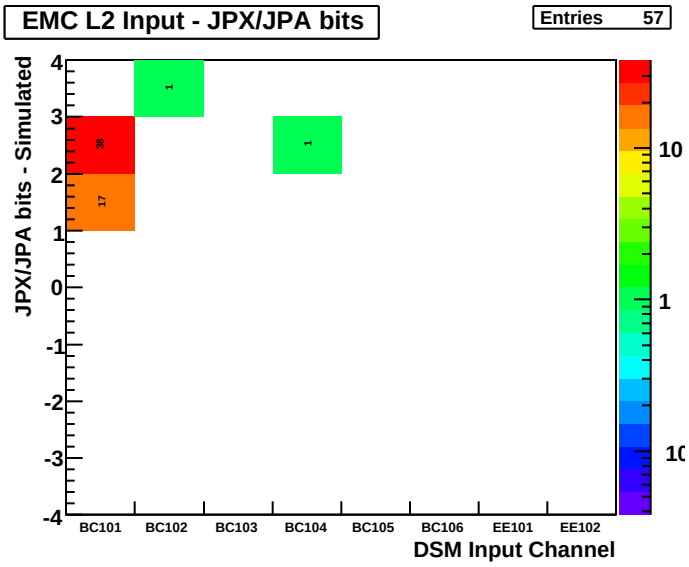

Endcap EMC L1 Input - High Eta Sum
Entries 650340
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

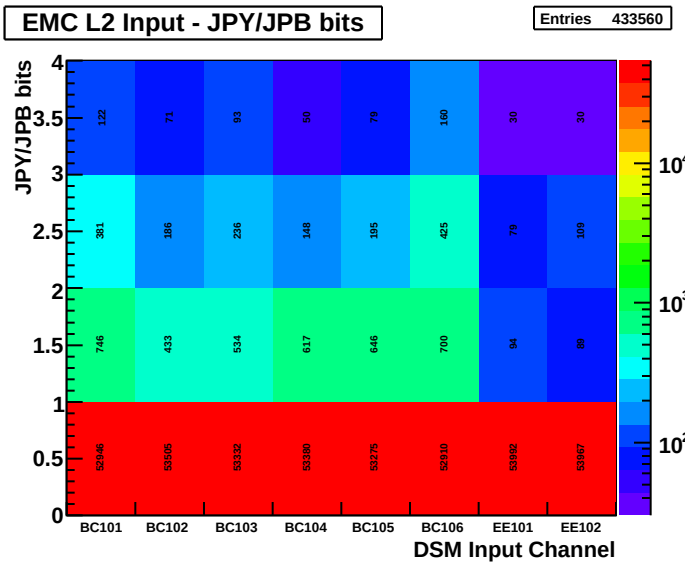

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

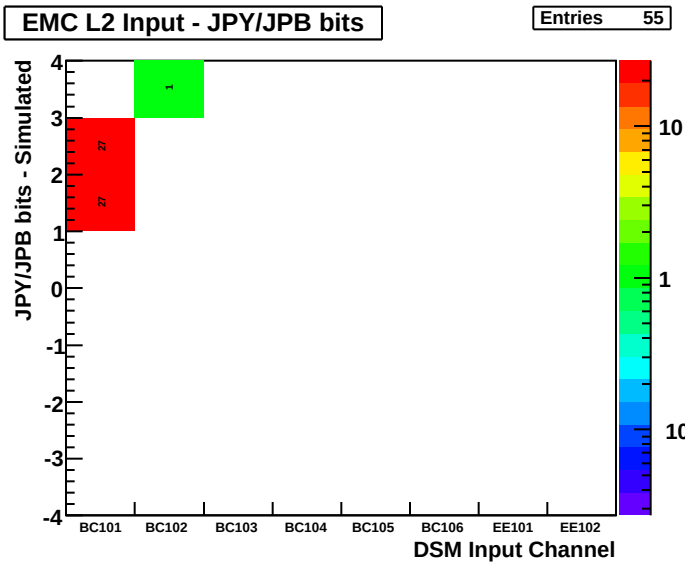

Endcap EMC L1 Input - High Tower Bits
Entries 650340
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

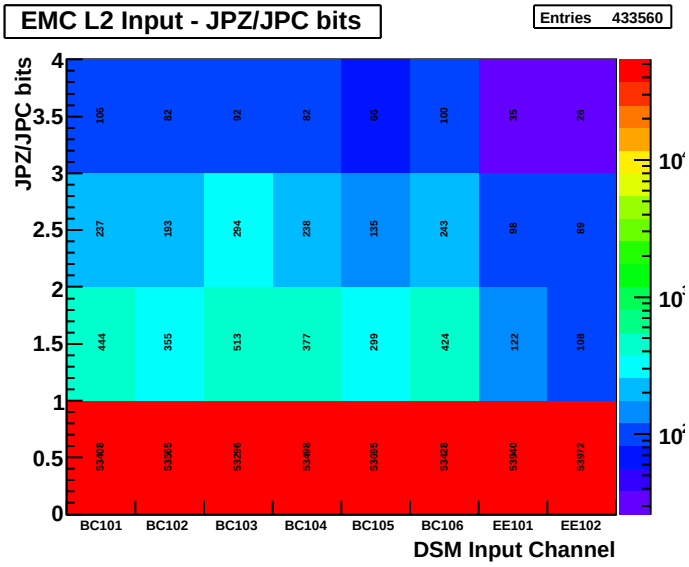

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

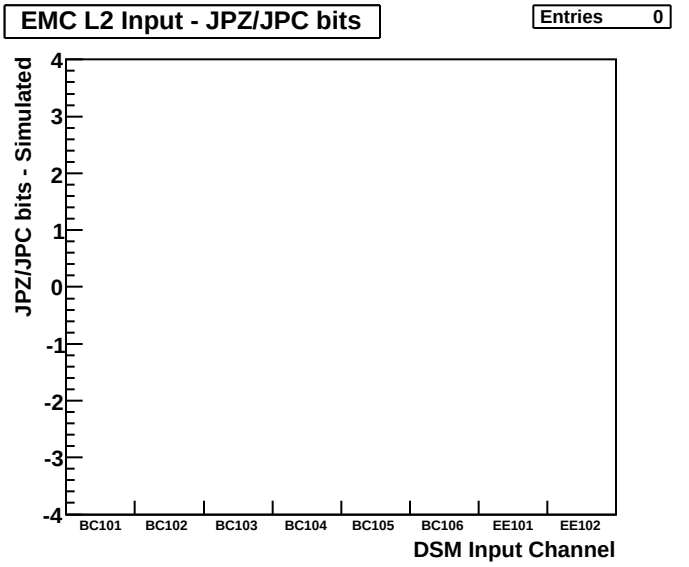

EMC L2 Input - JPX/JPA bits
Entries 433560
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
174 104 101 407 201 112 30 35
511 337 345 2018 670 311 113 107
798 554 786 3059 1523 592 82 132
52712 53200 52963 49711 51801 53180 53970 53921

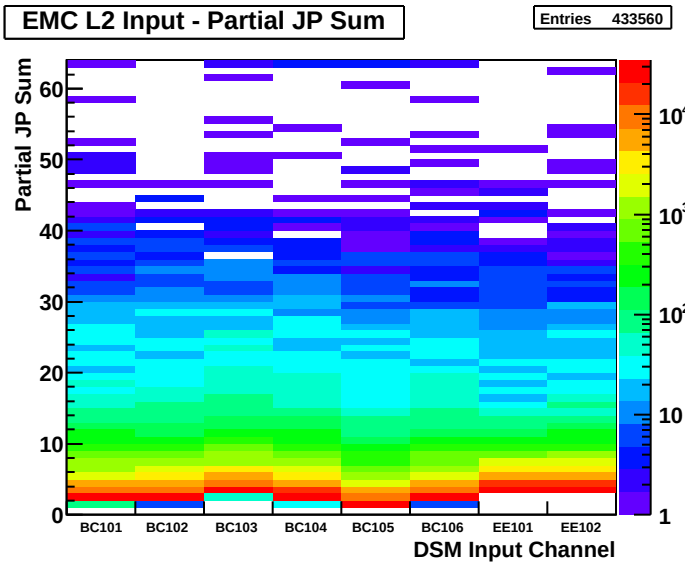

EMC L2 Input - JPX/JPA bits
Entries 57
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
38
1
17

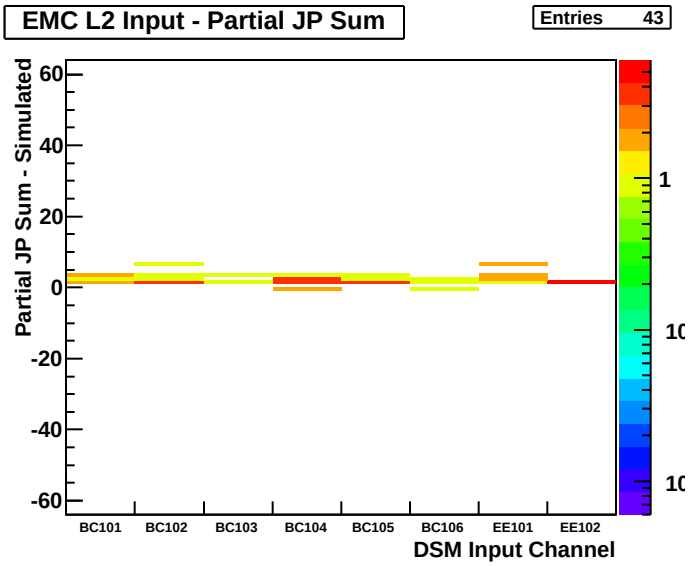

EMC L2 Input - JPY/JPB bits
Entries 433560
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
122 71 93 50 79 160 30 30
381 186 226 148 195 425 79 109
746 433 534 617 646 700 94 88
52946 53505 53332 53380 53275 52910 53992 53967

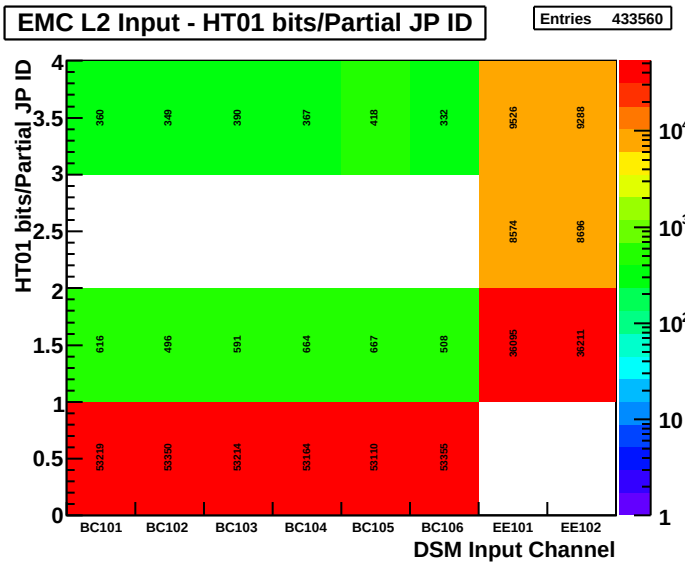

EMC L2 Input - JPY/JPB bits
Entries 55
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
27
27

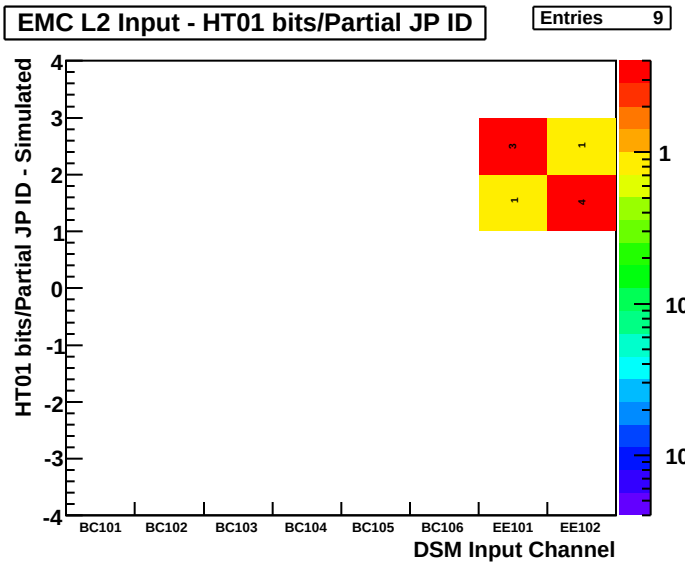

EMC L2 Input - JPZ/JPC bits
Entries 433560
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
106 82 92 82 66 100 35 26
237 193 294 238 135 243 98 89
444 355 513 377 299 424 122 108
53408 53365 53296 53408 53695 53428 53940 53972

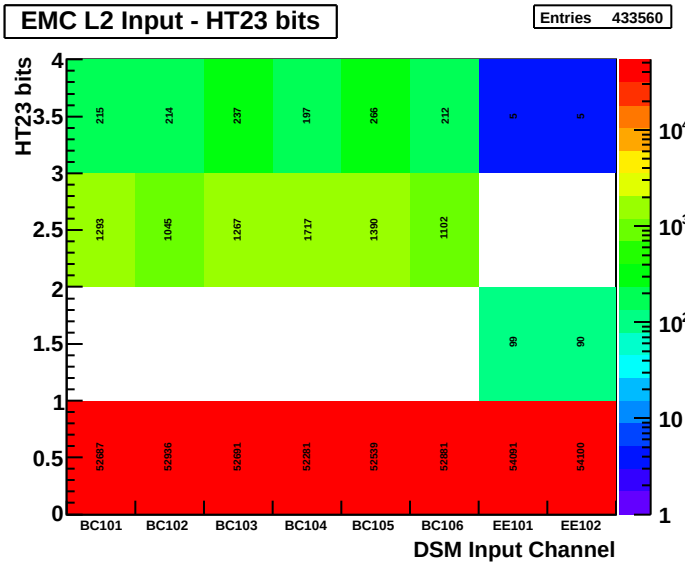

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

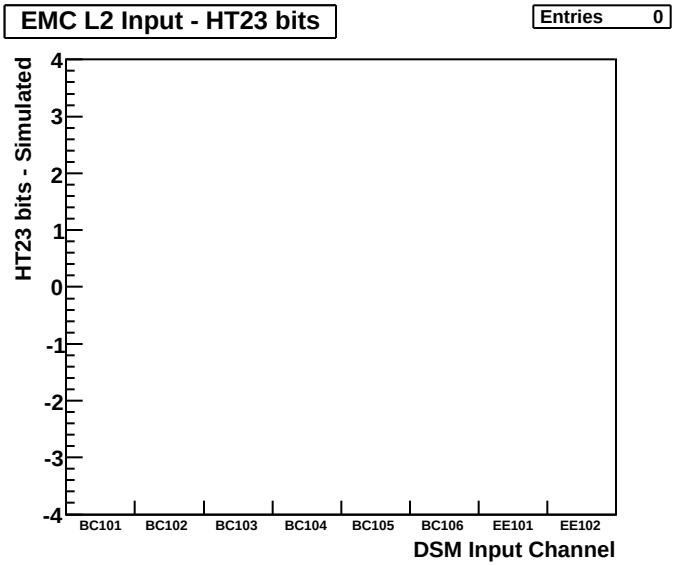

EMC L2 Input - Partial JP Sum
Entries 433560
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

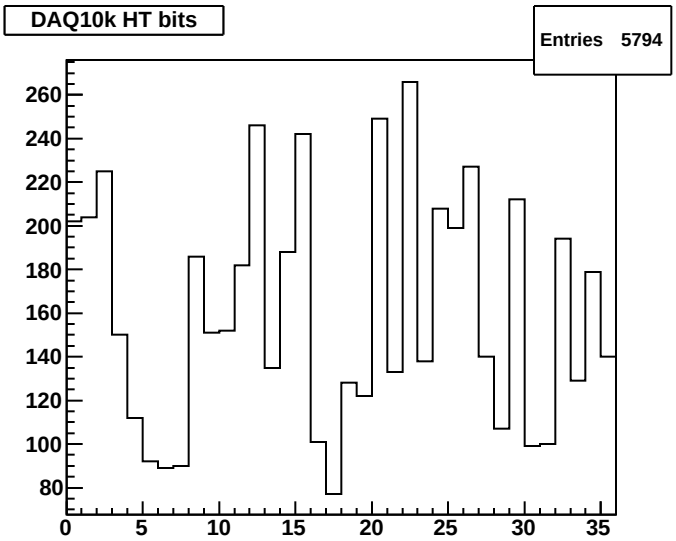

EMC L2 Input - Partial JP Sum
Entries 43
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

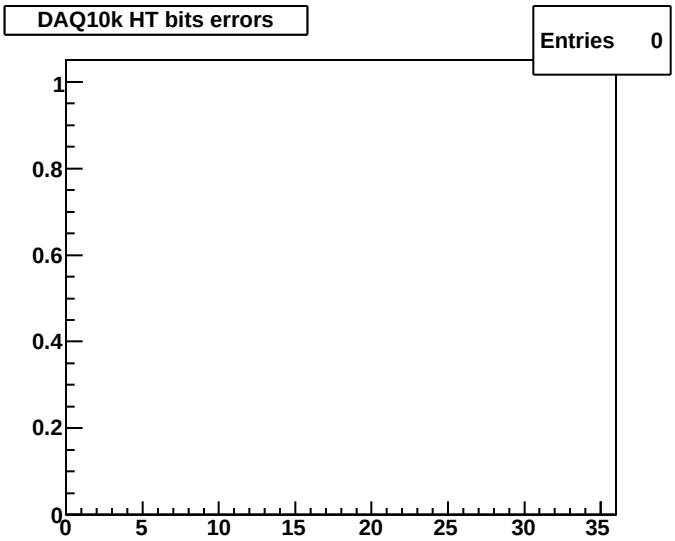

EMC L2 Input - HT01 bits/Partial JP ID
Entries 433560
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

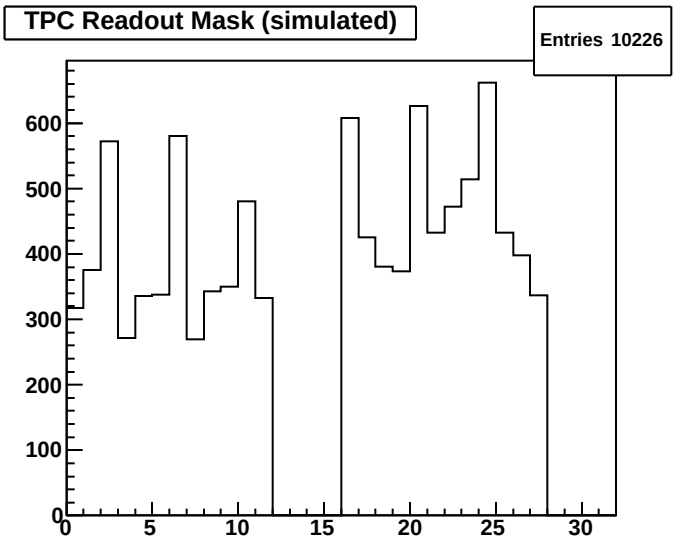

EMC L2 Input - HT01 bits/Partial JP ID
Entries 9
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

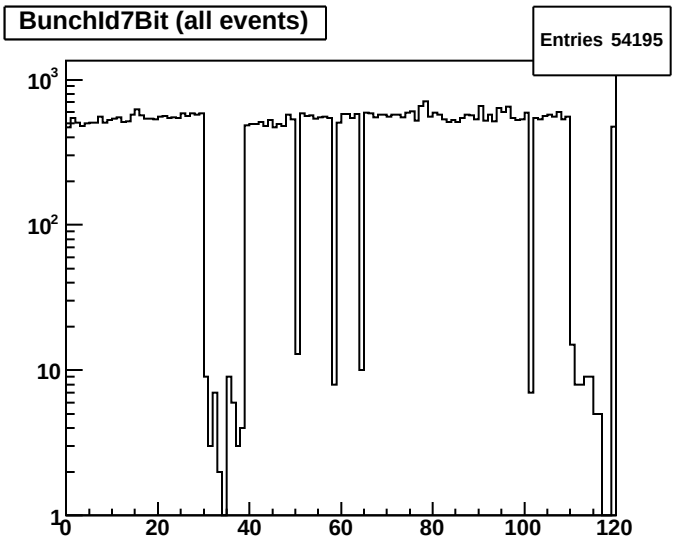

EMC L2 Input - HT23 bits
Entries 433560
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

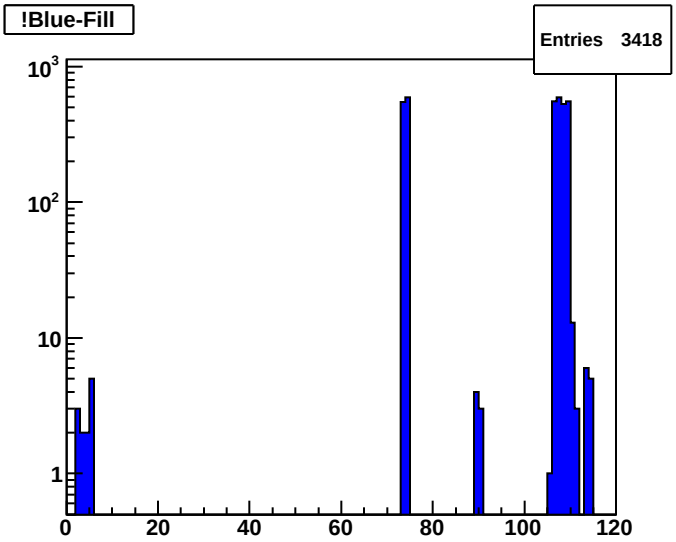

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

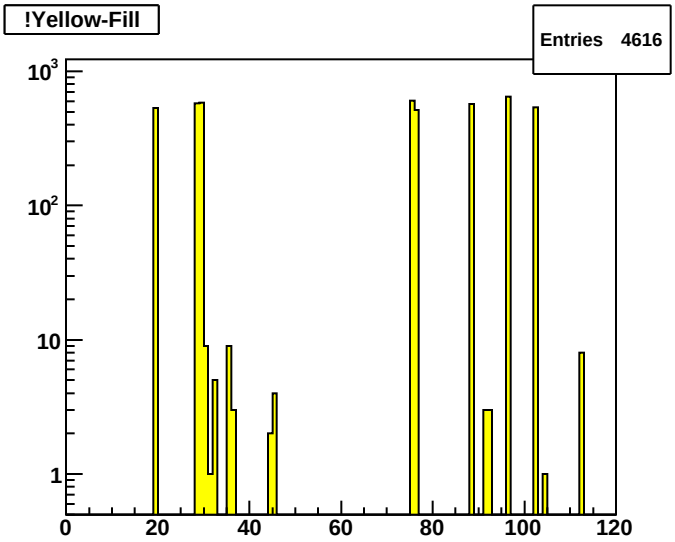

DAQ10k HT bits
Entries 5794

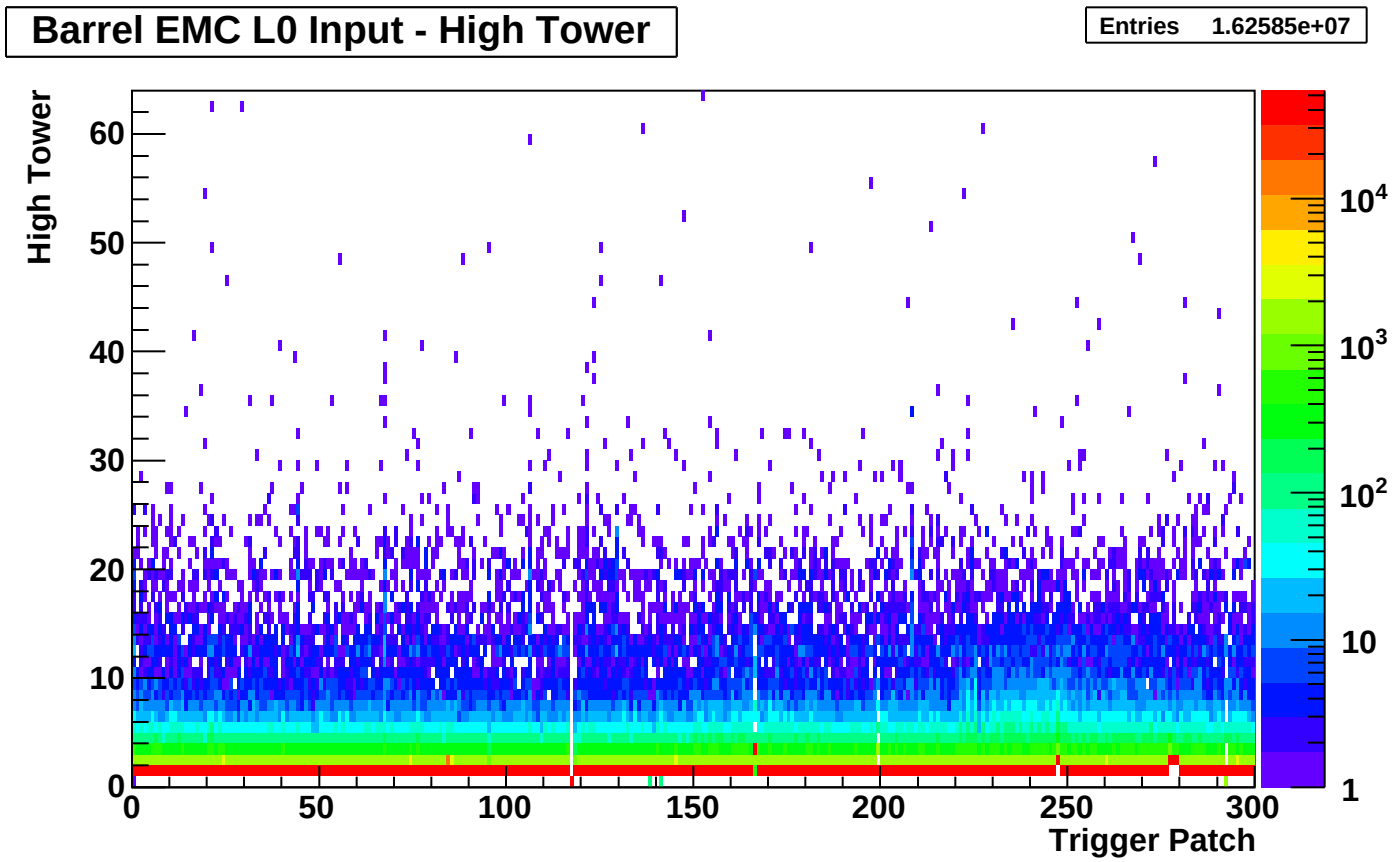

DAQ10k HT bits errors
Entries 0

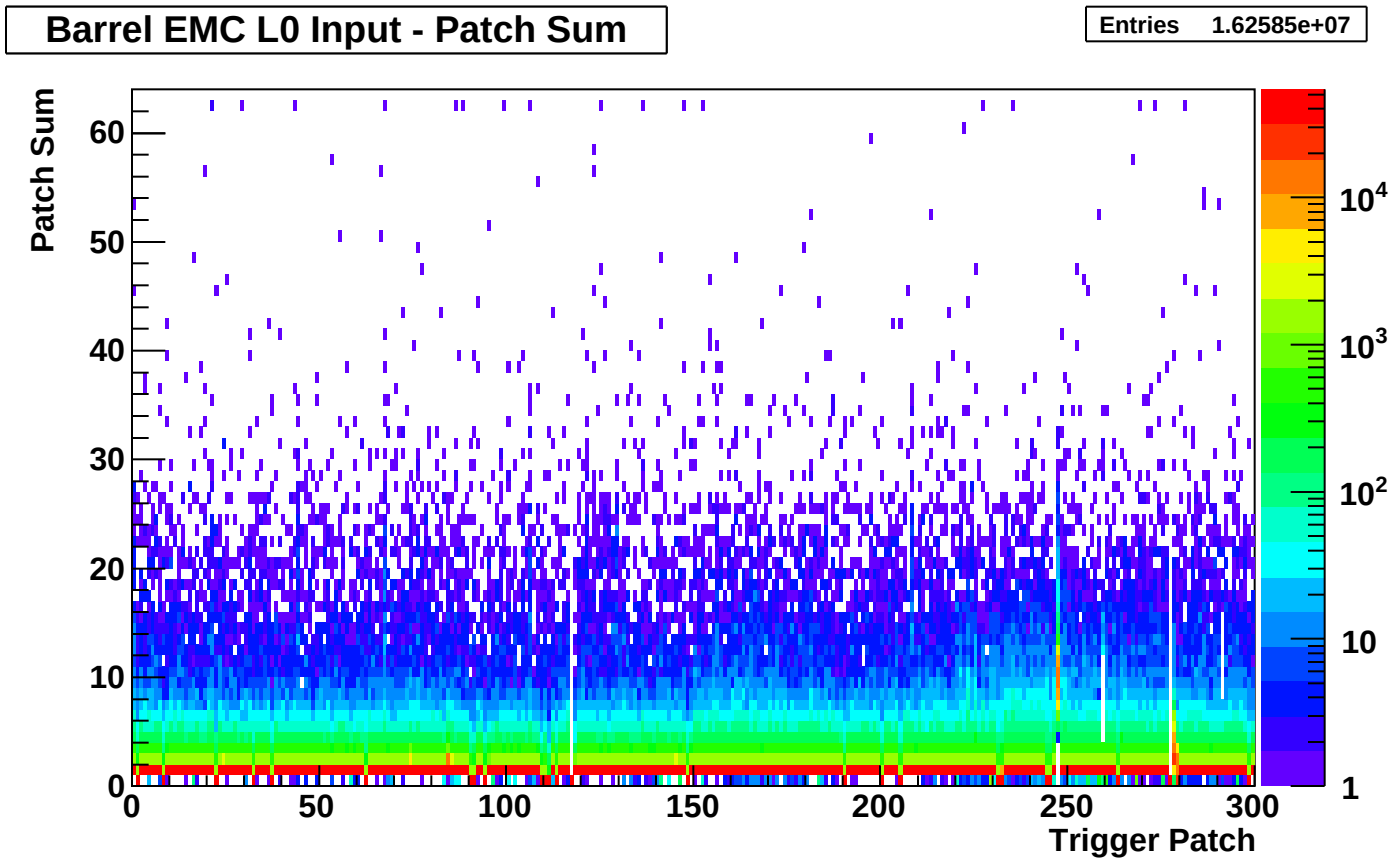

TPC Readout Mask (simulated)
Entries 10226

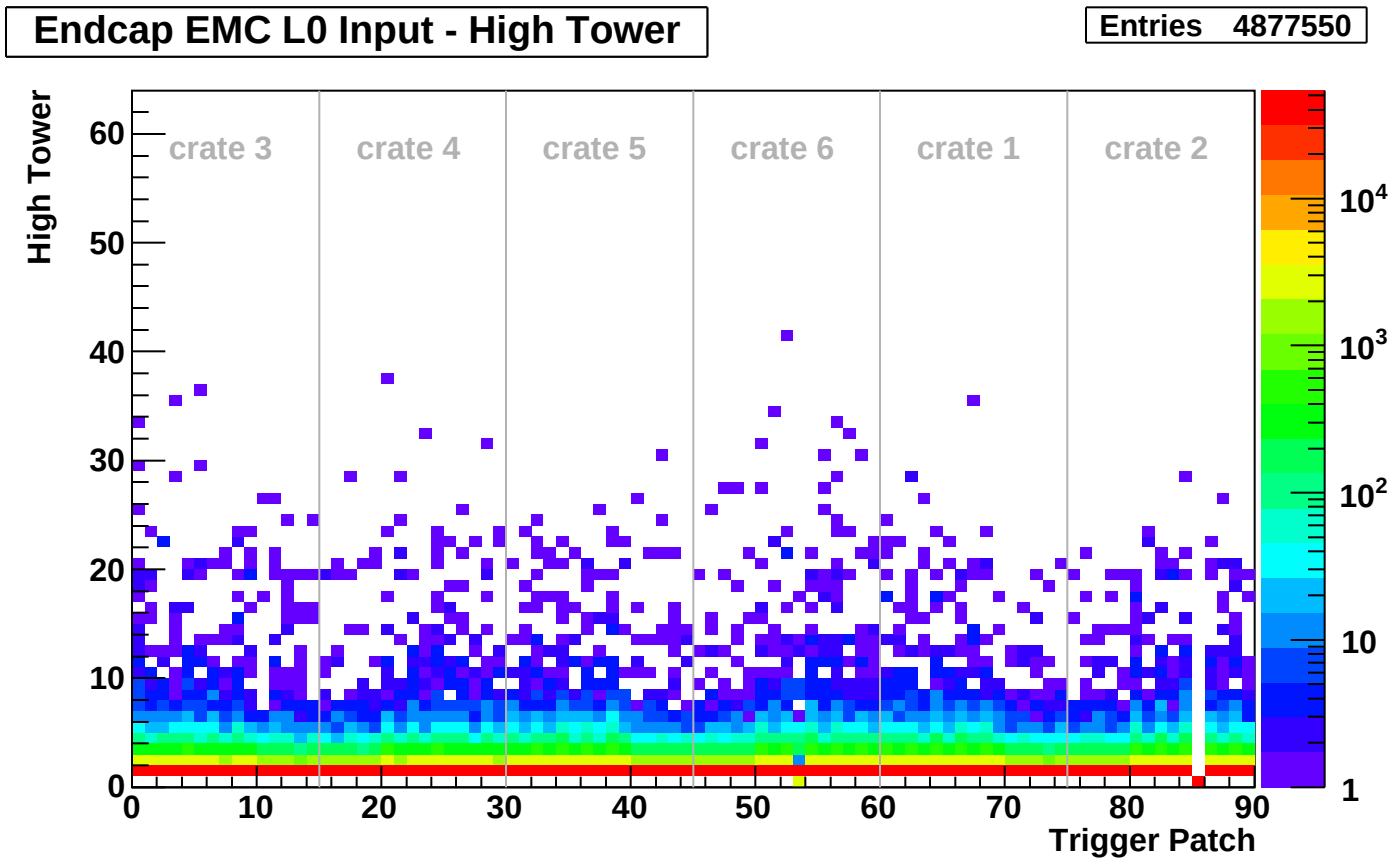

BunchId7Bit (all events)
Entries 54195

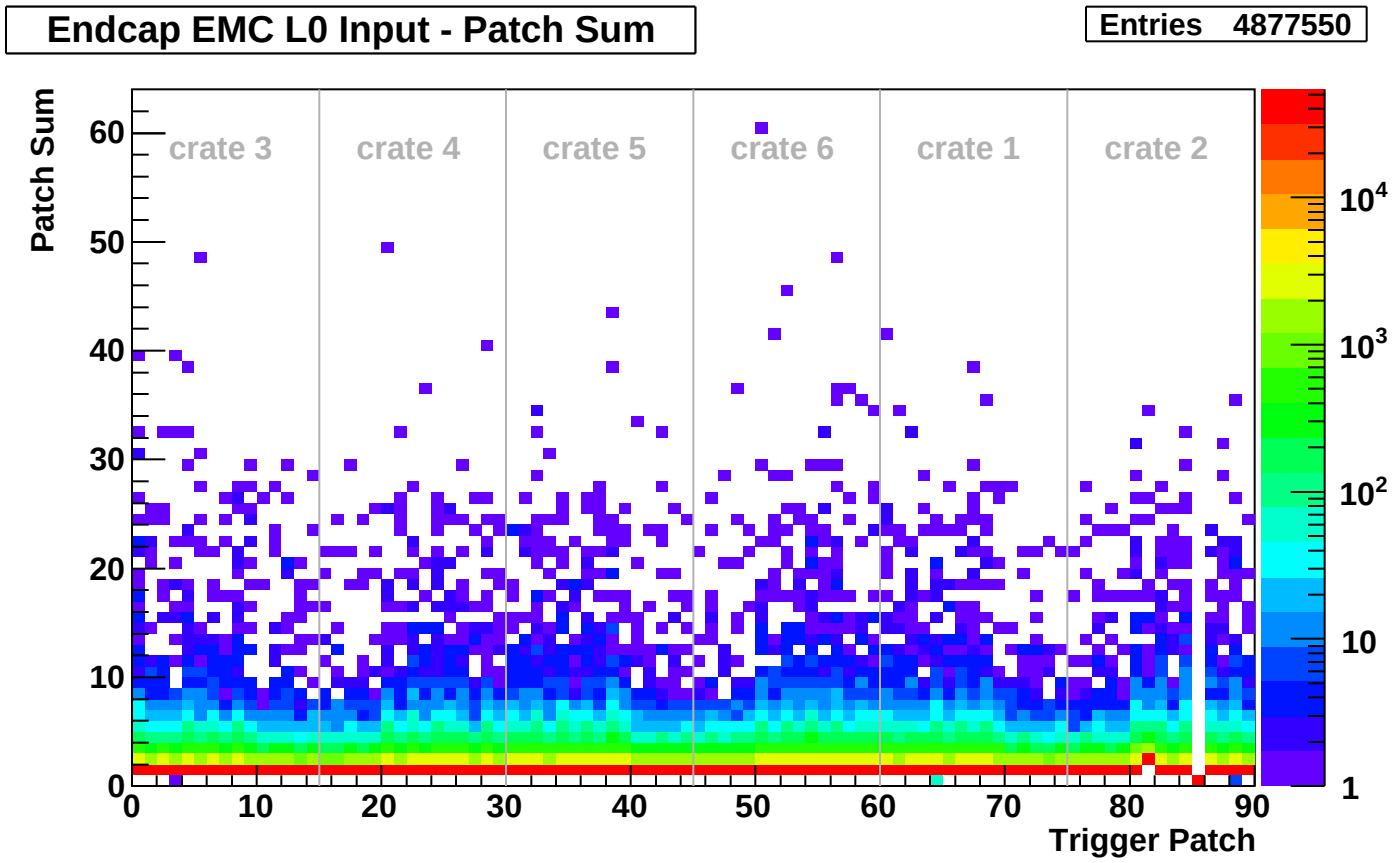

!Blue-Fill
Entries 3418

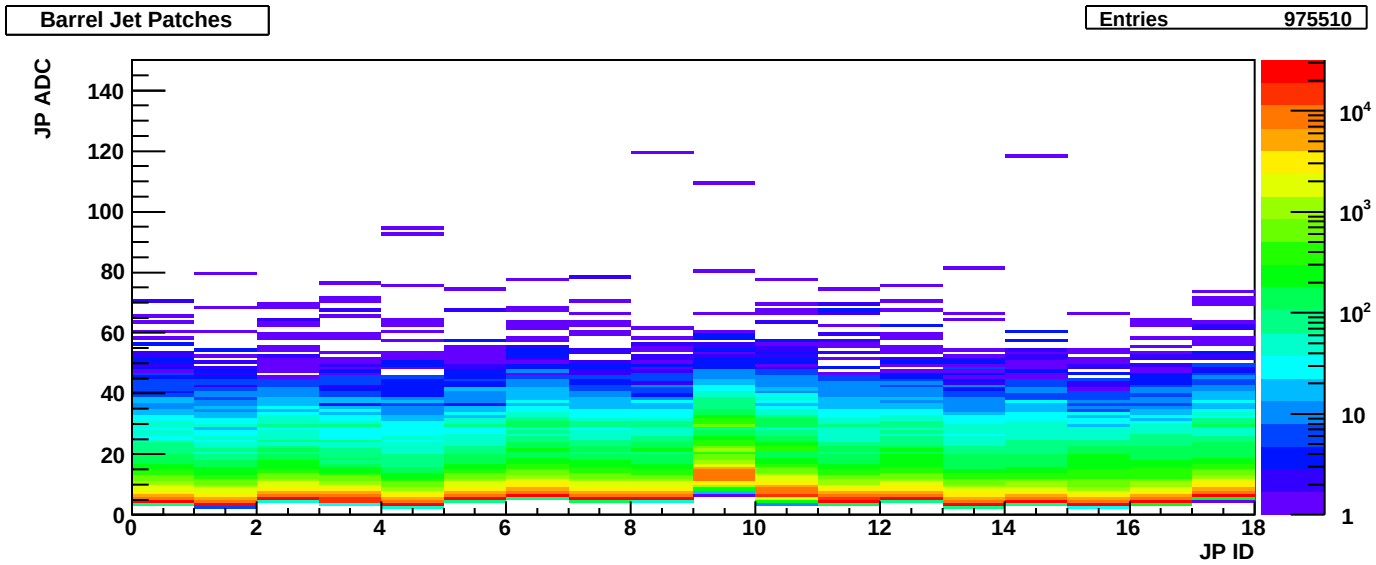

!Yellow-Fill
Entries 4616

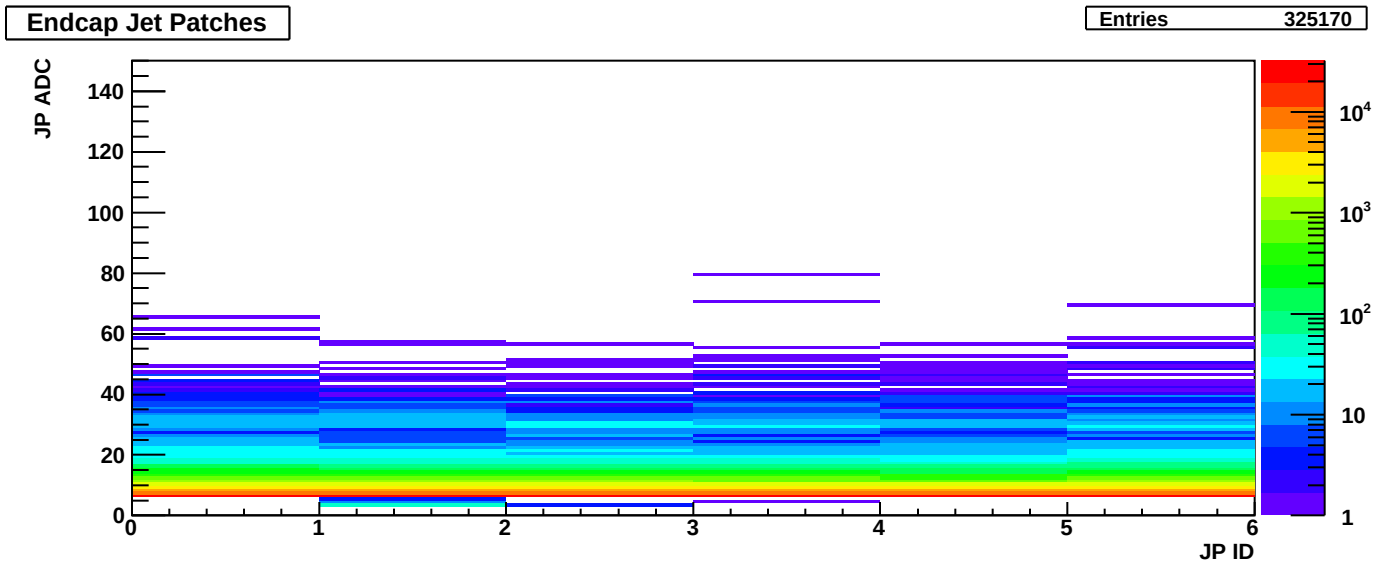

Barrel EMC L0 Input - High Tower
Entries 1.62585e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

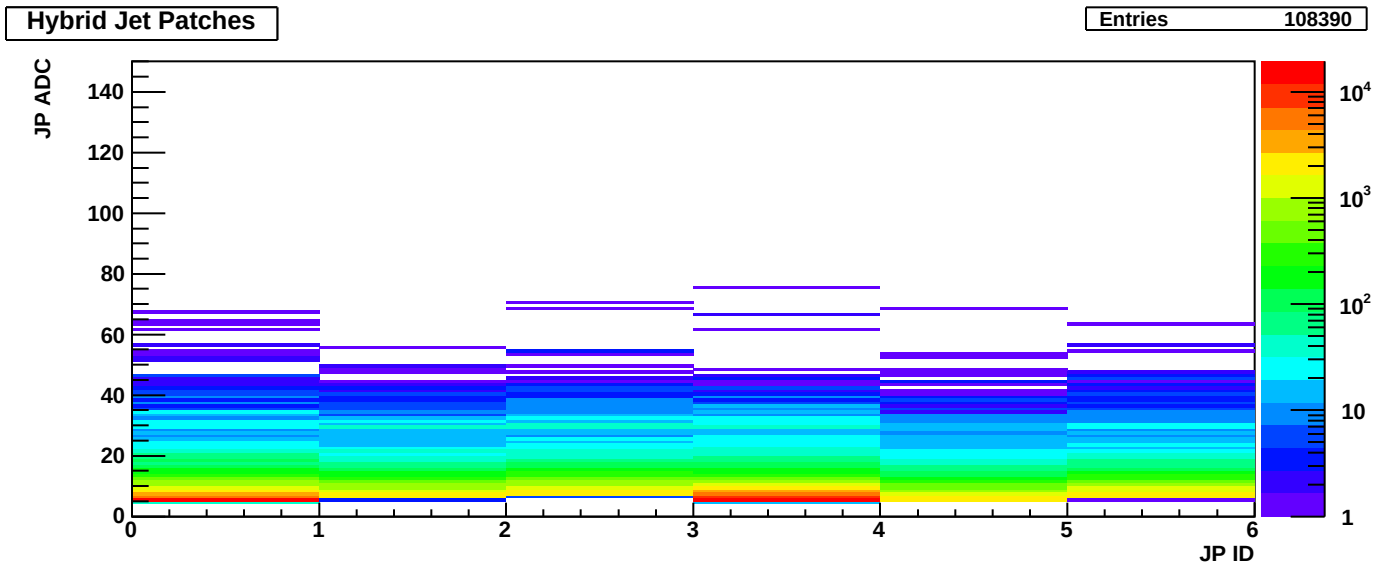

Barrel EMC L0 Input - Patch Sum
Entries 1.62585e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

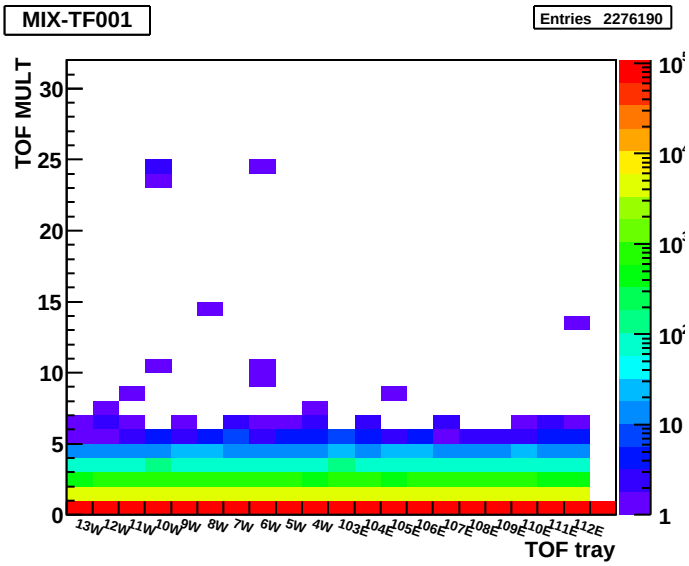

Endcap EMC L0 Input - High Tower
Entries 4877550
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

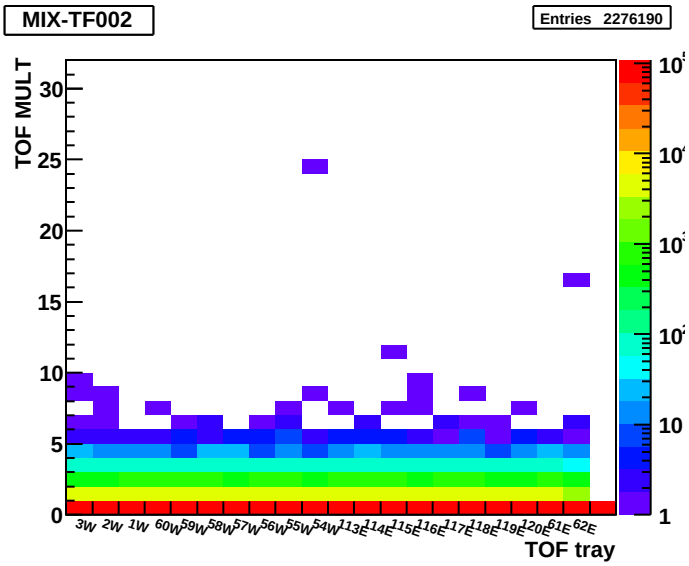

Endcap EMC L0 Input - Patch Sum
Entries 4877550
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

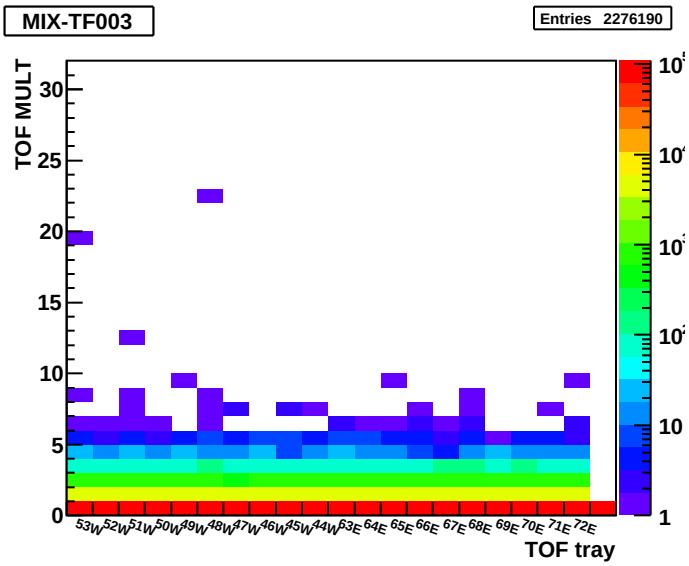

Barrel Jet Patches
Entries 975510
JP ADC
JP ID

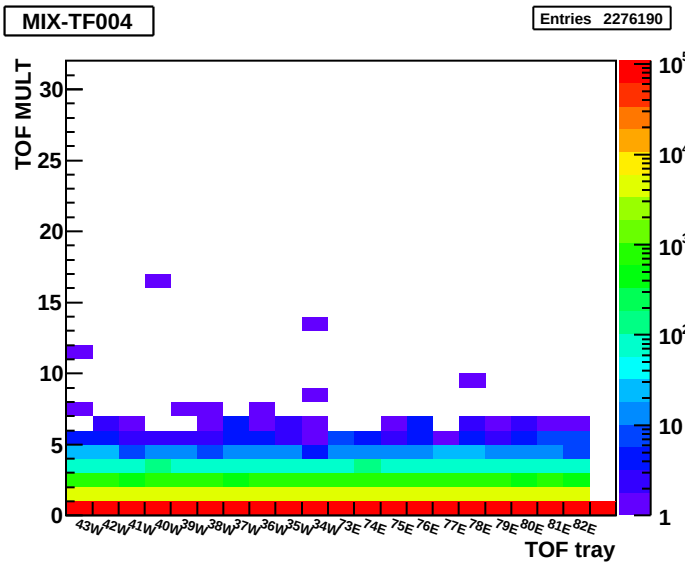

Endcap Jet Patches
Entries 325170
JP ADC
JP ID

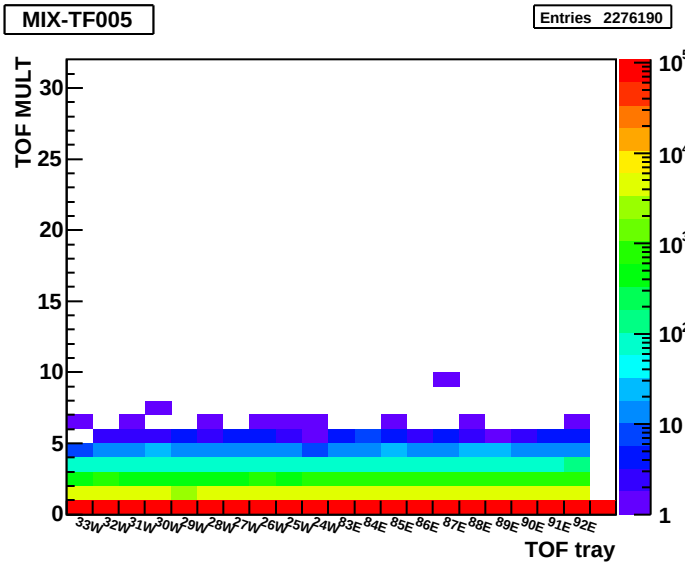

Hybrid Jet Patches
Entries 108390
JP ADC
JP ID

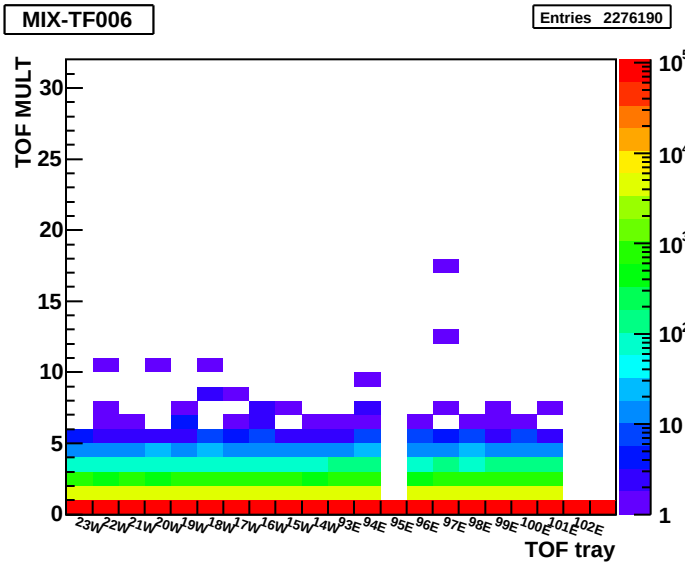

MIX-TF001
Entries 2276190
TOF MULT
TOF tray

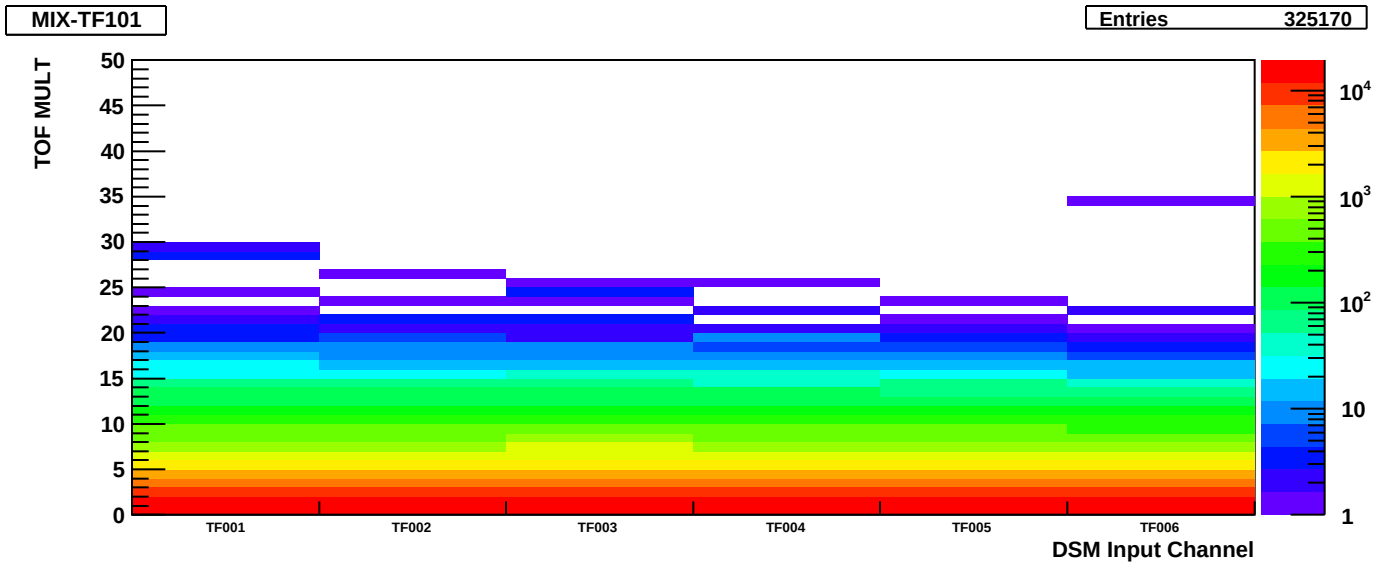

MIX-TF002
Entries 2276190
TOF MULT
TOF tray

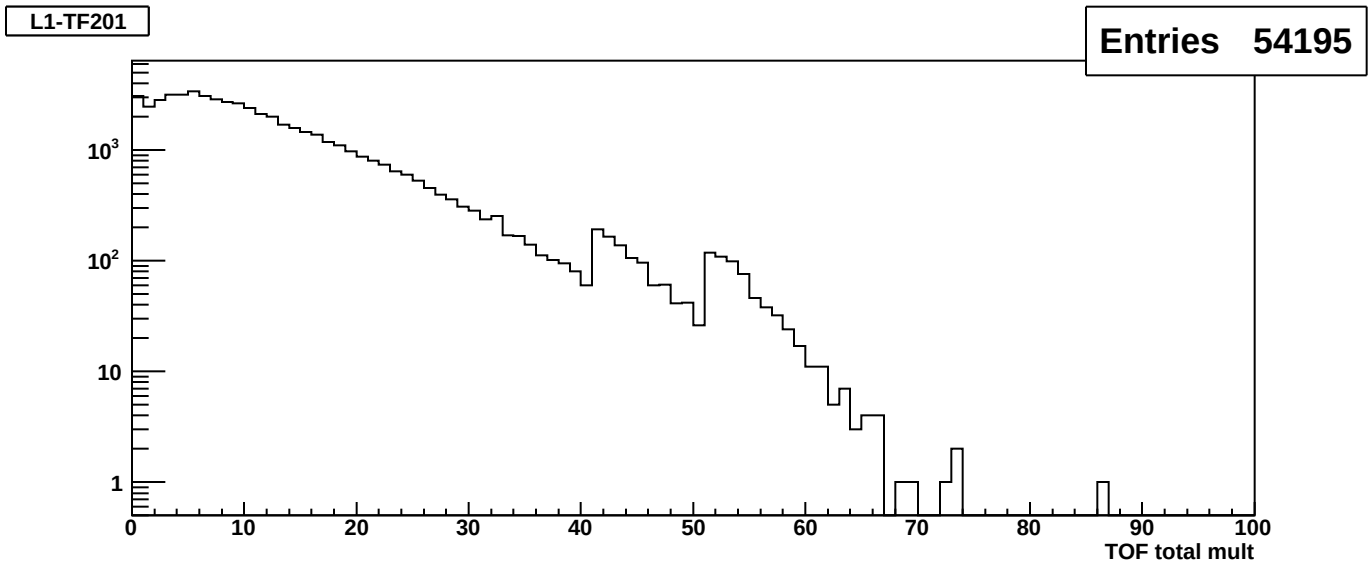

MIX-TF003
Entries 2276190
TOF MULT
TOF tray

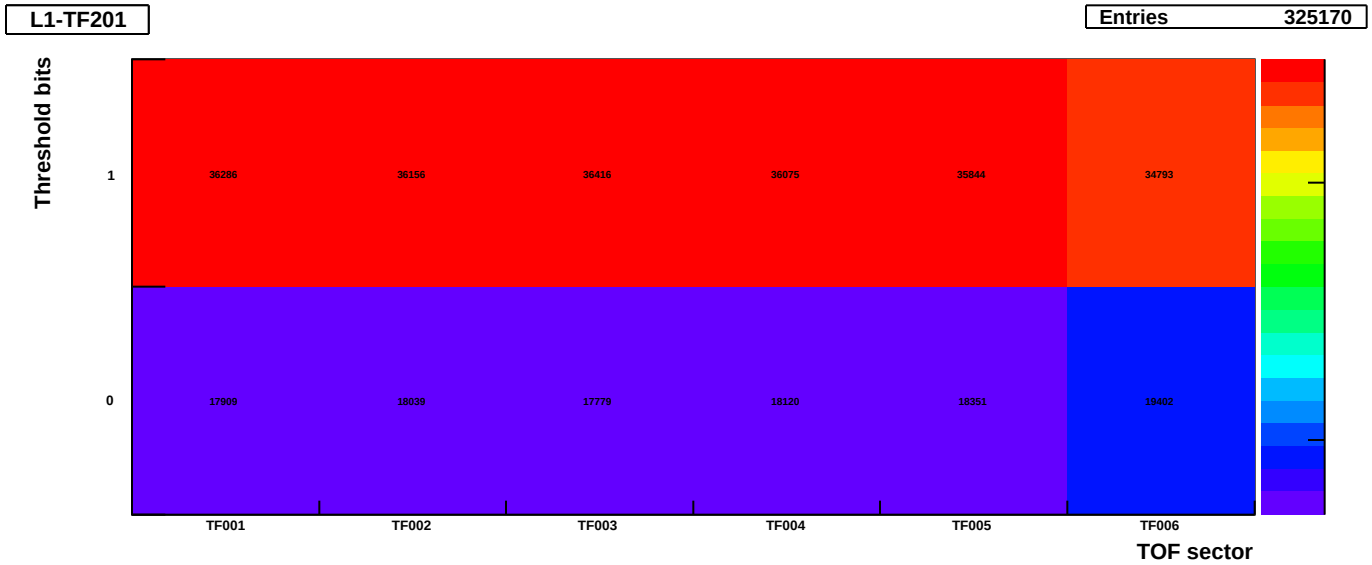

MIX-TF004
Entries 2276190
TOF MULT
TOF tray

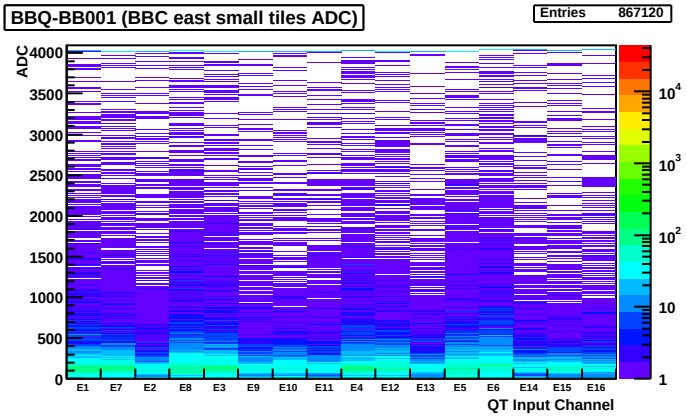

MIX-TF005
Entries 2276190
TOF MULT
TOF tray

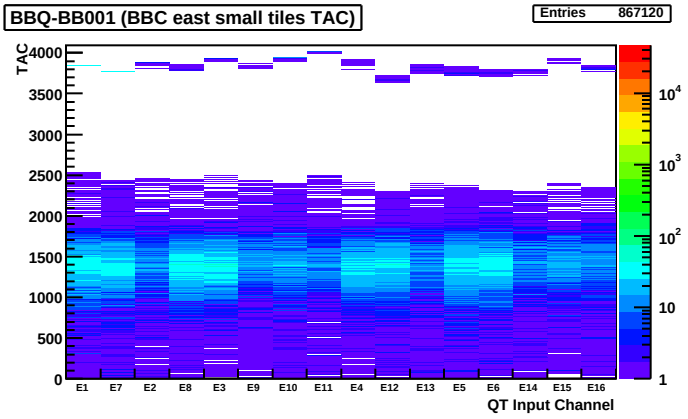

MIX-TF006
Entries 2276190
TOF MULT
TOF tray

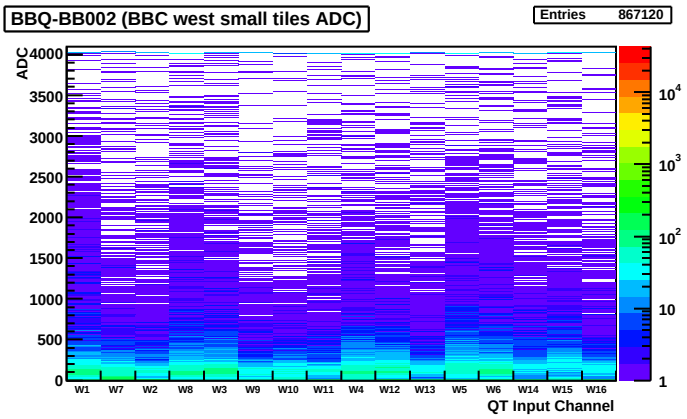

MIX-TF101
Entries 325170
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

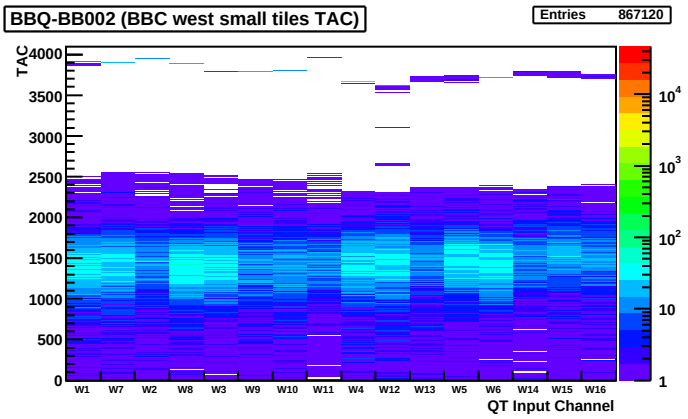

L1-TF201
Entries 54195
TOF total mult

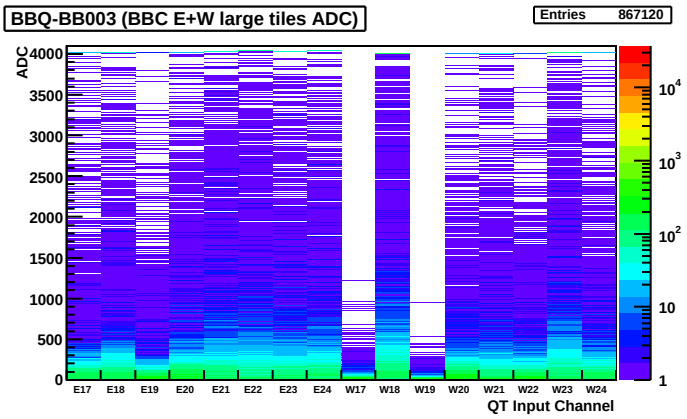

L1-TF201
Entries 325170
Threshold bits
36286
36156
36416
36075
35844
34793
17909
18039
17779
18120
18351
19402
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

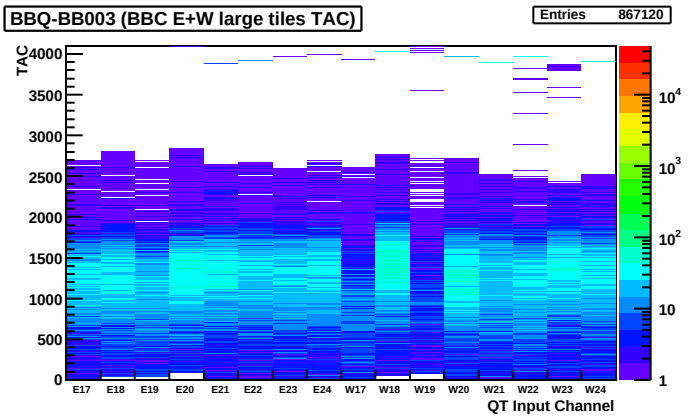

BBQ-BB001 (BBC east small tiles ADC)
Entries 867120
ADC
QT Input Channel

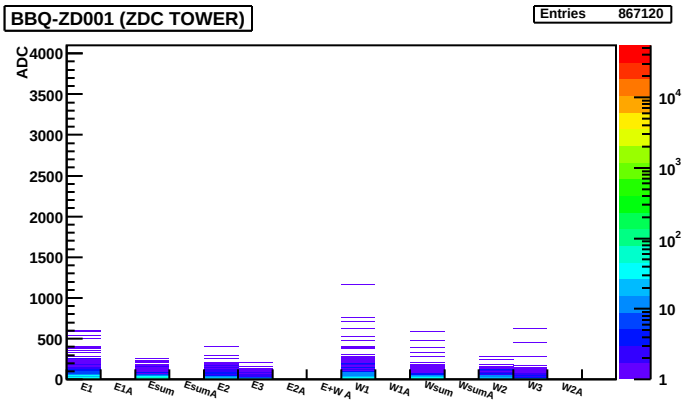

BBQ-BB001 (BBC east small tiles TAC)
Entries 867120
TAC
QT Input Channel

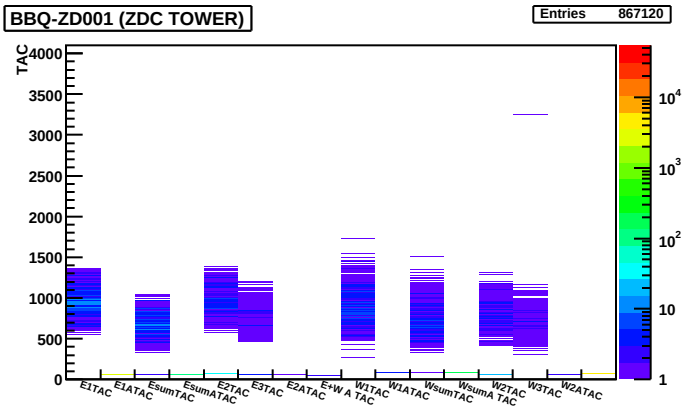

BBQ-BB002 (BBC west small tiles ADC)
Entries 867120
ADC
QT Input Channel

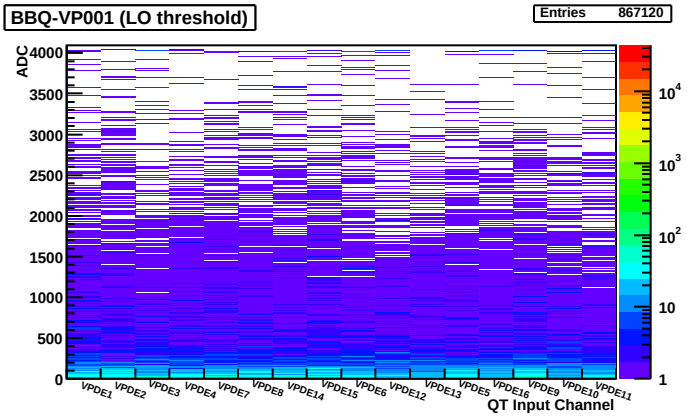

BBQ-BB002 (BBC west small tiles TAC)
Entries 867120
TAC
QT Input Channel

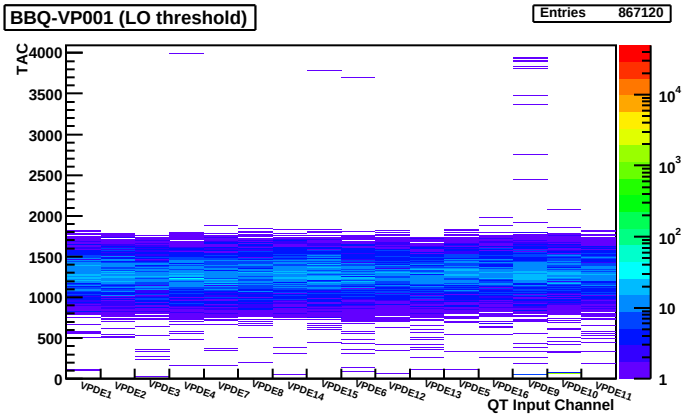

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 867120
ADC
QT Input Channel

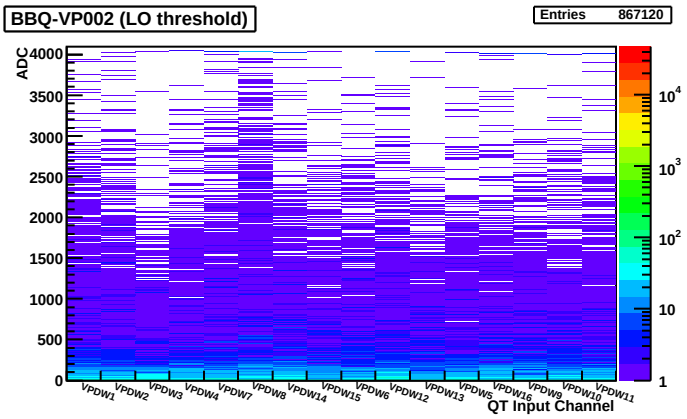

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 867120
TAC
QT Input Channel

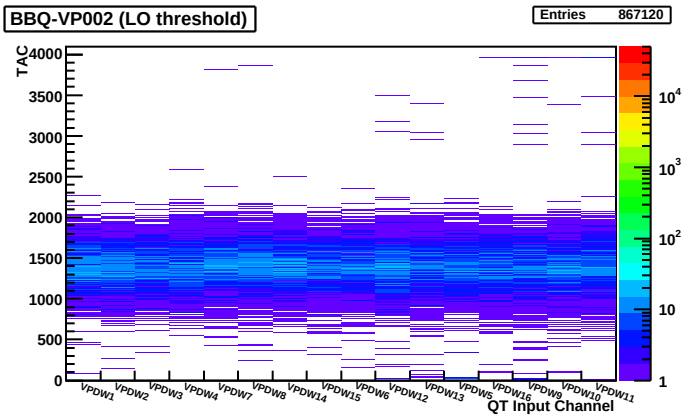

BBQ-ZD001 (ZDC TOWER)
Entries 867120
ADC

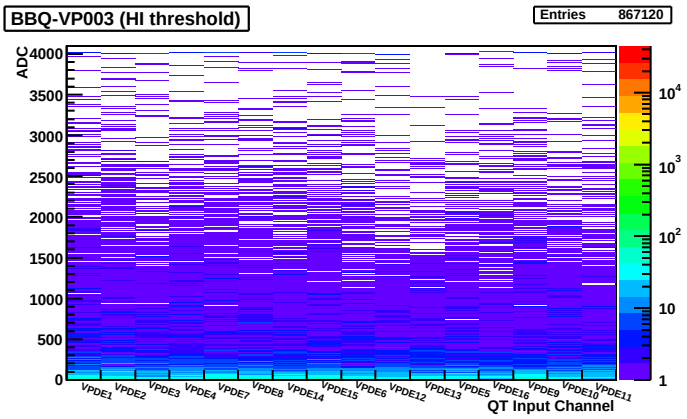

BBQ-ZD001 (ZDC TOWER)
Entries 867120
TAC

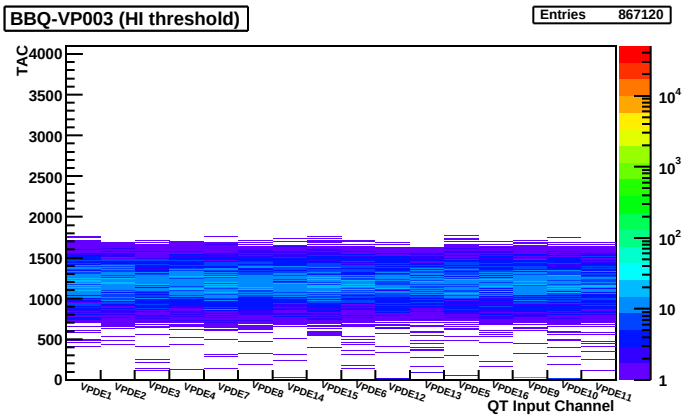

BBQ-VP001 (LO threshold)
Entries 867120
ADC
QT Input Channel

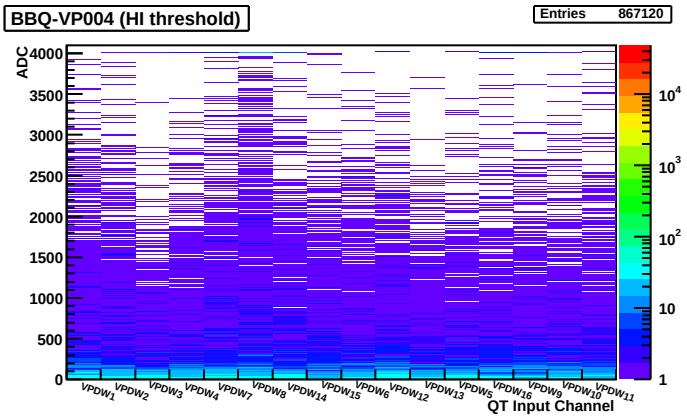

BBQ-VP001 (LO threshold)
Entries 867120
TAC
QT Input Channel

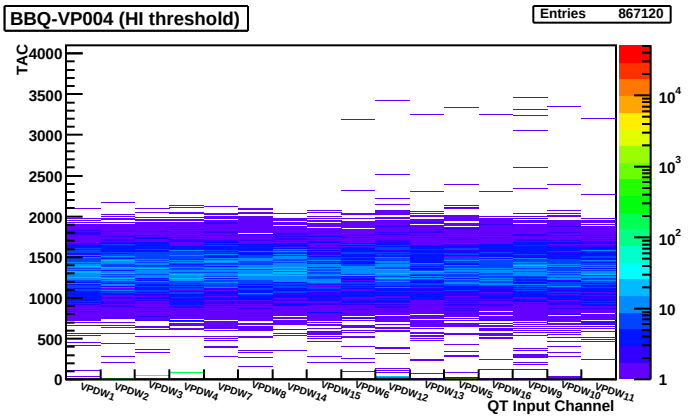

BBQ-VP002 (LO threshold)
Entries 867120
ADC
QT Input Channel

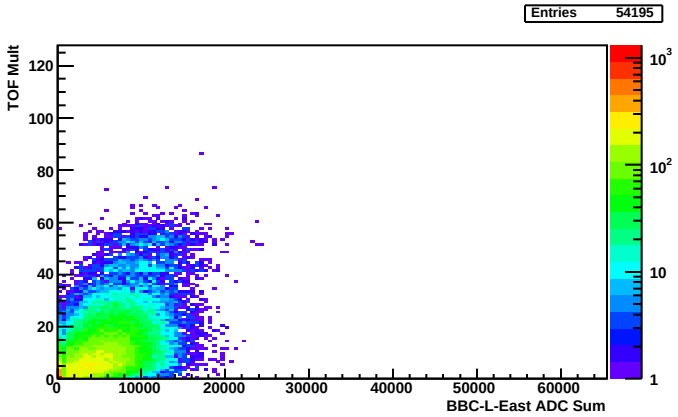

BBQ-VP002 (LO threshold)
Entries 867120
TAC
QT Input Channel

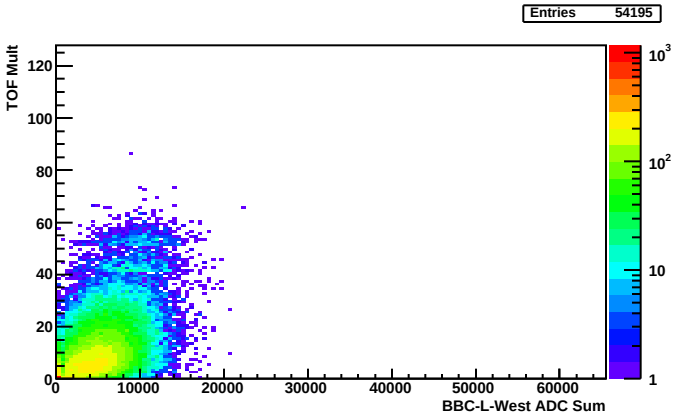

BBQ-VP003 (HI threshold)
Entries 867120
ADC
QT Input Channel

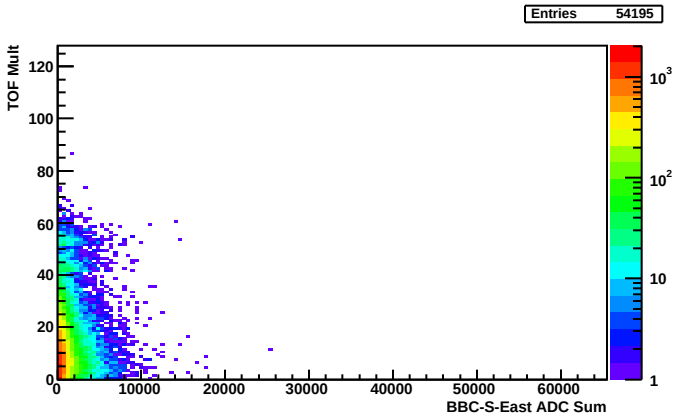

BBQ-VP003 (HI threshold)
Entries 867120
TAC
QT Input Channel

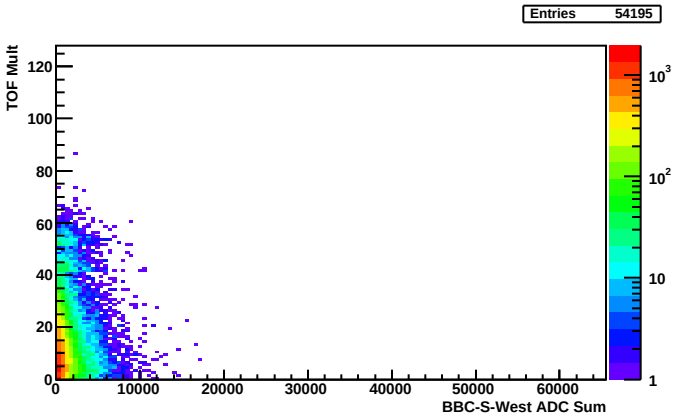

BBQ-VP004 (HI threshold)
Entries 867120
ADC
QT Input Channel

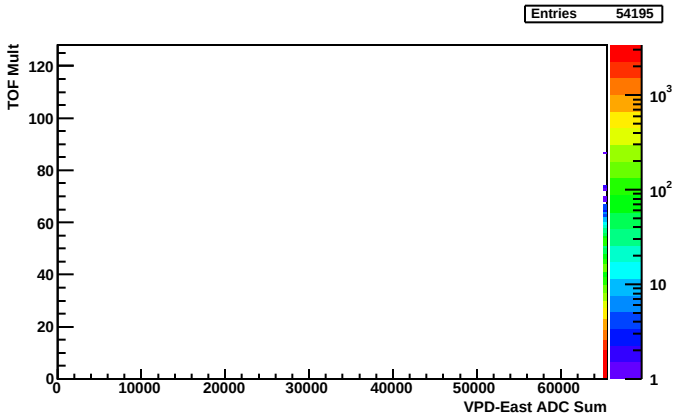

BBQ-VP004 (HI threshold)
Entries 867120
TAC
QT Input Channel

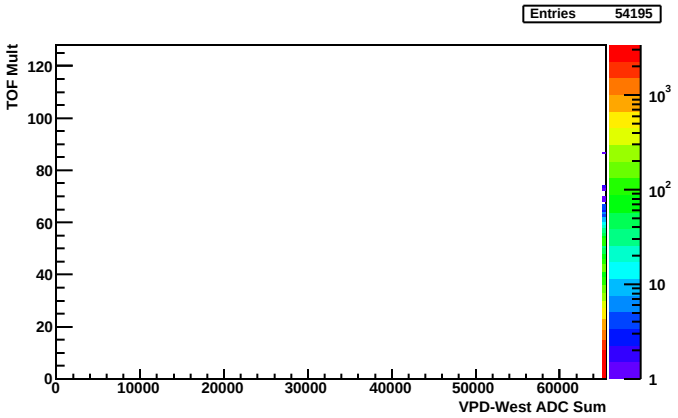

Entries 54195
TOF Mult
BBC-L-East ADC Sum

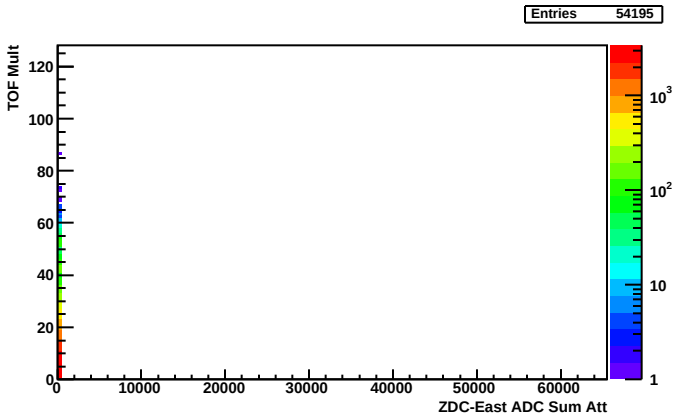

Entries 54195
TOF Mult
BBC-L-West ADC Sum

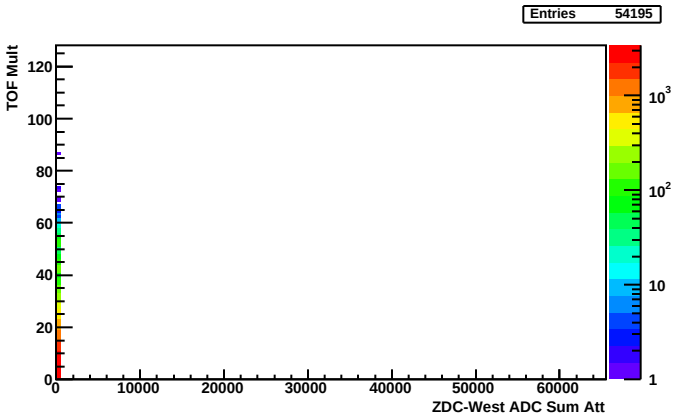

Entries 54195
TOF Mult
BBC-S-East ADC Sum

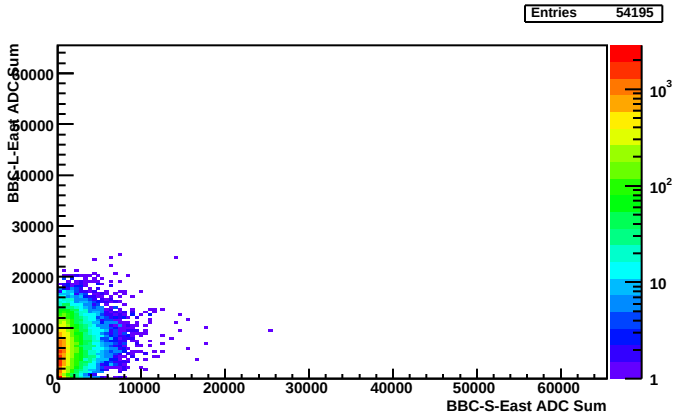

Entries 54195
TOF Mult
BBC-S-West ADC Sum

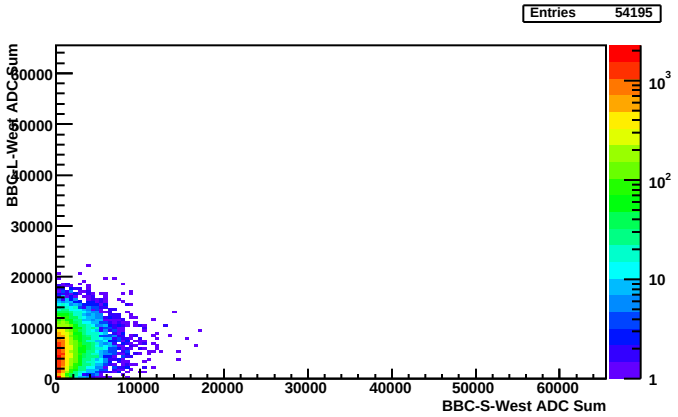

Entries 54195
TOF Mult
VPD-East ADC Sum

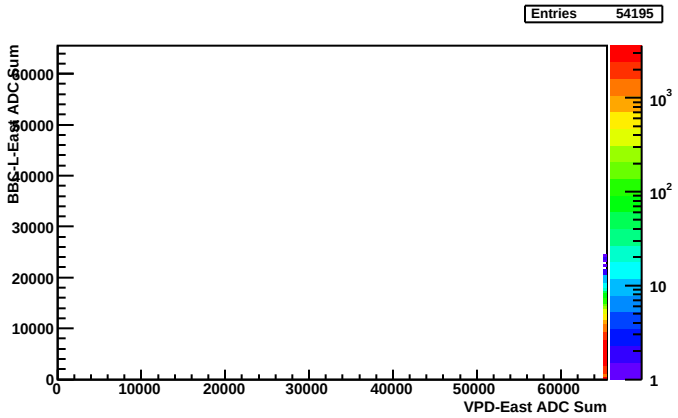

Entries 54195
TOF Mult
VPD-West ADC Sum

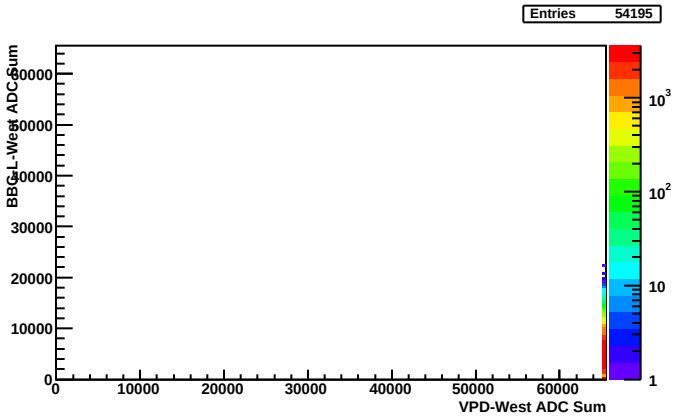

Entries 54195
TOF Mult
ZDC-East ADC Sum Att

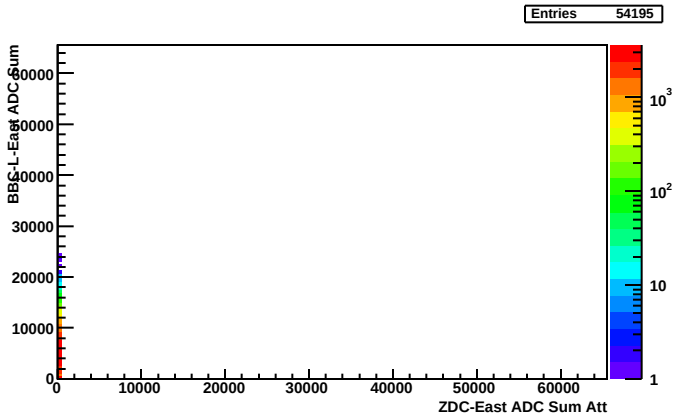

Entries 54195
TOF Mult
ZDC-West ADC Sum Att

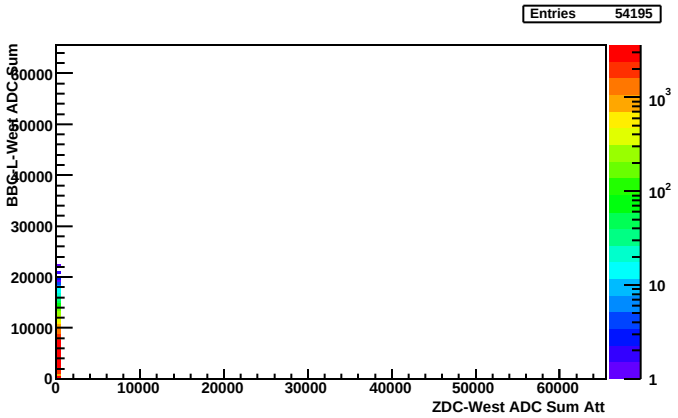

Entries 54195
BBC-L-East ADC Sum
BBC-S-East ADC Sum

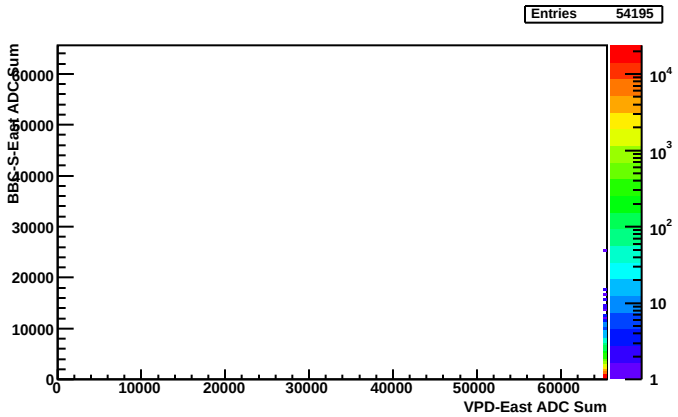

Entries 54195
BBC-L-West ADC Sum
BBC-S-West ADC Sum

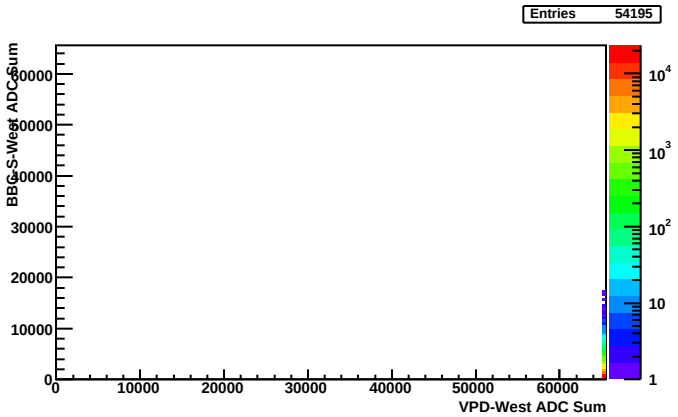

Entries 54195
BBC-L-East ADC Sum
VPD-East ADC Sum

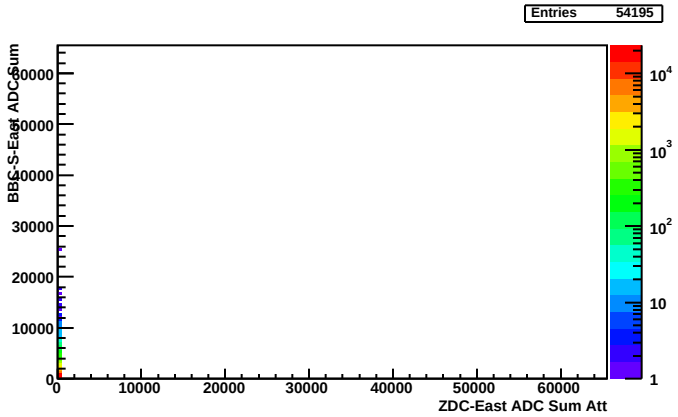

Entries 54195
BBC-L-West ADC Sum
VPD-West ADC Sum

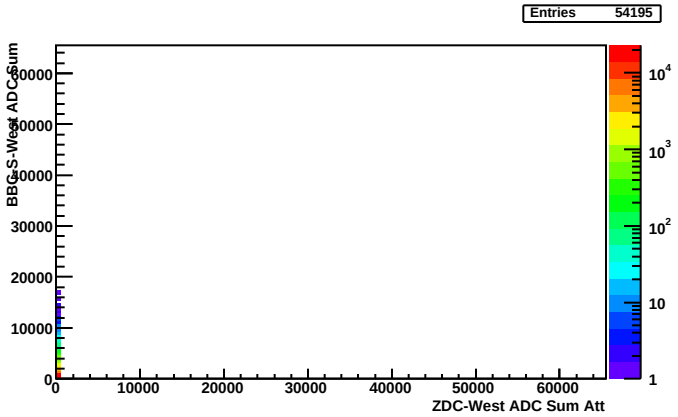

Entries 54195
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

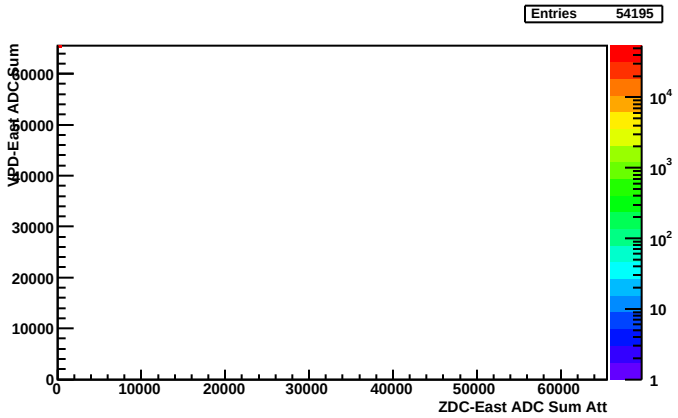

Entries 54195
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

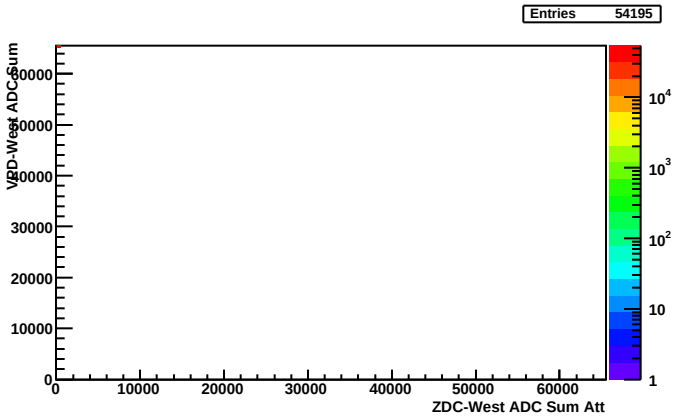

Entries 54195
BBC-S-East ADC Sum
VPD-East ADC Sum

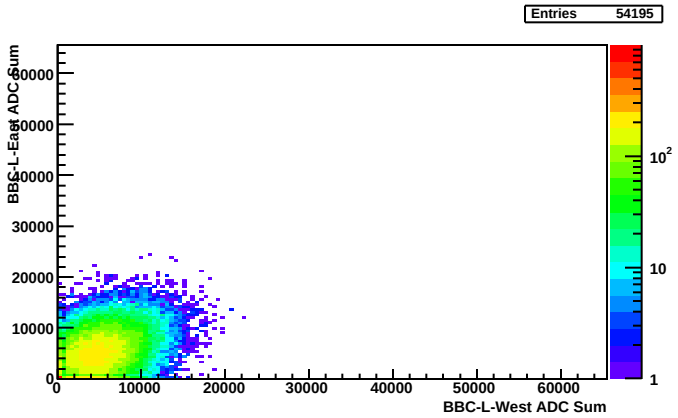

Entries 54195
BBC-S-West ADC Sum
VPD-West ADC Sum

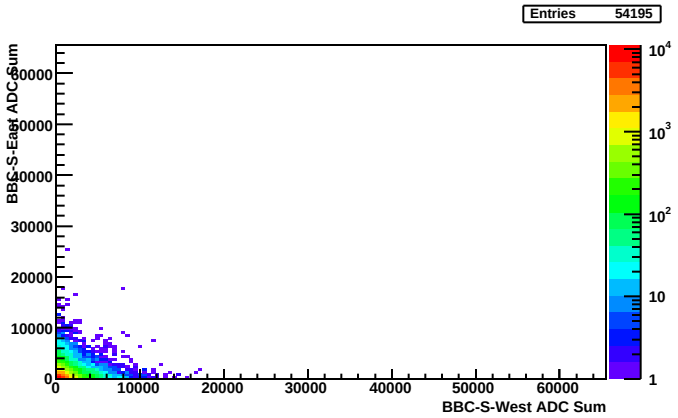

Entries 54195
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

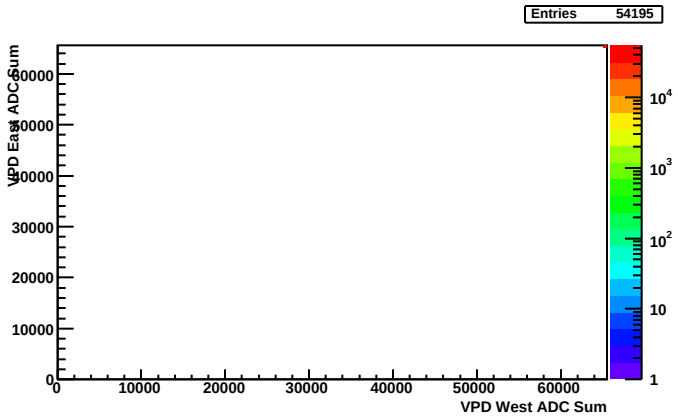

Entries 54195
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

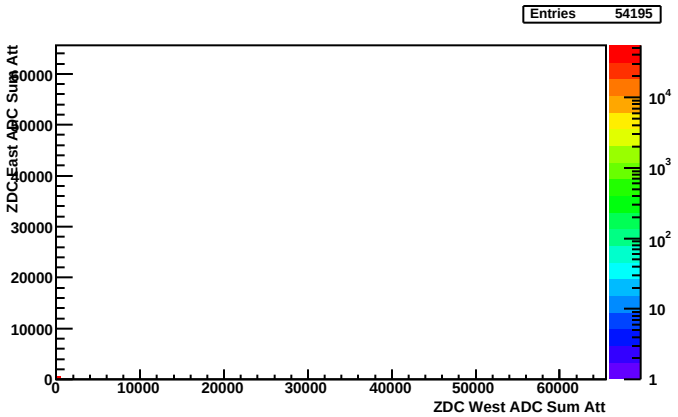

Entries 54195
VPD-East ADC Sum
ZDC-East ADC Sum Att

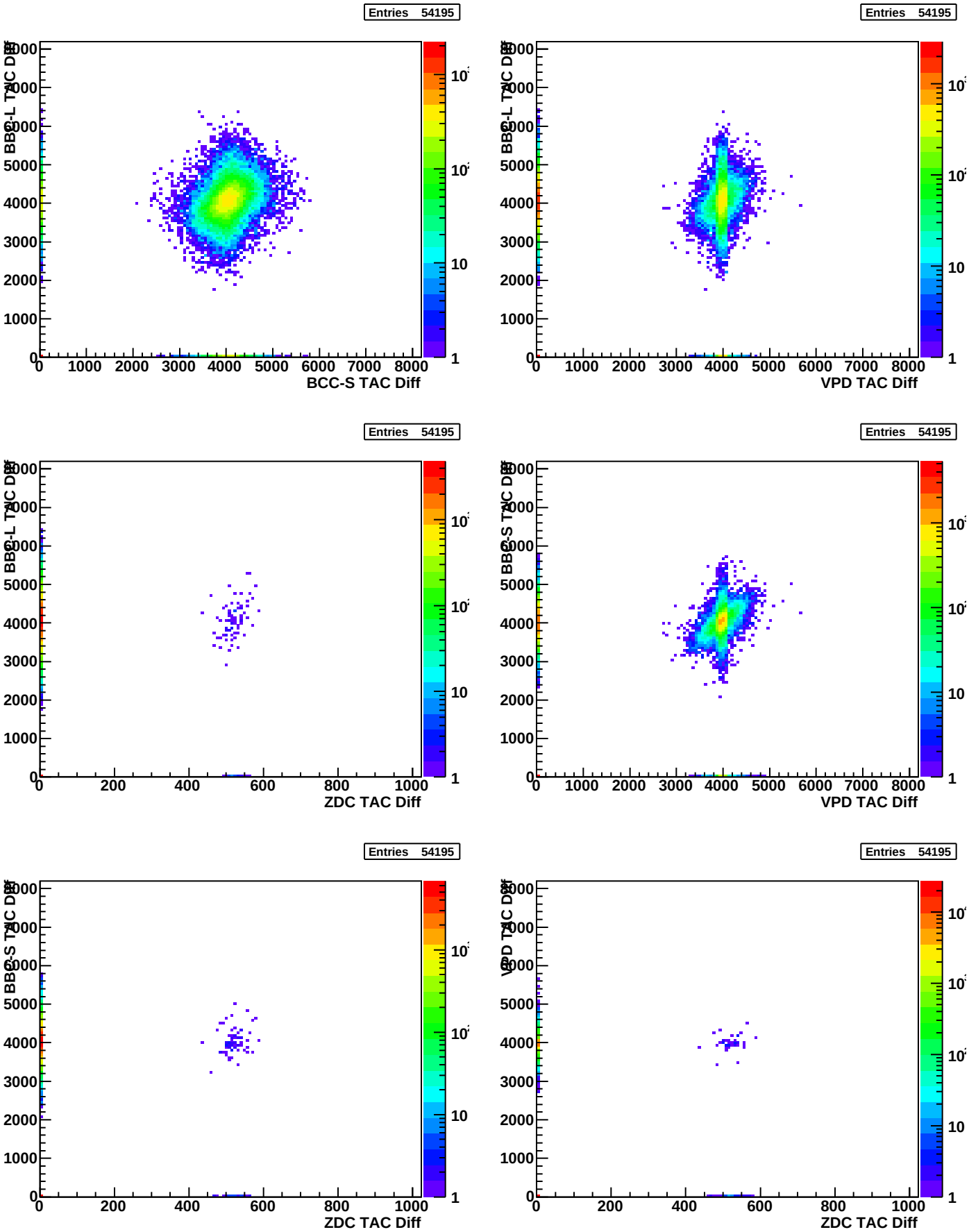

Entries 54195
VPD-West ADC Sum
ZDC-West ADC Sum Att

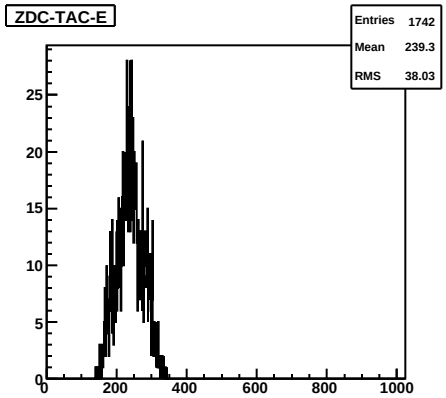

Entries 54195
BBC-L-East ADC Sum
BBC-L-West ADC Sum

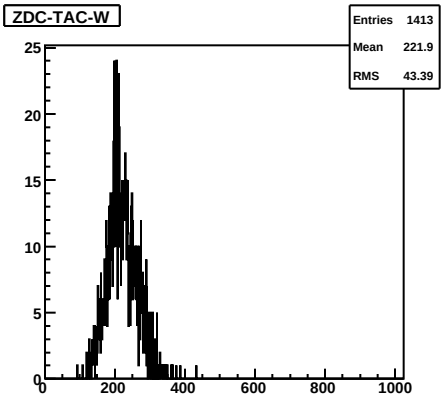

Entries 54195
BBC-S-East ADC Sum
BBC-S-West ADC Sum

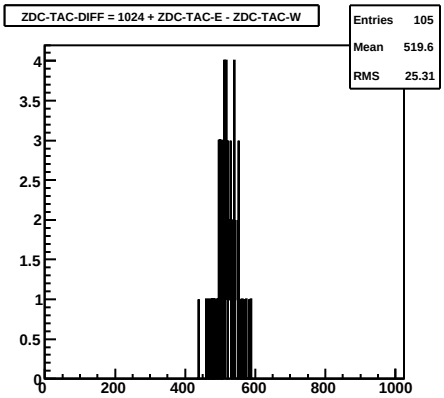

Entries 54195
VPD East ADC Sum
VPD West ADC Sum

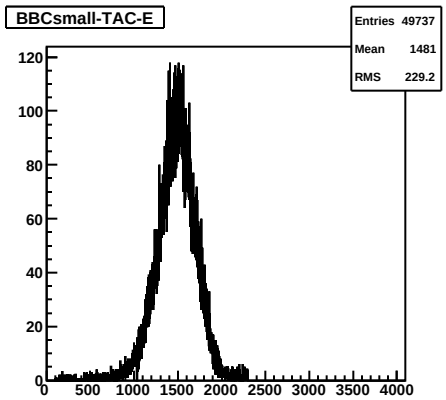

Entries 54195
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

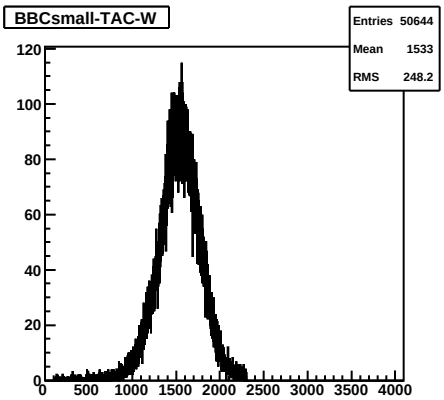

Entries 54195
BBC-L TAC Diff
BCC-S TAC Diff
Entries 54195
BBC-L TAC Diff
VPD TAC Diff
Entries 54195
BBC-L TAC Diff
ZDC TAC Diff
Entries 54195
BBC-S TAC Diff
VPD TAC Diff
Entries 54195
BBC-S TAC Diff
ZDC TAC Diff
Entries 54195
VPD TAC Diff
ZDC TAC Diff

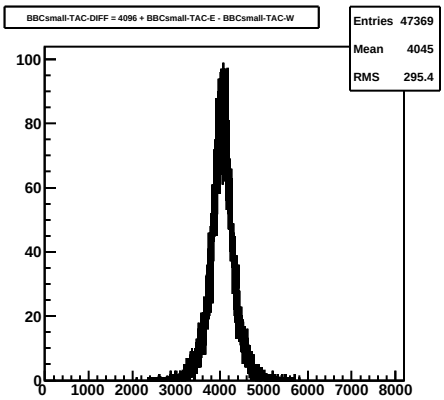

ZDC-TAC-E
Entries 1742
Mean 239.3
RMS 38.03

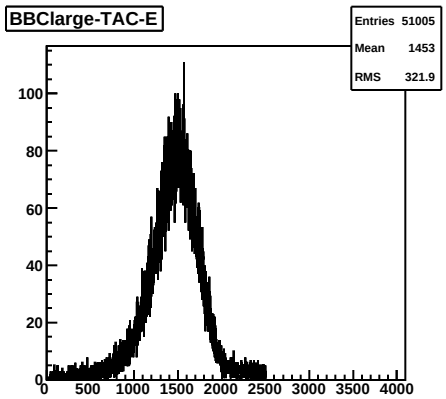

ZDC-TAC-W
Entries 1413
Mean 221.9
RMS 43.39

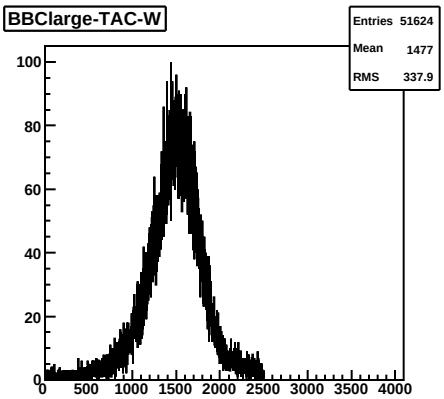

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 105
Mean 519.6
RMS 25.31

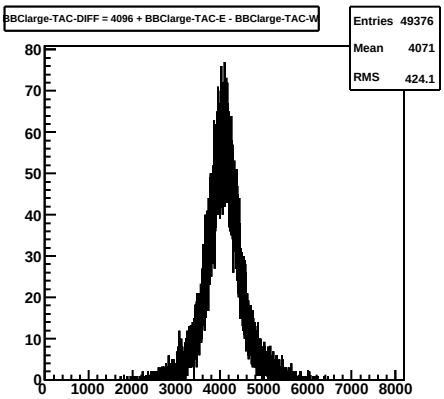

BBCsmall-TAC-E
Entries 49737
Mean 1481
RMS 229.2

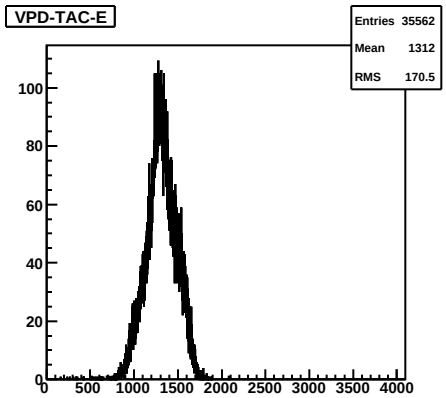

BBCsmall-TAC-W
Entries 50644
Mean 1533
RMS 248.2

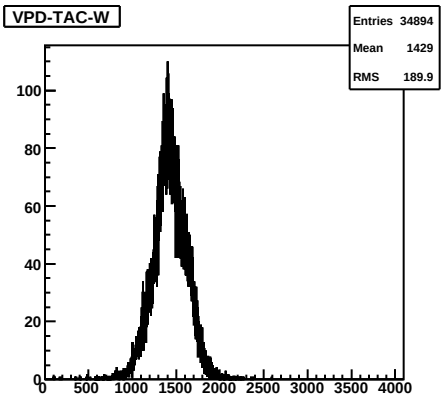

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 47369
Mean 4045
RMS 295.4

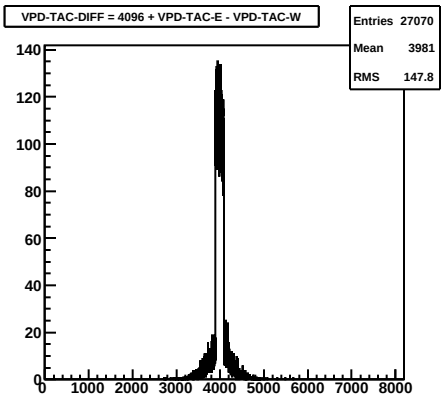

BBClarge-TAC-E
Entries 51005
Mean 1453
RMS 321.9

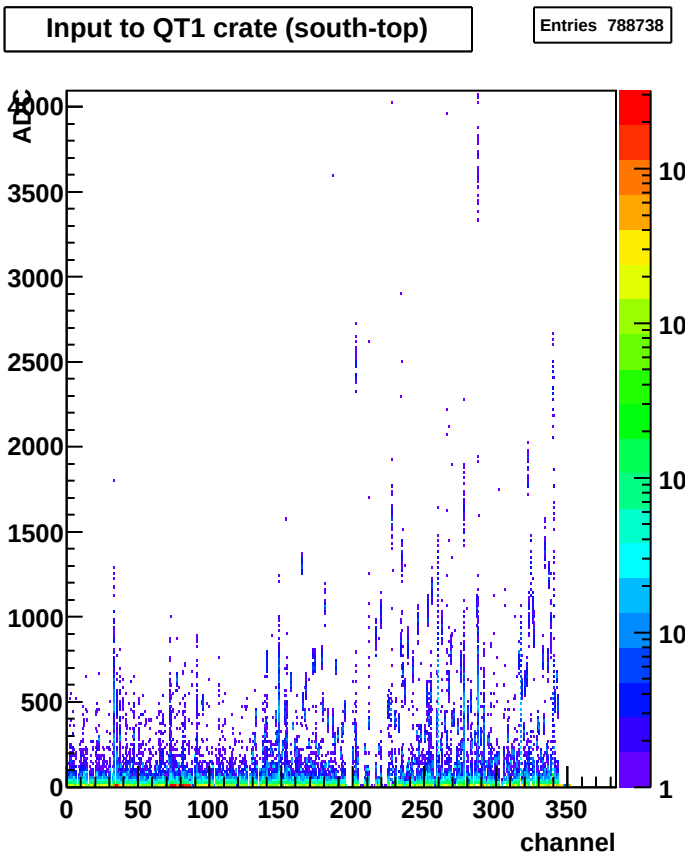

BBClarge-TAC-W
Entries 51624
Mean 1477
RMS 337.9

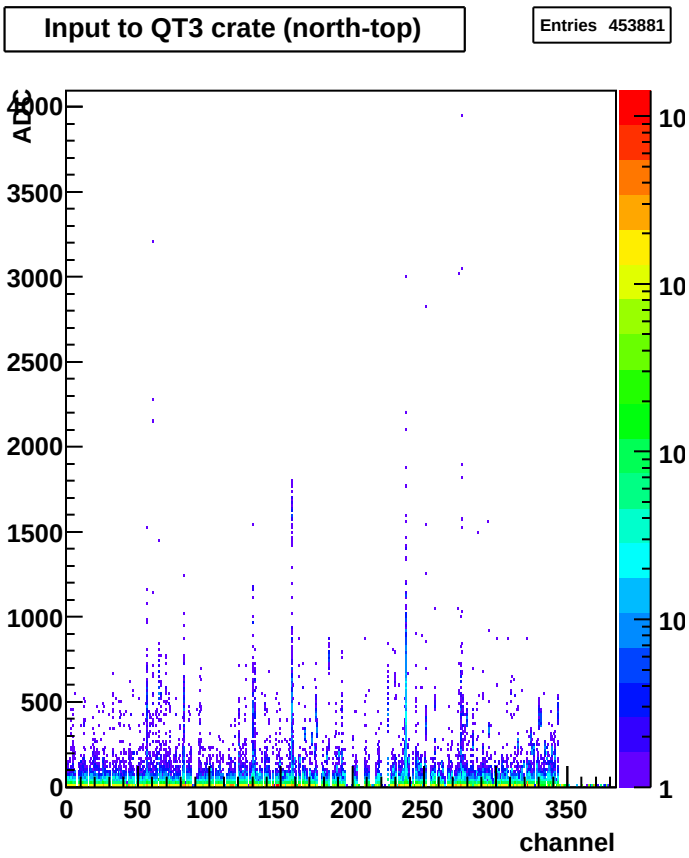

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 49376
Mean 4071
RMS 424.1

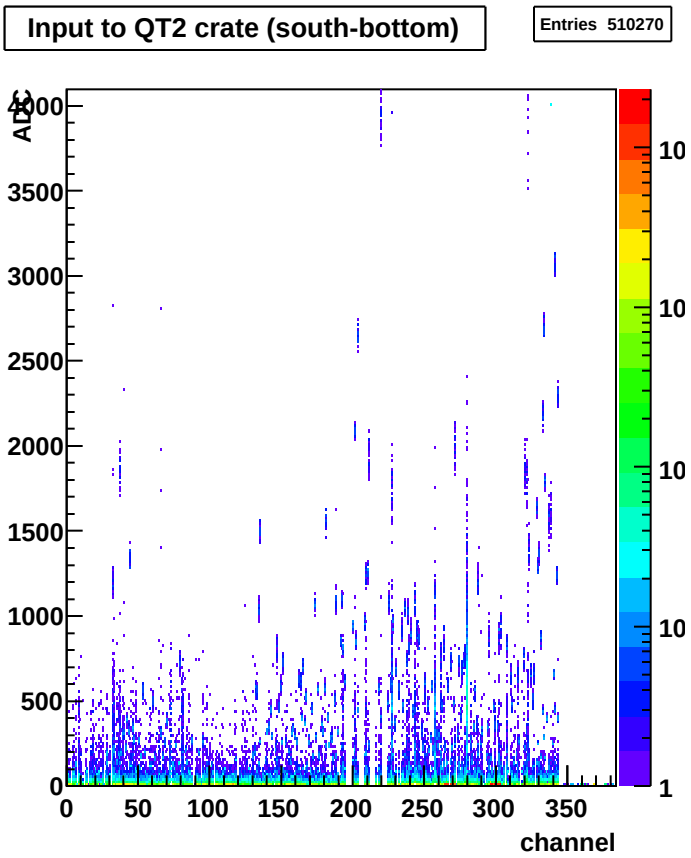

VPD-TAC-E
Entries 35562
Mean 1312
RMS 170.5

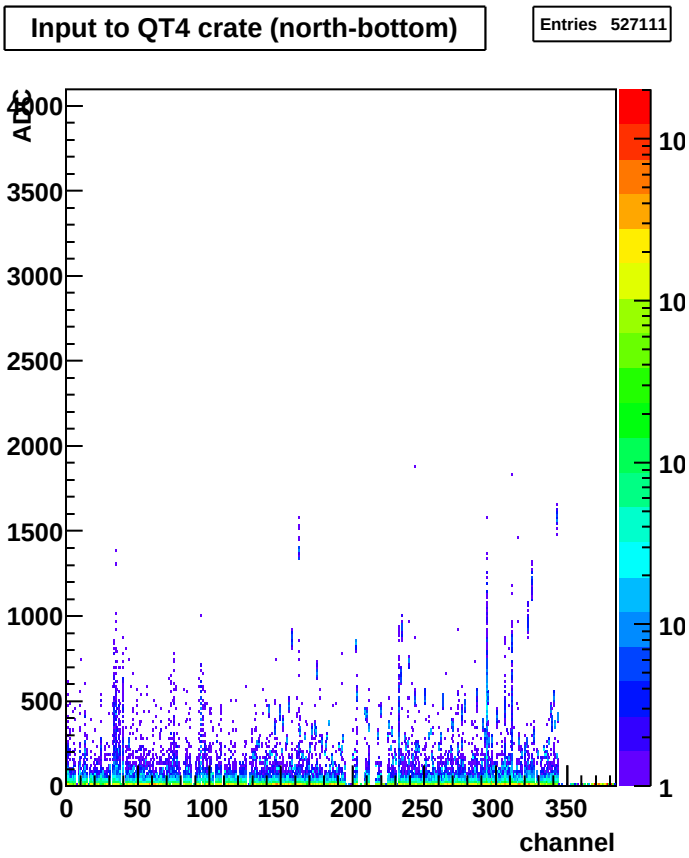

VPD-TAC-W
Entries 34894
Mean 1429
RMS 189.9

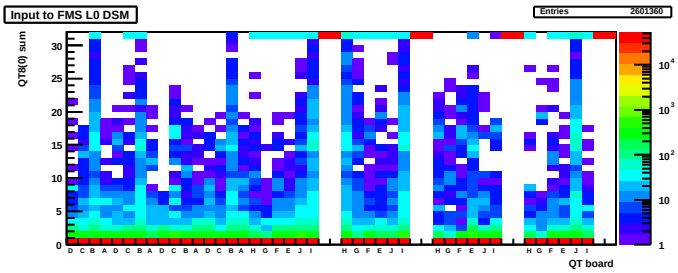

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 27070
Mean 3981
RMS 147.8

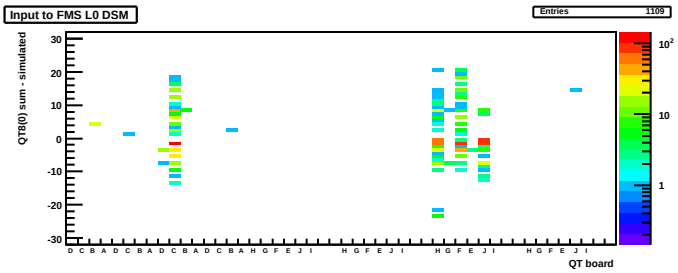

Input to QT1 crate (south-top)
Entries 788738
ADC
channel

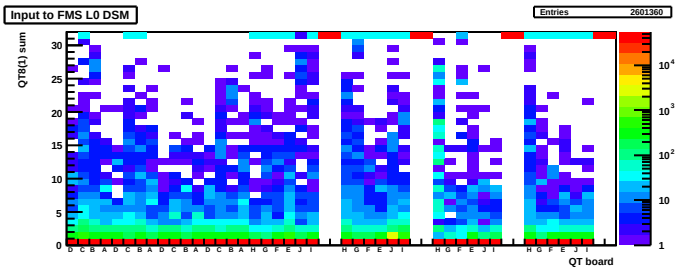

Input to QT3 crate (north-top)
Entries 453881
ADC
channel

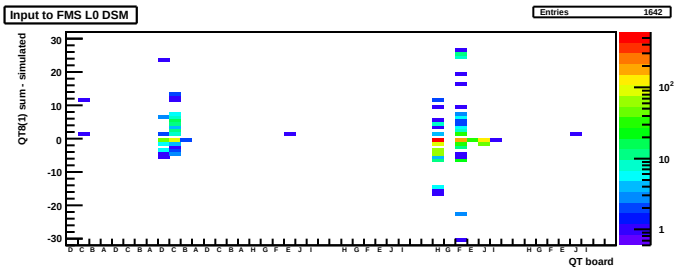

Input to QT2 crate (south-bottom)
Entries 510270
ADC
channel

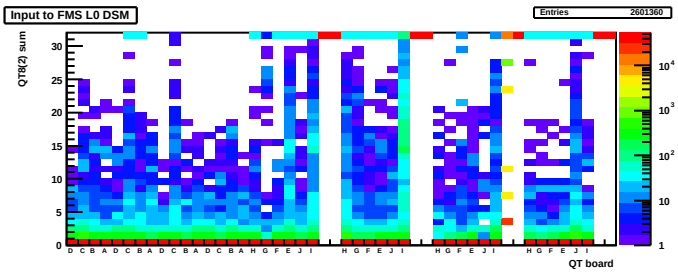

Input to QT4 crate (north-bottom)
Entries 527111
ADC
channel

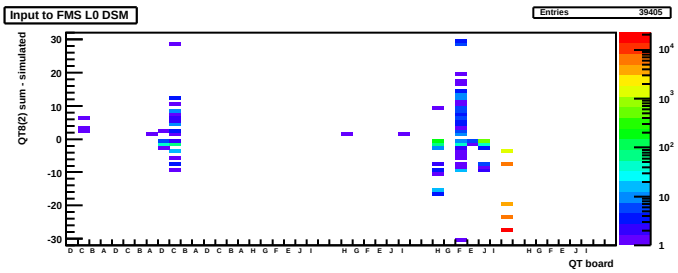

Input to FMS L0 DSM
Entries 2601360
QT8(0) sum
QT board

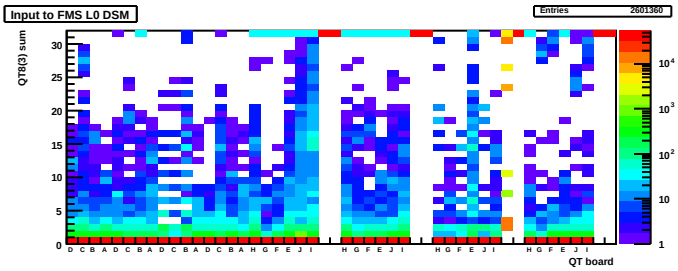

Input to FMS L0 DSM
Entries 1109
QT8(0) sum - simulated
QT board

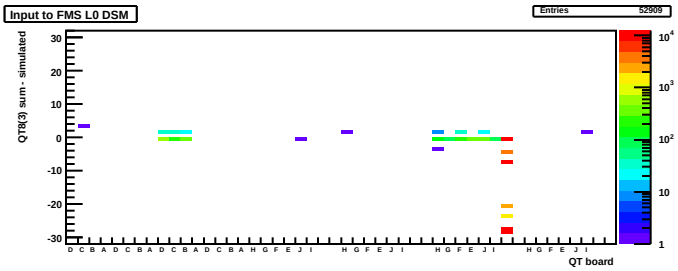

Input to FMS L0 DSM
Entries 2601360
QT8(1) sum
QT board

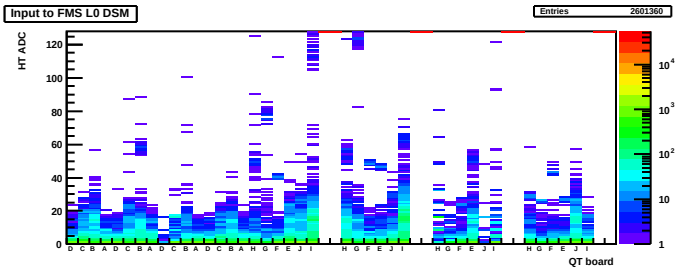

Input to FMS L0 DSM
Entries 1642
QT8(1) sum - simulated
QT board

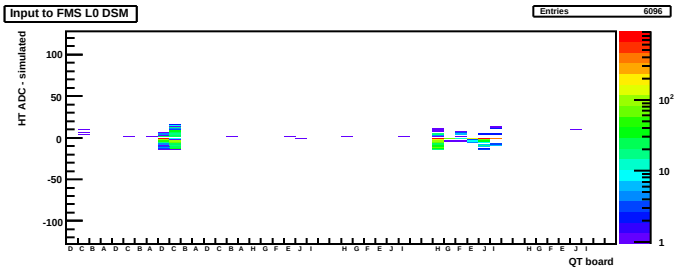

Input to FMS L0 DSM
Entries 2601360
QT8(2) sum
QT board

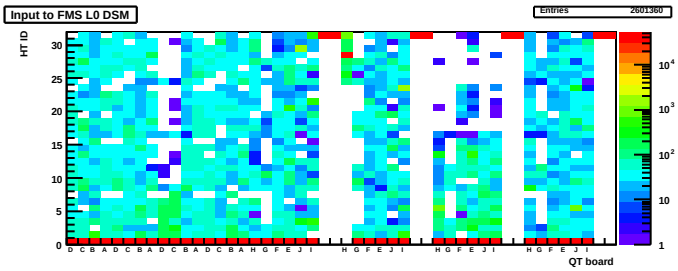

Input to FMS L0 DSM
Entries 39405
QT8(2) sum - simulated
QT board

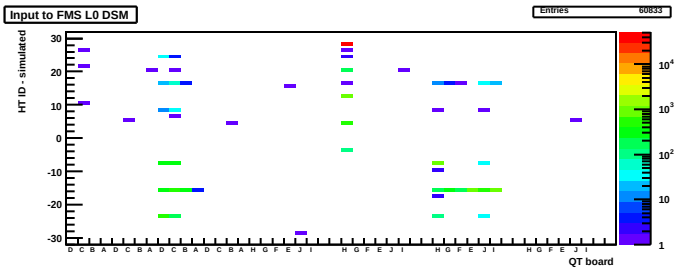

Input to FMS L0 DSM
Entries 2601360
QT8(3) sum
QT board

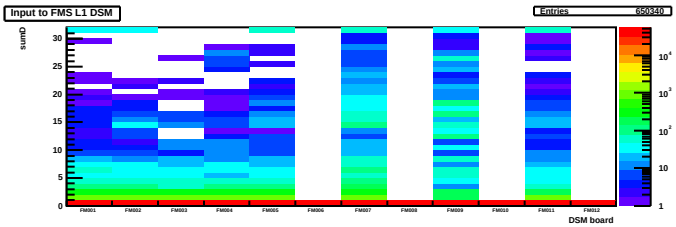

Input to FMS L0 DSM
Entries 52909
QT8(3) sum - simulated
QT board

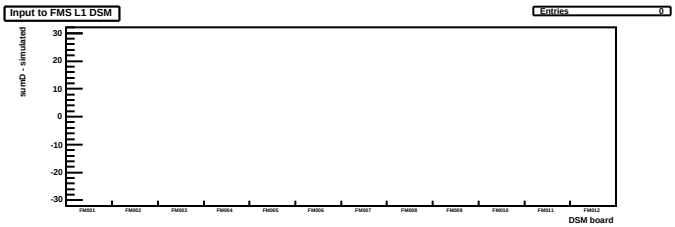

Input to FMS L0 DSM
Entries 2601360
HT ADC
QT board

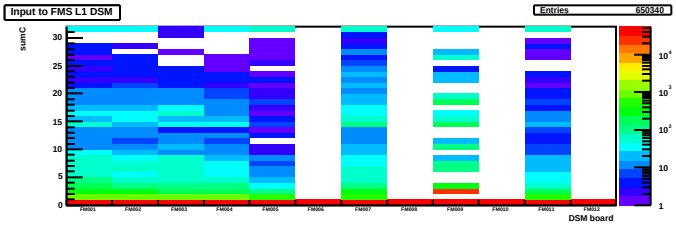

Input to FMS L0 DSM
Entries 6096
HT ADC - simulated
QT board

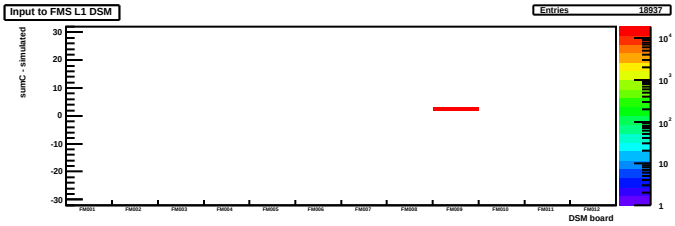

Input to FMS L0 DSM
Entries 2601360
HT ID
QT board

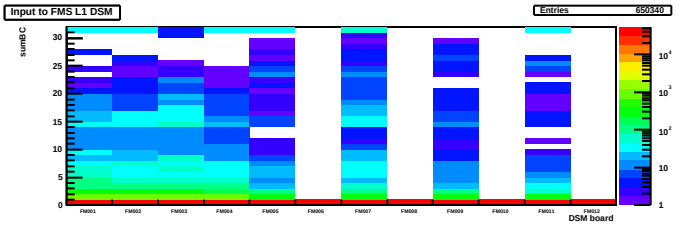

Input to FMS L0 DSM
Entries 60833
HT ID - simulated
QT board

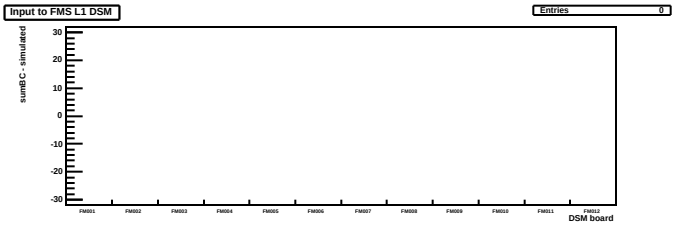

Input to FMS L1 DSM
Entries 650340
sumD
DSM board

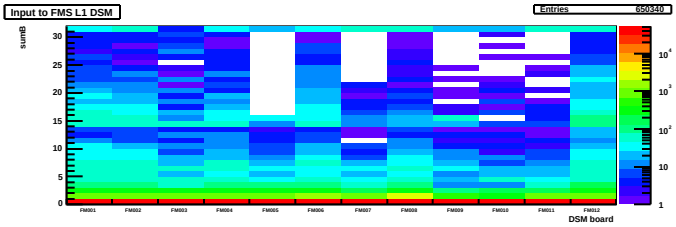

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

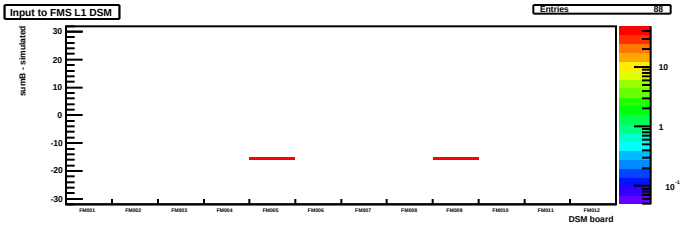

Input to FMS L1 DSM
Entries 650340
sumC
DSM board

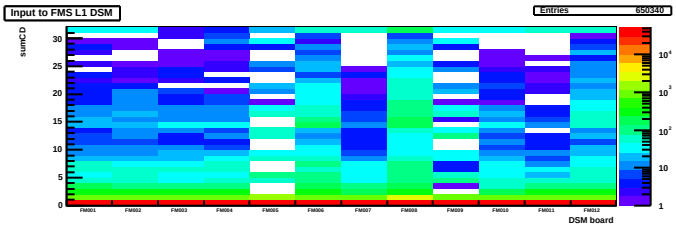

Input to FMS L1 DSM
Entries 18937
sumC - simulated
DSM board

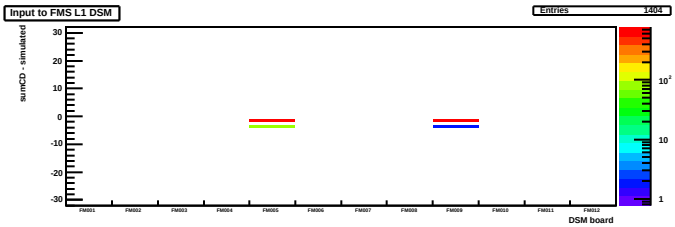

Input to FMS L1 DSM
Entries 650340
sumBC
DSM board

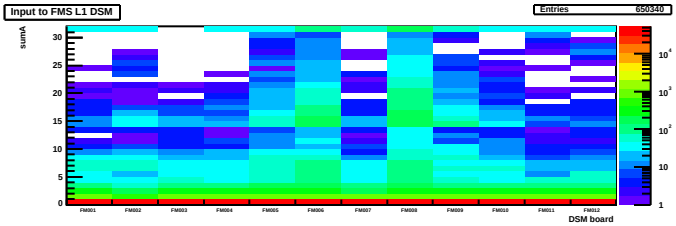

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

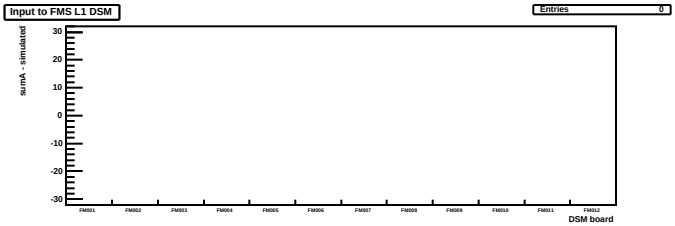

Input to FMS L1 DSM
Entries 650340
sumB
DSM board

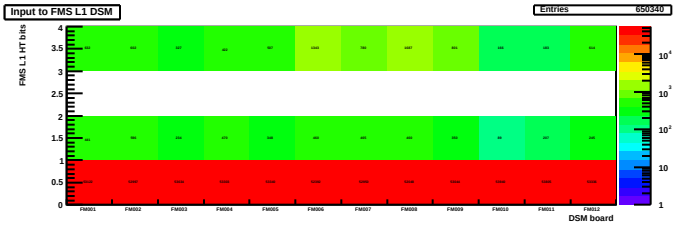

Input to FMS L1 DSM
Entries 88
sumB - simulated
DSM board

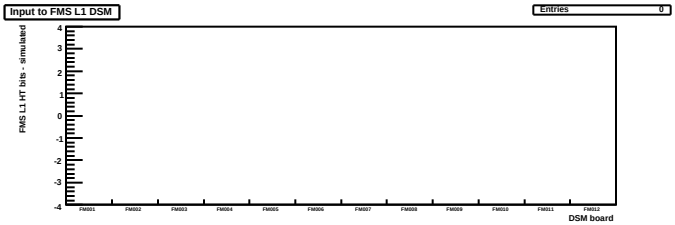

Input to FMS L1 DSM
Entries 650340
sumCD
DSM board

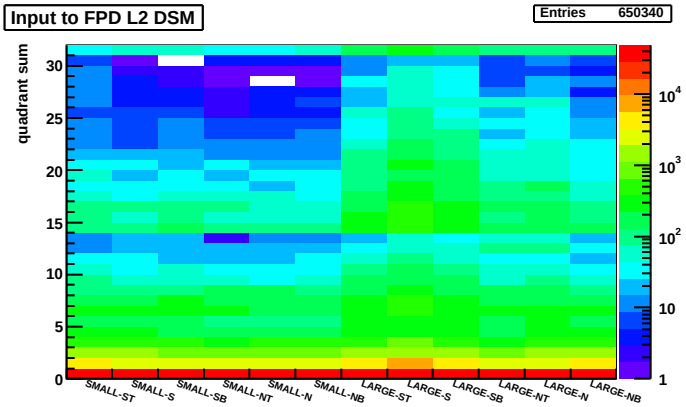

Input to FMS L1 DSM
Entries 1404
sumCD - simulated
DSM board

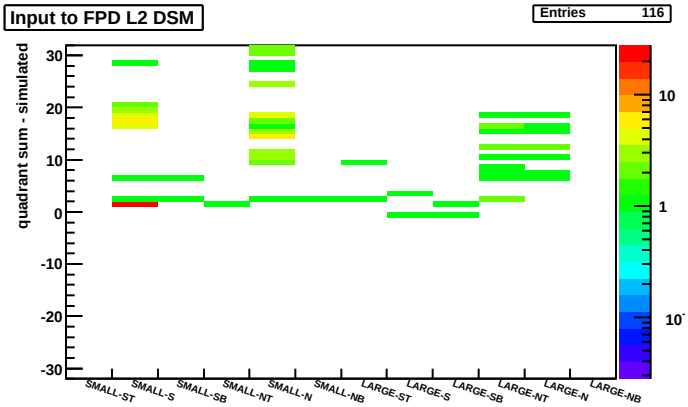

Input to FMS L1 DSM
Entries 650340
sumA
DSM board

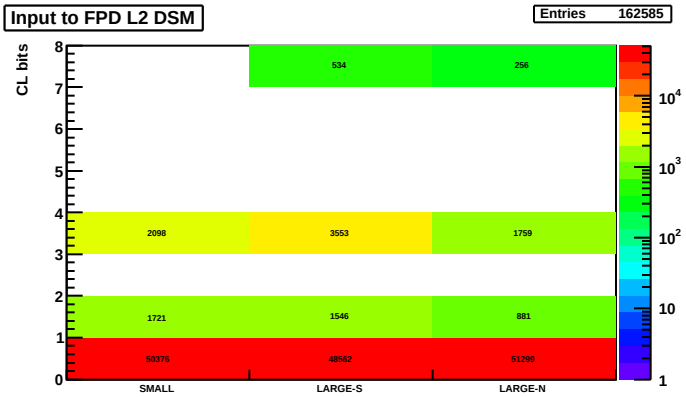

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

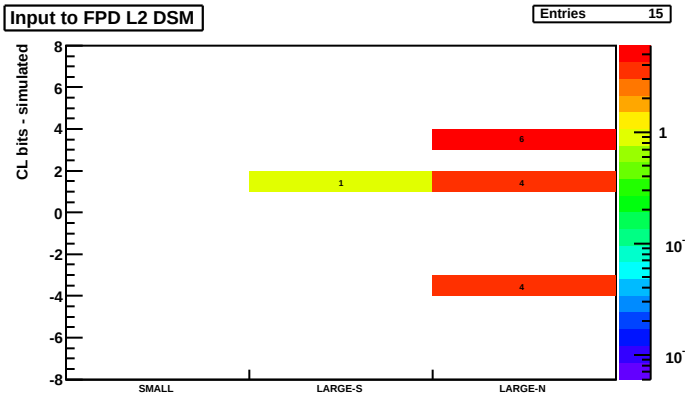

Input to FMS L1 DSM
Entries 650340
FMS L1 HT bits
DSM board

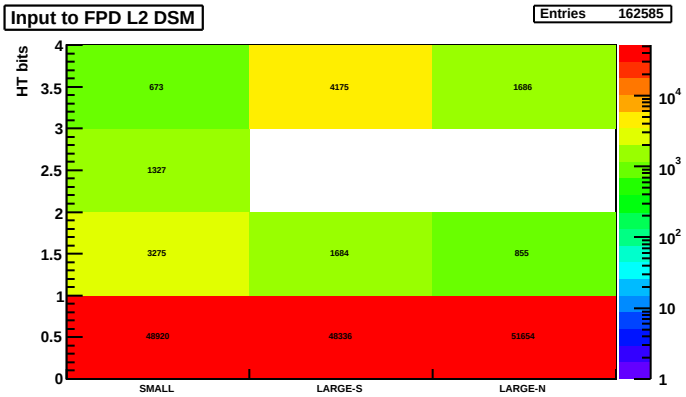

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

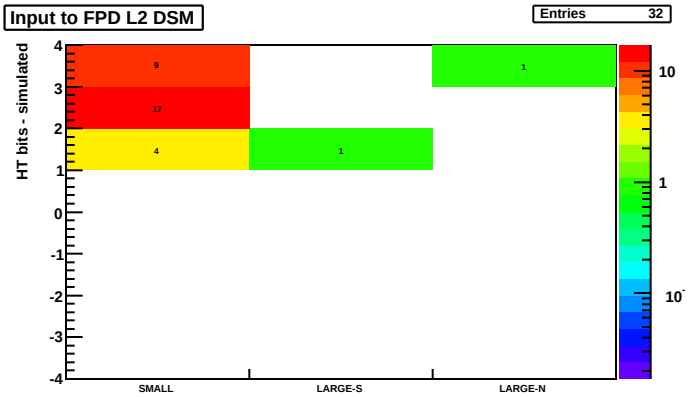

Input to FPD L2 DSM
Entries 650340
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

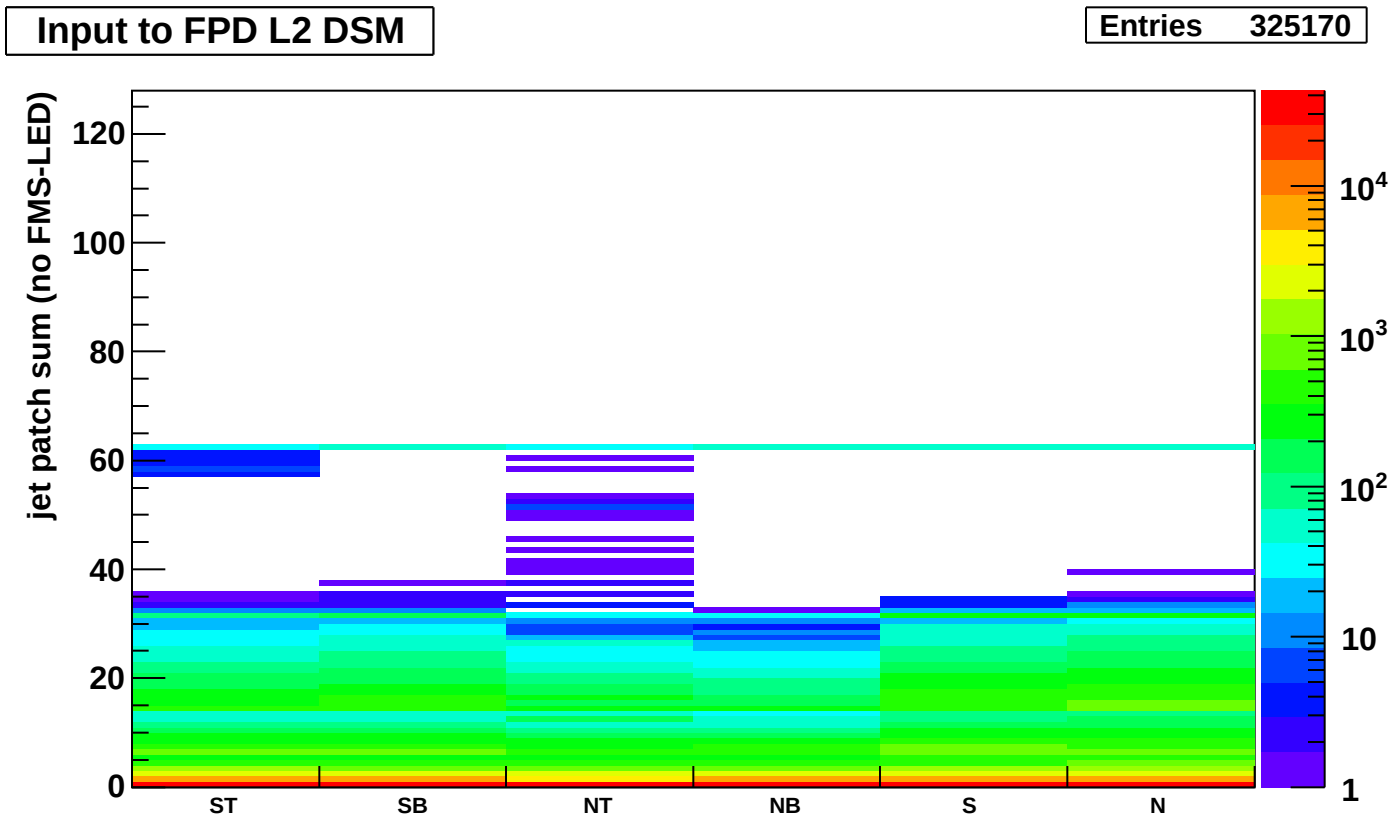

Input to FPD L2 DSM
Entries 116
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

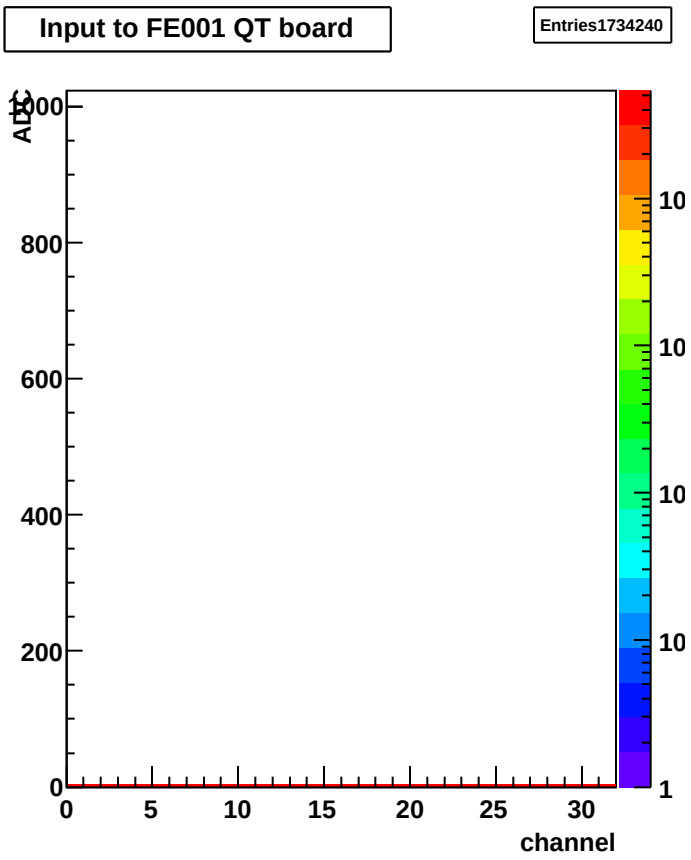

Input to FPD L2 DSM
Entries 162585
CL bits
534
256
2098
3553
1759
1721
1546
881
50376
48562
51299
SMALL
LARGE-S
LARGE-N

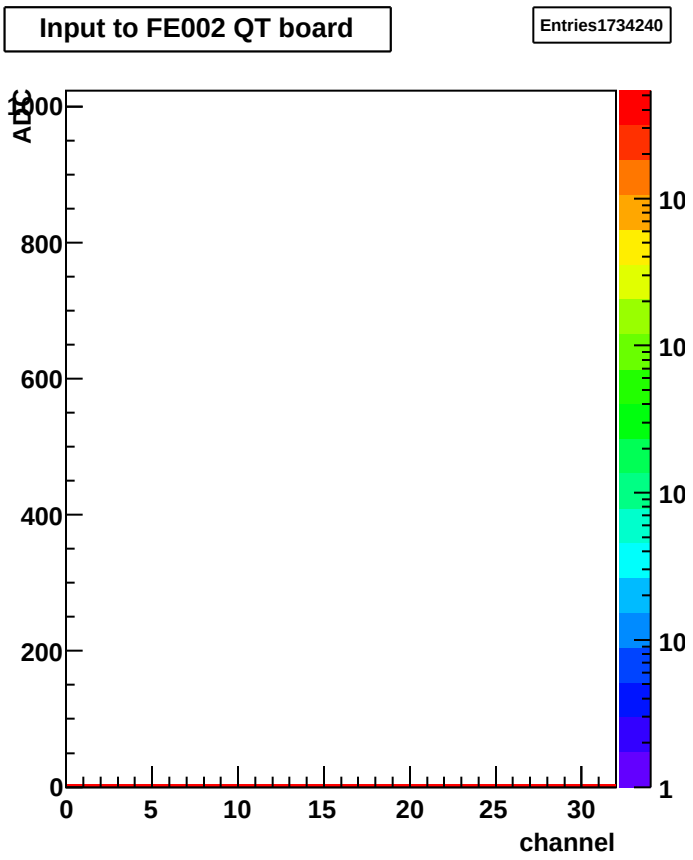

Input to FPD L2 DSM
Entries 15
CL bits - simulated
6
1
4
4
SMALL
LARGE-S
LARGE-N

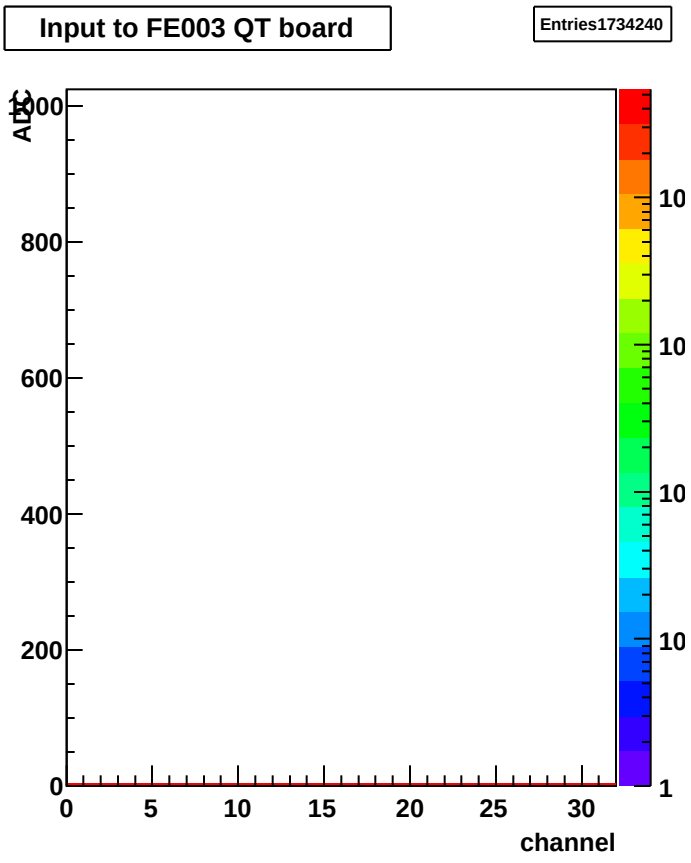

Input to FPD L2 DSM
Entries 162585
HT bits
673
4175
1686
1327
3275
1684
855
48920
48336
51654
SMALL
LARGE-S
LARGE-N

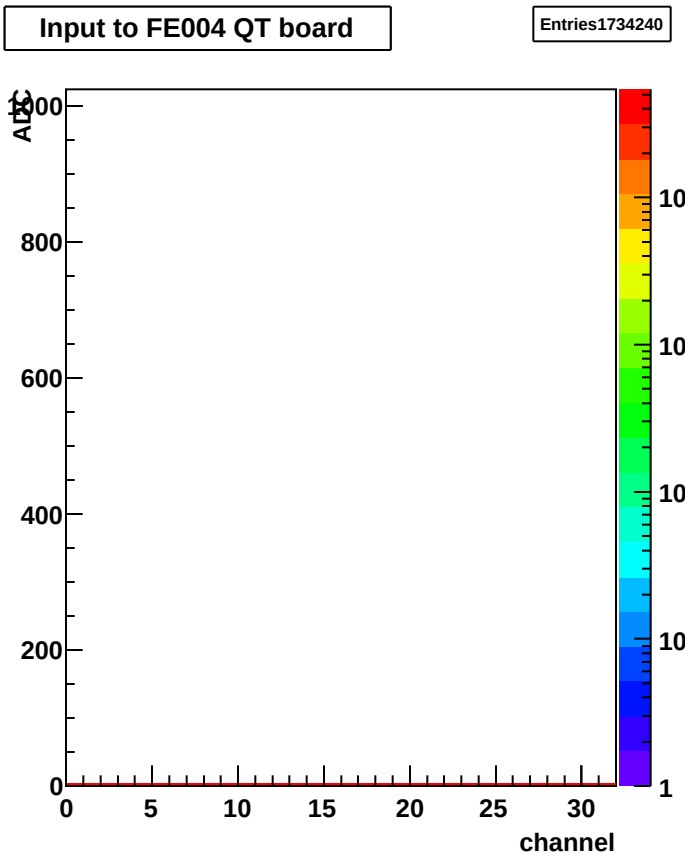

Input to FPD L2 DSM
Entries 32
HT bits - simulated
9
1
17
4
1
SMALL
LARGE-S
LARGE-N

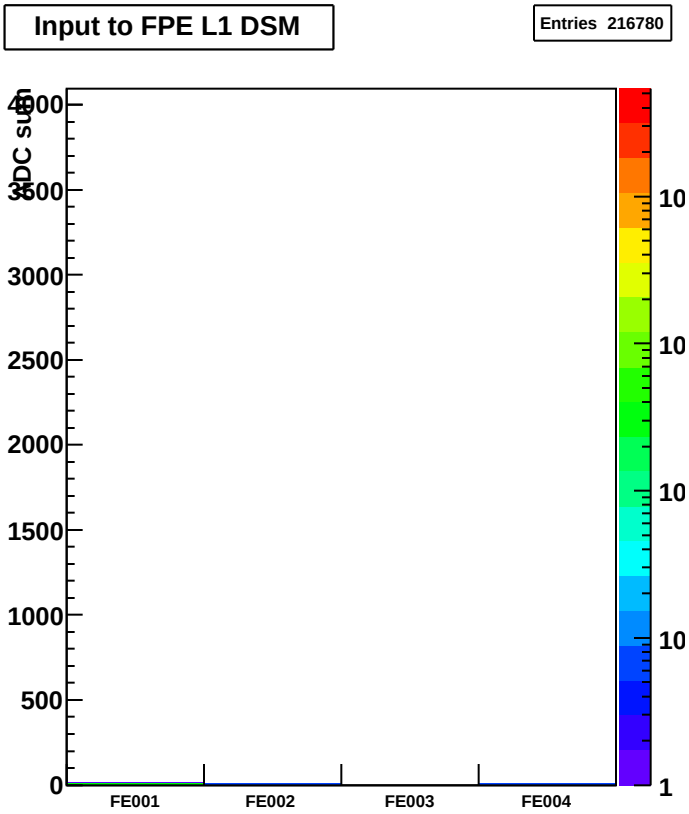

Input to FPD L2 DSM
Entries 325170
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

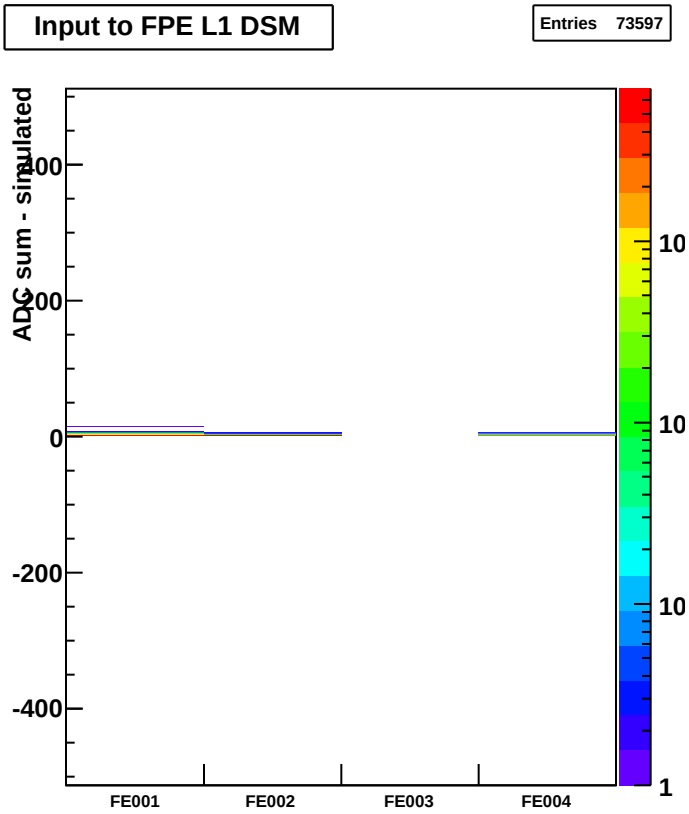

Input to FE001 QT board
Entries1734240
ADC
channel


Input to FE002 QT board
Entries1734240
ADC
channel

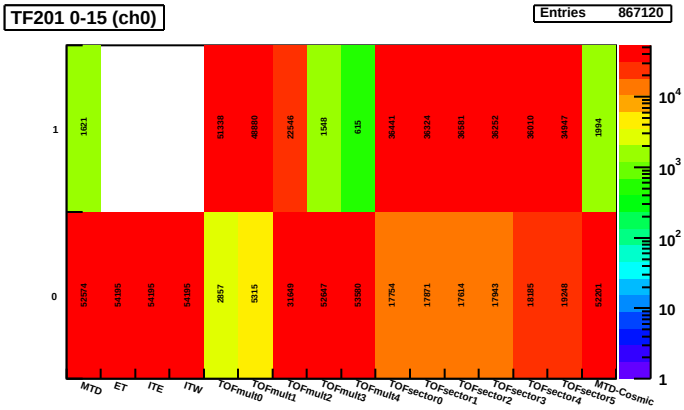

Input to FE003 QT board
Entries1734240
ADC
channel


Input to FE004 QT board
Entries1734240
ADC
channel

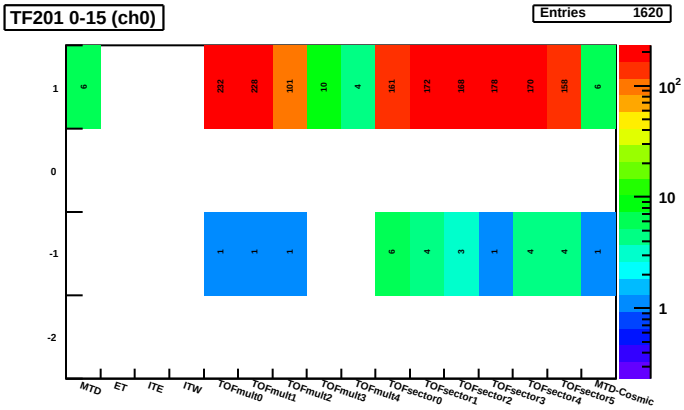

Input to FPE L1 DSM
Entries 216780
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 73597
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
1

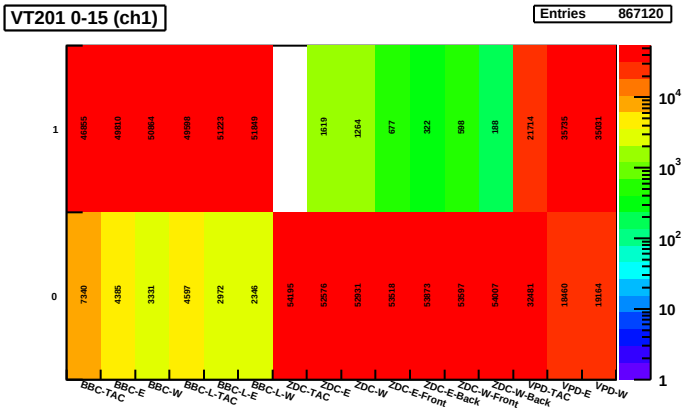
TF201 0-15 (ch0)



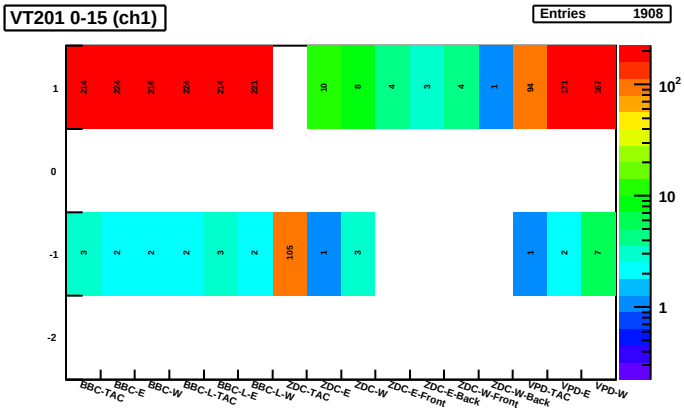
TF201 0-15 (ch0)



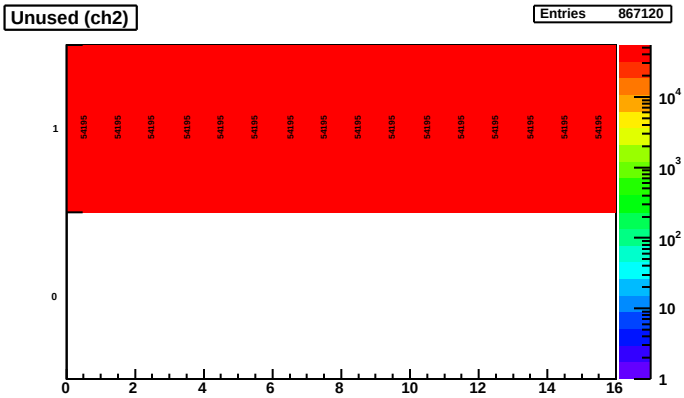
VT201 0-15 (ch1)

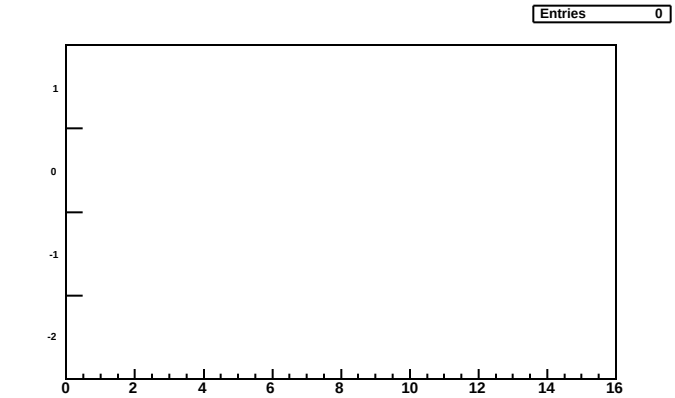


VT201 0-15 (ch1)

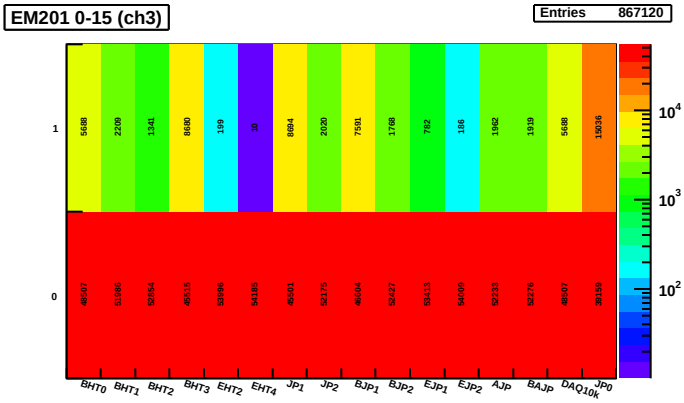


Unused (ch2)

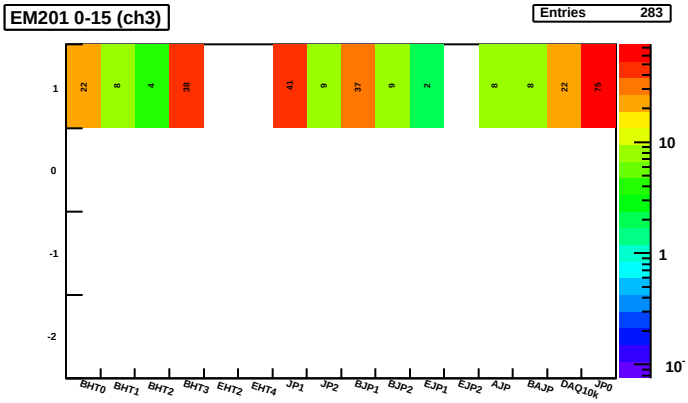




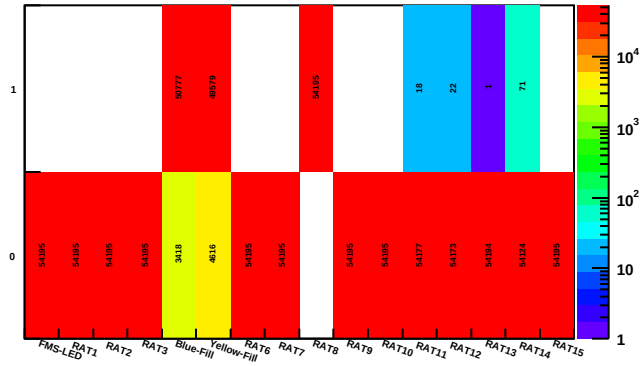
EM201 0-15 (ch3)

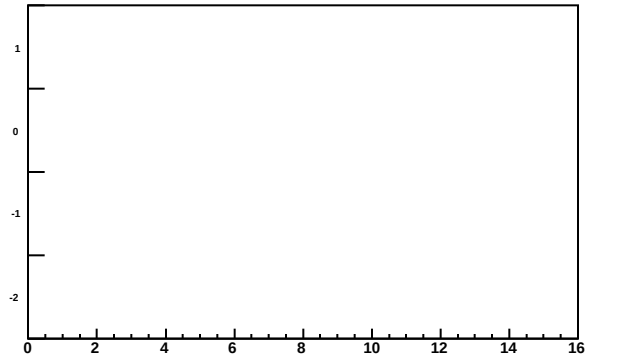


EM201 0-15 (ch3)

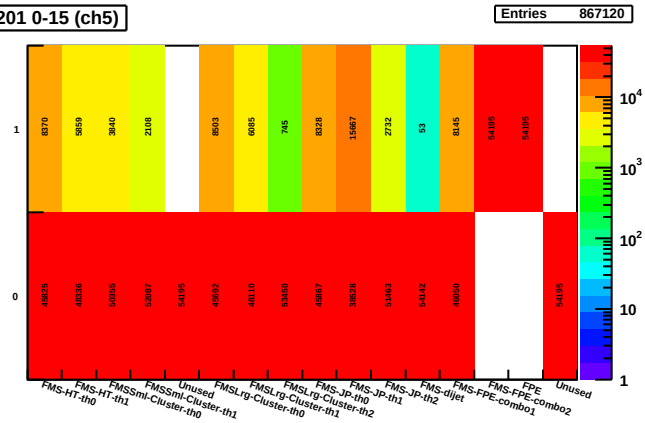


RAT board (ch4)

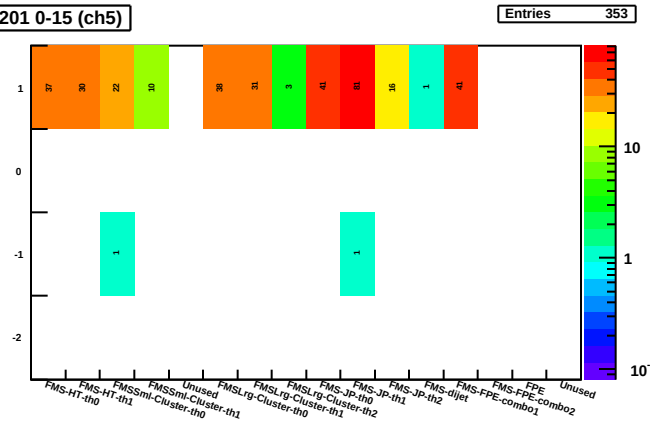




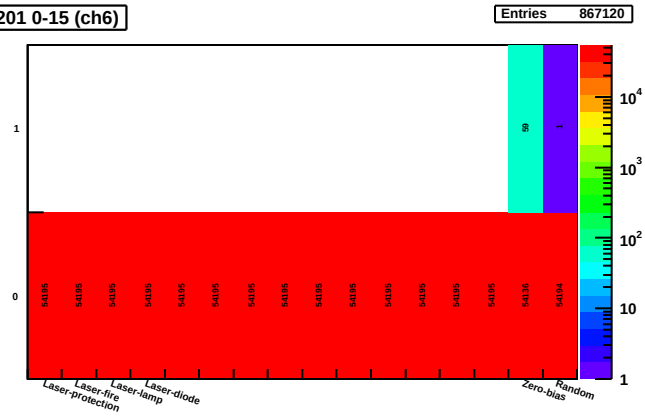
FP201 0-15 (ch5)

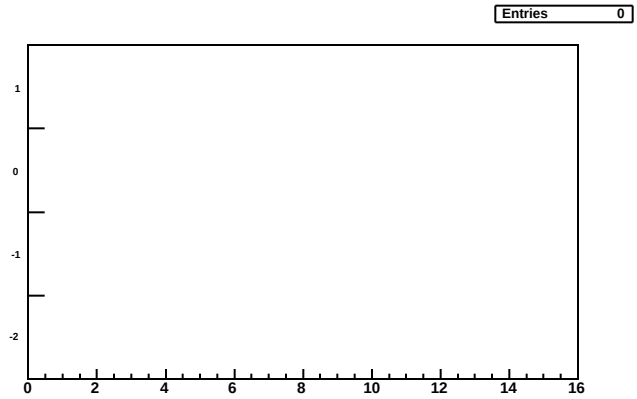
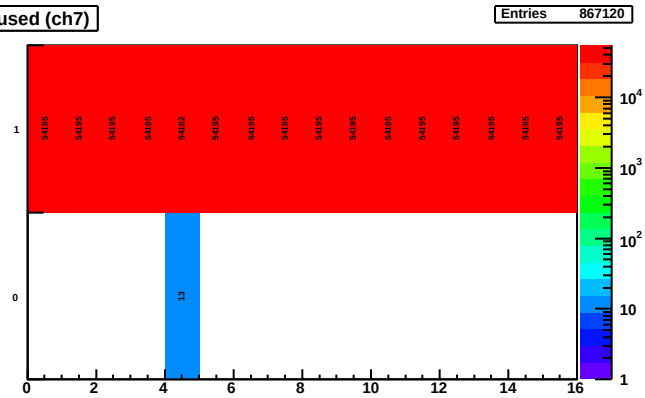
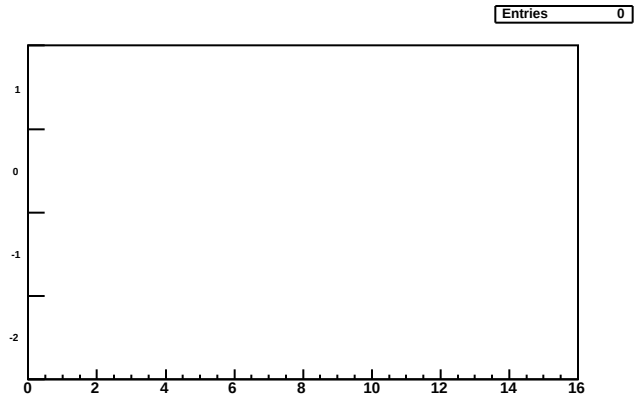


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)