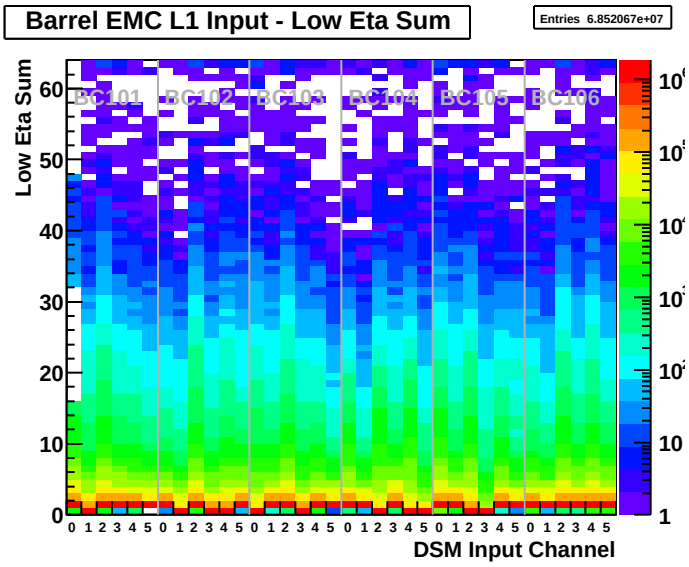
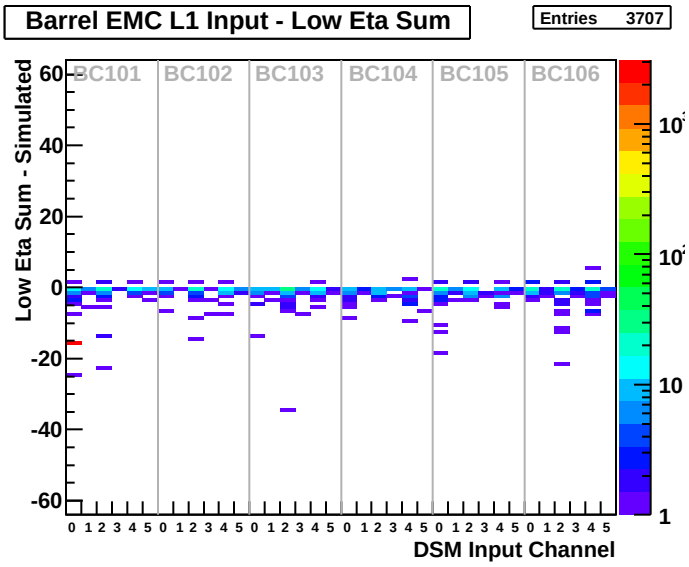
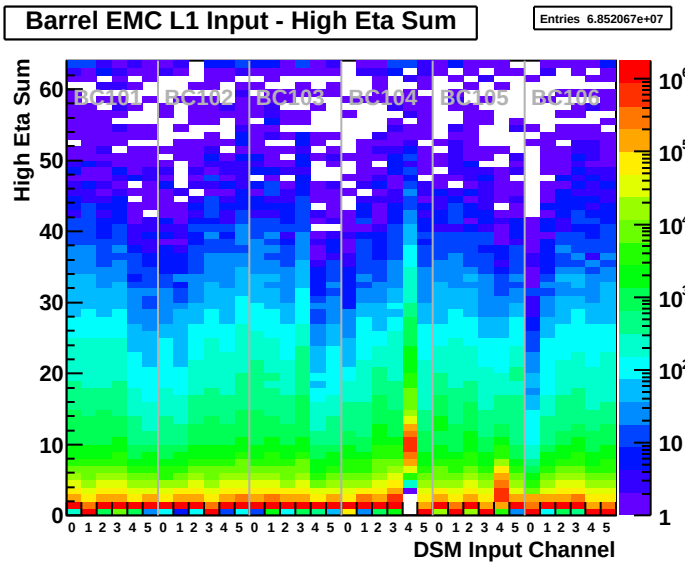
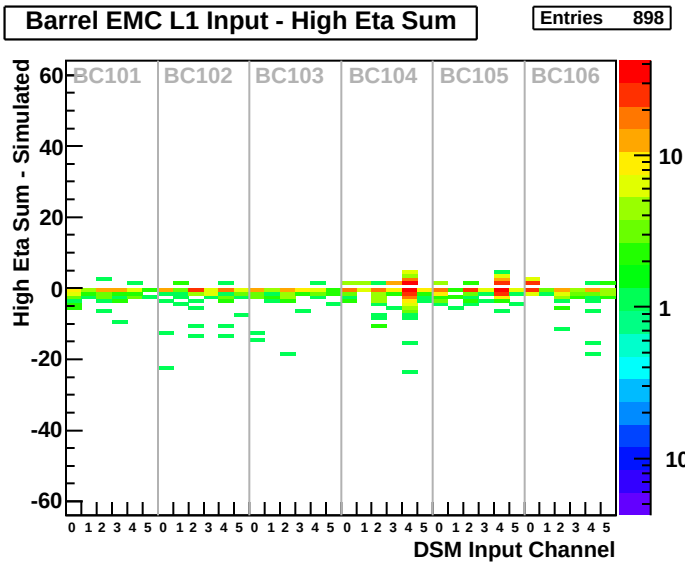
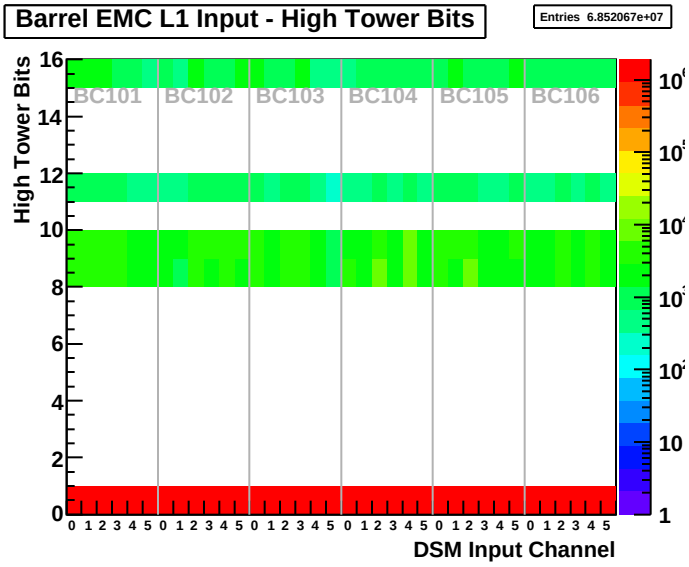
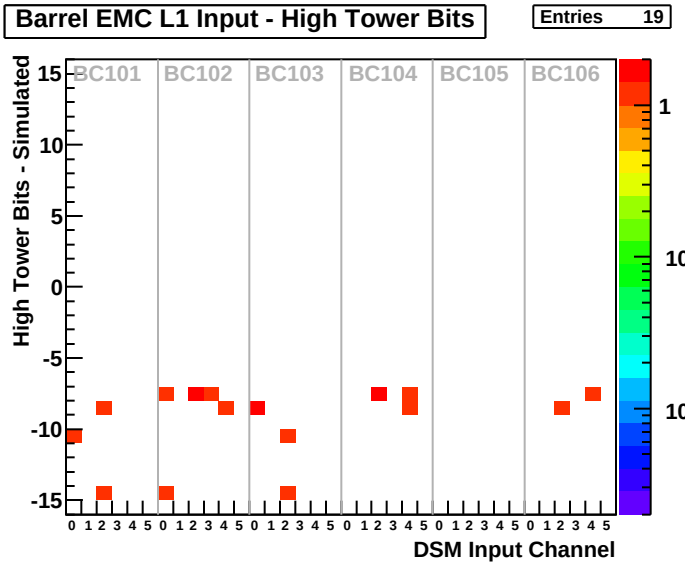

Barrel EMC L1 Input - Low Eta Sum
Entries 6.852067e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

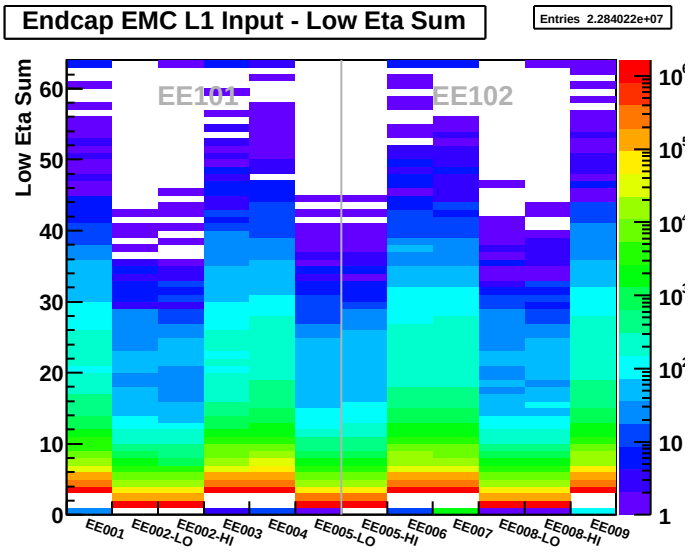

Barrel EMC L1 Input - Low Eta Sum
Entries 3707
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

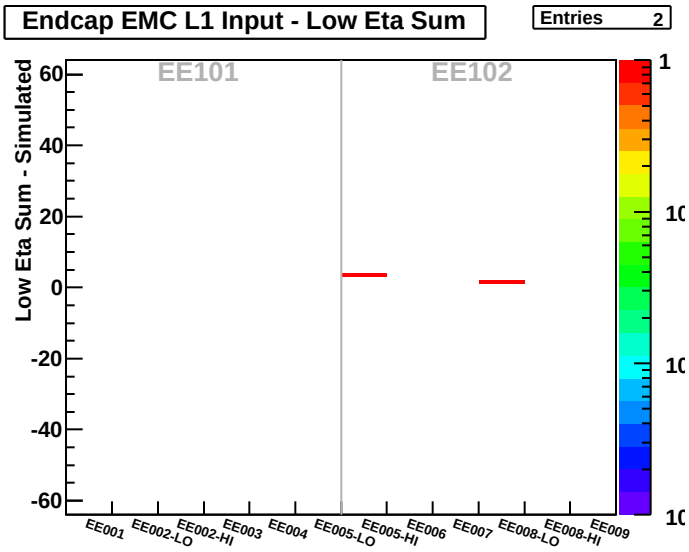

Barrel EMC L1 Input - High Eta Sum
Entries 6.852067e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

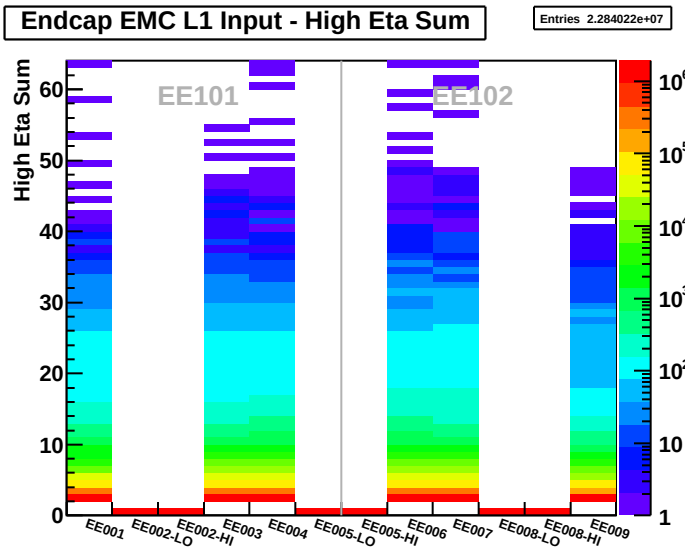

Barrel EMC L1 Input - High Eta Sum
Entries 898
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

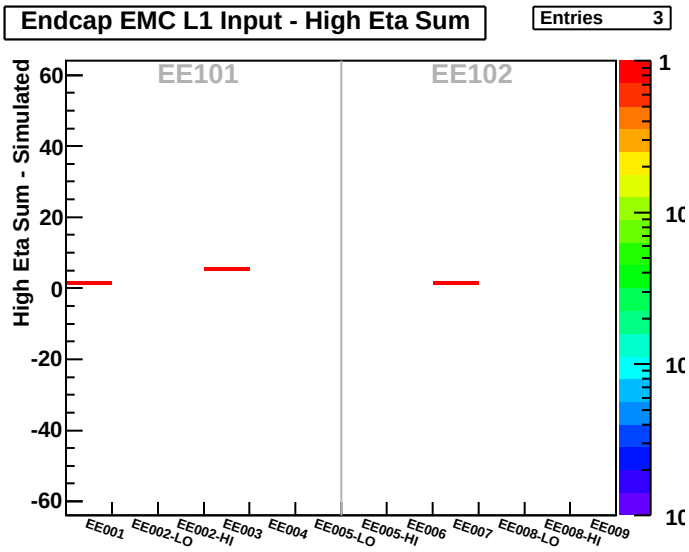

Barrel EMC L1 Input - High Tower Bits
Entries 6.852067e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

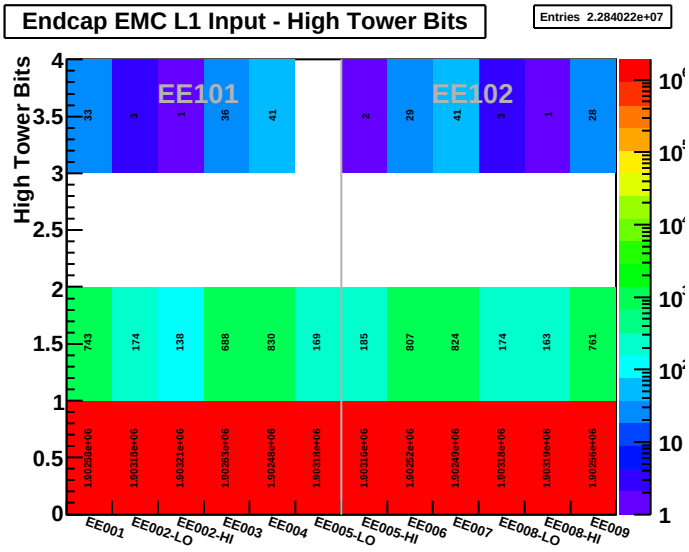

Barrel EMC L1 Input - High Tower Bits
Entries 19
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

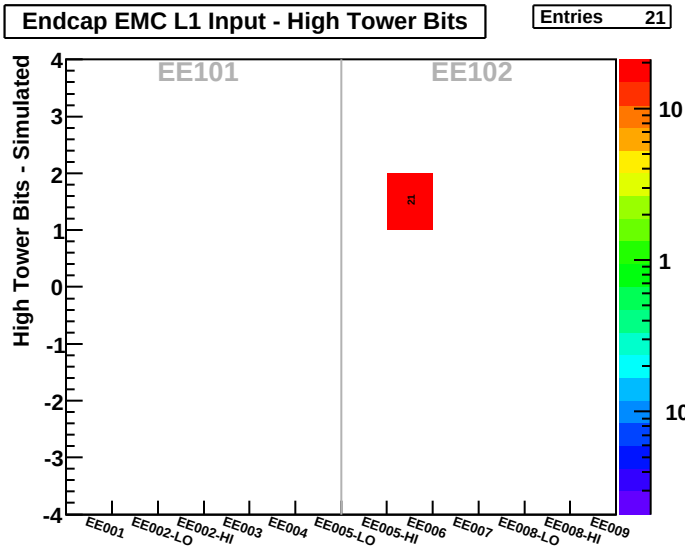

Endcap EMC L1 Input - Low Eta Sum
Entries 2.284022e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

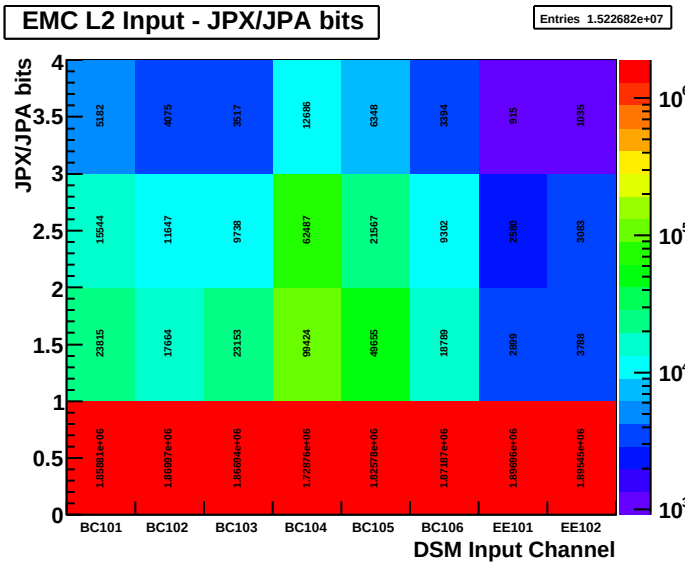

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

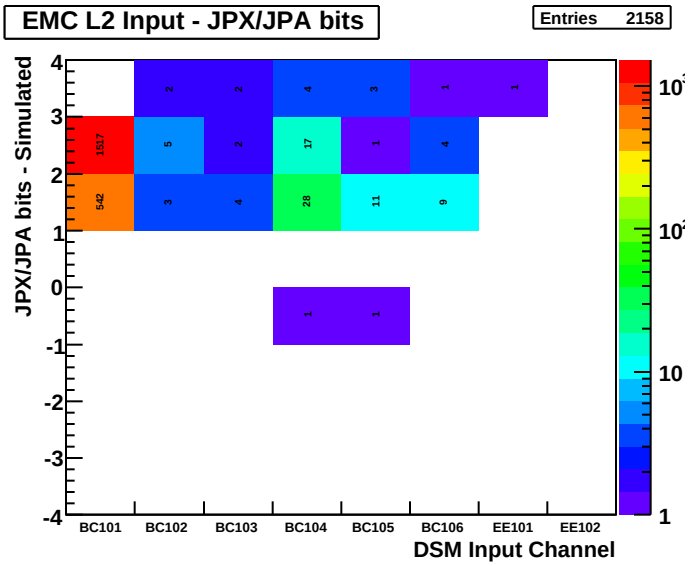

Endcap EMC L1 Input - High Eta Sum
Entries 2.284022e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

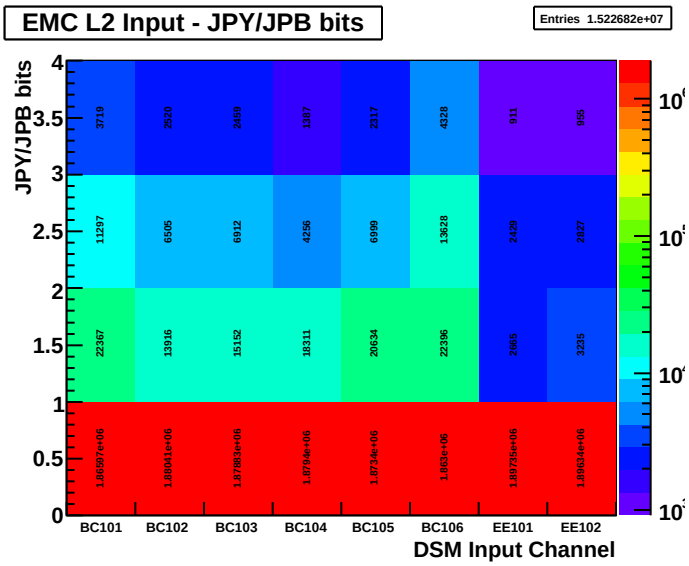

Endcap EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

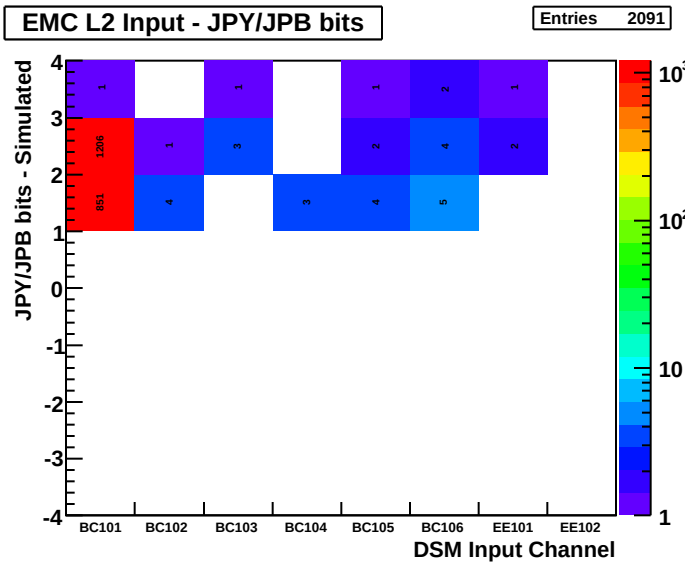

Endcap EMC L1 Input - High Tower Bits
Entries 2.284022e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

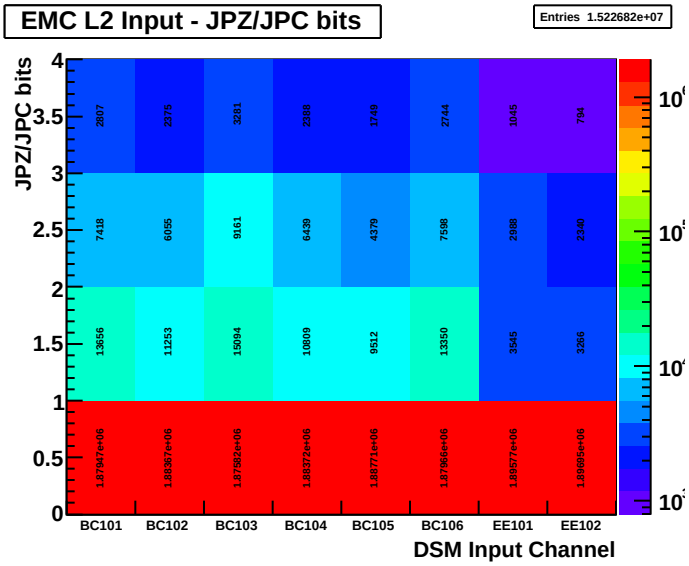

Endcap EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

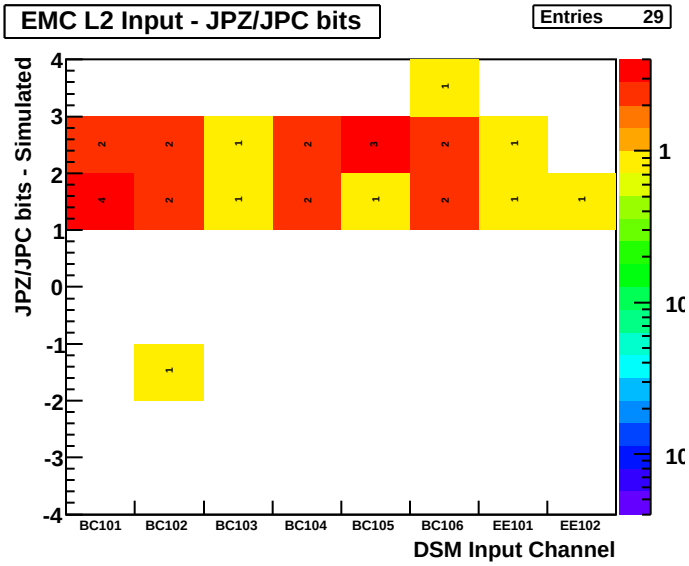

EMC L2 Input - JPX/JPA bits
Entries 1.522682e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

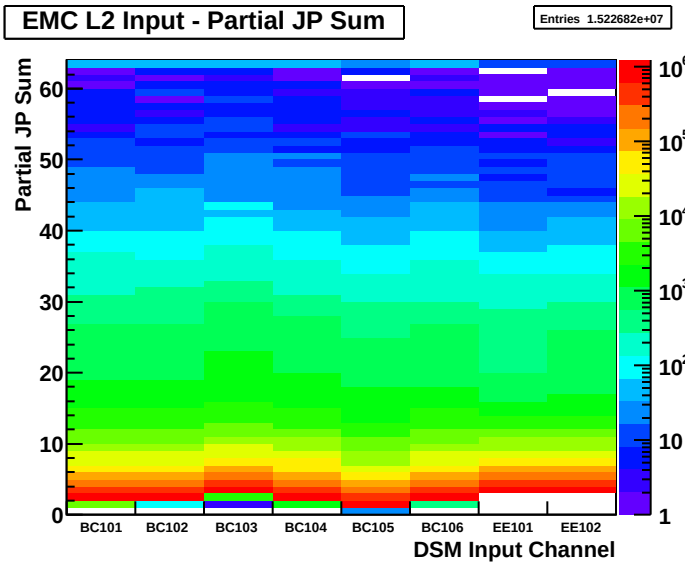

EMC L2 Input - JPX/JPA bits
Entries 2158
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

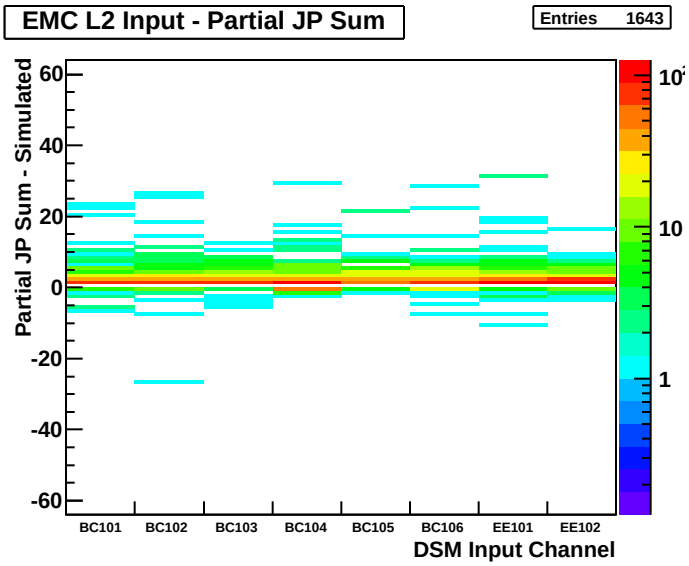

EMC L2 Input - JPY/JPB bits
Entries 1.522682e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

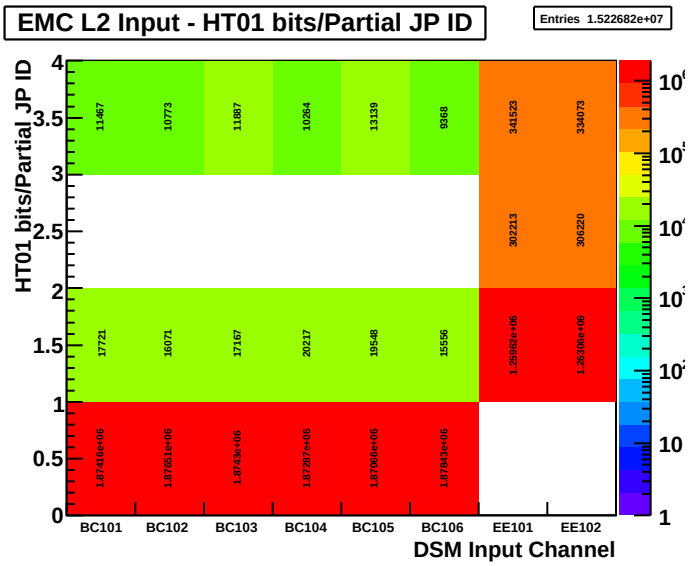

EMC L2 Input - JPY/JPB bits
Entries 2091
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

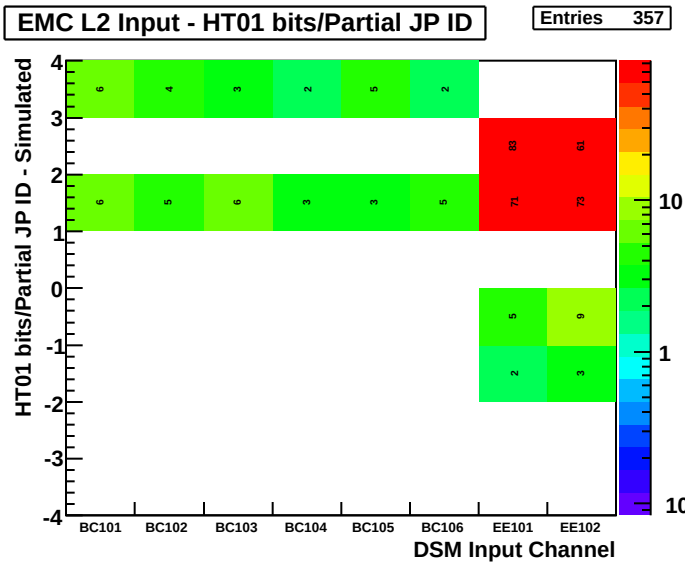

EMC L2 Input - JPZ/JPC bits
Entries 1.522682e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

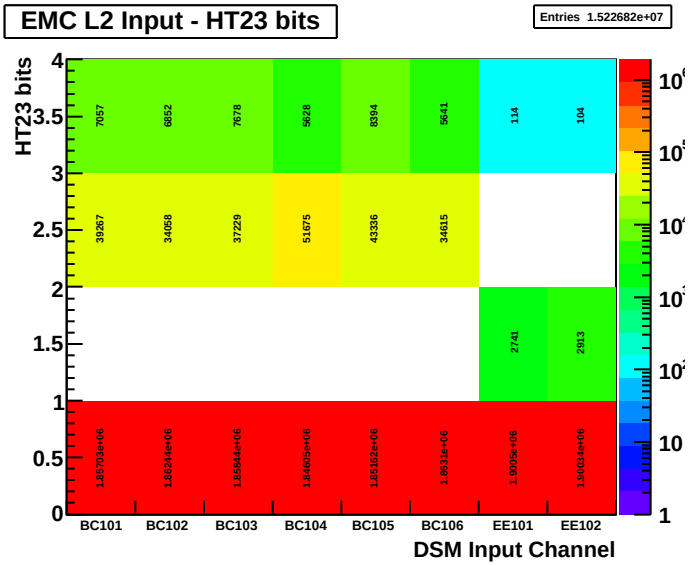

EMC L2 Input - JPZ/JPC bits
Entries 29
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

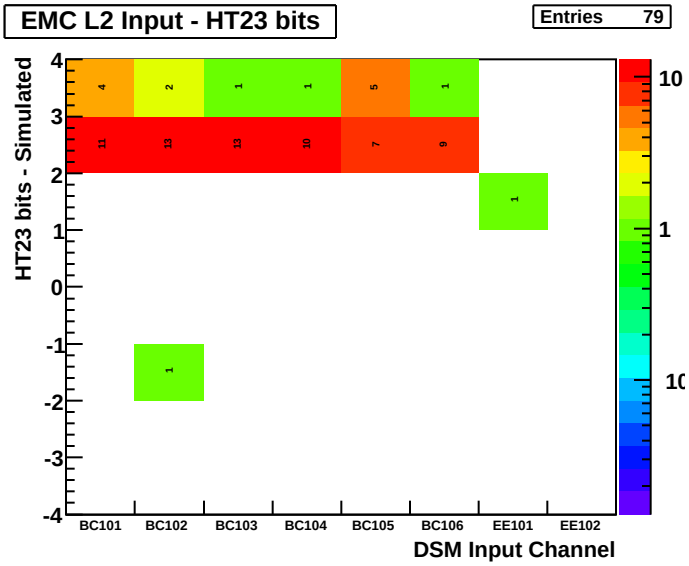

EMC L2 Input - Partial JP Sum
Entries 1.522682e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

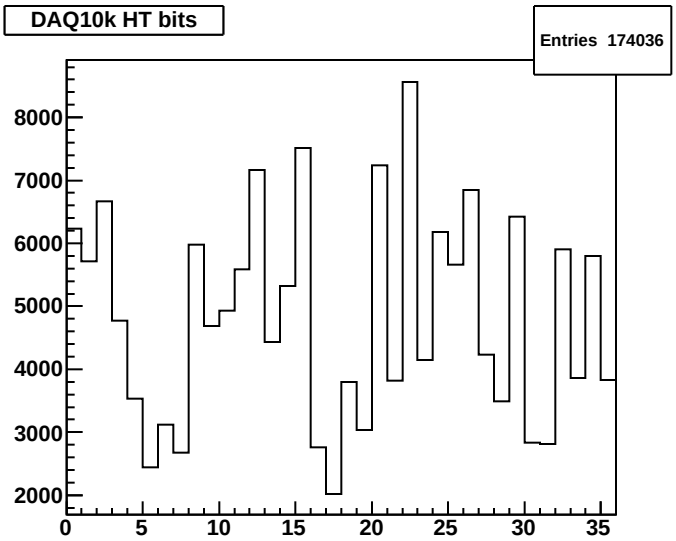

EMC L2 Input - Partial JP Sum
Entries 1643
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

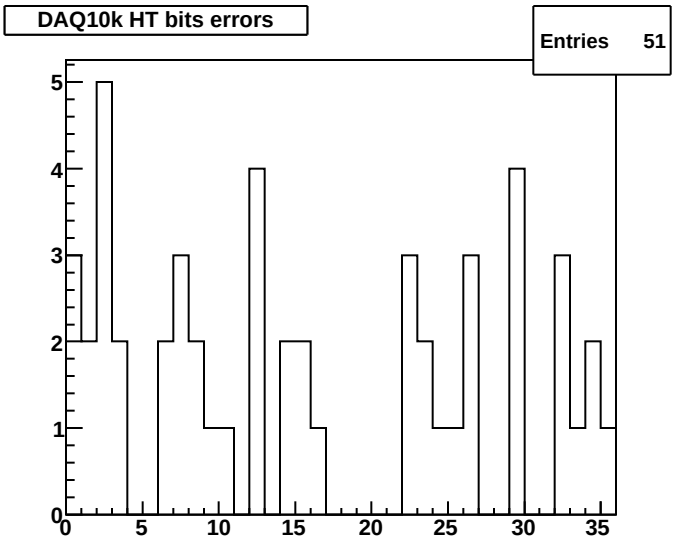

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.522682e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

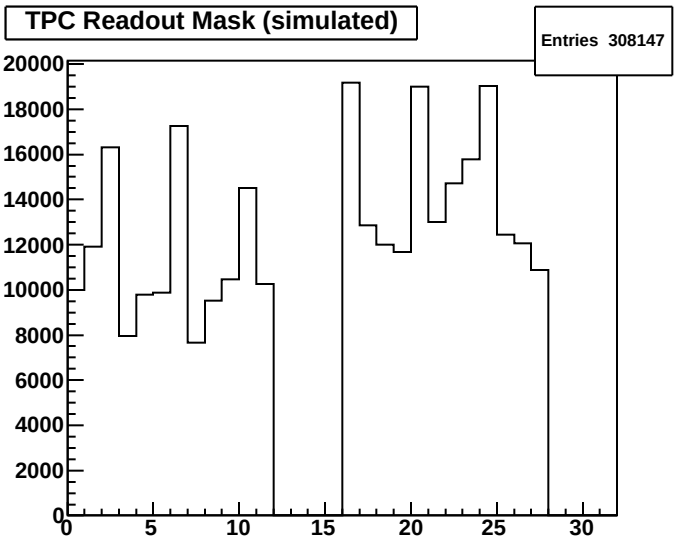

EMC L2 Input - HT01 bits/Partial JP ID
Entries 357
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

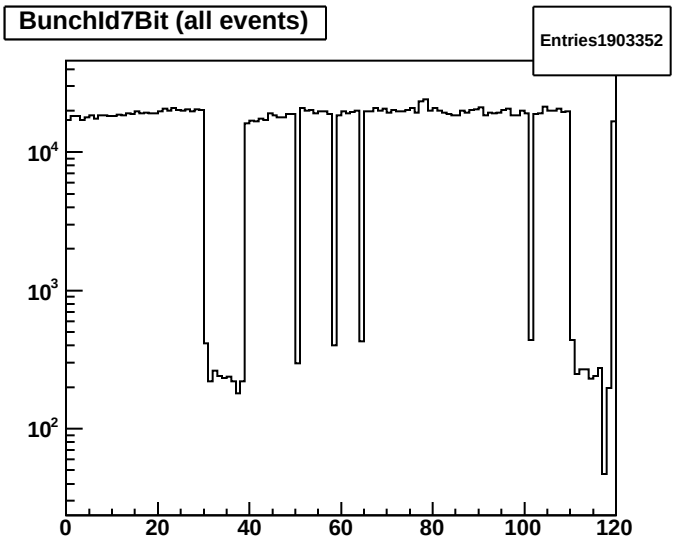

EMC L2 Input - HT23 bits
Entries 1.522682e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

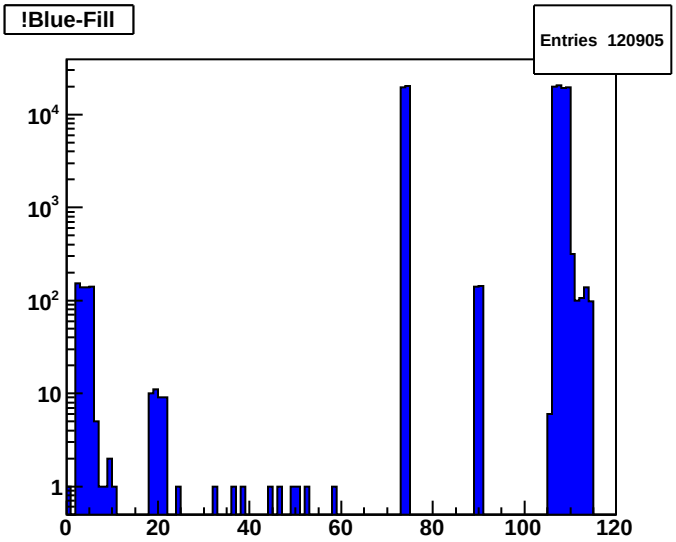

EMC L2 Input - HT23 bits
Entries 79
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

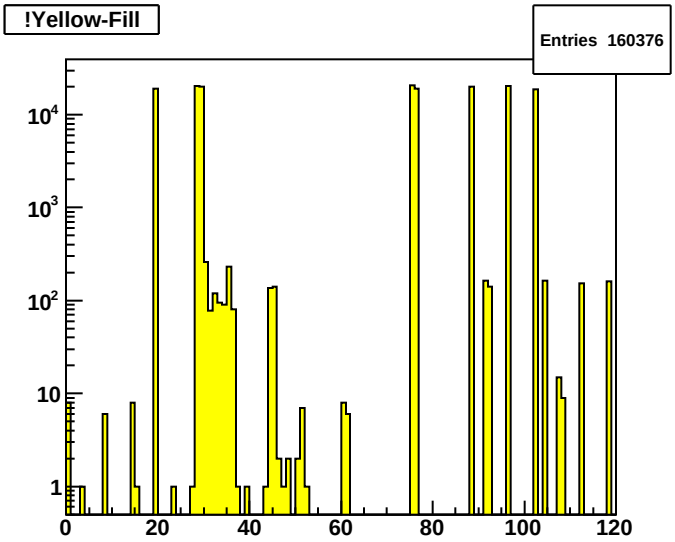

DAQ10k HT bits
Entries 174036

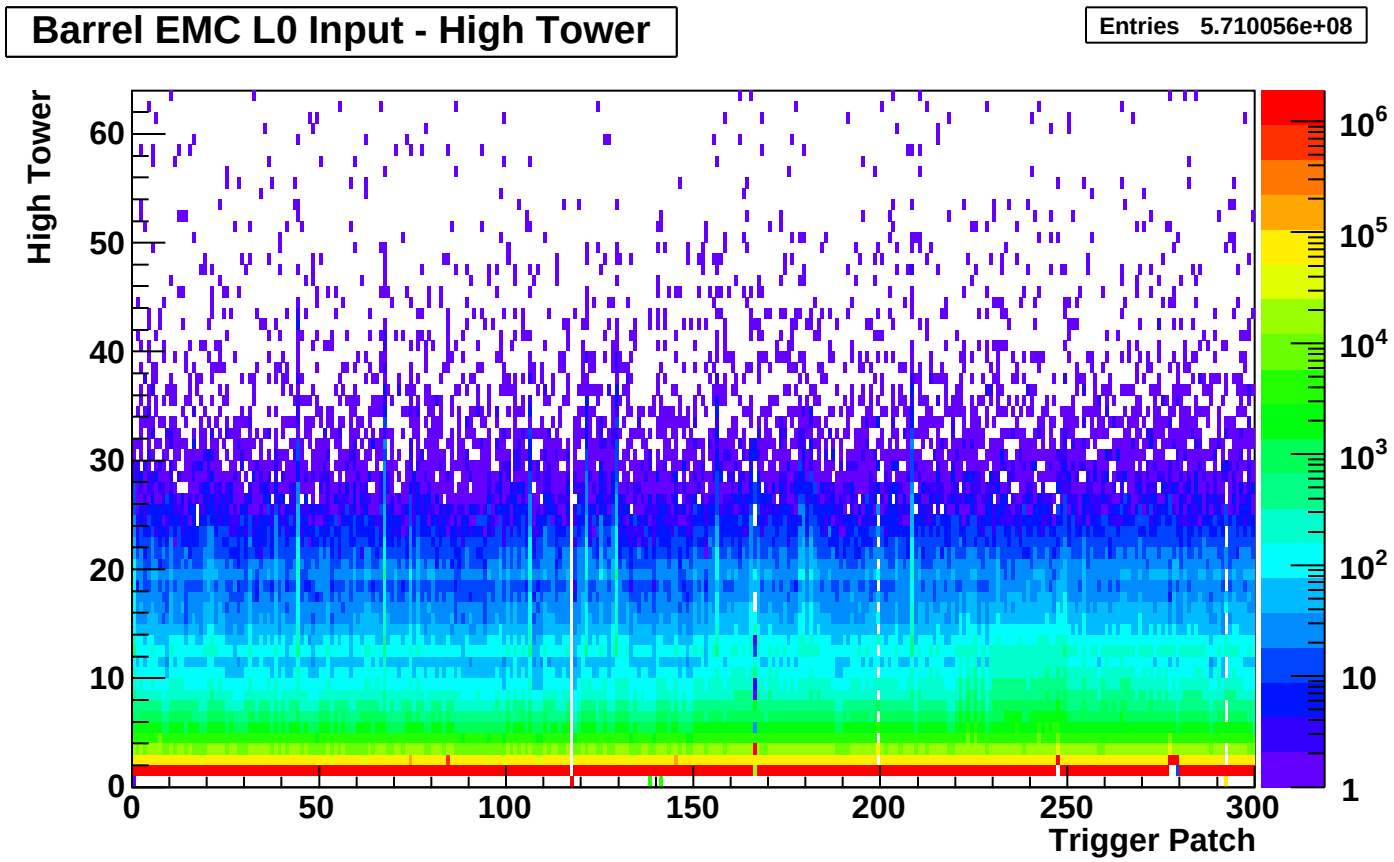

DAQ10k HT bits errors
Entries 51

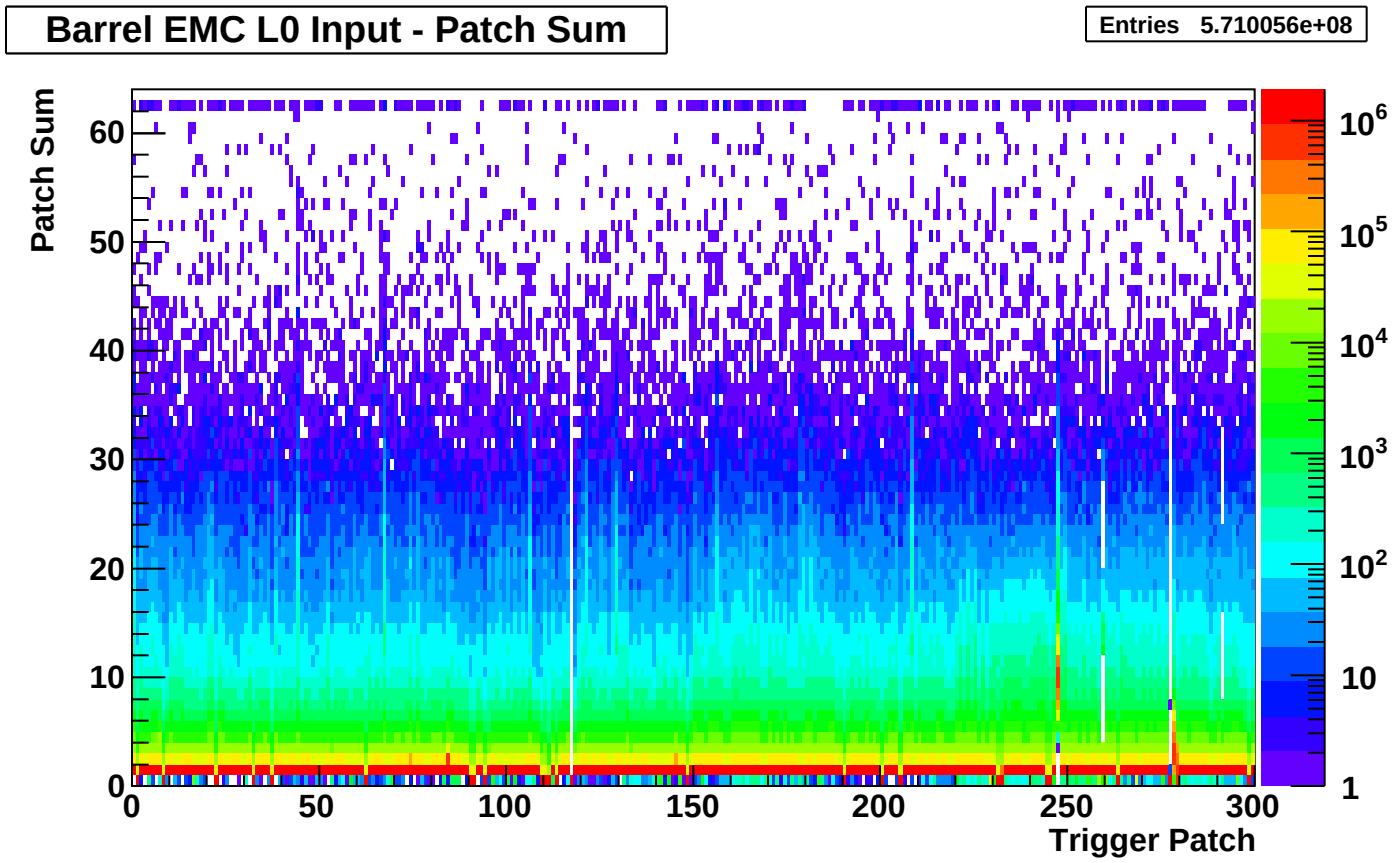

TPC Readout Mask (simulated)
Entries 308147

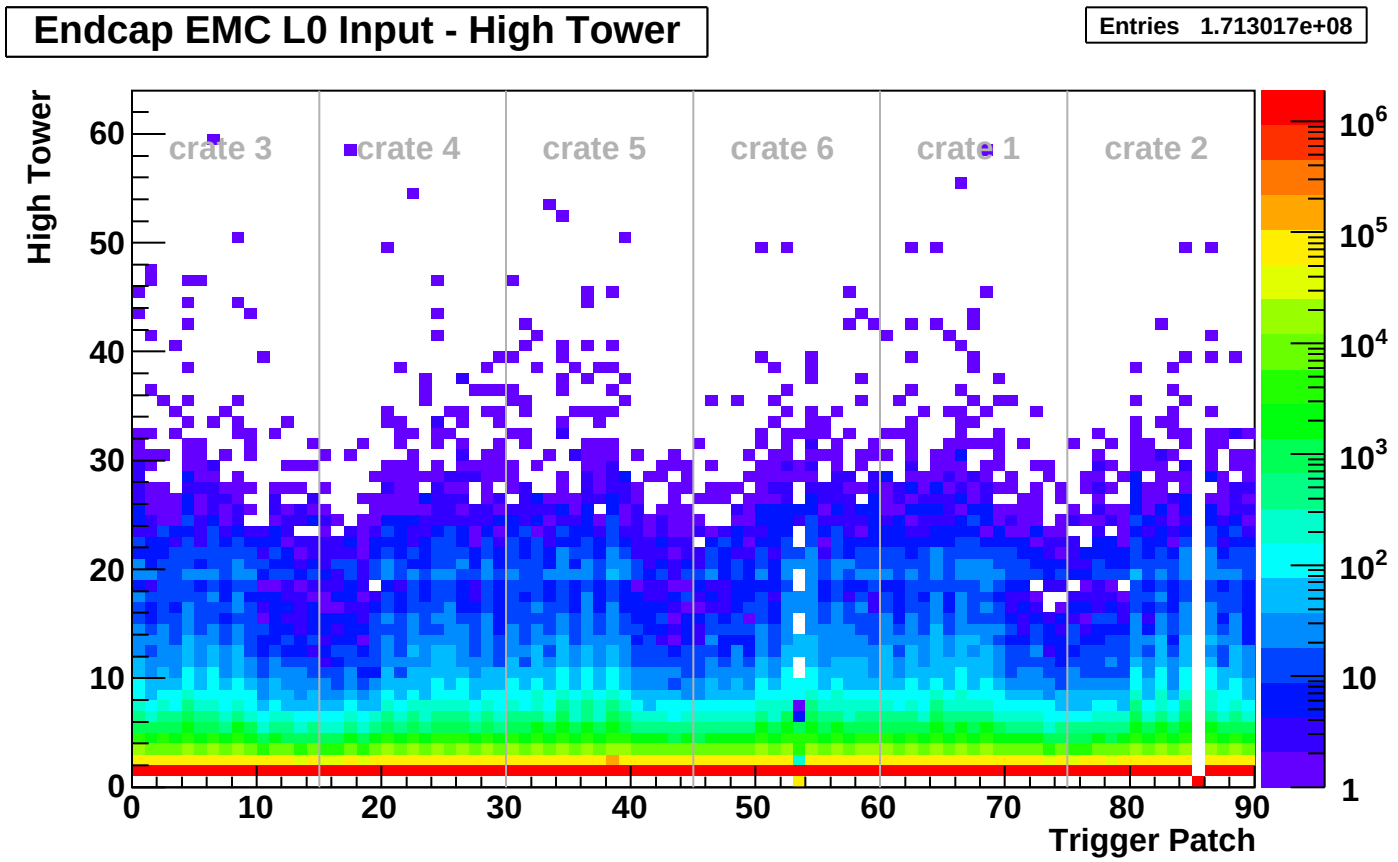

BunchId7Bit (all events)
Entries1903352

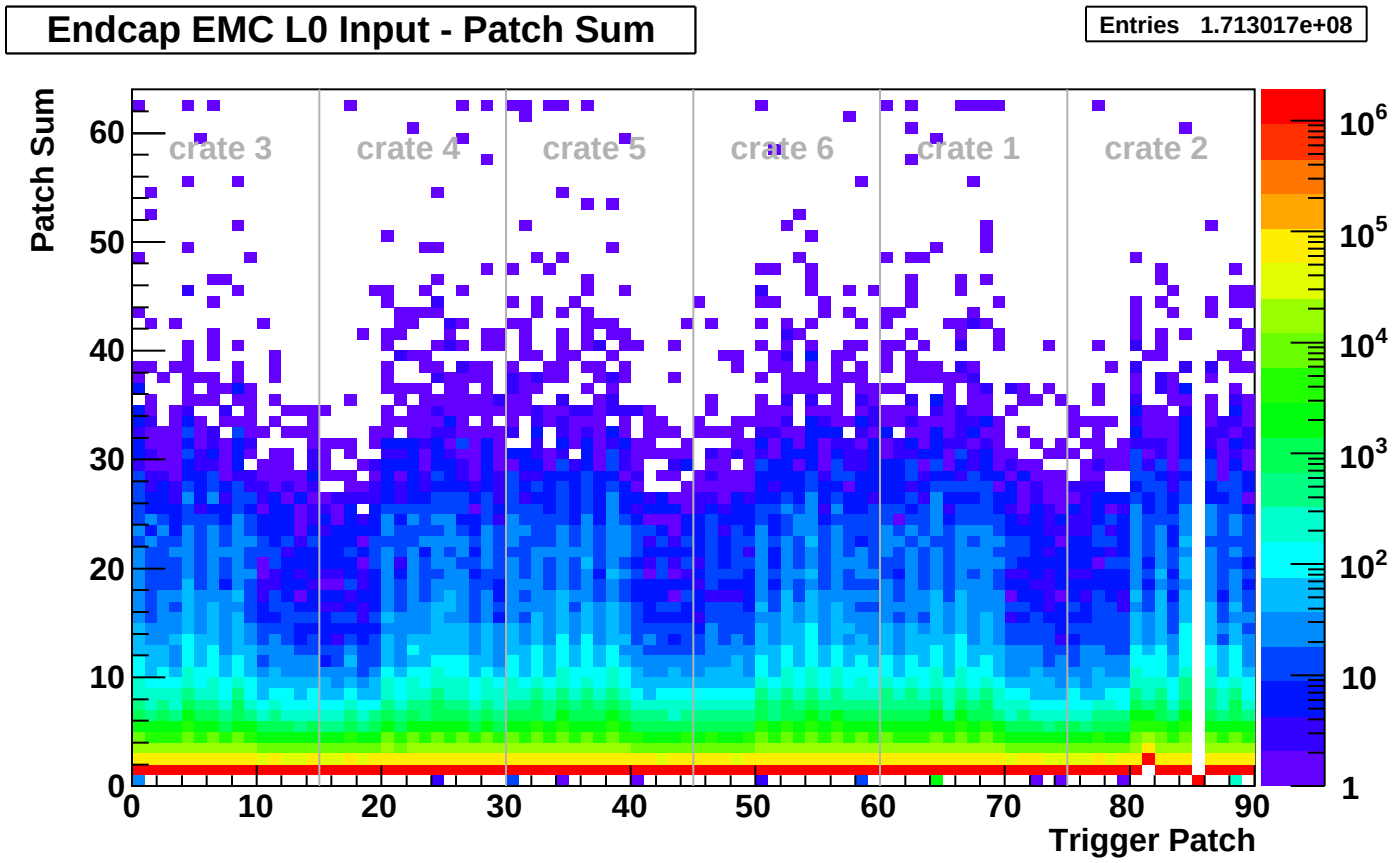

!Blue-Fill
Entries 120905

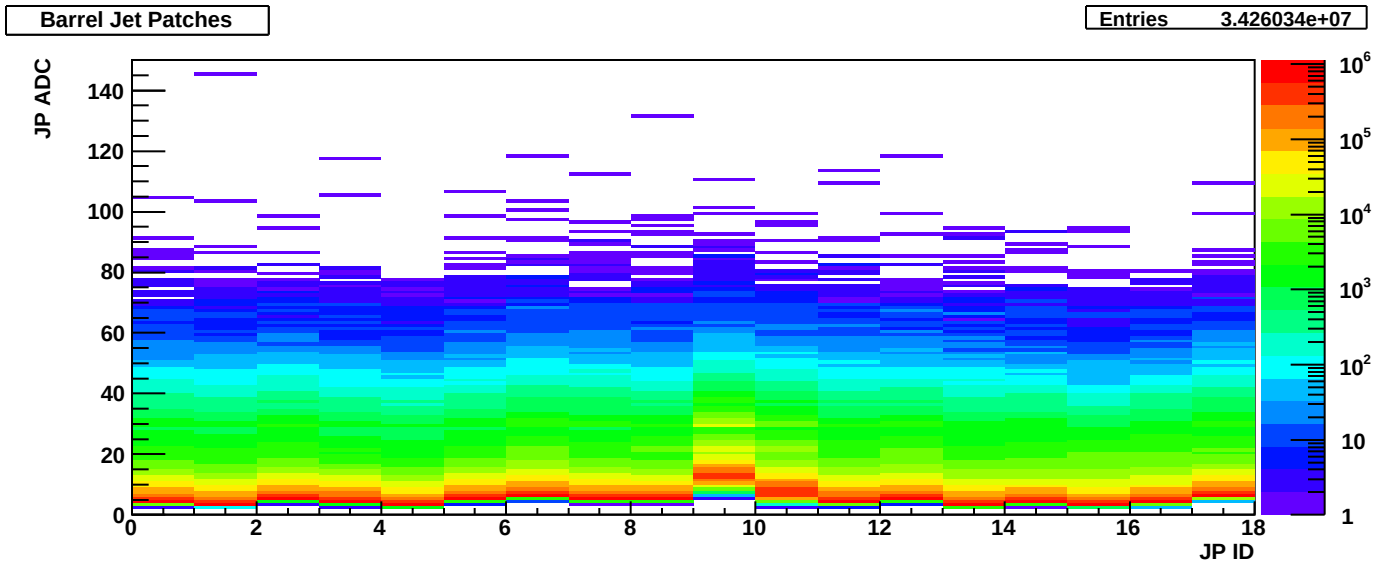

!Yellow-Fill
Entries 160376

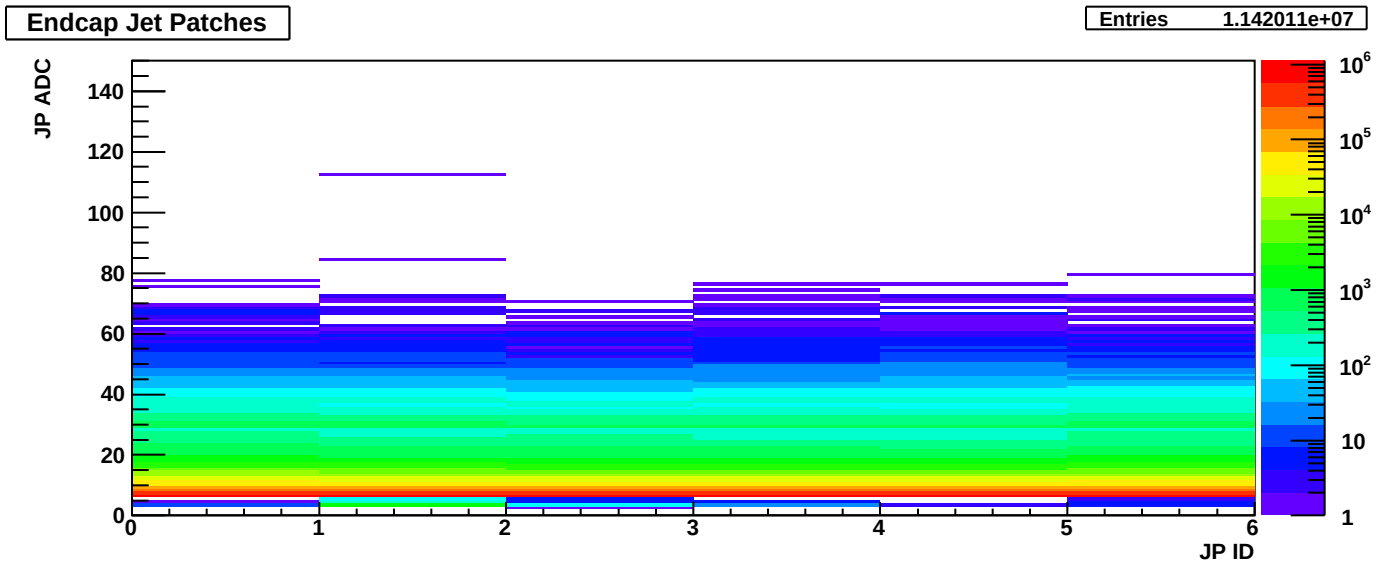

Barrel EMC L0 Input - High Tower
Entries 5.710056e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

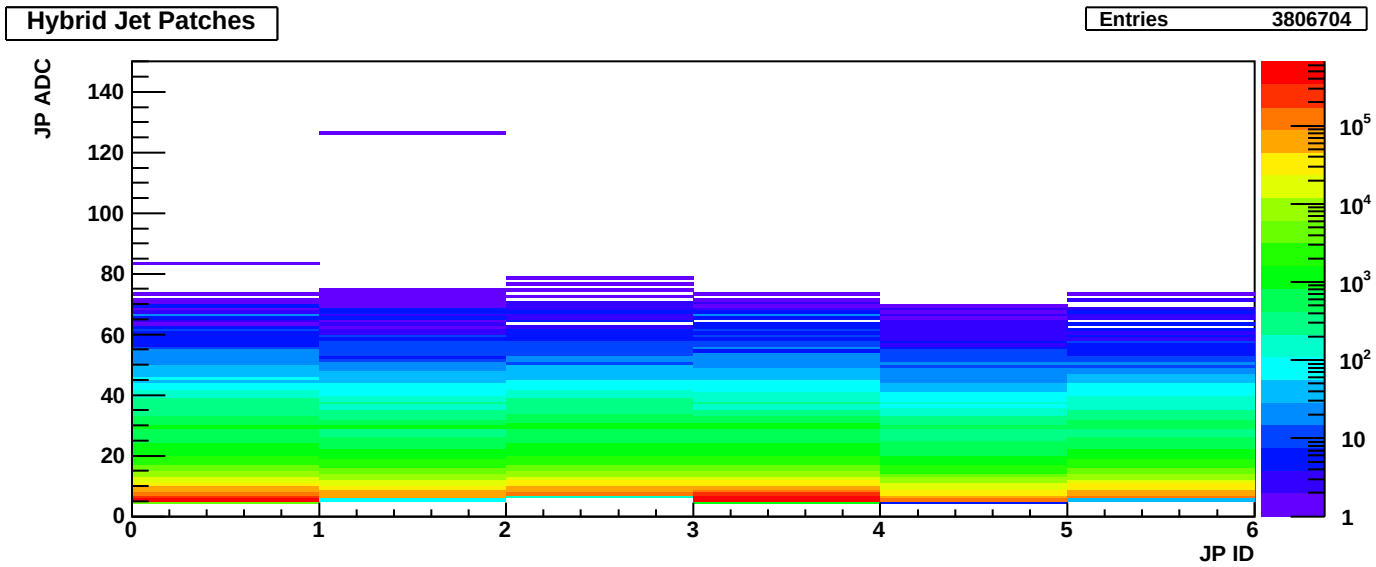

Barrel EMC L0 Input - Patch Sum
Entries 5.710056e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

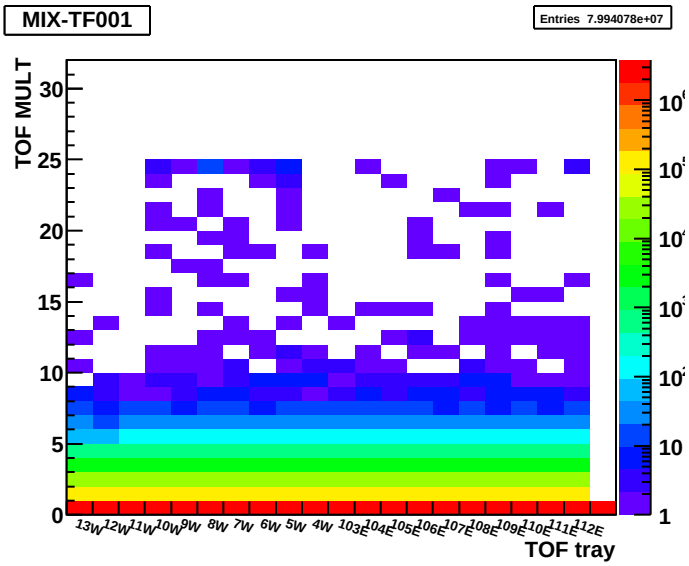

Endcap EMC L0 Input - High Tower
Entries 1.713017e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

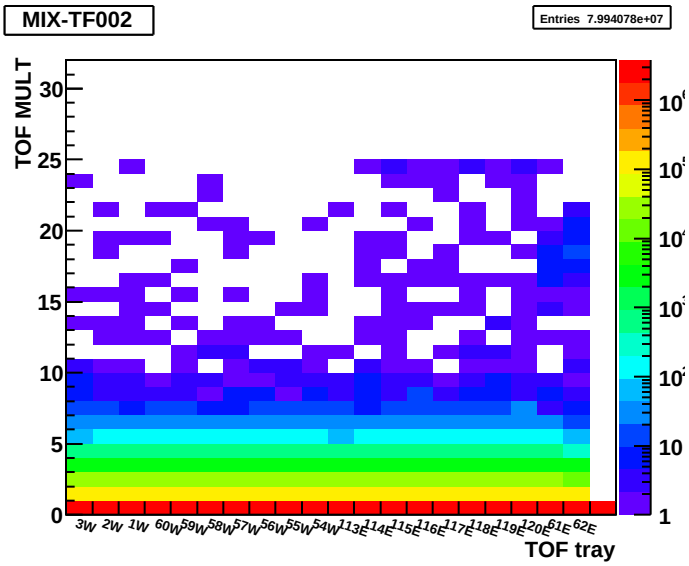

Endcap EMC L0 Input - Patch Sum
Entries 1.713017e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

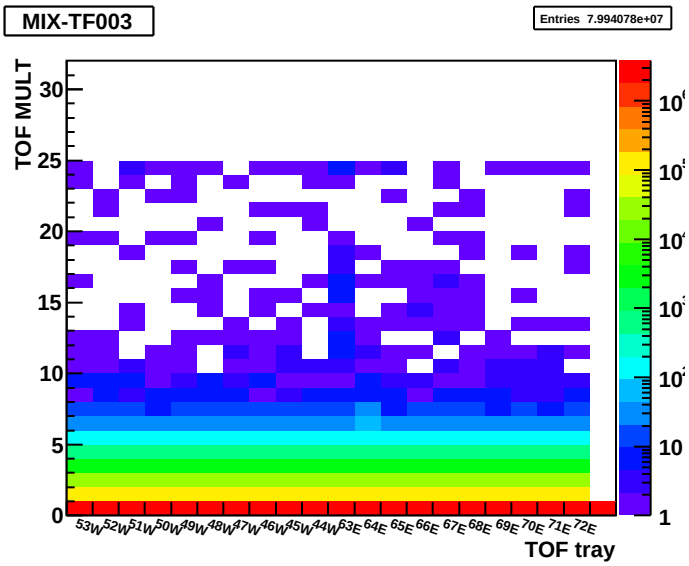

Barrel Jet Patches
Entries 3.426034e+07
JP ADC
JP ID

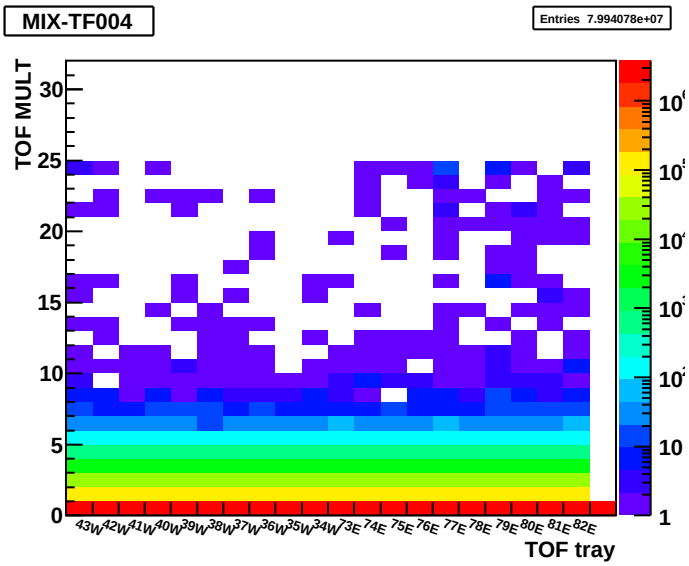

Endcap Jet Patches
Entries 1.142011e+07
JP ADC
JP ID

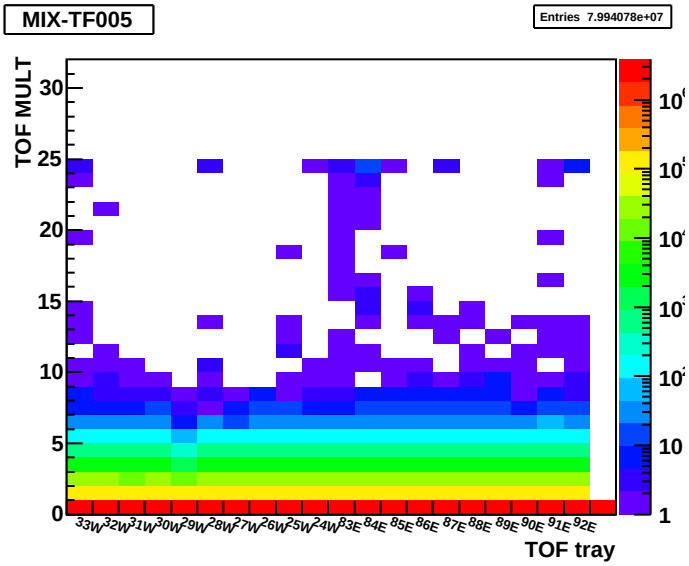

Hybrid Jet Patches
Entries 3806704
JP ADC
JP ID

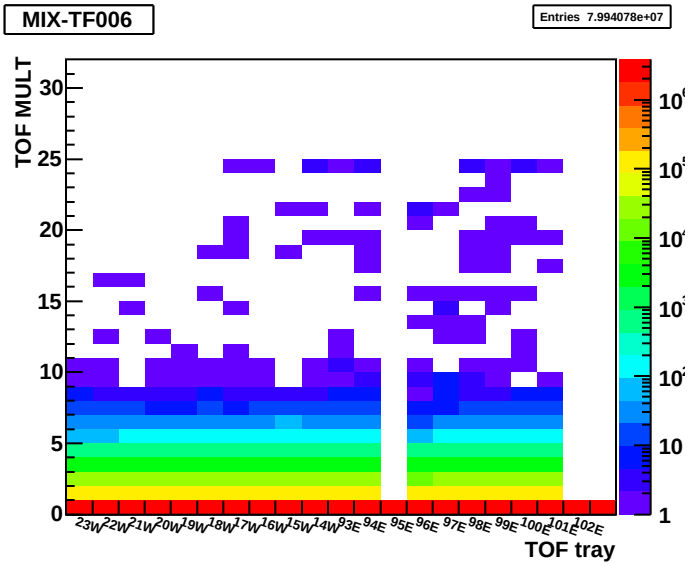

MIX-TF001
Entries 7.994078e+07
TOF MULT
TOF tray

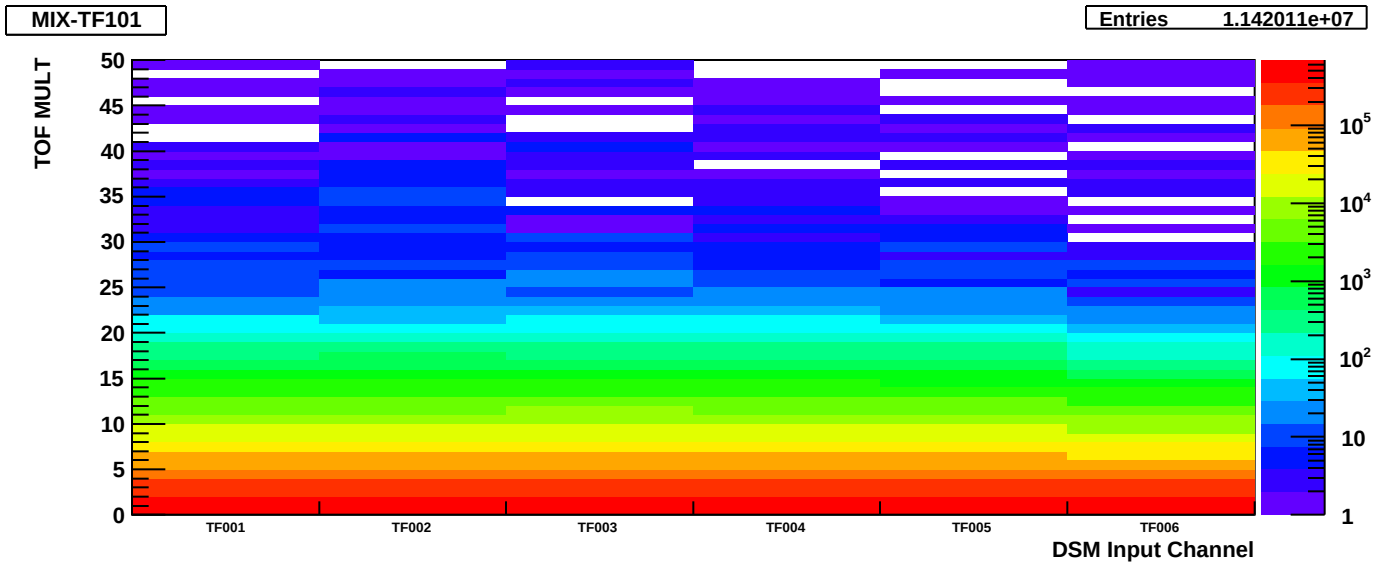

MIX-TF002
Entries 7.994078e+07
TOF MULT
TOF tray

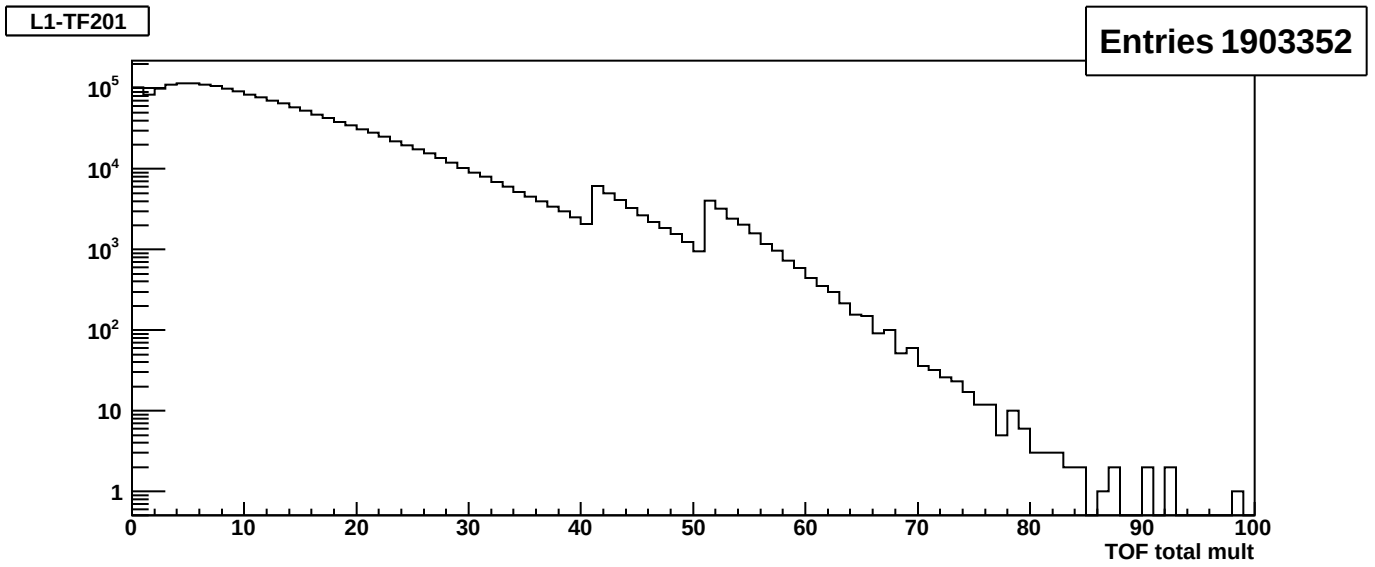

MIX-TF003
Entries 7.994078e+07
TOF MULT
TOF tray

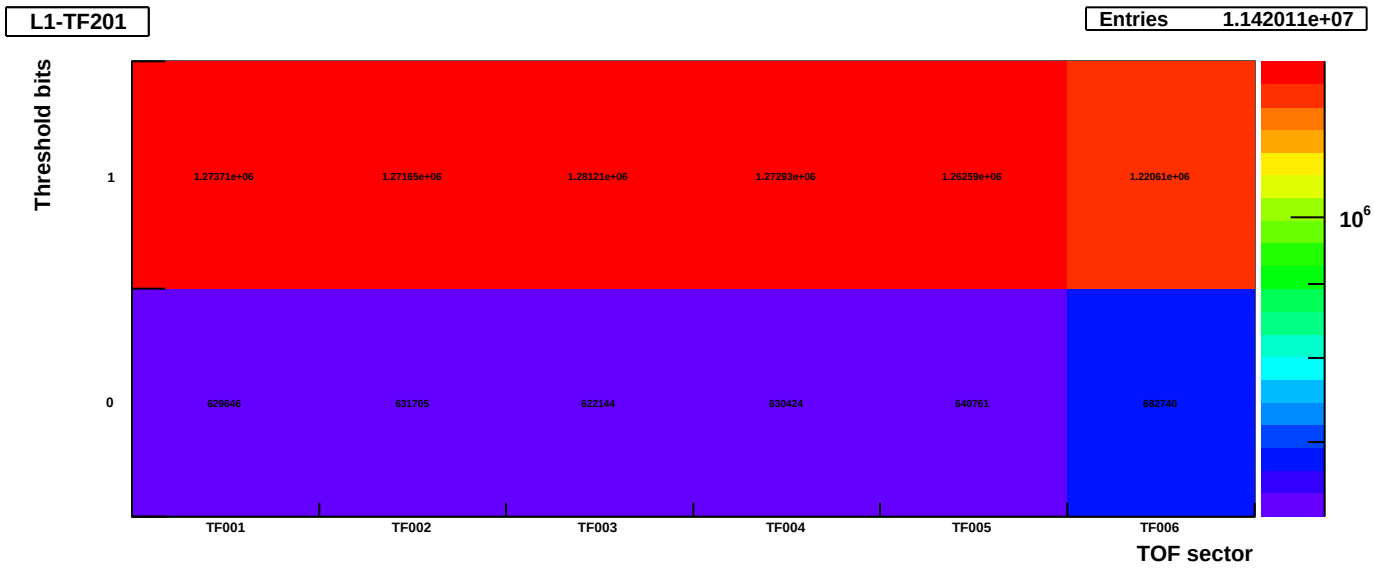

MIX-TF004
Entries 7.994078e+07
TOF MULT
TOF tray

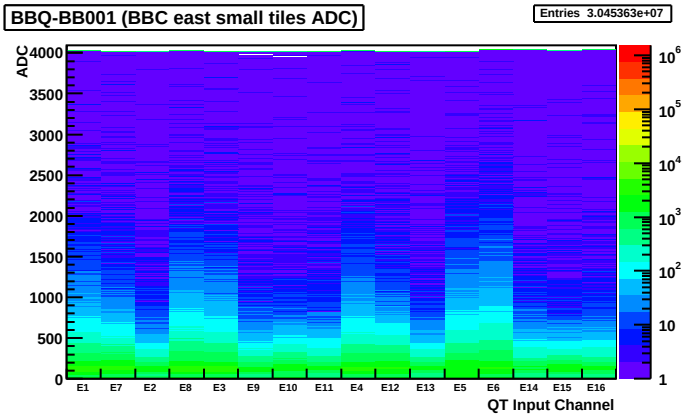

MIX-TF005
Entries 7.994078e+07
TOF MULT
TOF tray

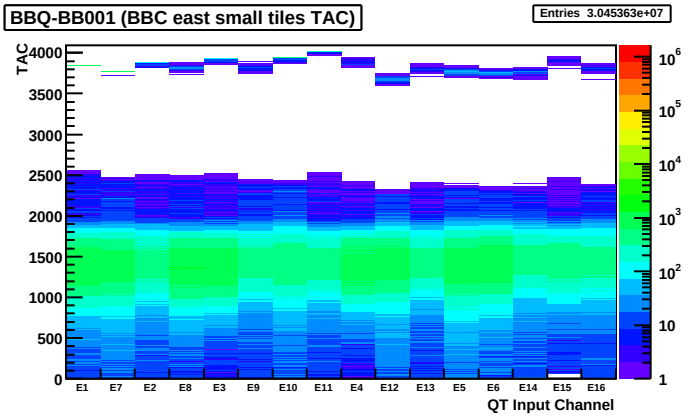

MIX-TF006
Entries 7.994078e+07
TOF MULT
TOF tray

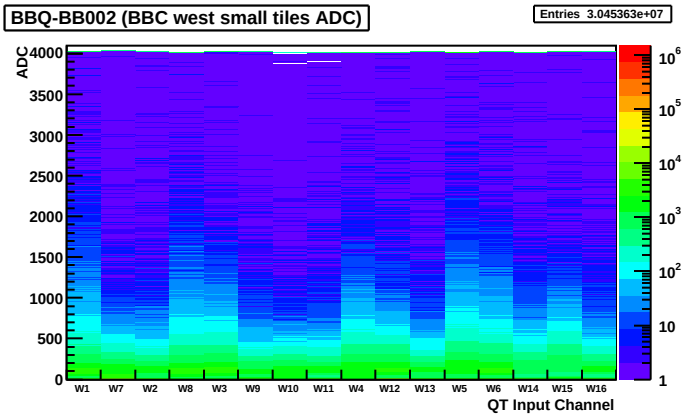

MIX-TF101
Entries 1.142011e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

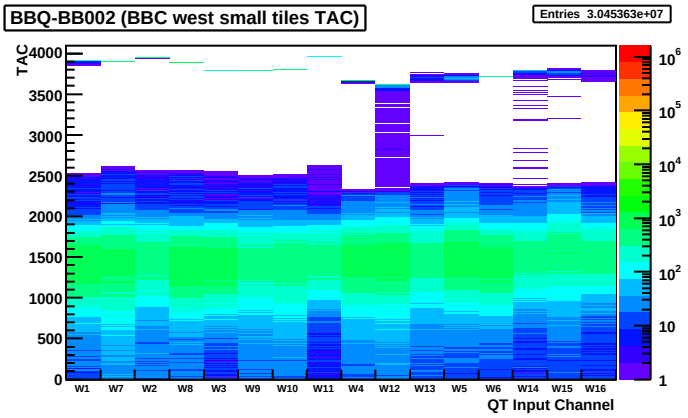

L1-TF201
Entries 1903352
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

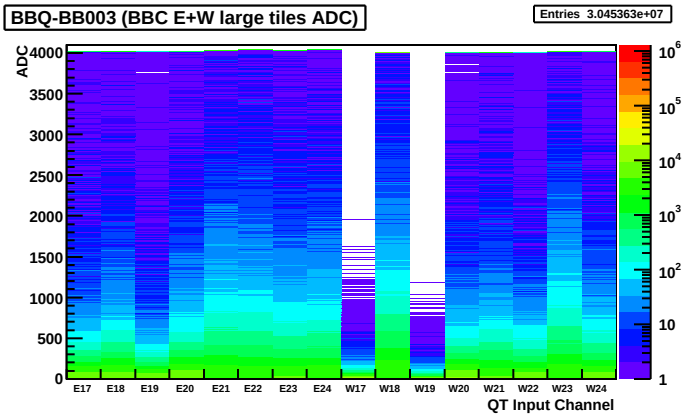

L1-TF201
Entries 1.142011e+07
Threshold bits
1
0
1.27371e+06
1.27165e+06
1.28121e+06
1.27293e+06
1.26259e+06
1.22061e+06
629646
631705
622144
630424
640761
682740
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

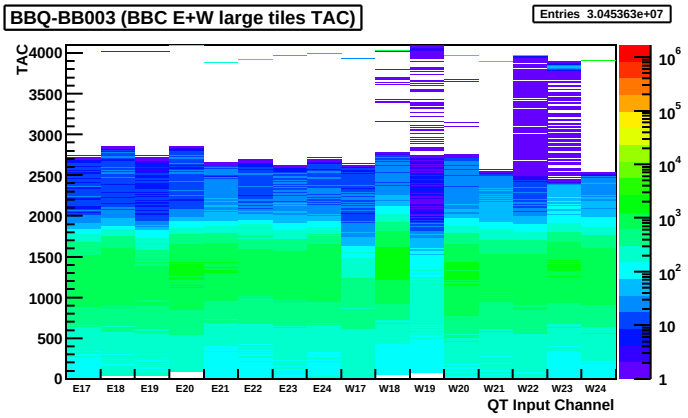

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.045363e+07
ADC
QT Input Channel

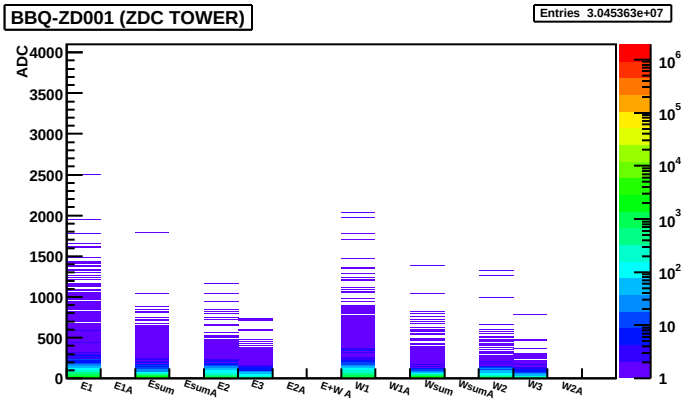

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.045363e+07
TAC
QT Input Channel

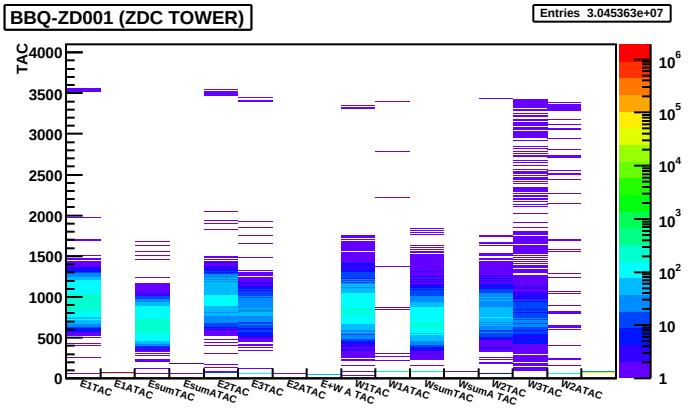

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.045363e+07
ADC
QT Input Channel

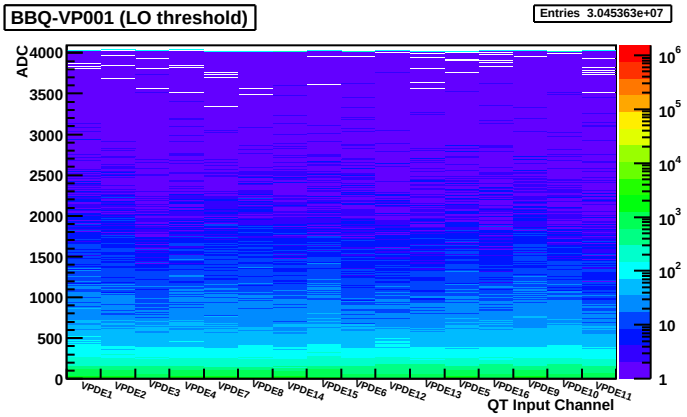

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.045363e+07
TAC
QT Input Channel

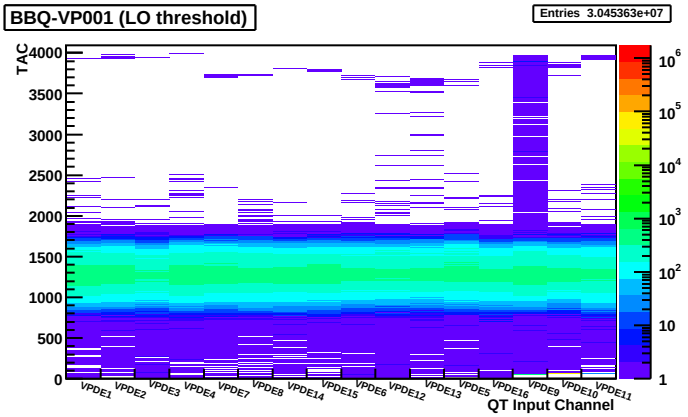

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.045363e+07
ADC
QT Input Channel

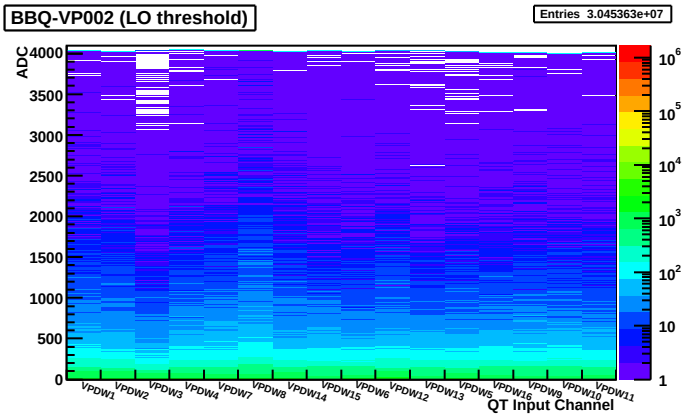

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.045363e+07
TAC
QT Input Channel

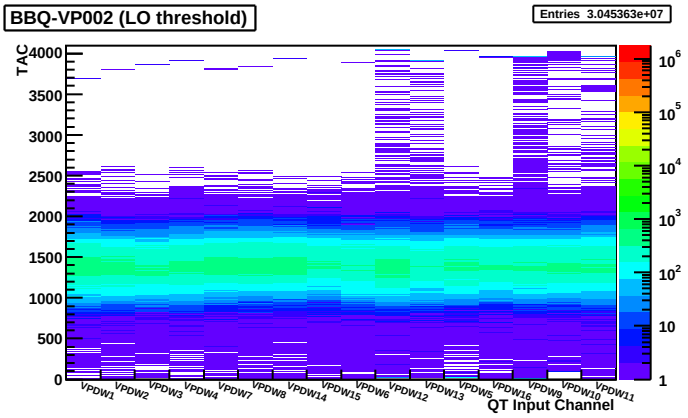

BBQ-ZD001 (ZDC TOWER)
Entries 3.045363e+07
ADC

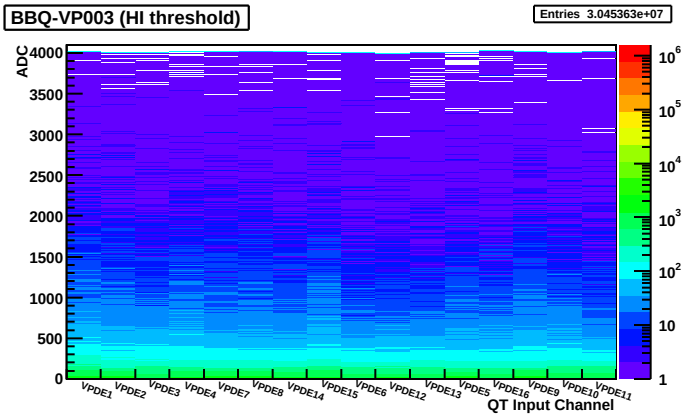

BBQ-ZD001 (ZDC TOWER)
Entries 3.045363e+07
TAC

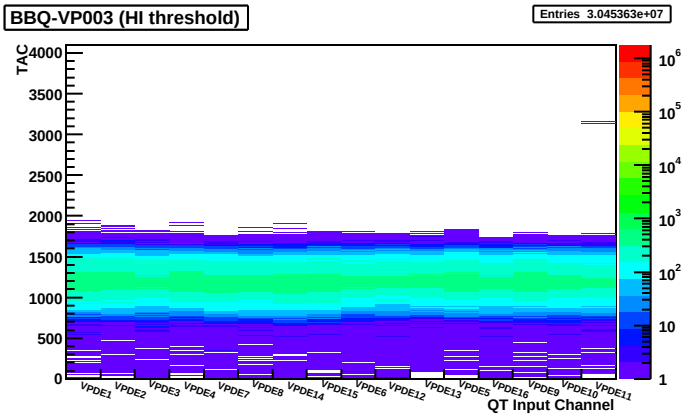

BBQ-VP001 (LO threshold)
Entries 3.045363e+07
ADC
QT Input Channel

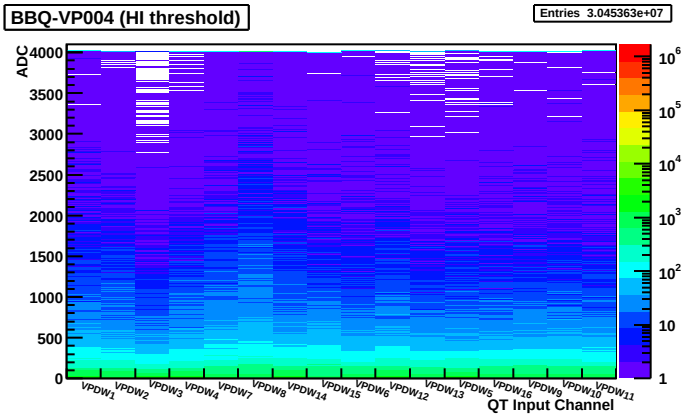

BBQ-VP001 (LO threshold)
Entries 3.045363e+07
TAC
QT Input Channel

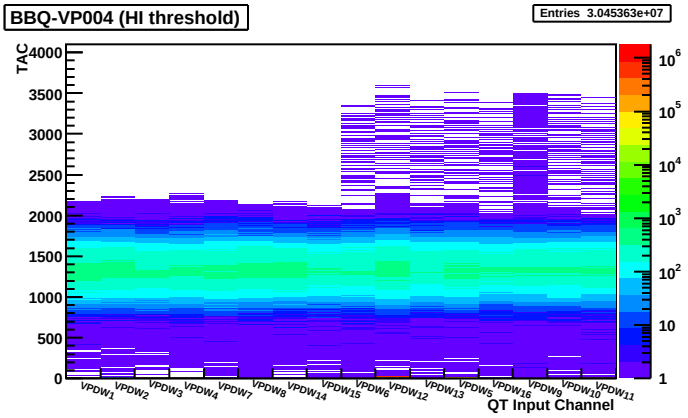

BBQ-VP002 (LO threshold)
Entries 3.045363e+07
ADC
QT Input Channel

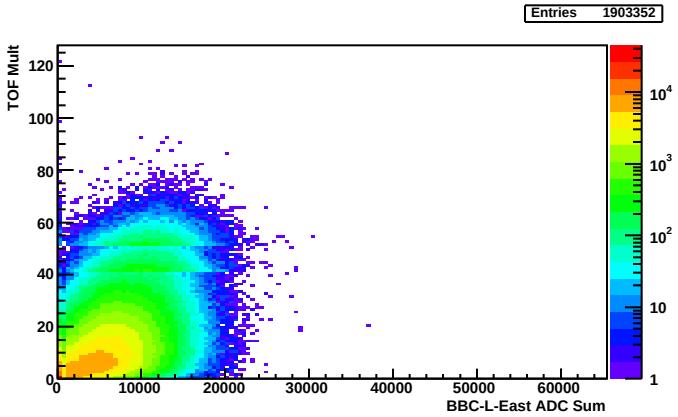

BBQ-VP002 (LO threshold)
Entries 3.045363e+07
TAC
QT Input Channel

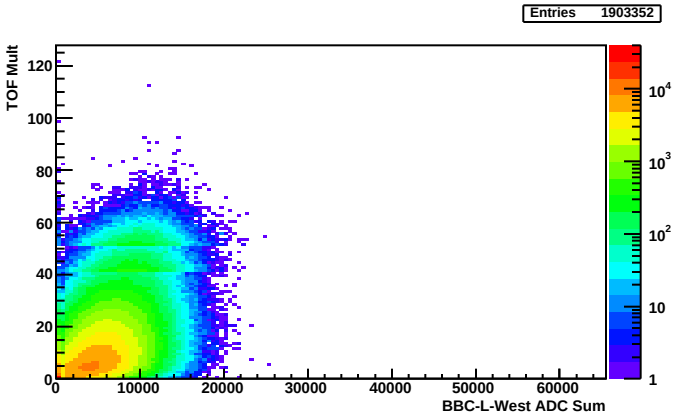

BBQ-VP003 (HI threshold)
Entries 3.045363e+07
ADC
QT Input Channel

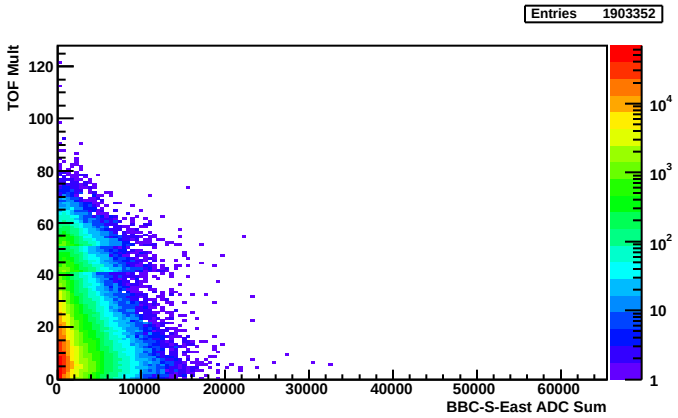

BBQ-VP003 (HI threshold)
Entries 3.045363e+07
TAC
QT Input Channel

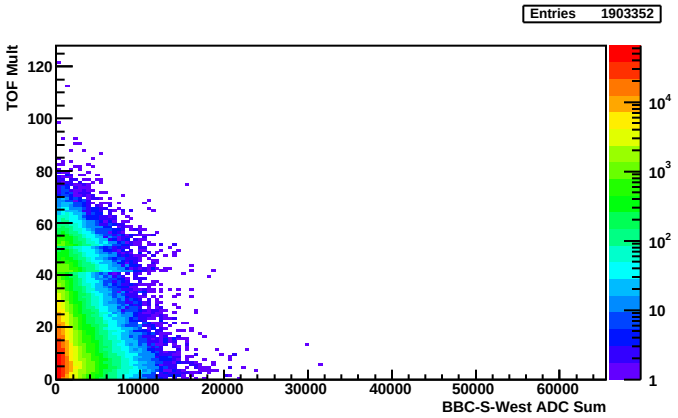

BBQ-VP004 (HI threshold)
Entries 3.045363e+07
ADC
QT Input Channel

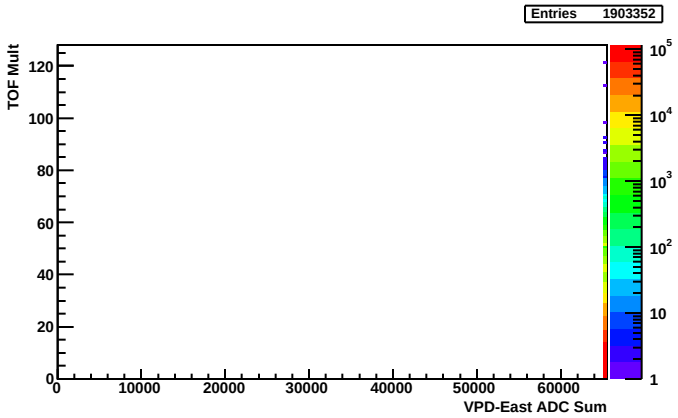

BBQ-VP004 (HI threshold)
Entries 3.045363e+07
TAC
QT Input Channel

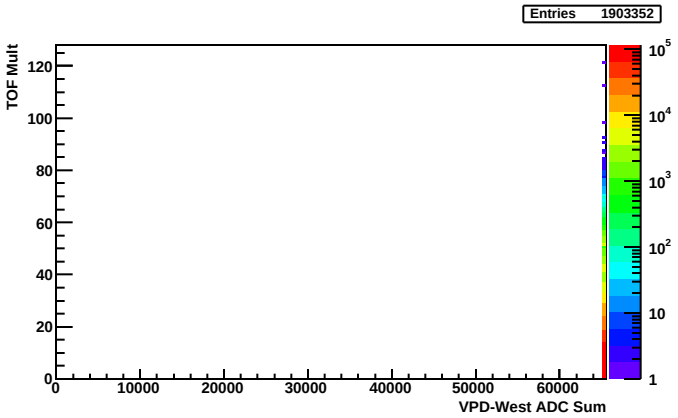

Entries 1903352
TOF Mult
BBC-L-East ADC Sum

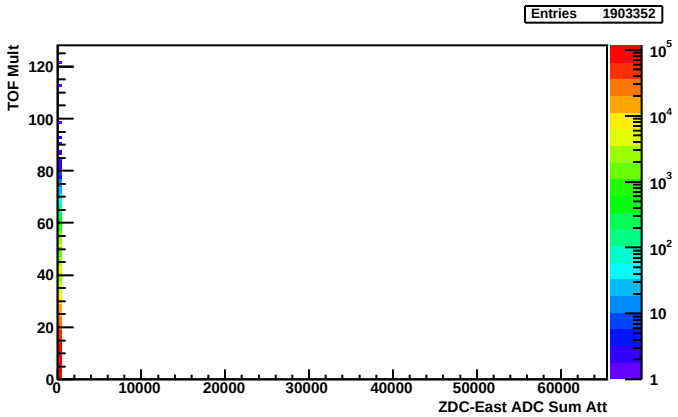

Entries 1903352
TOF Mult
BBC-L-West ADC Sum

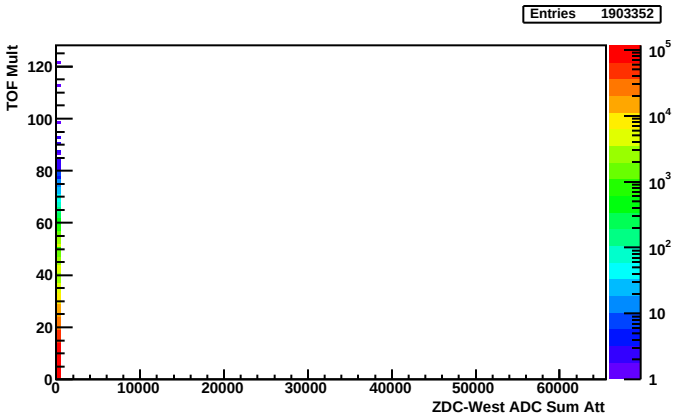

Entries 1903352
TOF Mult
BBC-S-East ADC Sum

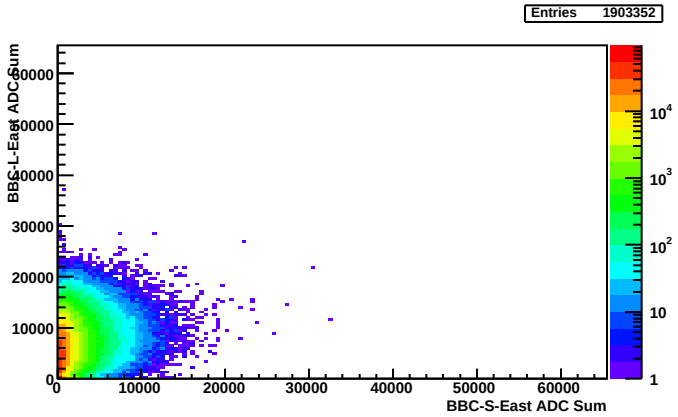

Entries 1903352
TOF Mult
BBC-S-West ADC Sum

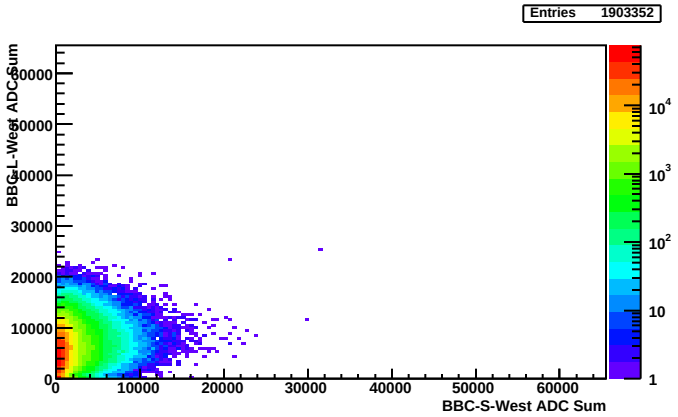

Entries 1903352
TOF Mult
VPD-East ADC Sum

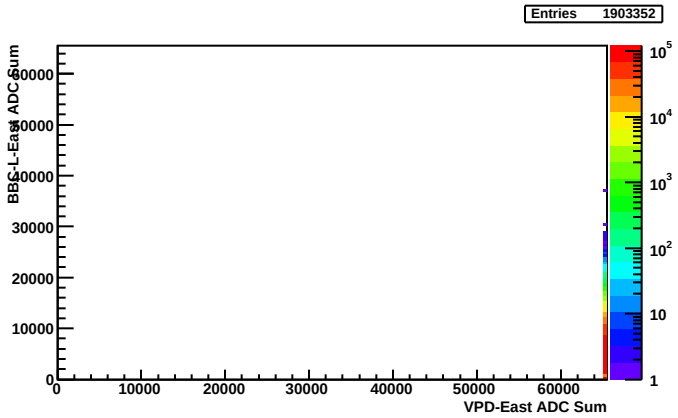

Entries 1903352
TOF Mult
VPD-West ADC Sum

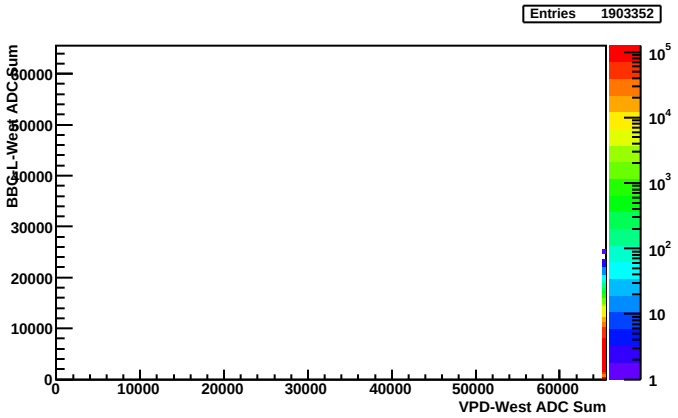

Entries 1903352
TOF Mult
ZDC-East ADC Sum Att

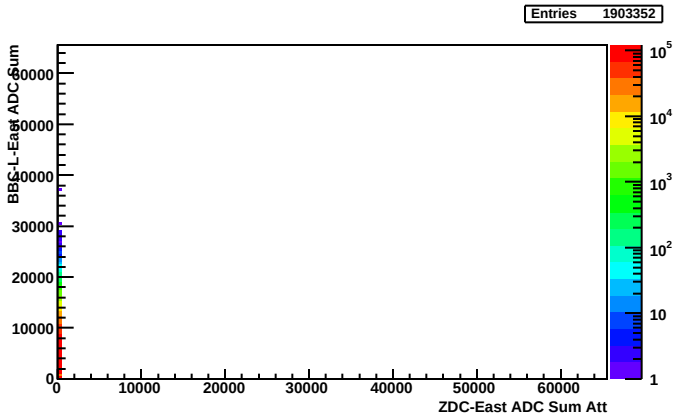

Entries 1903352
TOF Mult
ZDC-West ADC Sum Att

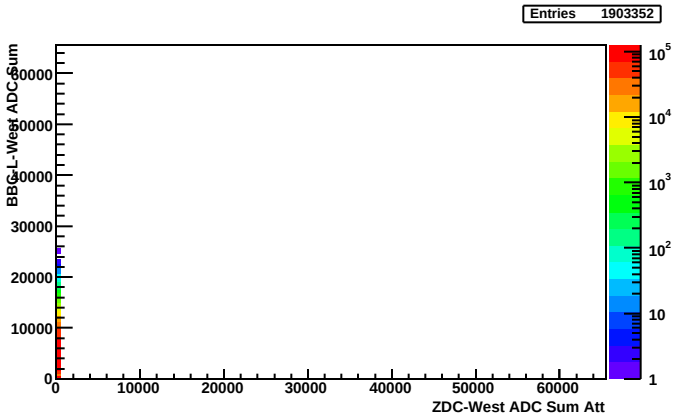

Entries 1903352
BBC-L-East ADC Sum
BBC-S-East ADC Sum

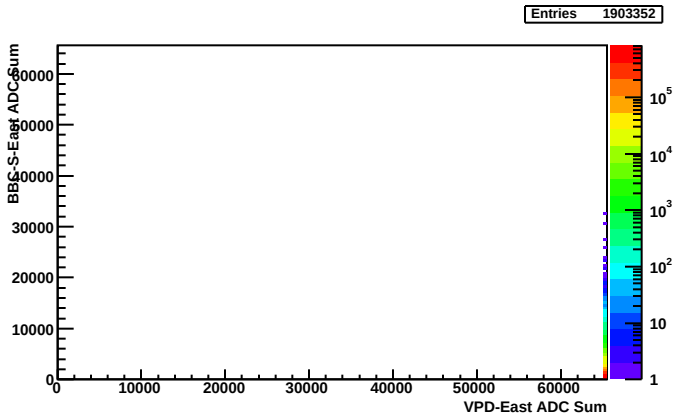

Entries 1903352
BBC-L-West ADC Sum
BBC-S-West ADC Sum

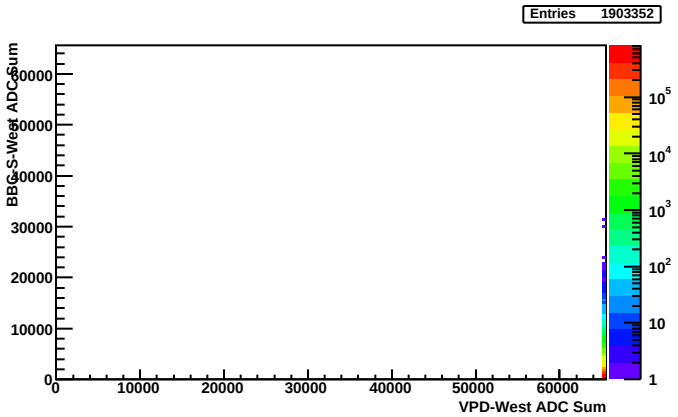

Entries 1903352
BBC-L-East ADC Sum
VPD-East ADC Sum

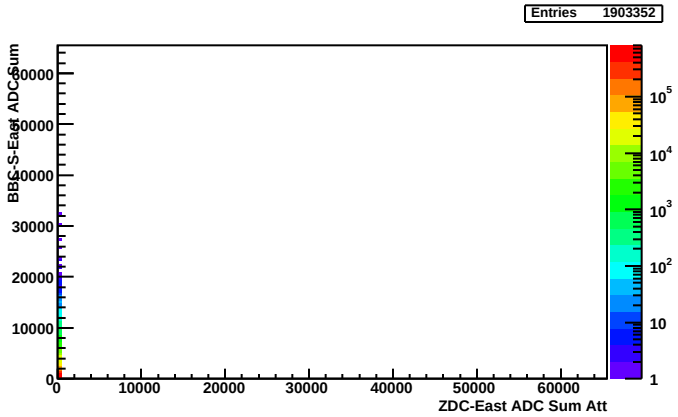

Entries 1903352
BBC-L-West ADC Sum
VPD-West ADC Sum

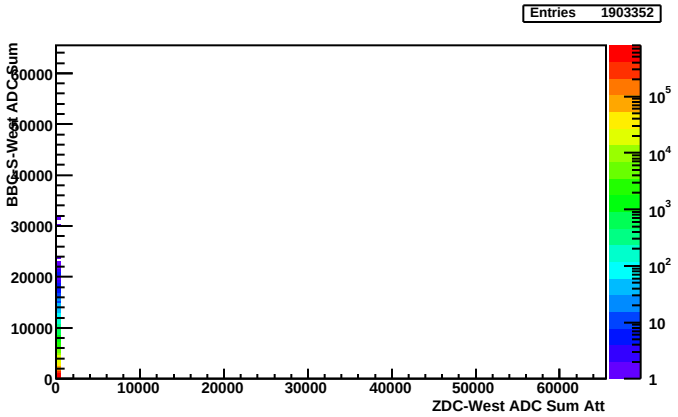

Entries 1903352
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

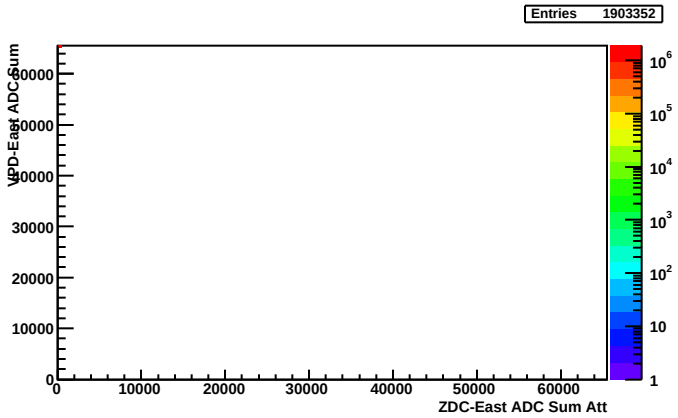

Entries 1903352
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

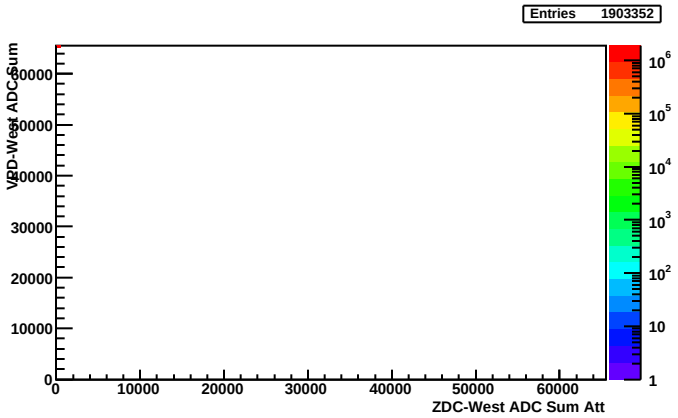

Entries 1903352
BBC-S-East ADC Sum
VPD-East ADC Sum

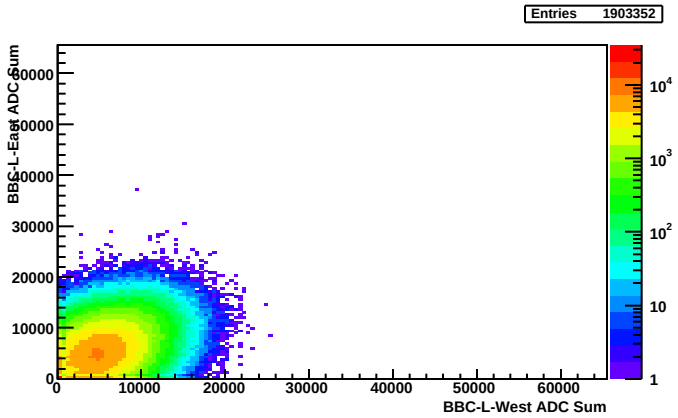

Entries 1903352
BBC-S-West ADC Sum
VPD-West ADC Sum

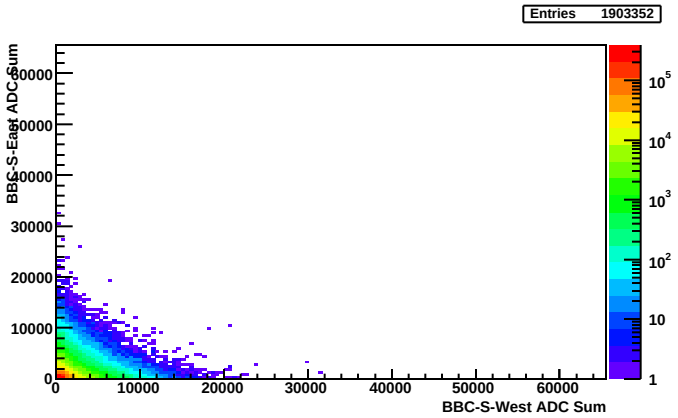

Entries 1903352
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

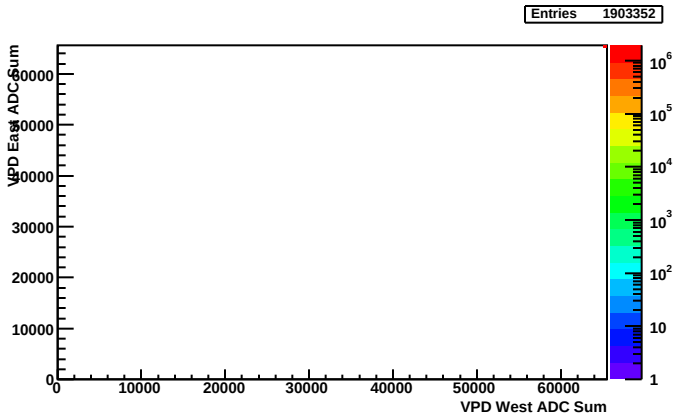

Entries 1903352
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

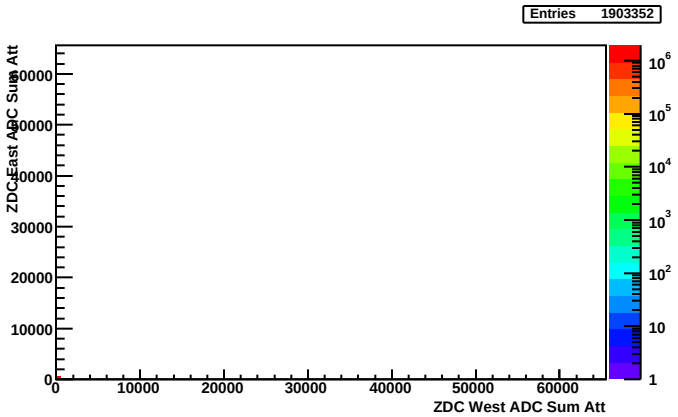

Entries 1903352
VPD-East ADC Sum
ZDC-East ADC Sum Att

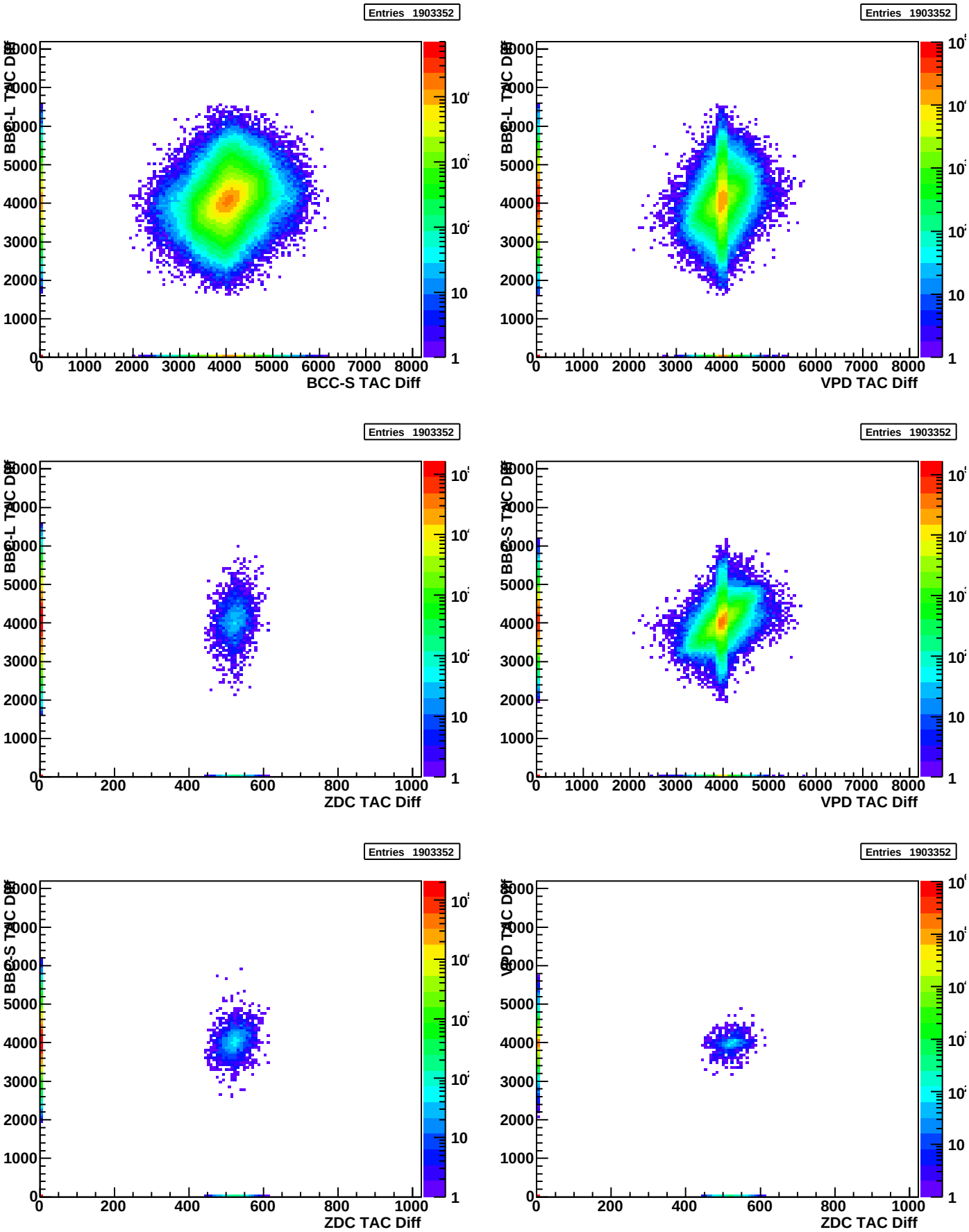

Entries 1903352
VPD-West ADC Sum
ZDC-West ADC Sum Att

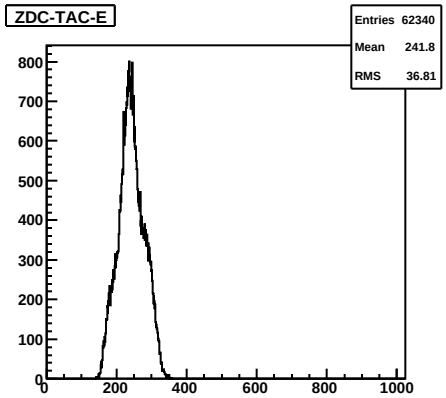

Entries 1903352
BBC-L-East ADC Sum
BBC-L-West ADC Sum

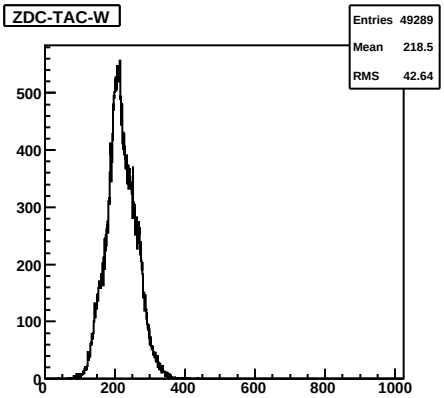

Entries 1903352
BBC-S-East ADC Sum
BBC-S-West ADC Sum

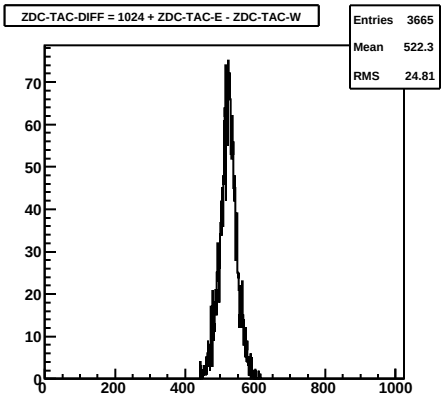

Entries 1903352
VPD East ADC Sum
VPD West ADC Sum

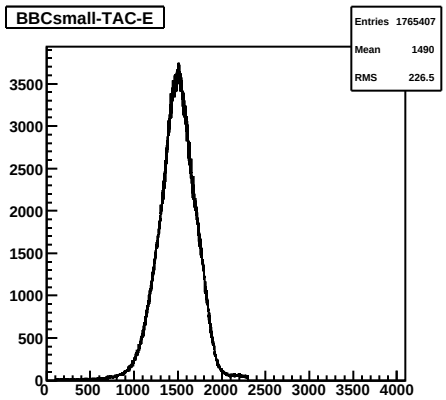

Entries 1903352
ZDC East ADC Sum Att
ZDC West ADC Sum Att

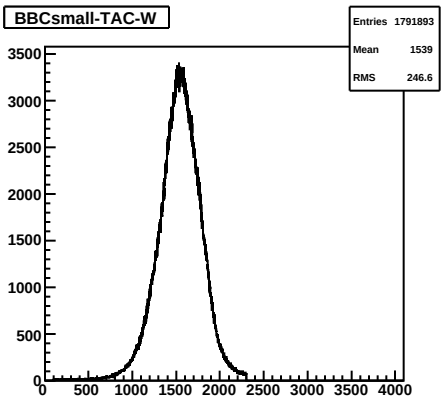

Entries 1903352
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1903352
BBC-L TAC Diff
VPD TAC Diff
Entries 1903352
BBC-L TAC Diff
ZDC TAC Diff
Entries 1903352
BBC-S TAC Diff
VPD TAC Diff
Entries 1903352
BBC-S TAC Diff
ZDC TAC Diff
Entries 1903352
VPD TAC Diff
ZDC TAC Diff

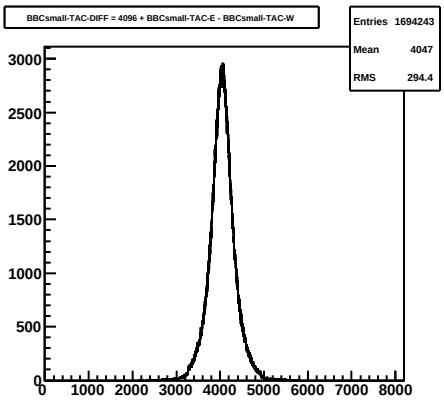

ZDC-TAC-E
Entries 62340
Mean 241.8
RMS 36.81

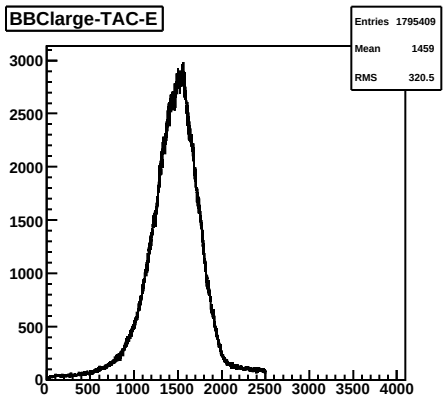

ZDC-TAC-W
Entries 49289
Mean 218.5
RMS 42.64

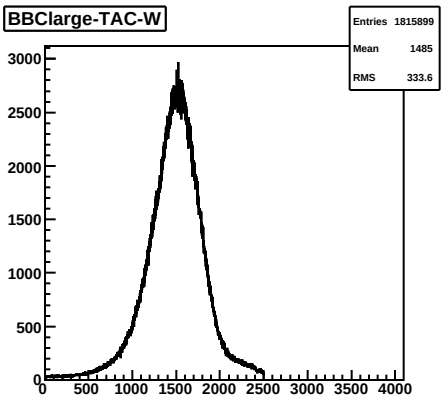

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3665
Mean 522.3
RMS 24.81

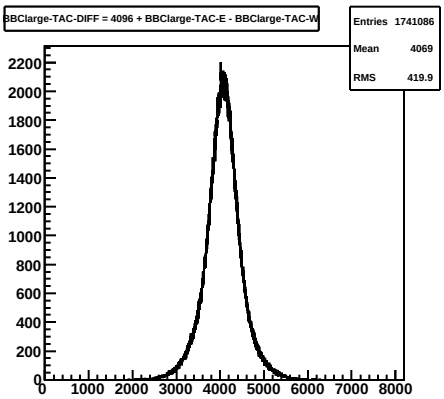

BBCsmall-TAC-E
Entries 1765407
Mean 1490
RMS 226.5

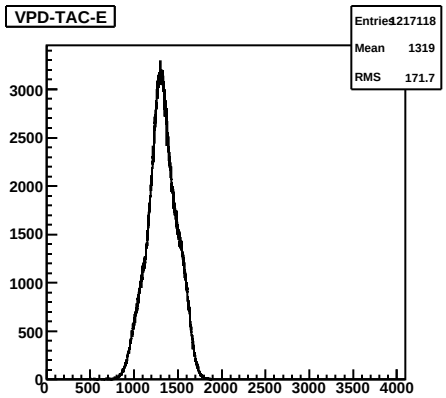

BBCsmall-TAC-W
Entries 1791893
Mean 1539
RMS 246.6

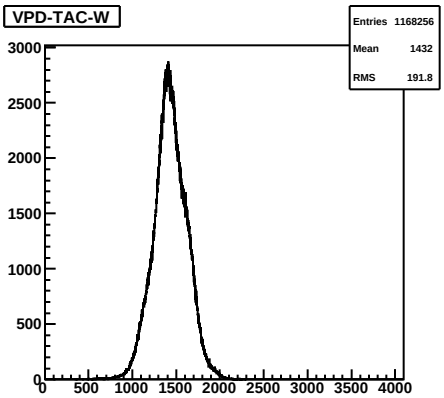

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1694243
Mean 4047
RMS 294.4

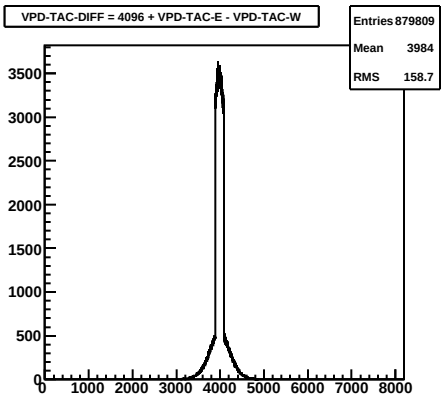

BBClarge-TAC-E
Entries 1795409
Mean 1459
RMS 320.5

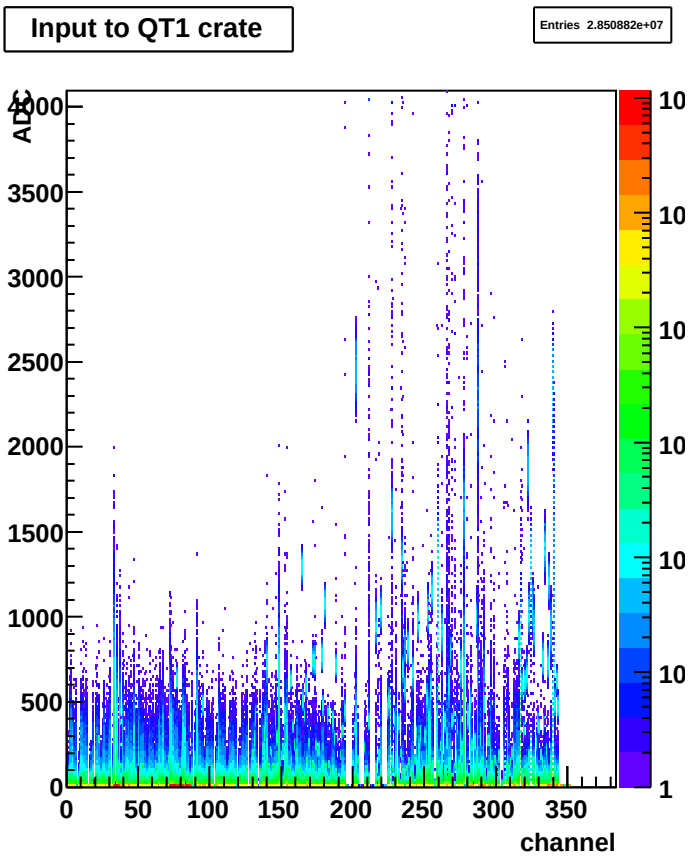

BBClarge-TAC-W
Entries 1815899
Mean 1485
RMS 333.6

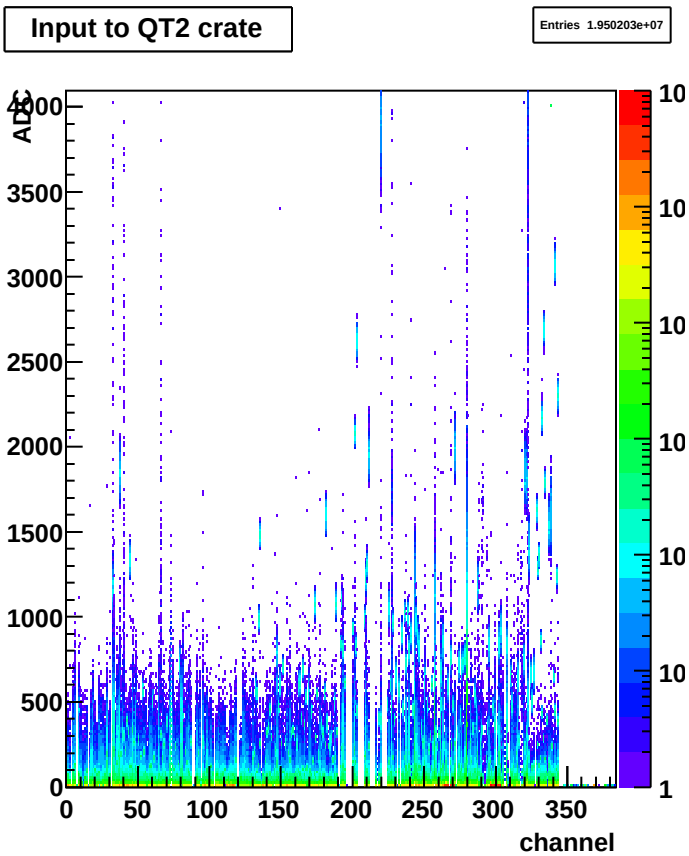

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1741086
Mean 4069
RMS 419.9

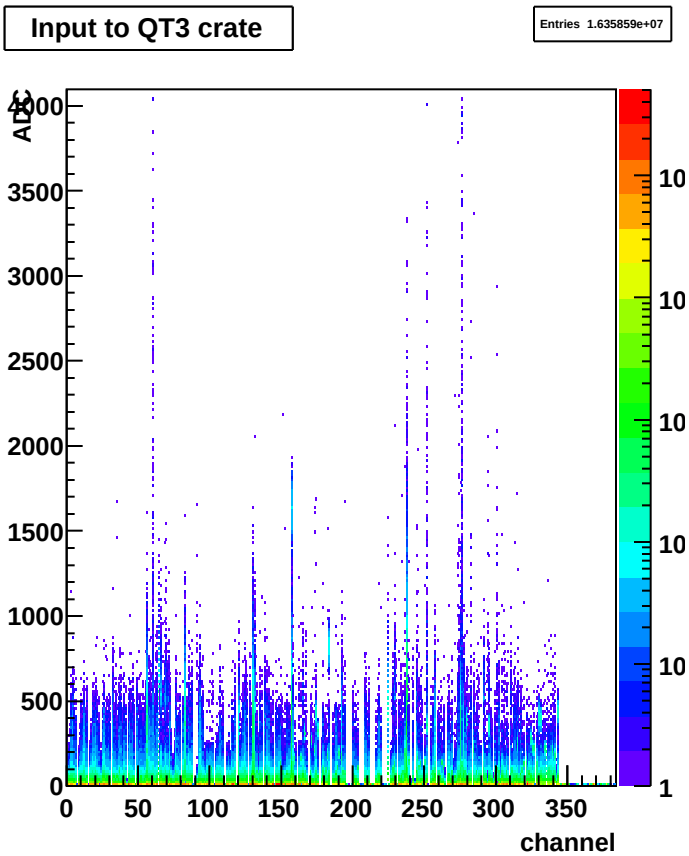

VPD-TAC-E
Entries 1217118
Mean 1319
RMS 171.7

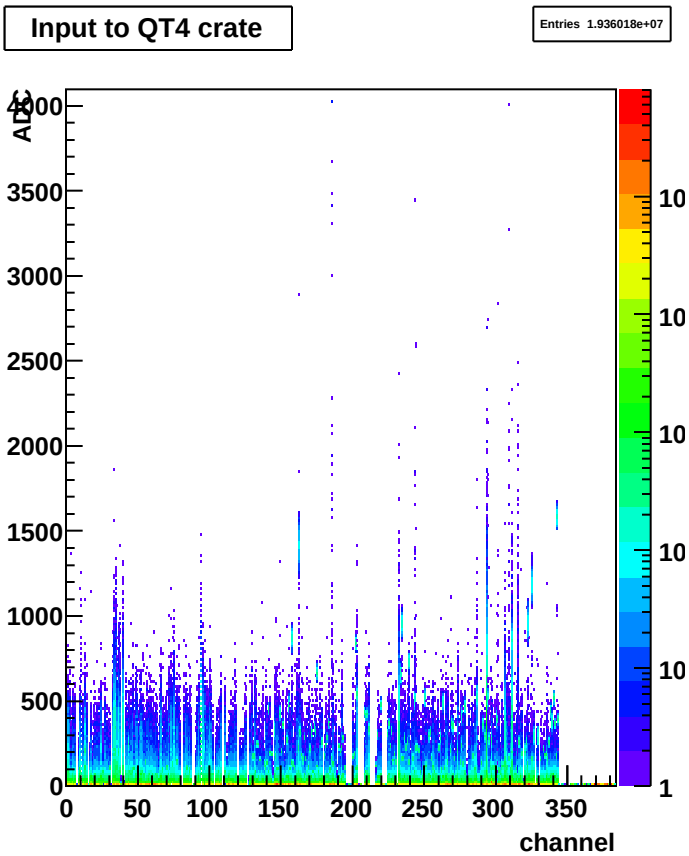

VPD-TAC-W
Entries 1168256
Mean 1432
RMS 191.8

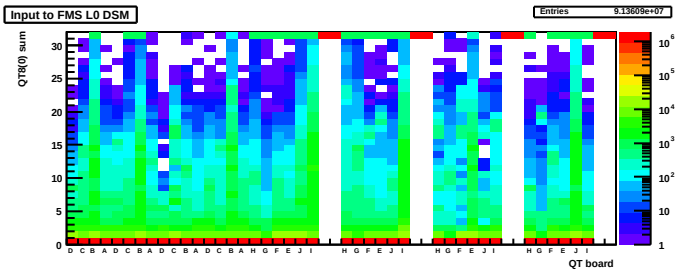

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 879809
Mean 3984
RMS 158.7

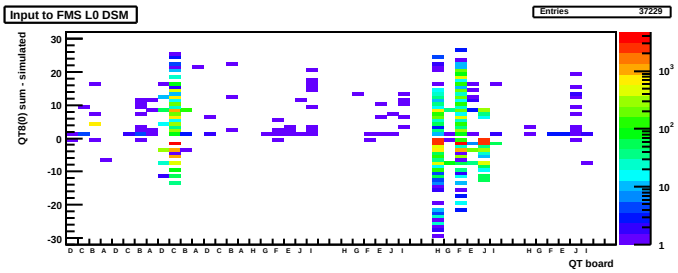

Input to QT1 crate
Entries 2.850882e+07
ADC
channel

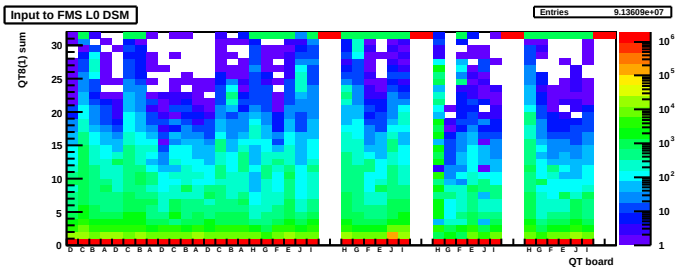

Input to QT2 crate
Entries 1.950203e+07
ADC
channel

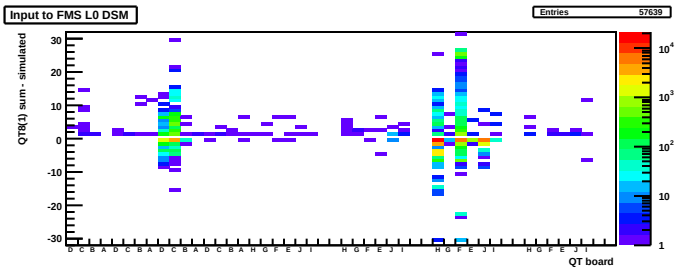

Input to QT3 crate
Entries 1.635859e+07
ADC
channel

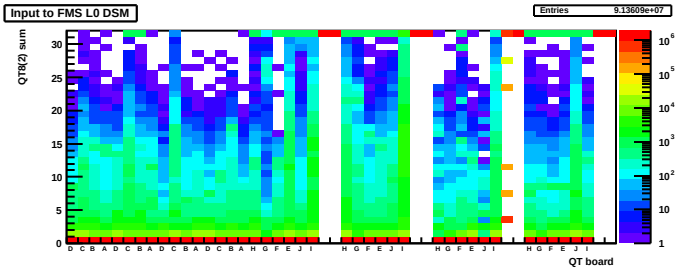

Input to QT4 crate
Entries 1.936018e+07
ADC
channel

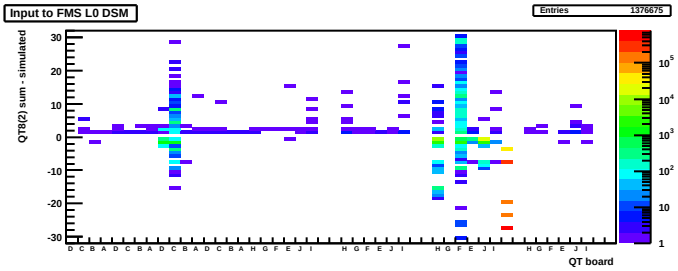

Input to FMS L0 DSM
Entries 9.13609e+07
QT8(0) sum
QT board

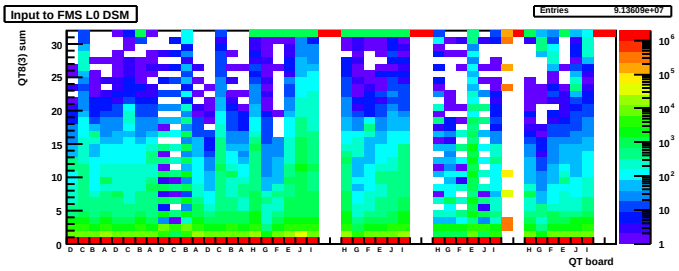

Input to FMS L0 DSM
Entries 37229
QT8(0) sum - simulated
QT board

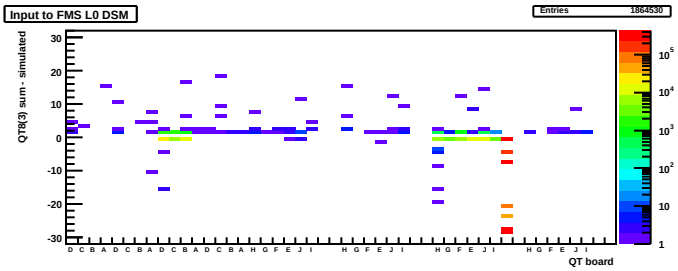

Input to FMS L0 DSM
Entries 9.13609e+07
QT8(1) sum
QT board

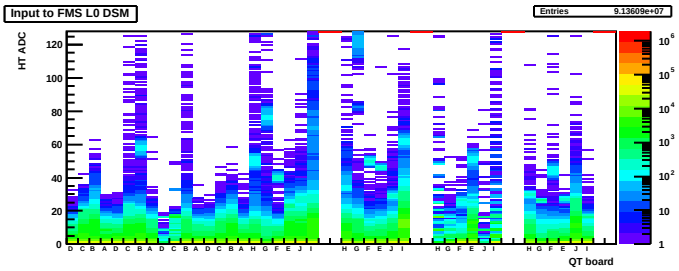

Input to FMS L0 DSM
Entries 57639
QT8(1) sum - simulated
QT board

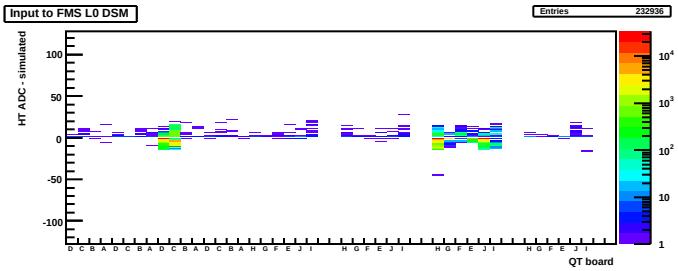

Input to FMS L0 DSM
Entries 9.13609e+07
QT8(2) sum
QT board

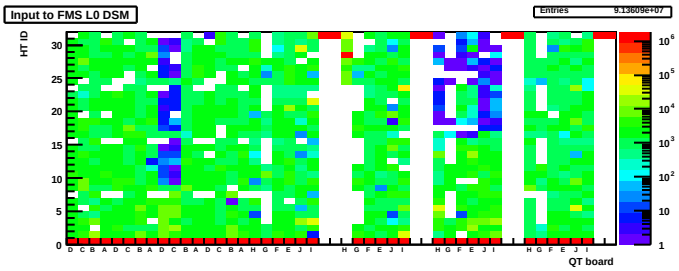

Input to FMS L0 DSM
Entries 1376675
QT8(2) sum - simulated
QT board

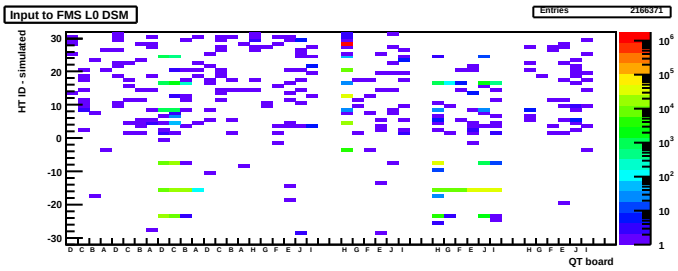

Input to FMS L0 DSM
Entries 9.13609e+07
QT8(3) sum
QT board

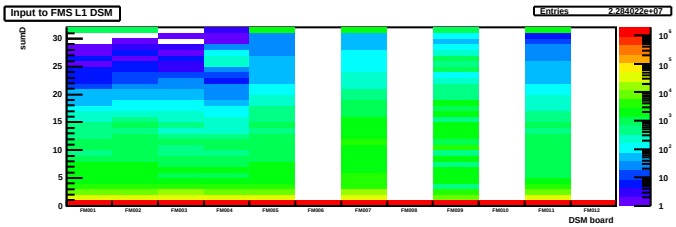

Input to FMS L0 DSM
Entries 1864530
QT8(3) sum - simulated
QT board

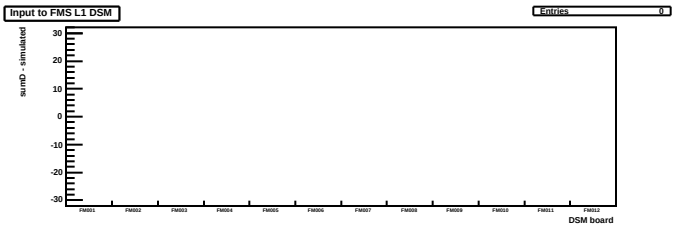

Input to FMS L0 DSM
Entries 9.13609e+07
HT ADC
QT board

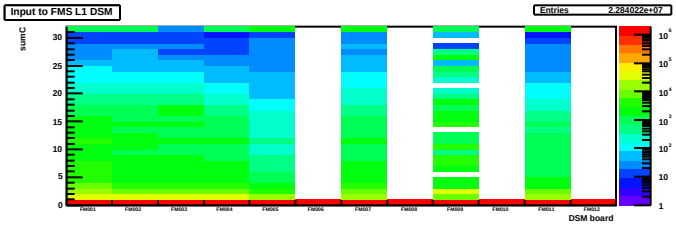

Input to FMS L0 DSM
Entries 232936
HT ADC - simulated
QT board

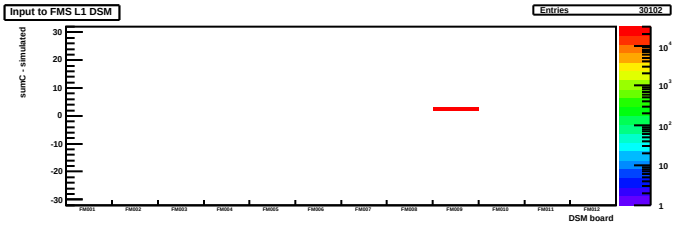

Input to FMS L0 DSM
Entries 9.13609e+07
HT ID
QT board

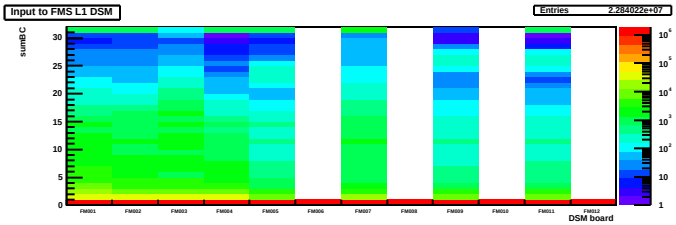

Input to FMS L0 DSM
Entries 2166371
HT ID - simulated
QT board

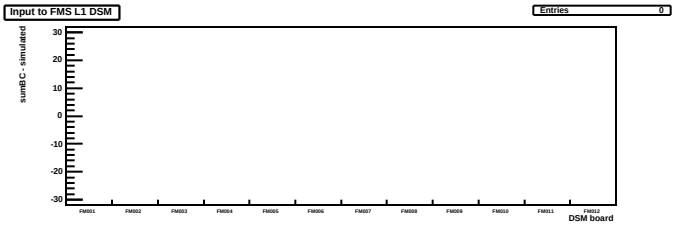

Input to FMS L1 DSM
Entries 2.284022e+07
sumD
DSM board

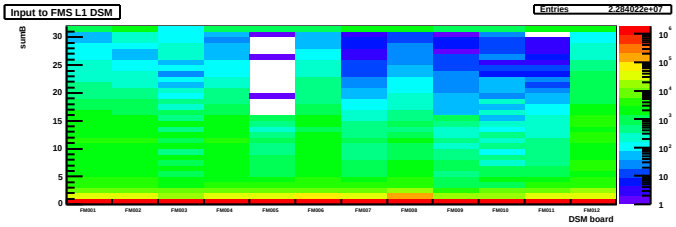

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

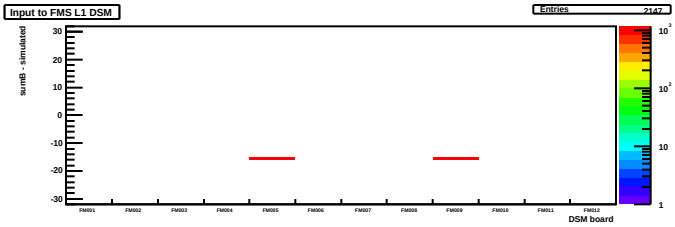

Input to FMS L1 DSM
Entries 2.284022e+07
sumC
DSM board

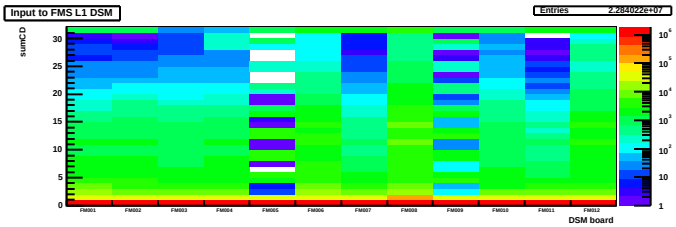

Input to FMS L1 DSM
Entries 30102
sumC - simulated
DSM board

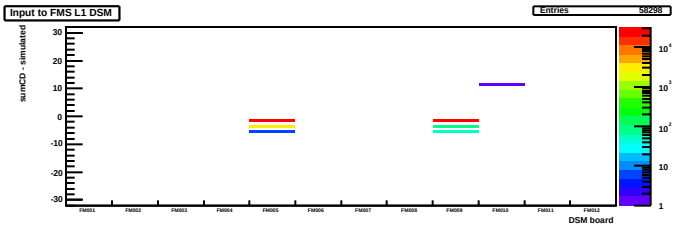

Input to FMS L1 DSM
Entries 2.284022e+07
sumBC
DSM board

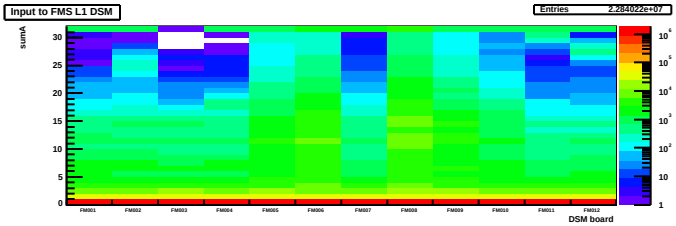

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

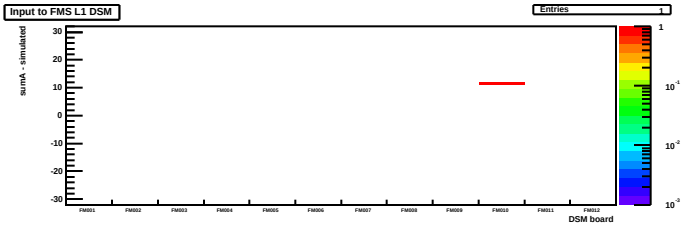

Input to FMS L1 DSM
Entries 2.284022e+07
sumB
DSM board

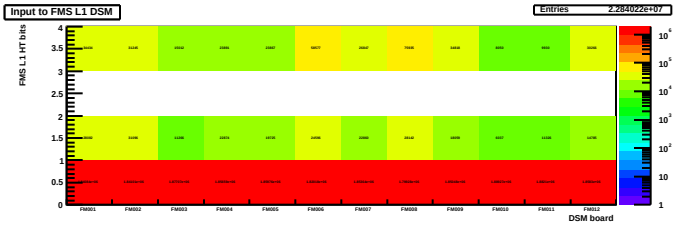

Input to FMS L1 DSM
Entries 2147
sumB - simulated
DSM board

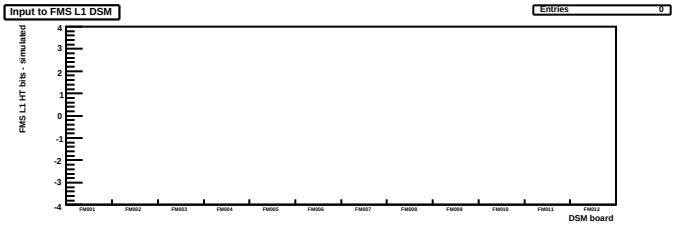

Input to FMS L1 DSM
Entries 2.284022e+07
sumCD
DSM board

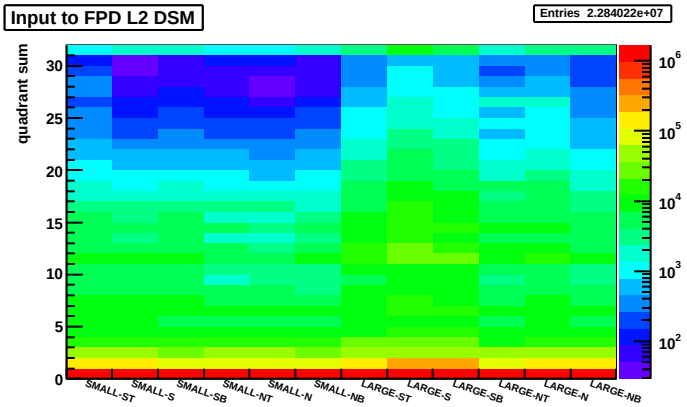

Input to FMS L1 DSM
Entries 58298
sumCD - simulated
DSM board

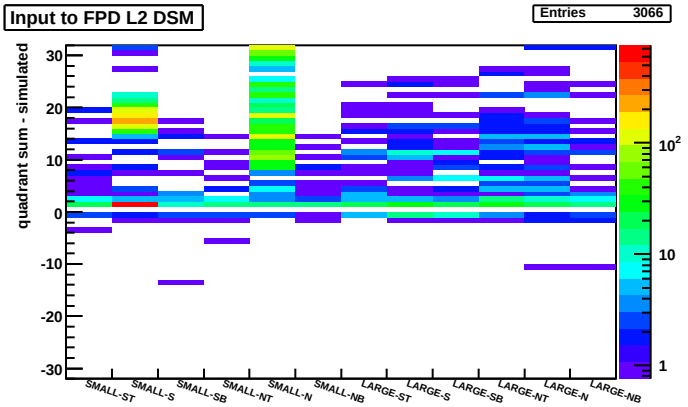

Input to FMS L1 DSM
Entries 2.284022e+07
sumA
DSM board

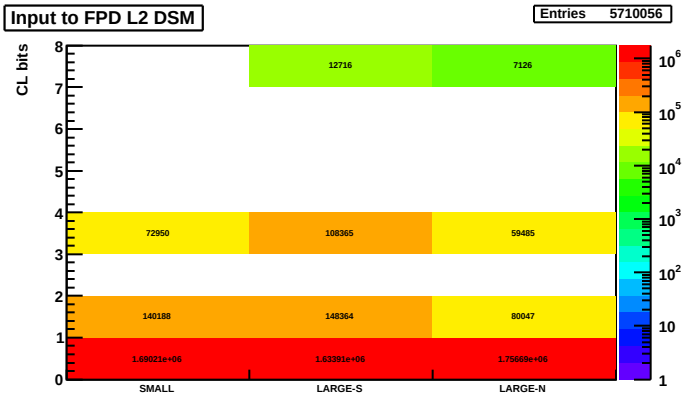

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

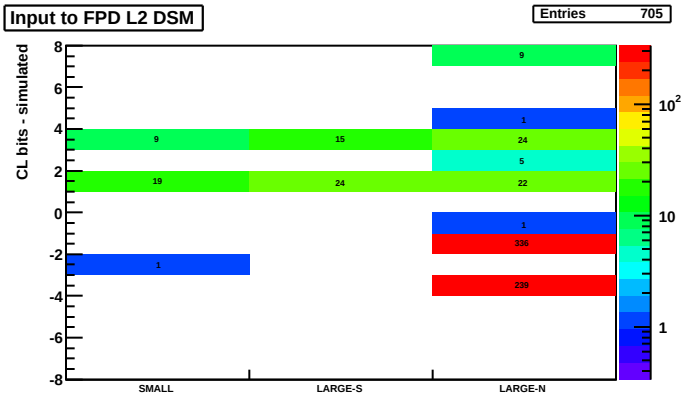

Input to FMS L1 DSM
Entries 2.284022e+07
FMS L1 HT bits
DSM board

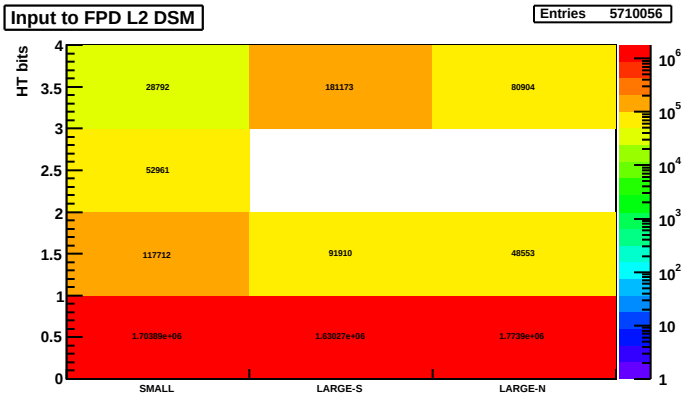

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

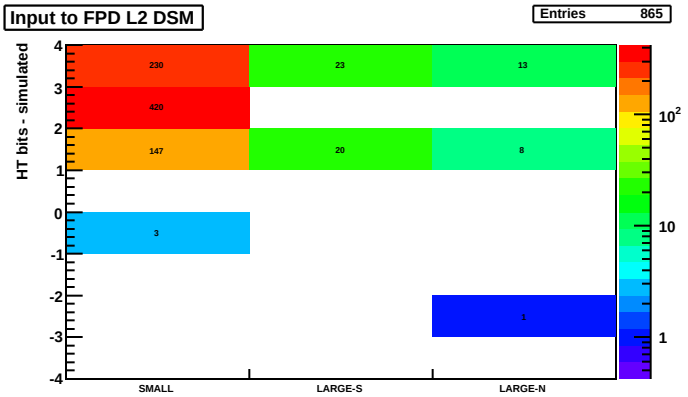

Input to FPD L2 DSM
Entries 2.284022e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

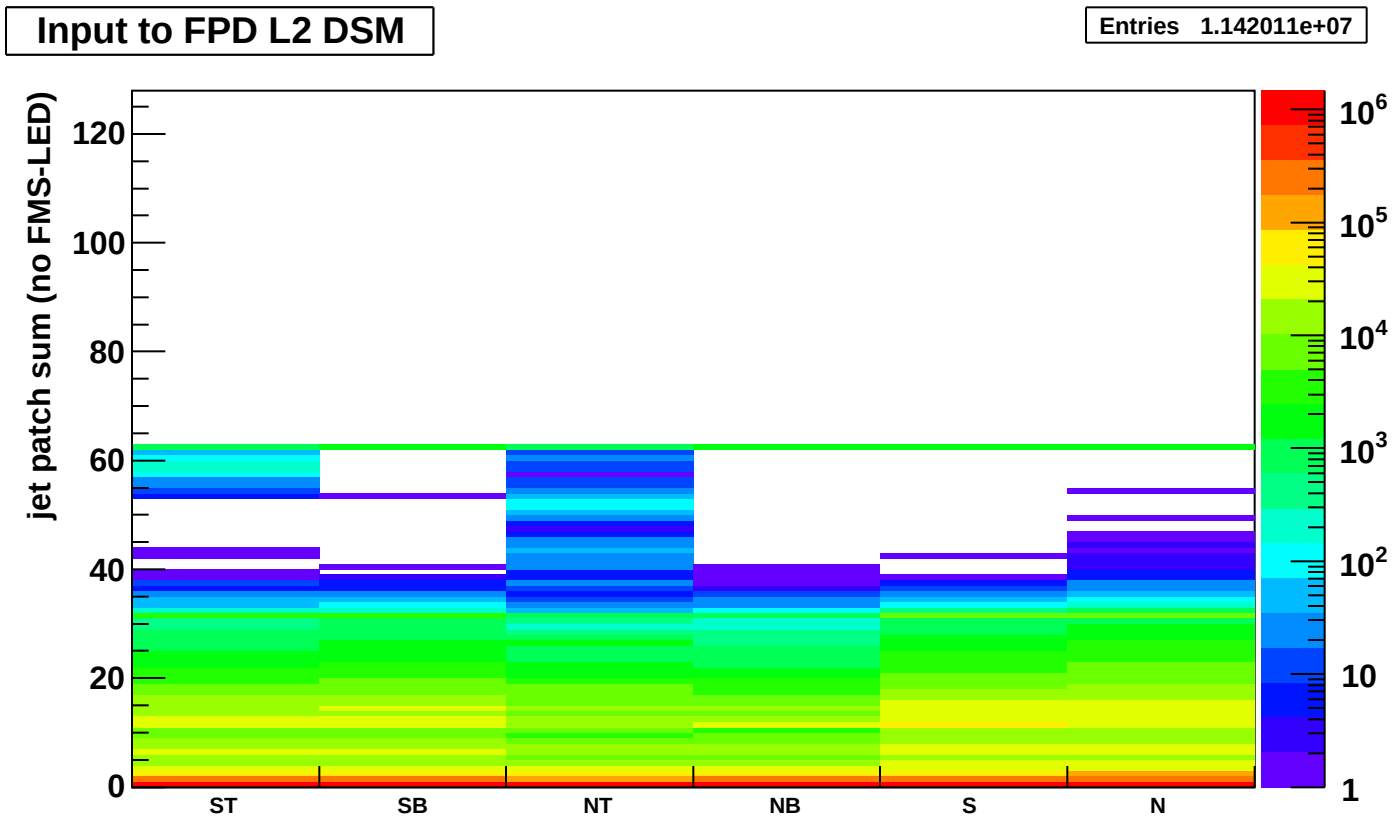

Input to FPD L2 DSM
Entries 3066
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

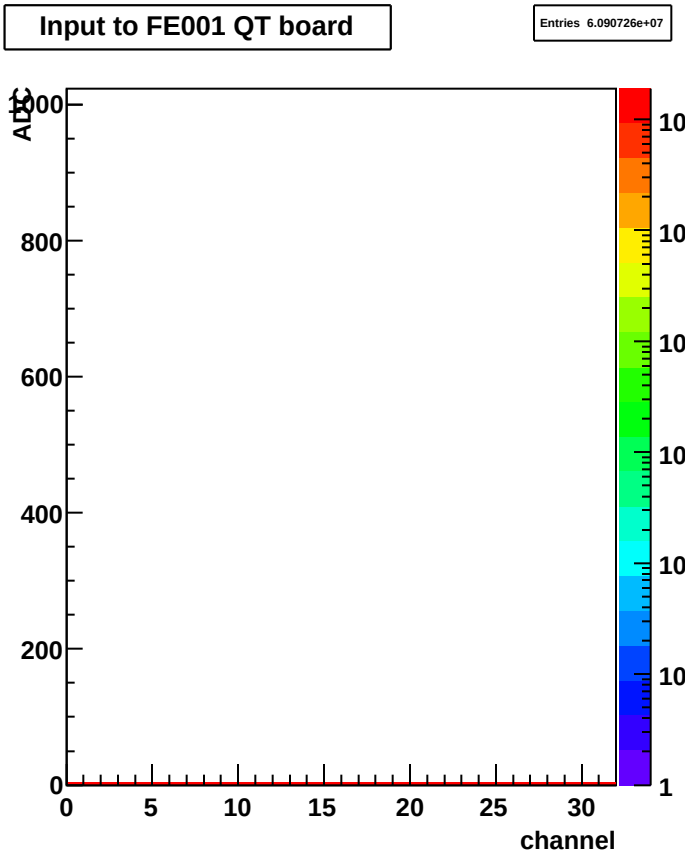

Input to FPD L2 DSM
Entries 5710056
CL bits
12716
7126
72950
108365
59485
140188
148364
80047
1.69021e+06
1.63391e+06
1.75669e+06
SMALL
LARGE-S
LARGE-N

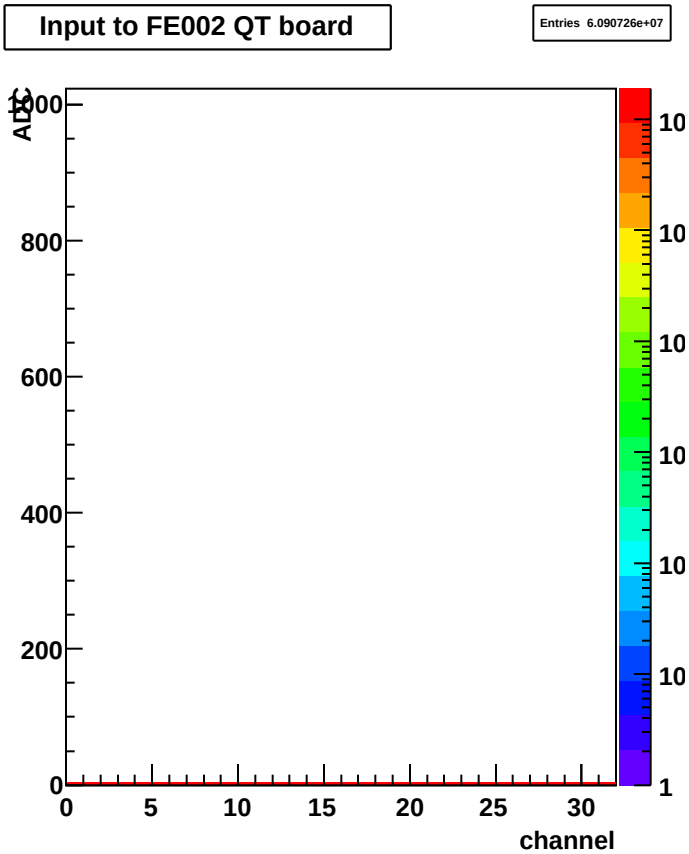

Input to FPD L2 DSM
Entries 705
CL bits - simulated
9
1
9
15
24
5
19
24
22
1
336
1
239
SMALL
LARGE-S
LARGE-N

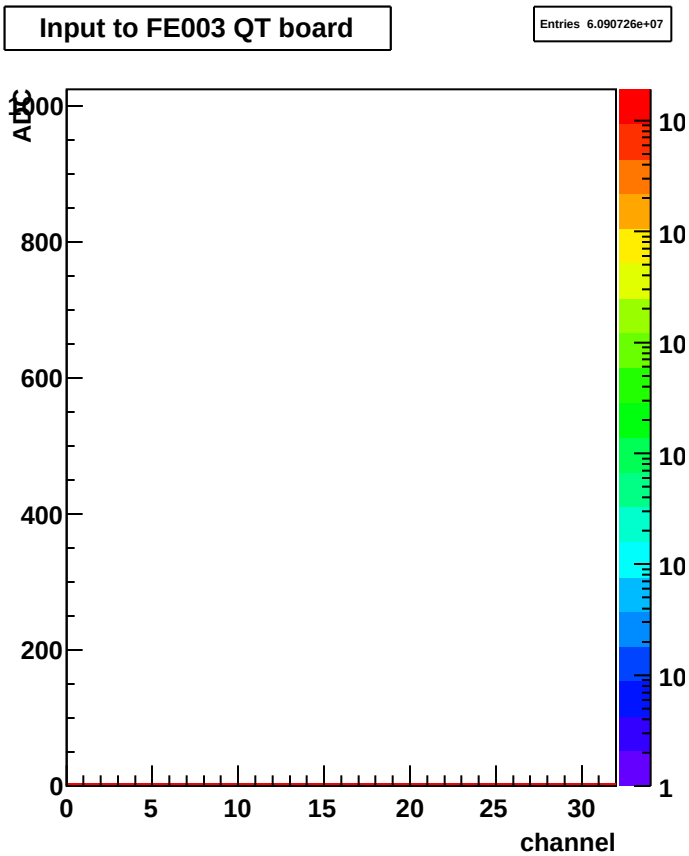

Input to FPD L2 DSM
Entries 5710056
HT bits
28792
181173
80904
52961
117712
91910
48553
1.70389e+06
1.63027e+06
1.7739e+06
SMALL
LARGE-S
LARGE-N

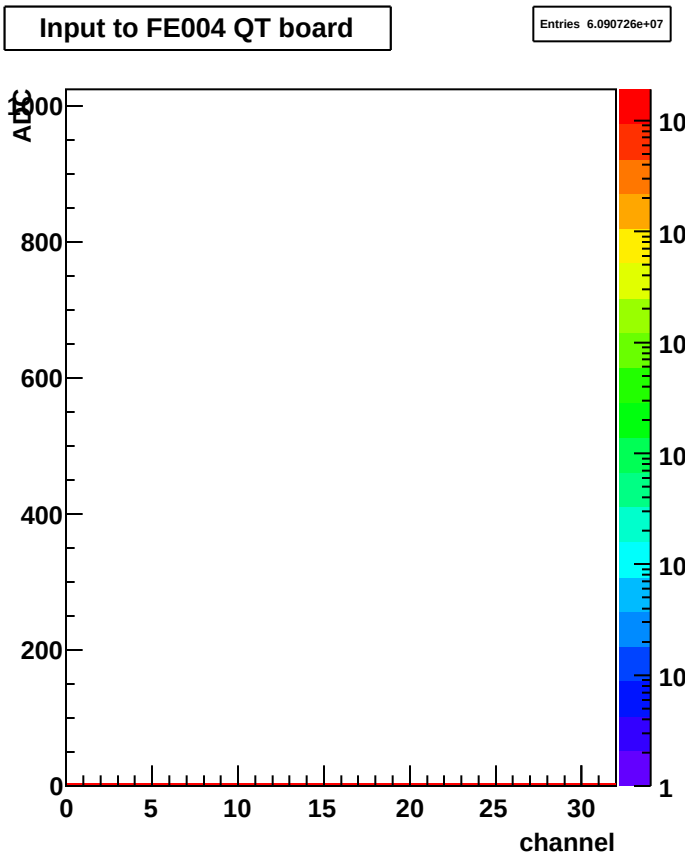

Input to FPD L2 DSM
Entries 865
HT bits - simulated
230
23
13
420
147
20
8
3
1
SMALL
LARGE-S
LARGE-N

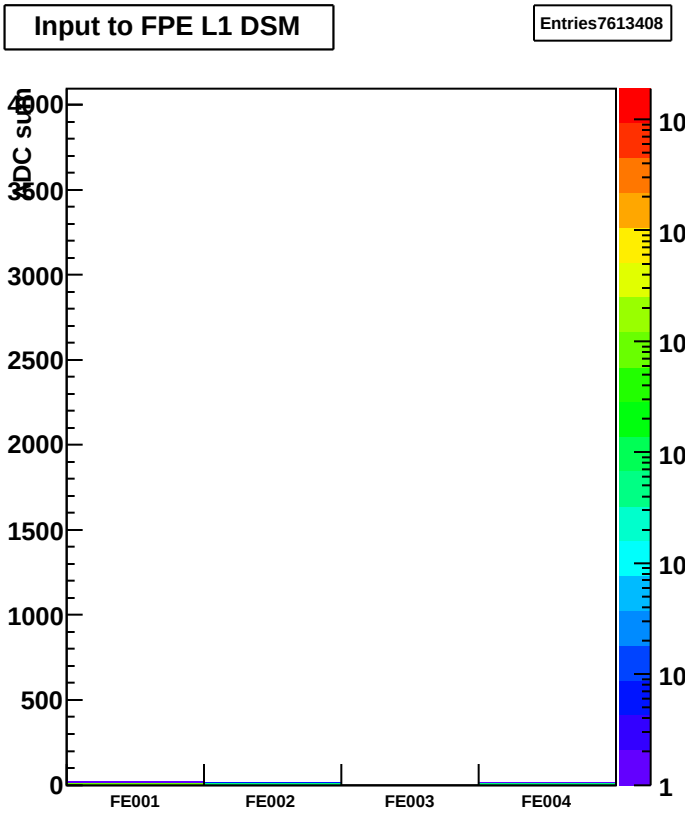

Input to FPD L2 DSM
Entries 1.142011e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

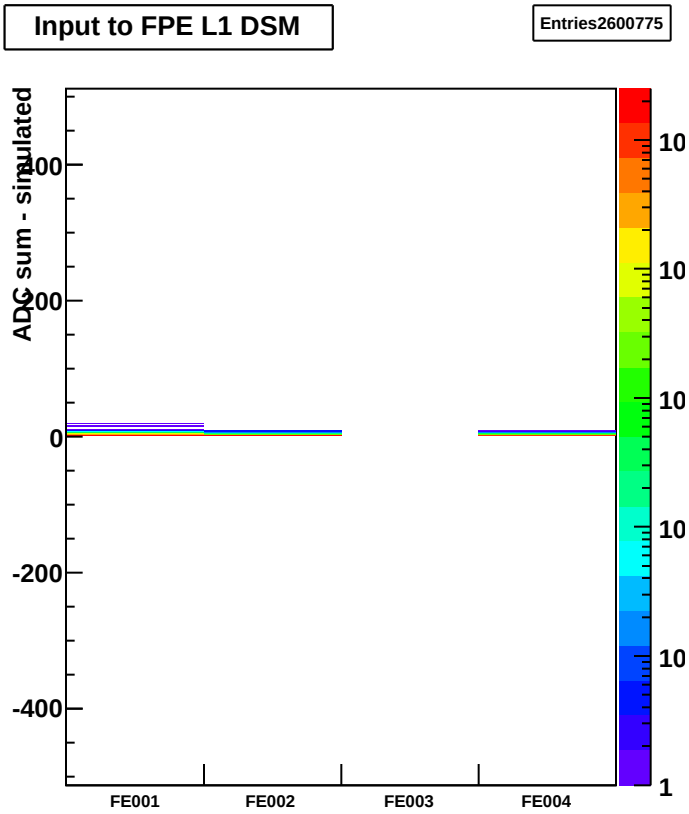

Input to FE001 QT board
Entries 6.090726e+07
ADC
channel


Input to FE002 QT board
Entries 6.090726e+07
ADC
channel

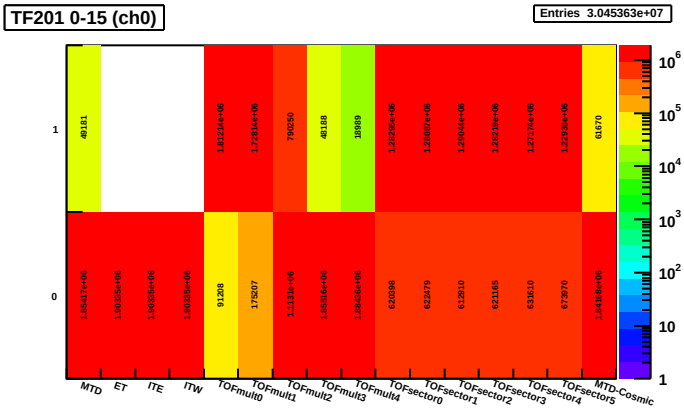

Input to FE003 QT board
Entries 6.090726e+07
ADC
channel


Input to FE004 QT board
Entries 6.090726e+07
ADC
channel

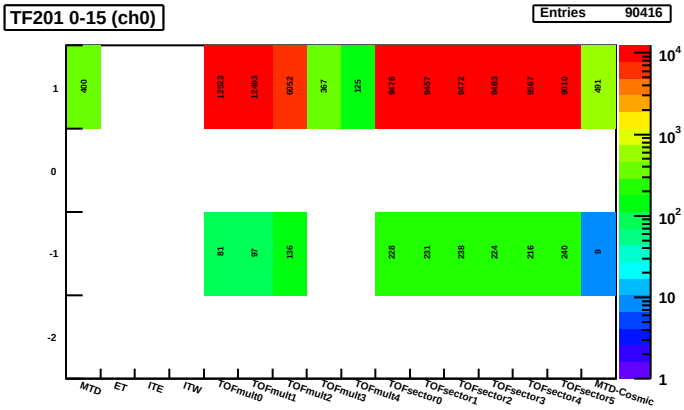

Input to FPE L1 DSM
Entries7613408
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2600775
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

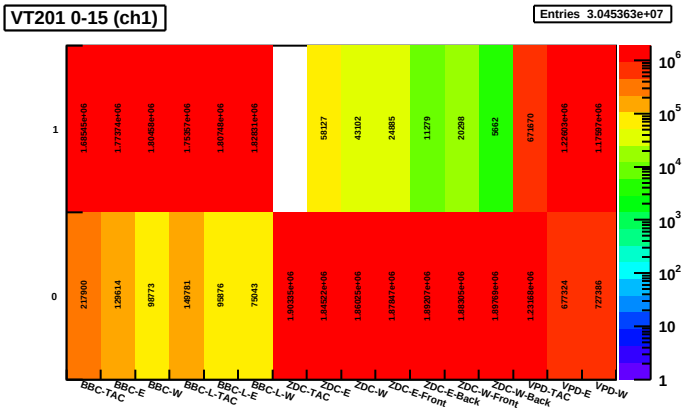
TF201 0-15 (ch0)



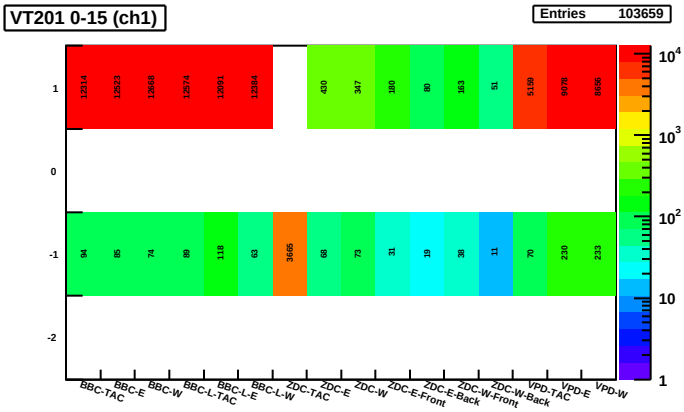
TF201 0-15 (ch0)



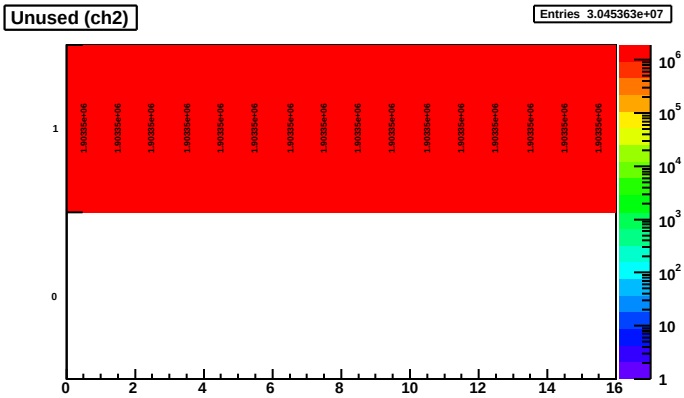
VT201 0-15 (ch1)

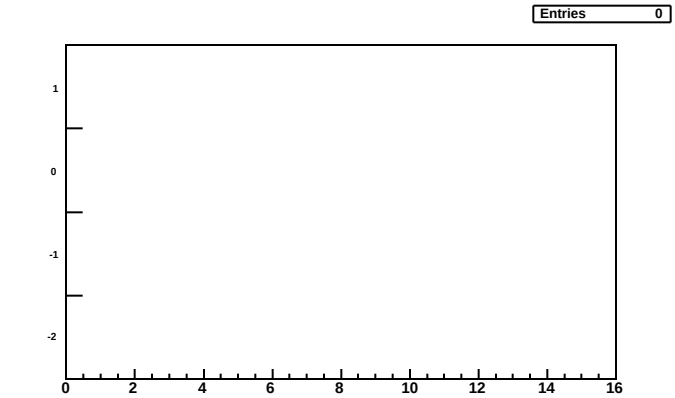


VT201 0-15 (ch1)

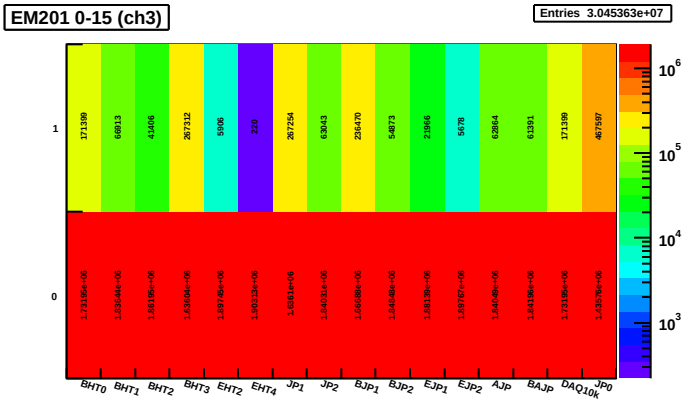


Unused (ch2)

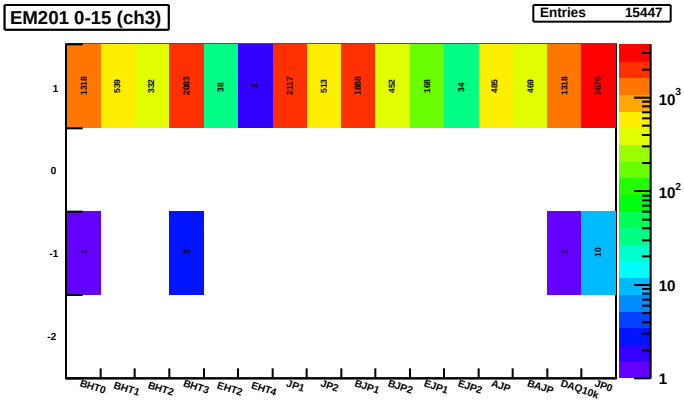




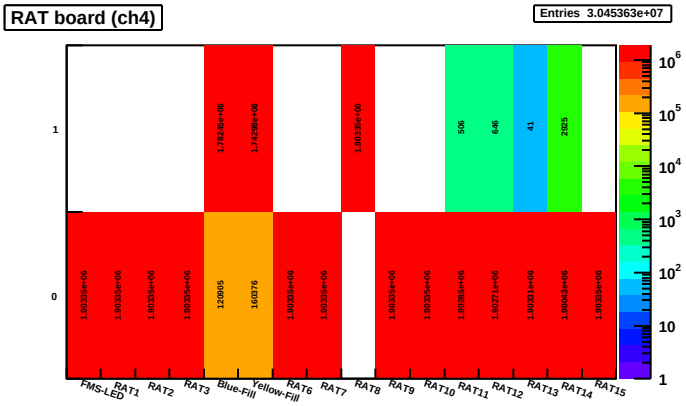
EM201 0-15 (ch3)

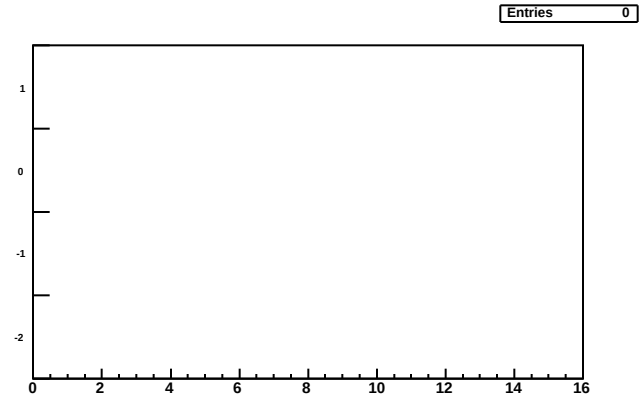


EM201 0-15 (ch3)

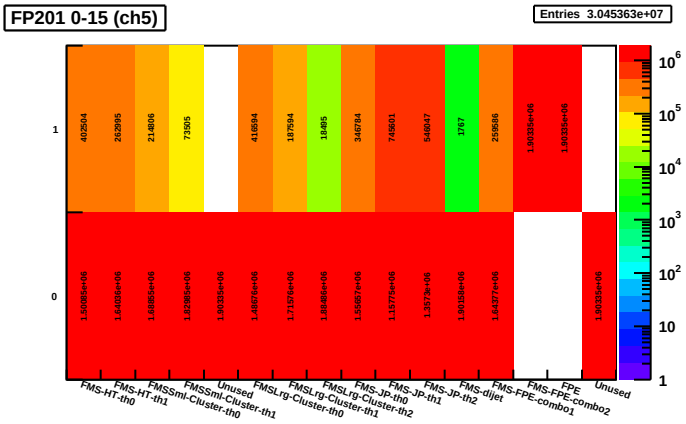


RAT board (ch4)

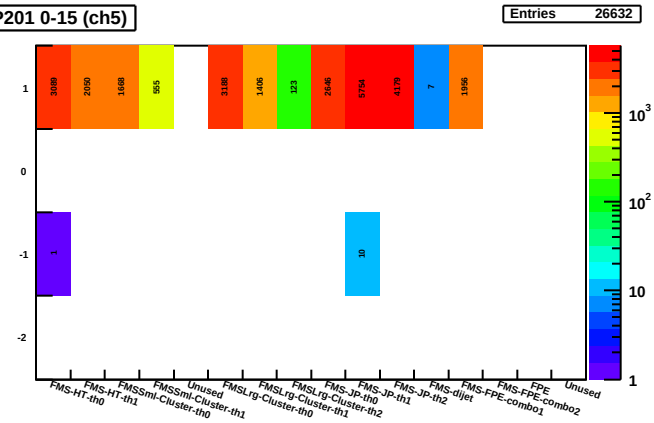




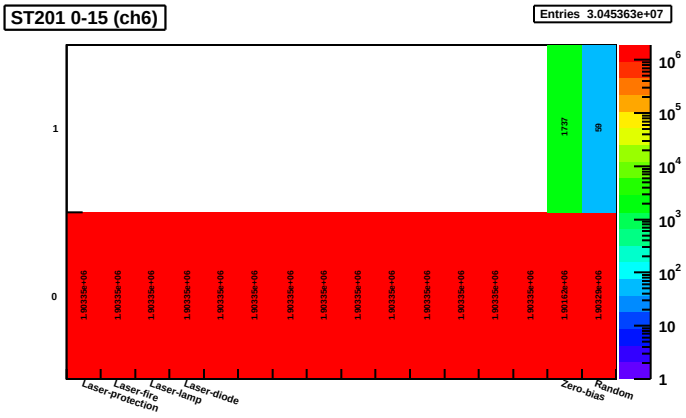
FP201 0-15 (ch5)

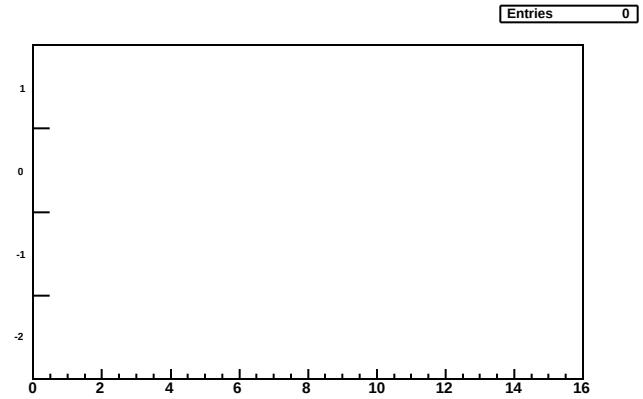
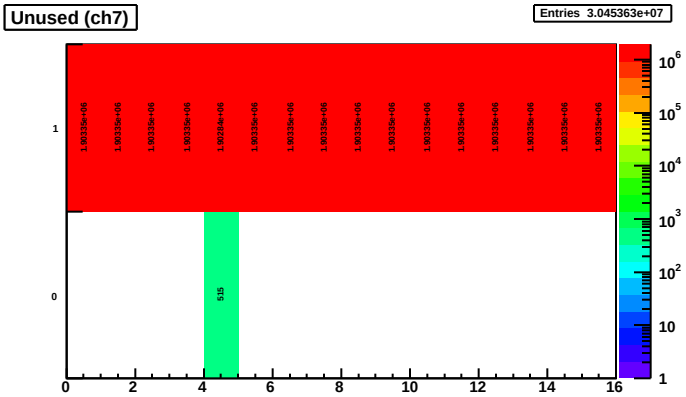
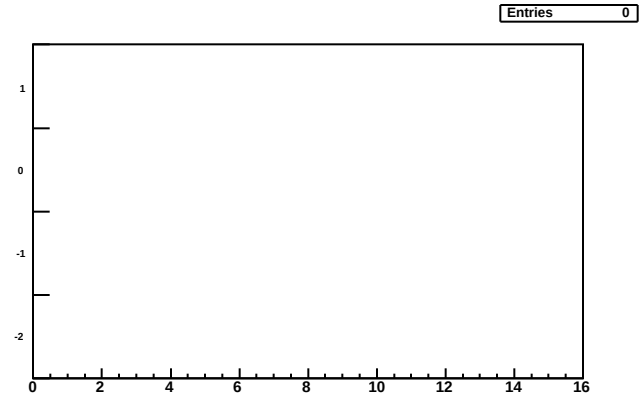


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)