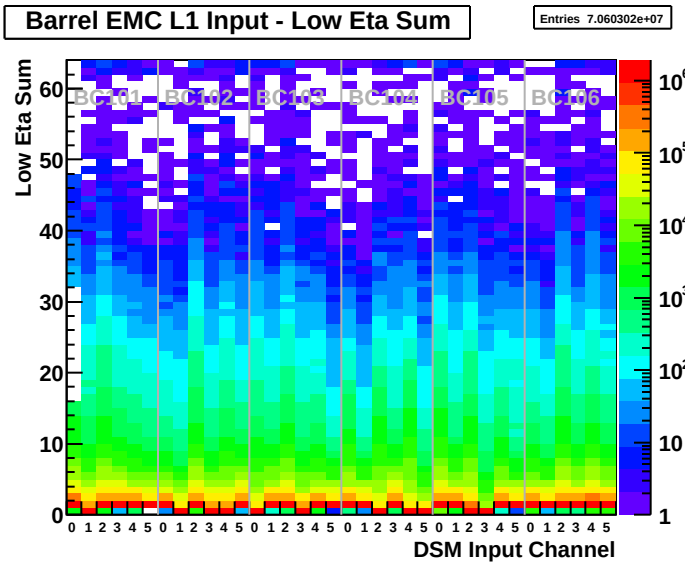
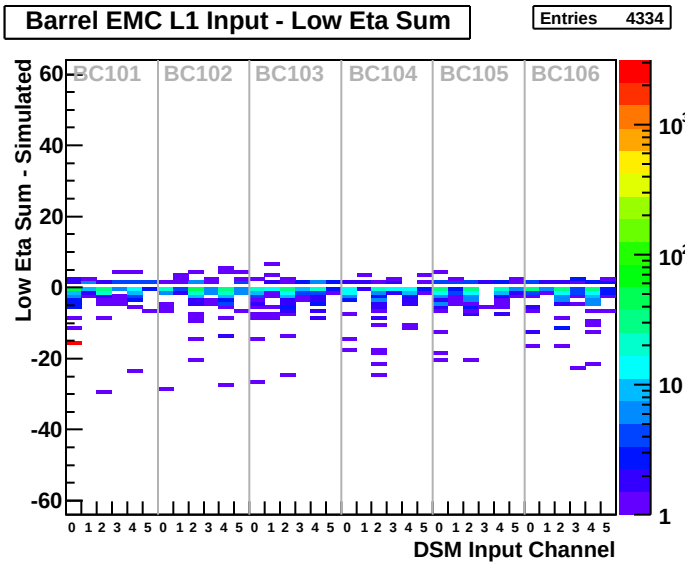
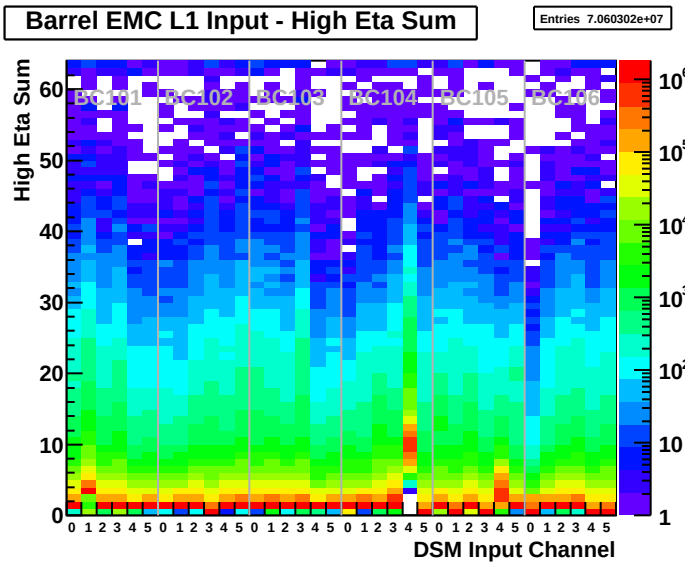
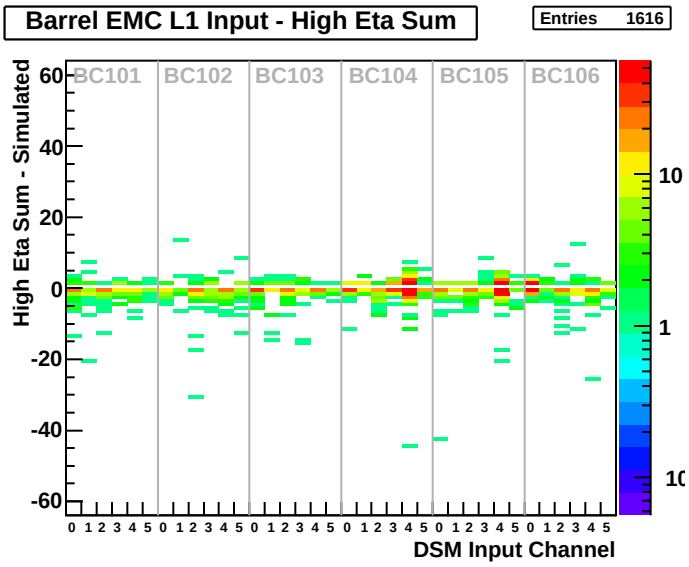
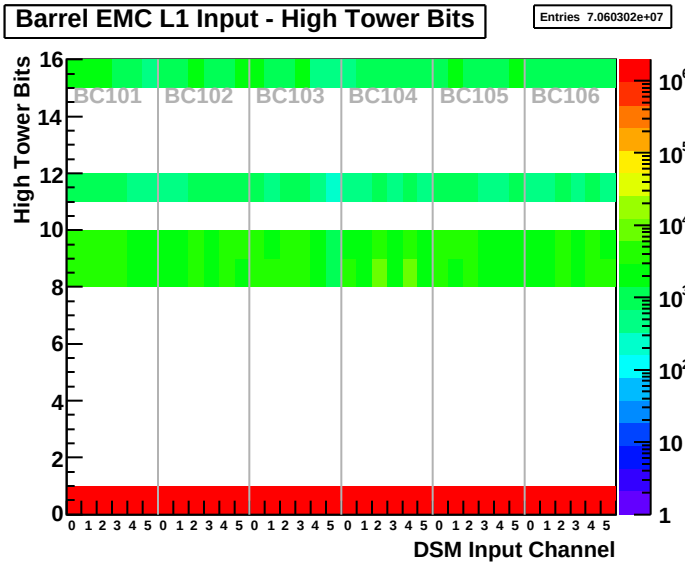
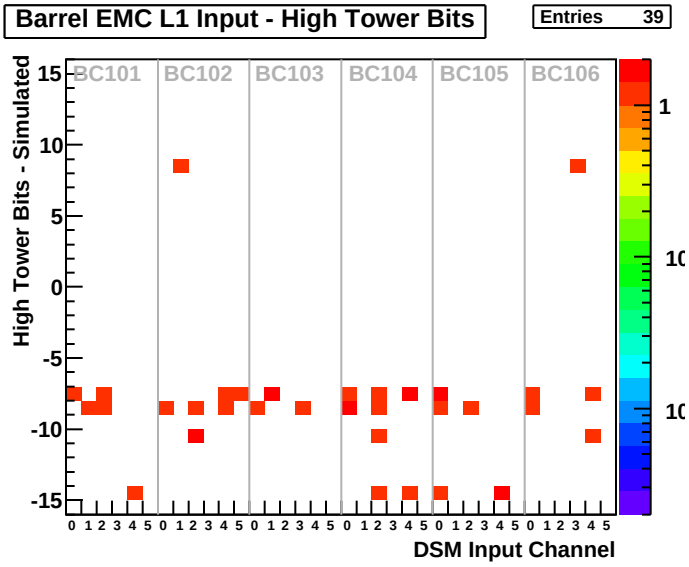

Barrel EMC L1 Input - Low Eta Sum
Entries 7.060302e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

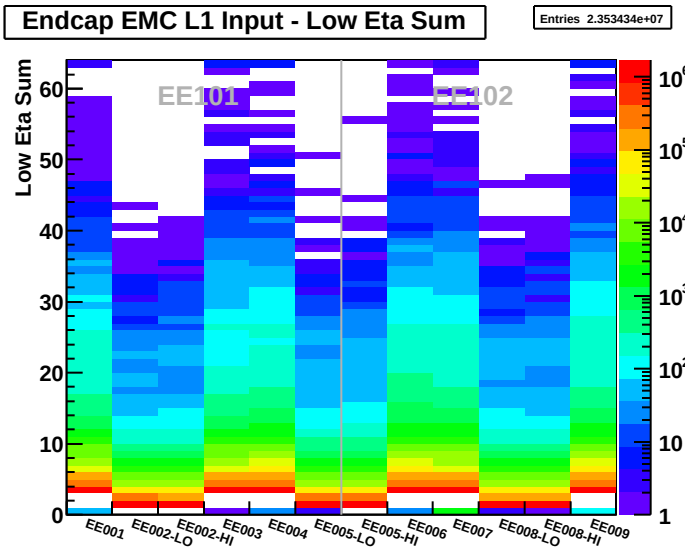

Barrel EMC L1 Input - Low Eta Sum
Entries 4334
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

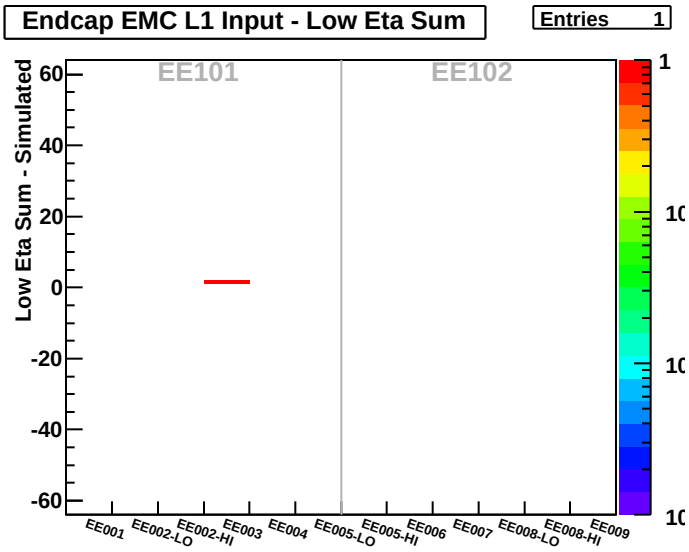

Barrel EMC L1 Input - High Eta Sum
Entries 7.060302e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

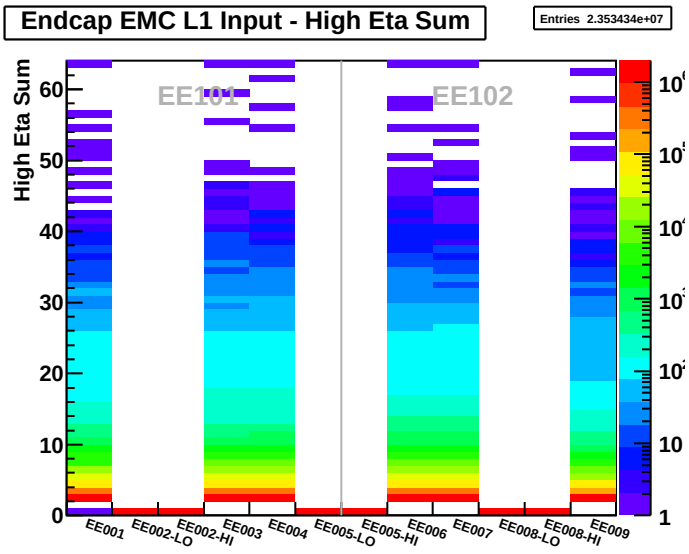

Barrel EMC L1 Input - High Eta Sum
Entries 1616
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

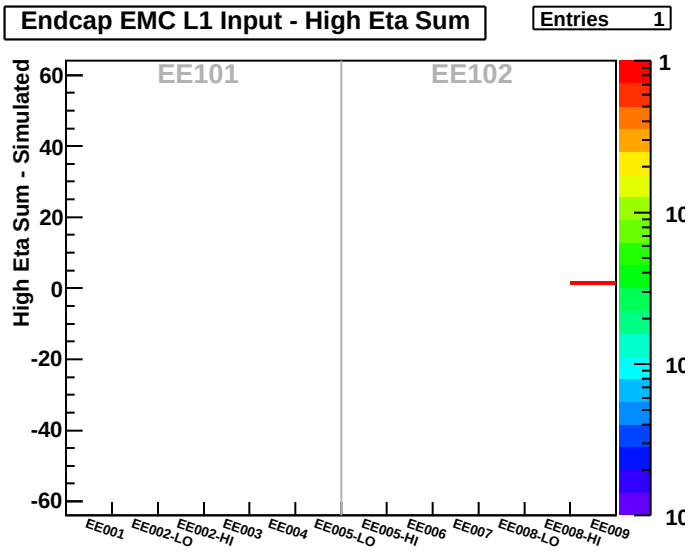

Barrel EMC L1 Input - High Tower Bits
Entries 7.060302e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

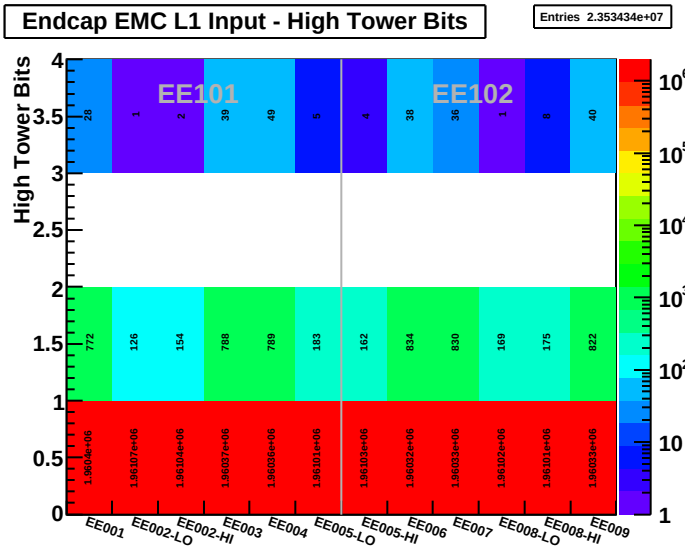

Barrel EMC L1 Input - High Tower Bits
Entries 39
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

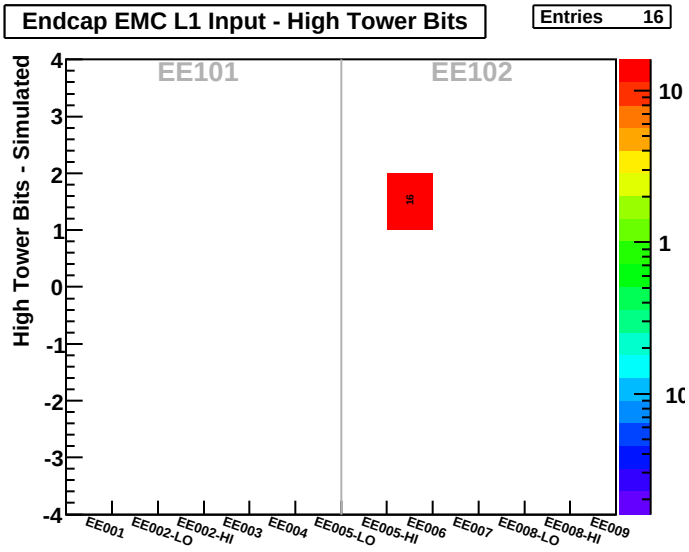

Endcap EMC L1 Input - Low Eta Sum
Entries 2.353434e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

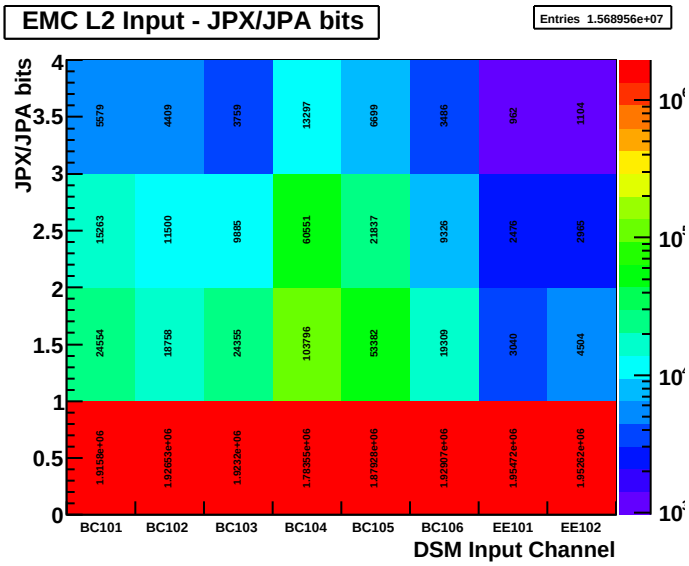

Endcap EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

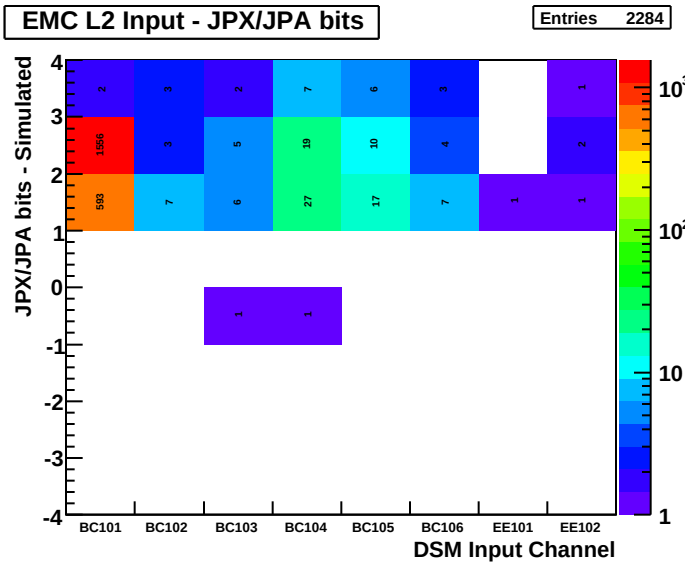

Endcap EMC L1 Input - High Eta Sum
Entries 2.353434e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

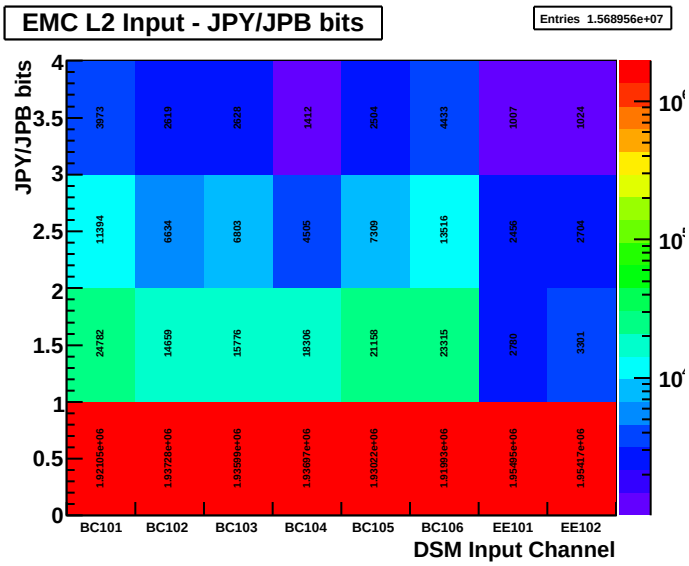

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

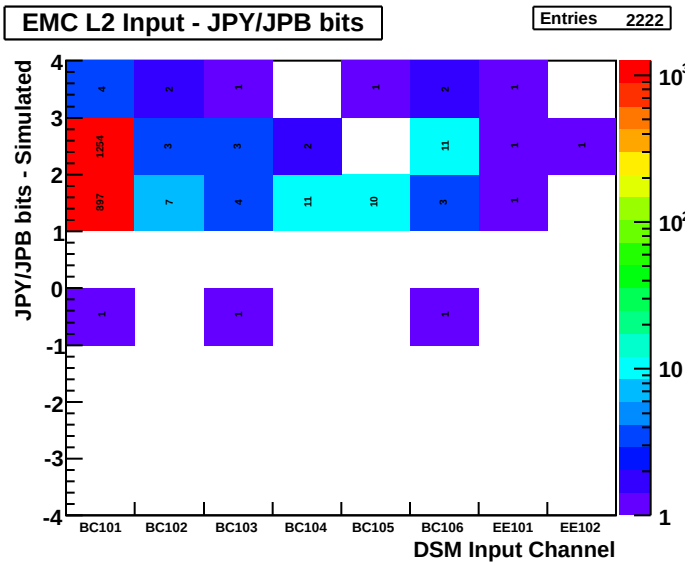

Endcap EMC L1 Input - High Tower Bits
Entries 2.353434e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

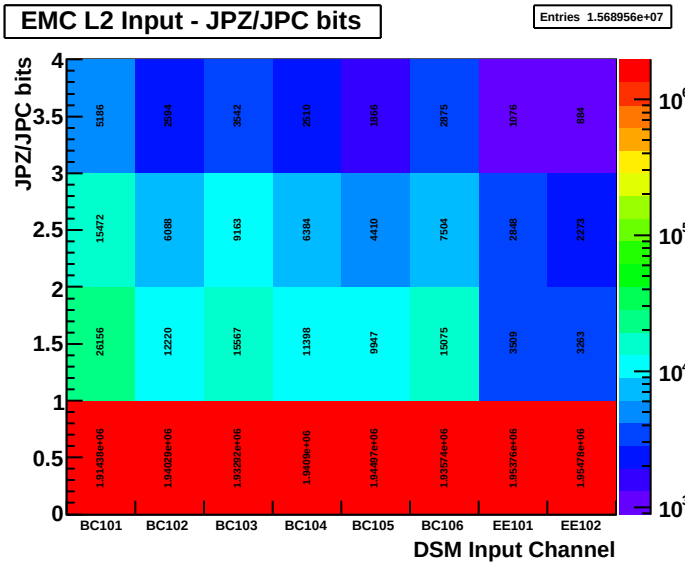

Endcap EMC L1 Input - High Tower Bits
Entries 16
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

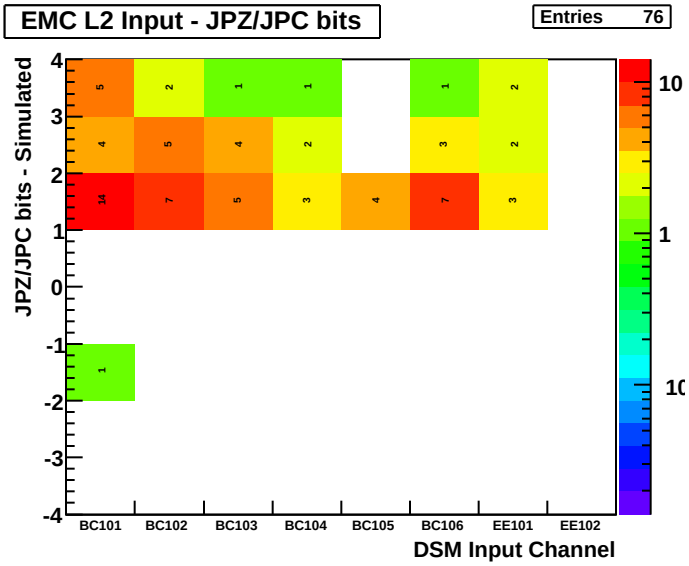

EMC L2 Input - JPX/JPA bits
Entries 1.568956e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

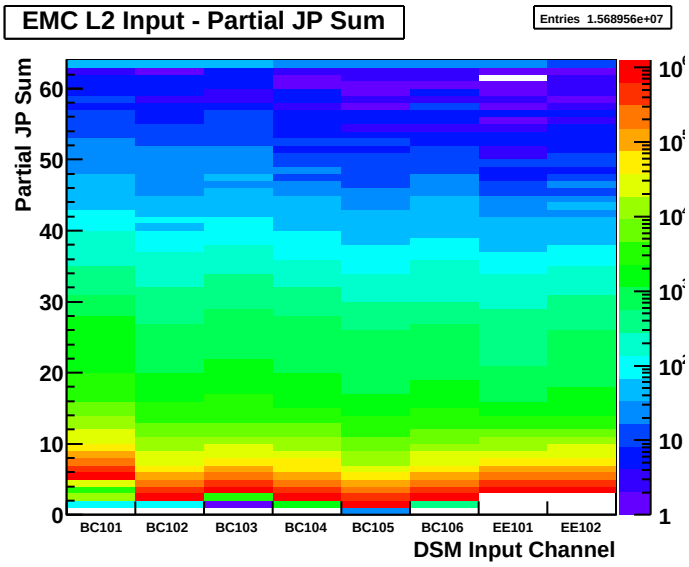

EMC L2 Input - JPX/JPA bits
Entries 2284
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

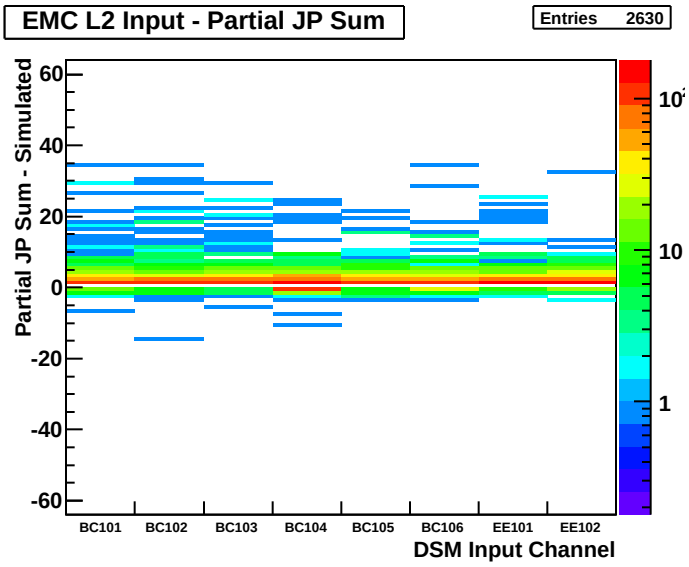

EMC L2 Input - JPY/JPB bits
Entries 1.568956e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

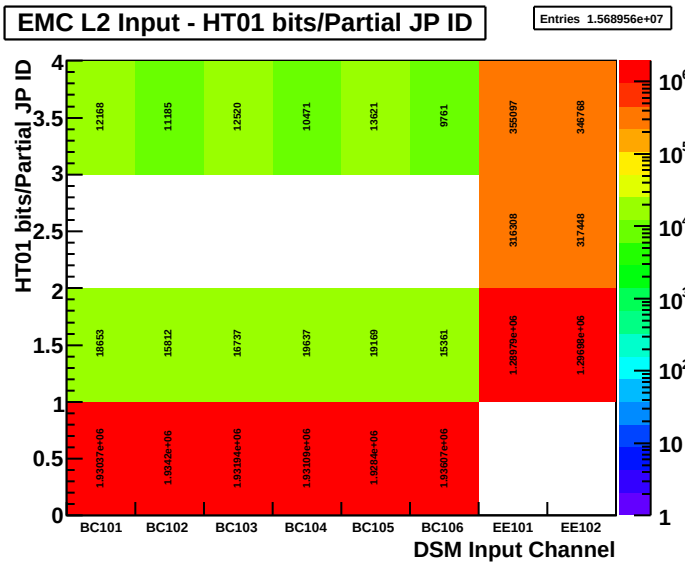

EMC L2 Input - JPY/JPB bits
Entries 2222
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

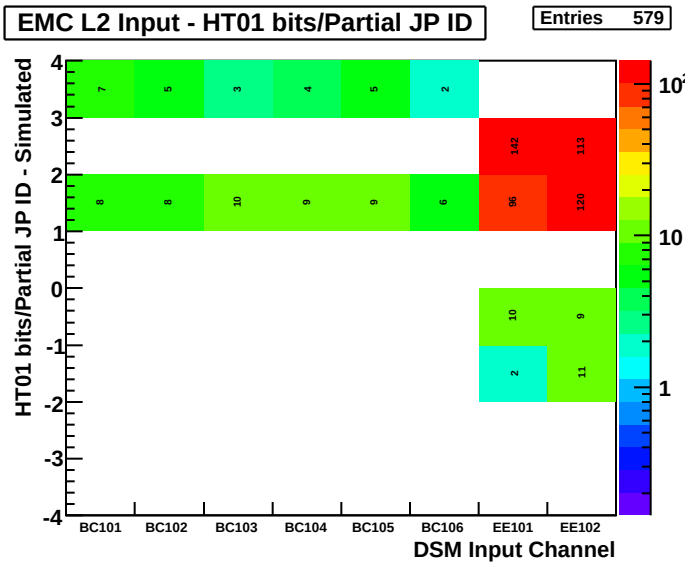

EMC L2 Input - JPZ/JPC bits
Entries 1.568956e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

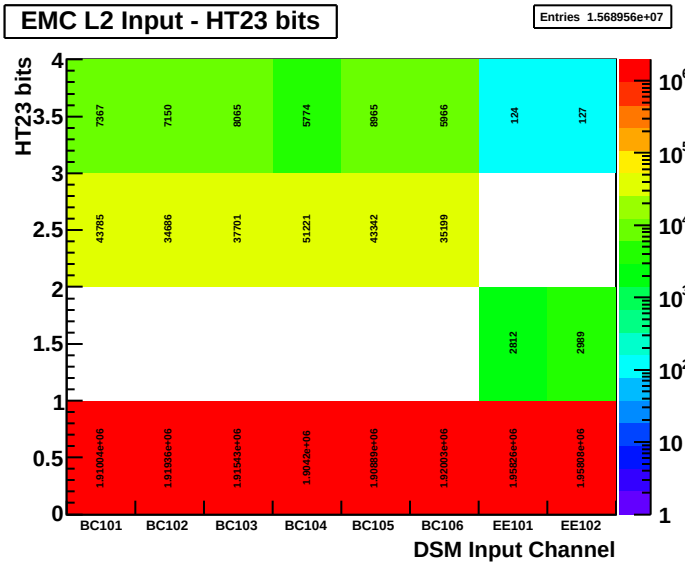

EMC L2 Input - JPZ/JPC bits
Entries 76
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

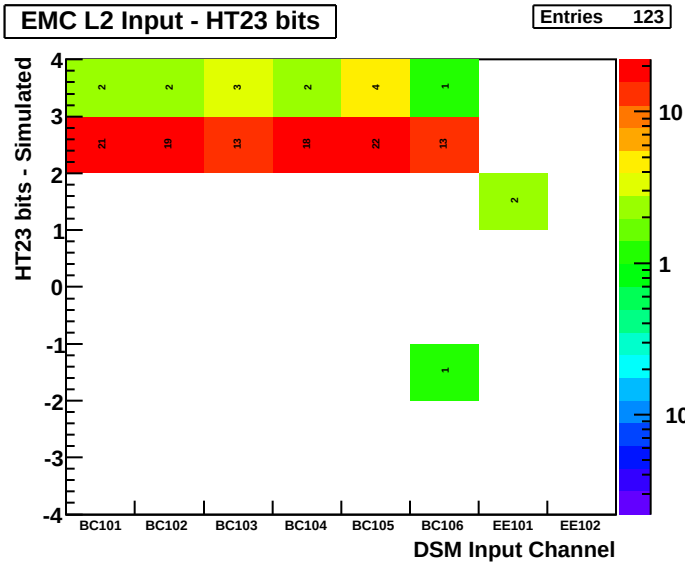

EMC L2 Input - Partial JP Sum
Entries 1.568956e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

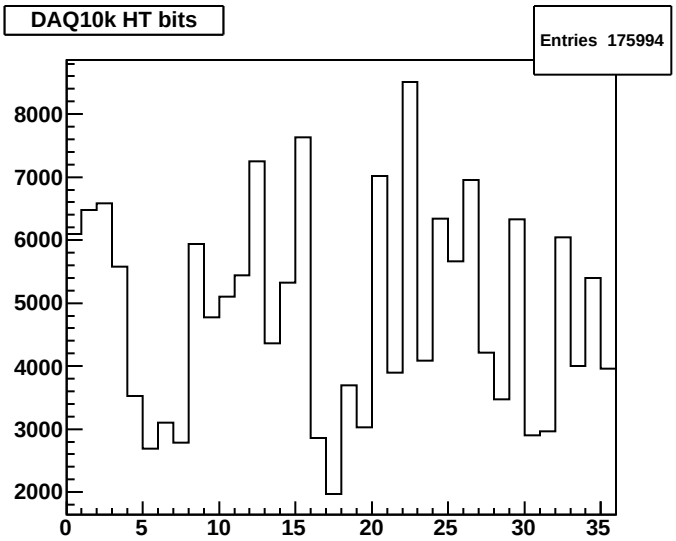

EMC L2 Input - Partial JP Sum
Entries 2630
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

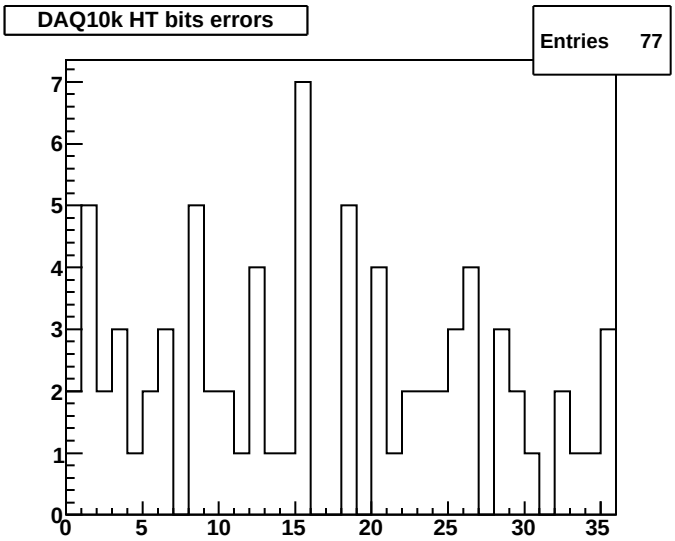

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.568956e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

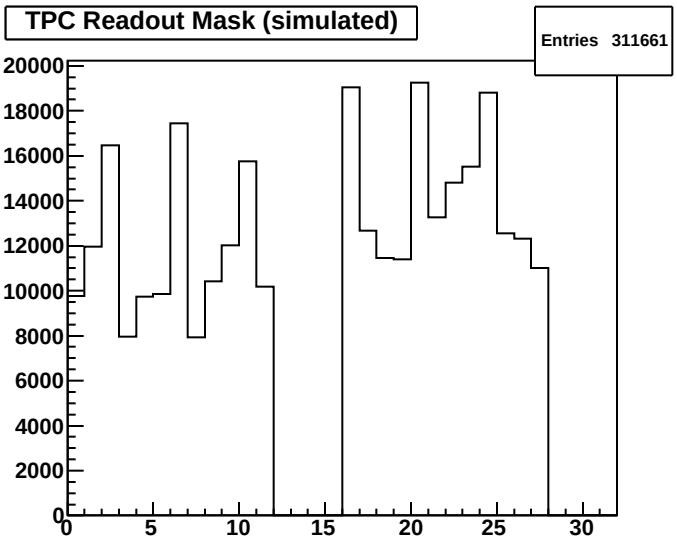

EMC L2 Input - HT01 bits/Partial JP ID
Entries 579
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

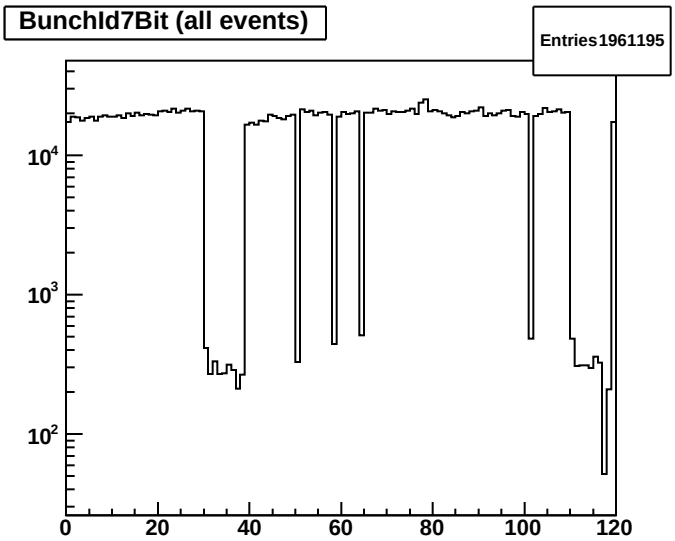

EMC L2 Input - HT23 bits
Entries 1.568956e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

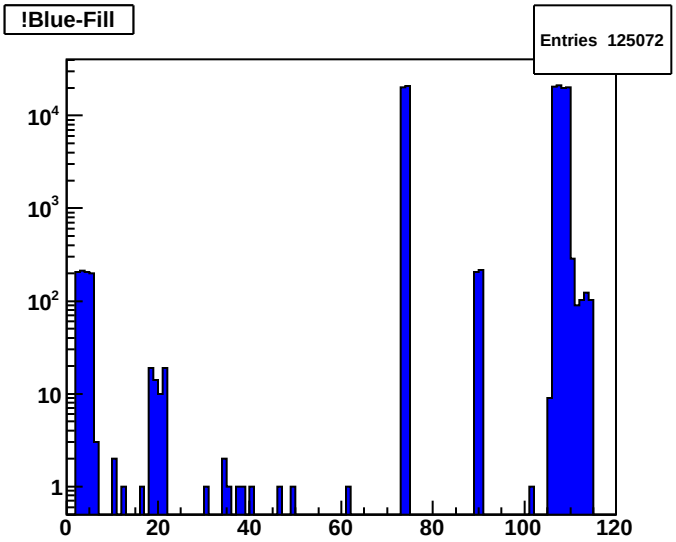

EMC L2 Input - HT23 bits
Entries 123
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

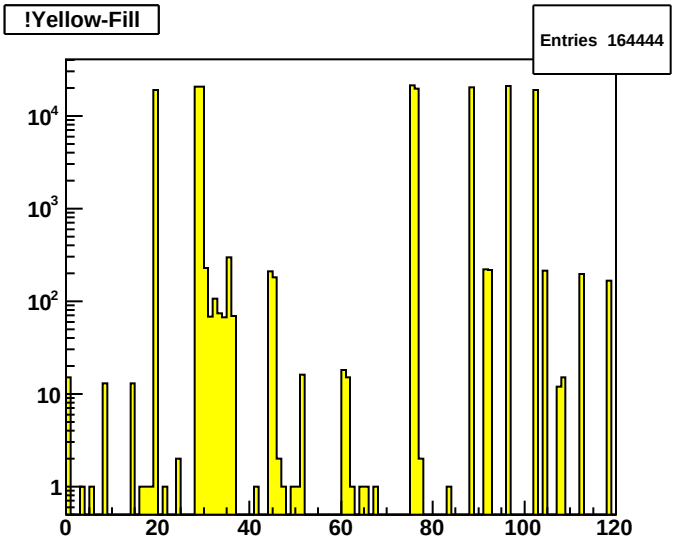

DAQ10k HT bits
Entries 175994

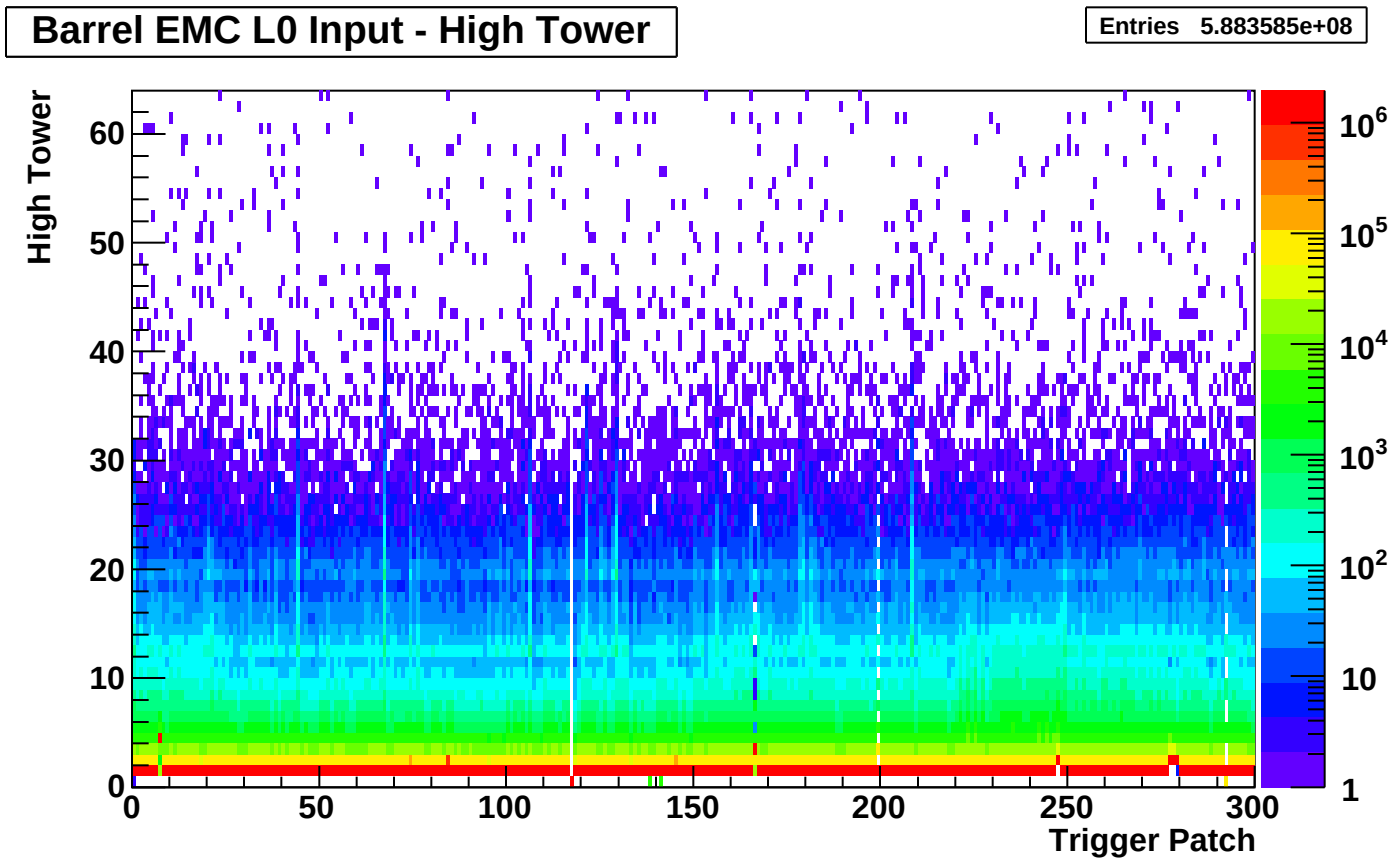

DAQ10k HT bits errors
Entries 77

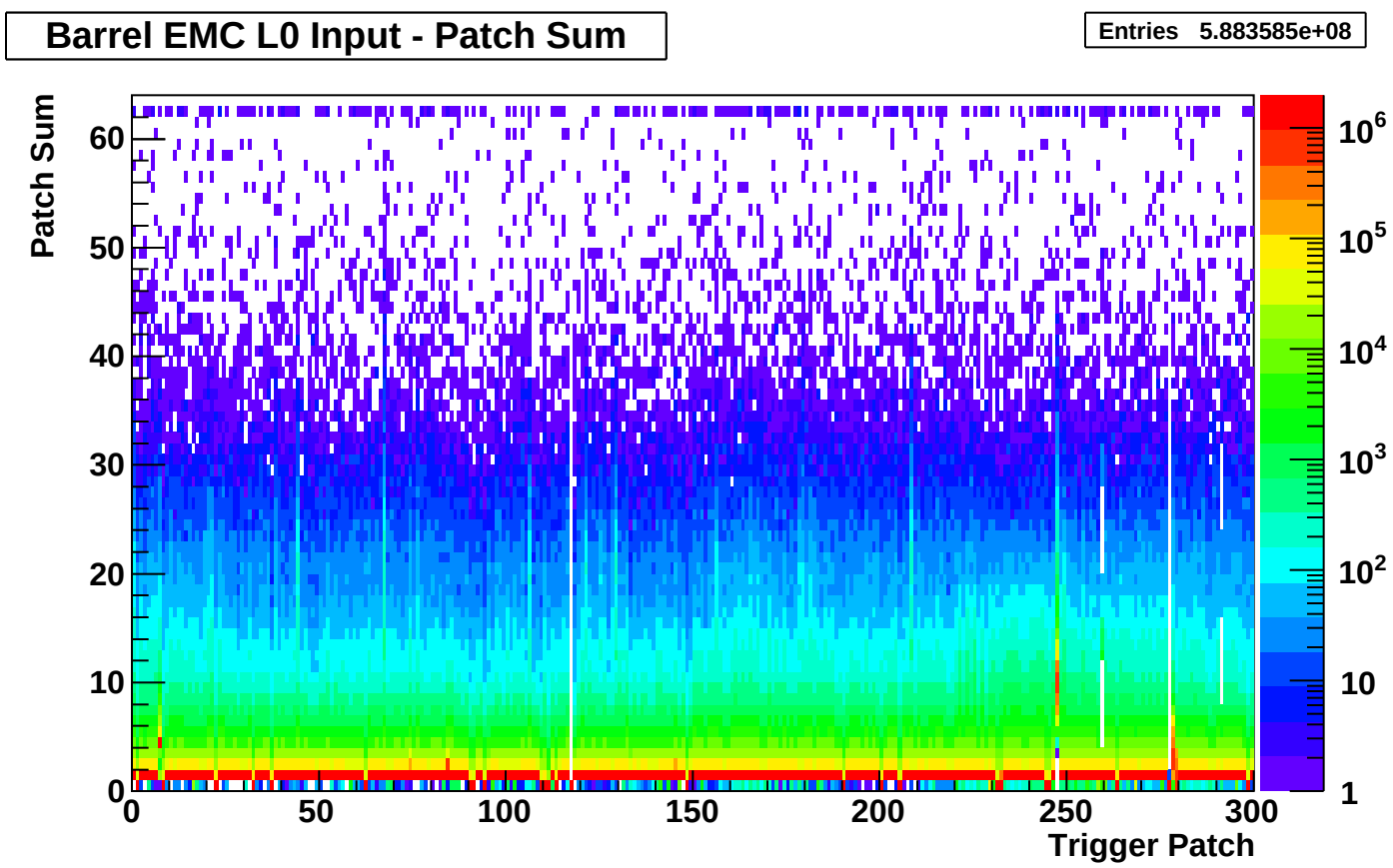

TPC Readout Mask (simulated)
Entries 311661

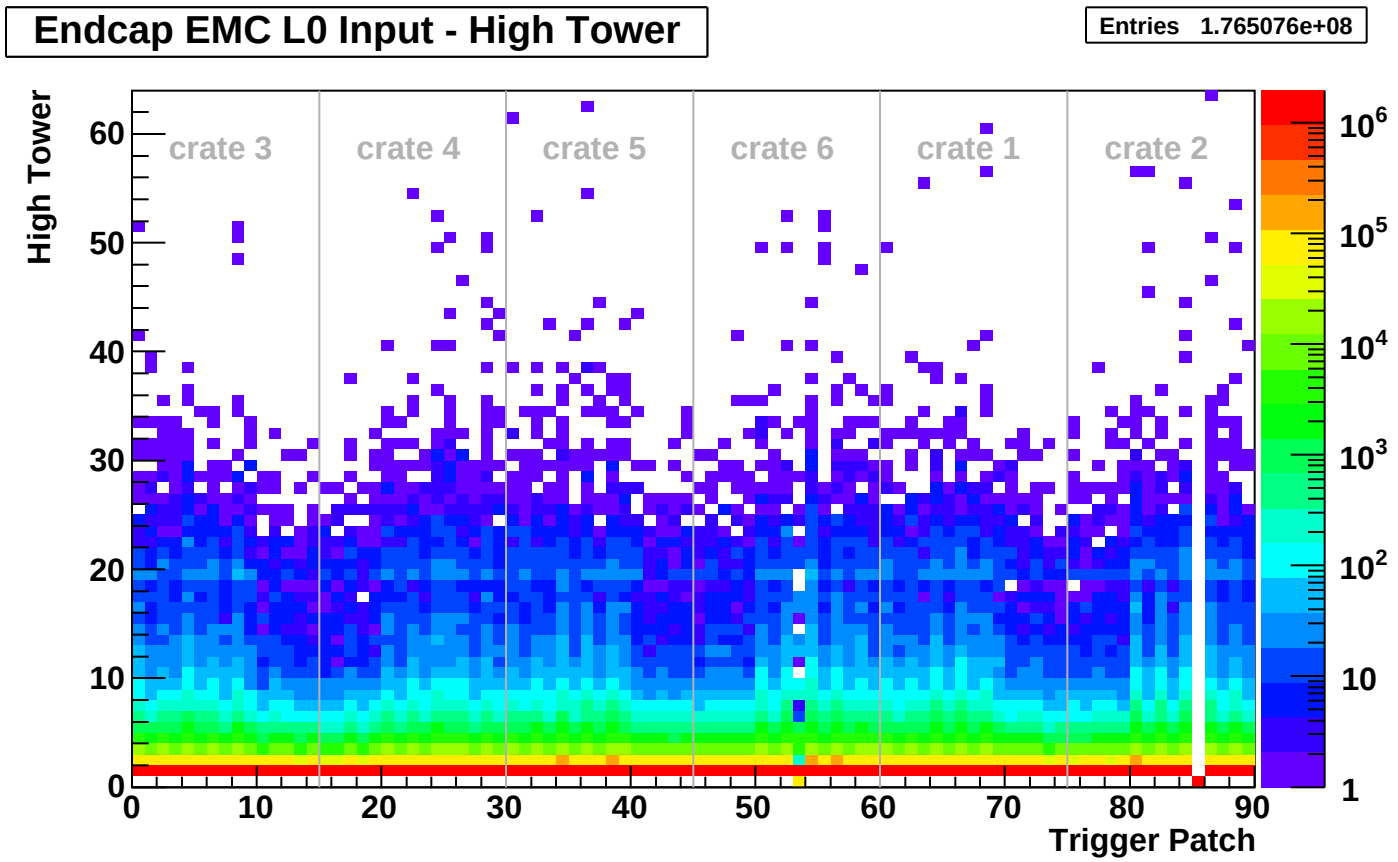

BunchId7Bit (all events)
Entries 1961195

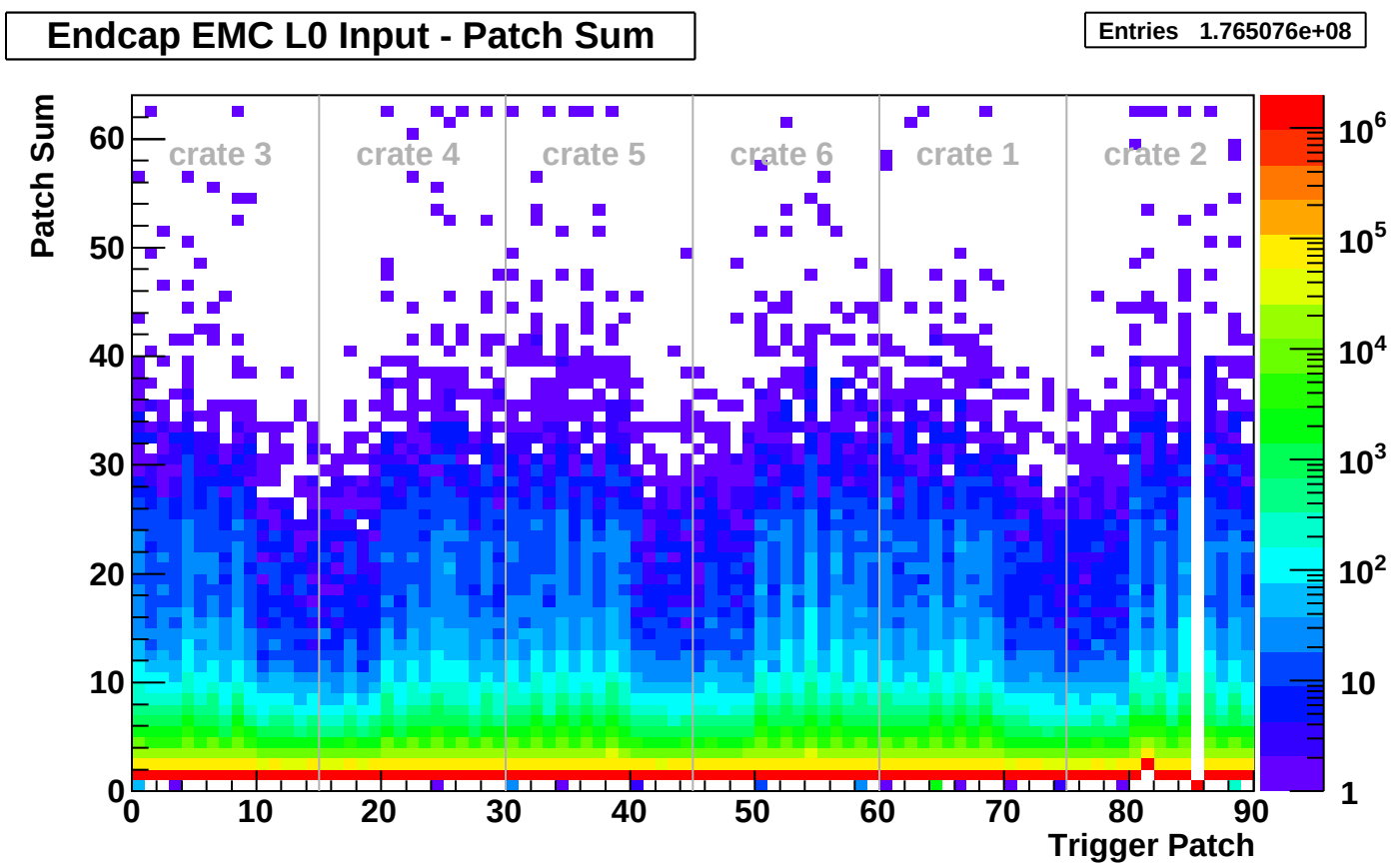

!Blue-Fill
Entries 125072

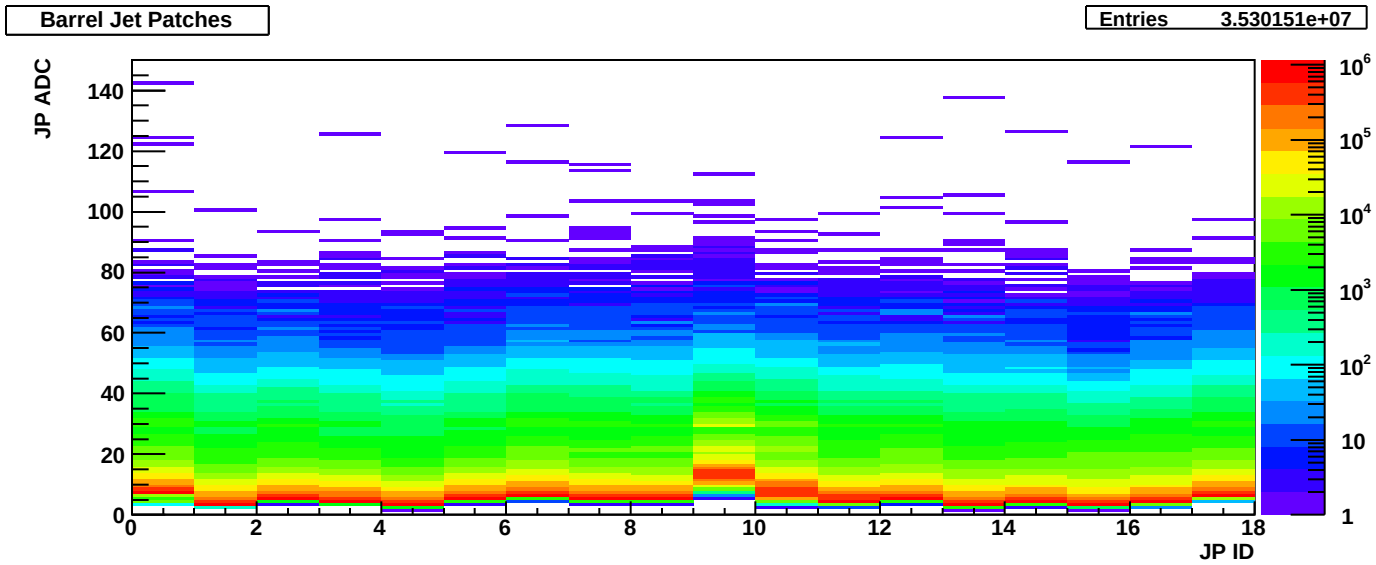

!Yellow-Fill
Entries 164444

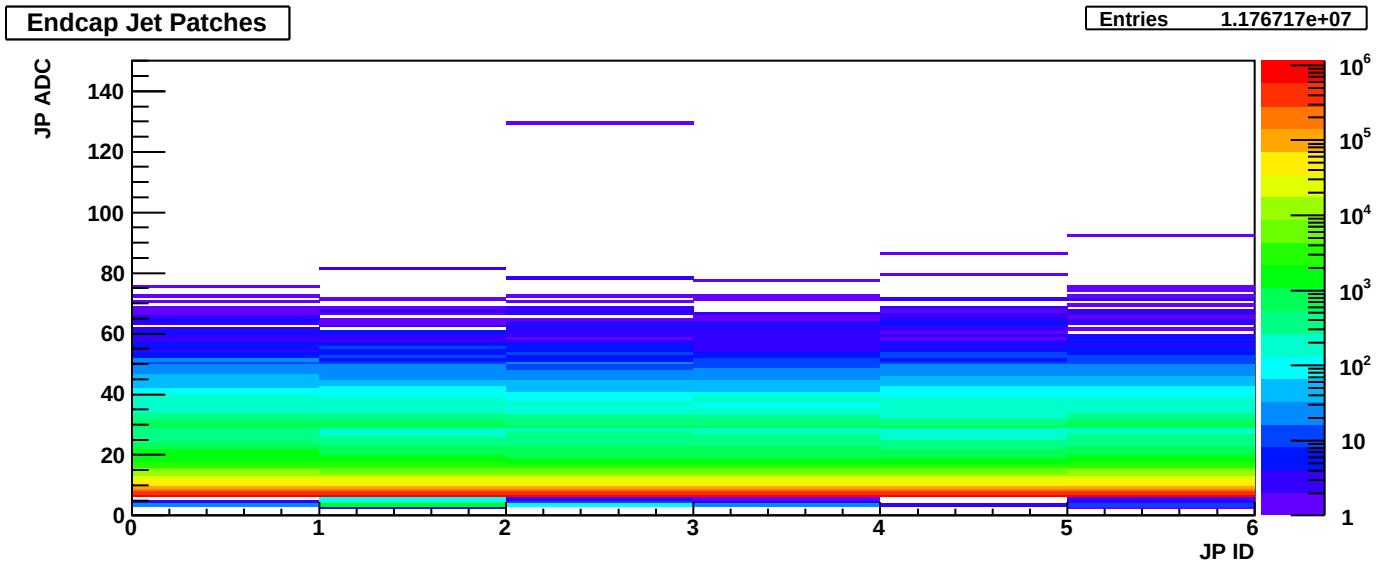

Barrel EMC L0 Input - High Tower
Entries 5.883585e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

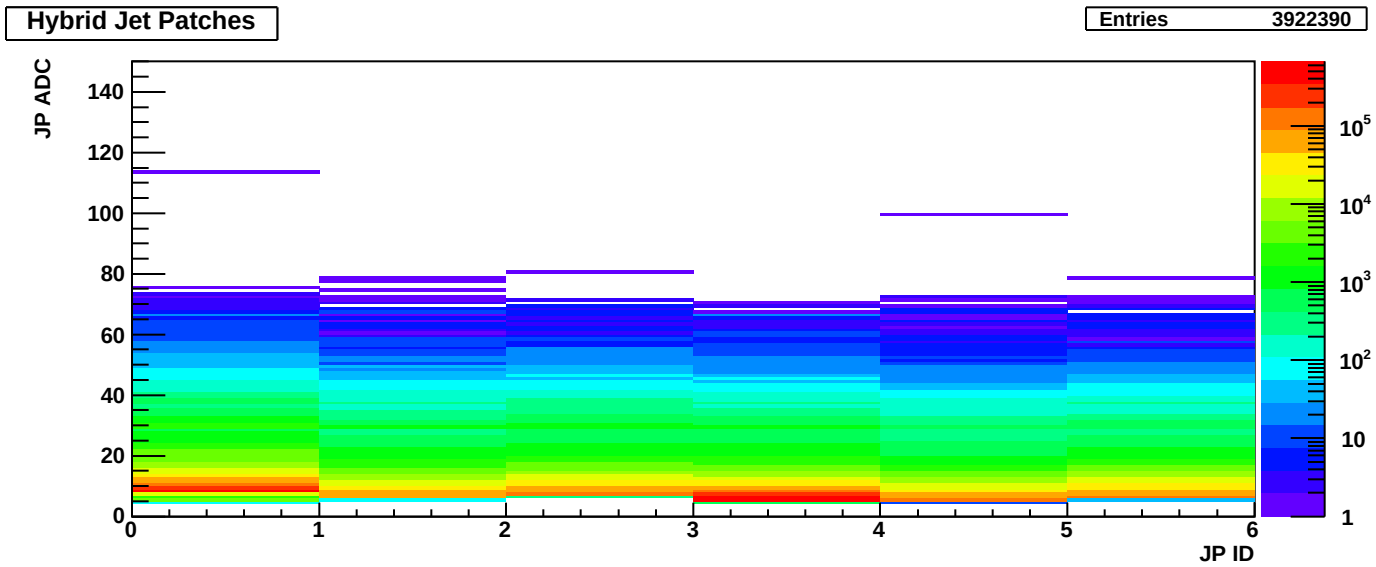

Barrel EMC L0 Input - Patch Sum
Entries 5.883585e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

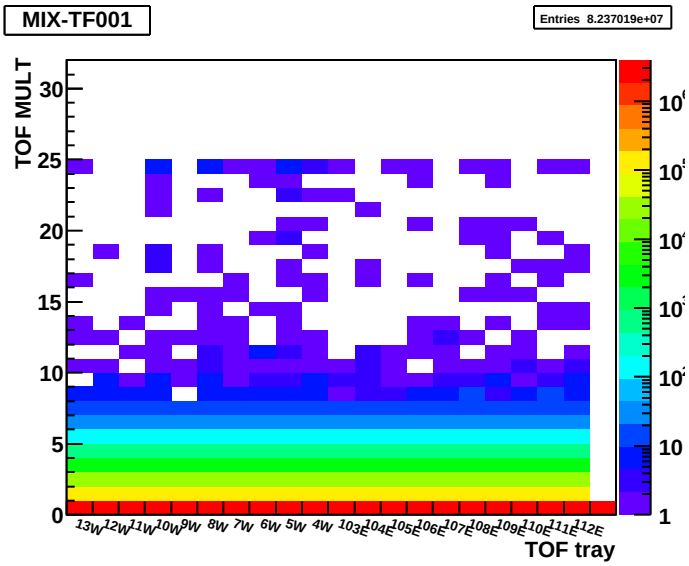

Endcap EMC L0 Input - High Tower
Entries 1.765076e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

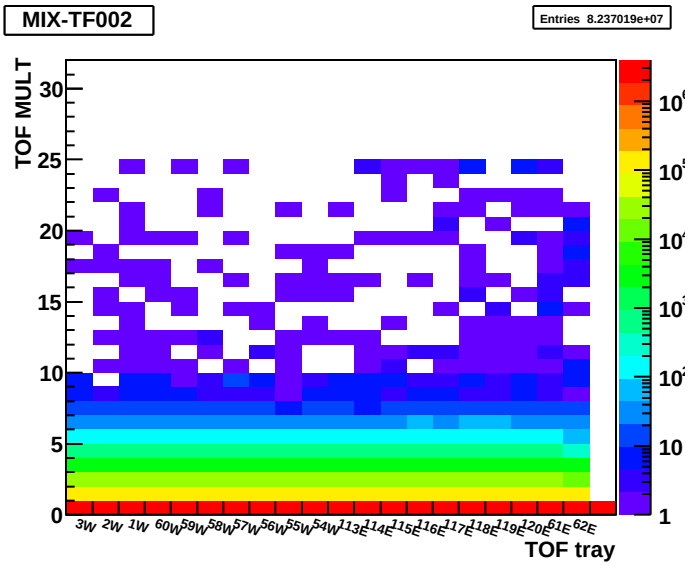

Endcap EMC L0 Input - Patch Sum
Entries 1.765076e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

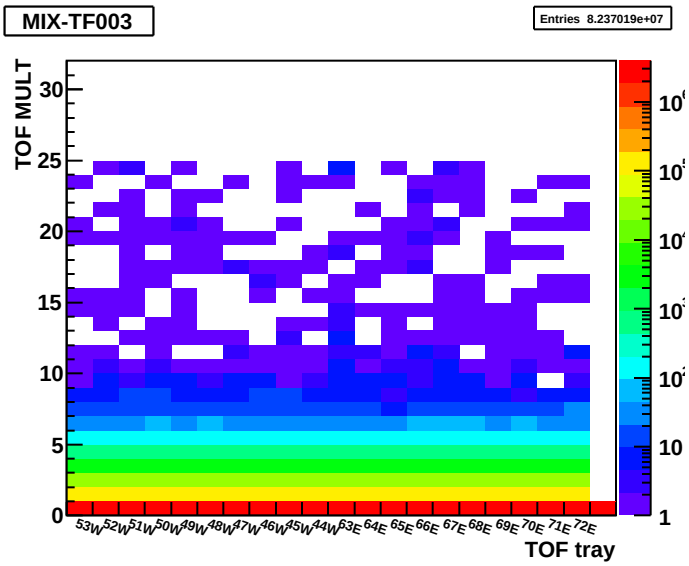

Barrel Jet Patches
Entries 3.530151e+07
JP ADC
JP ID

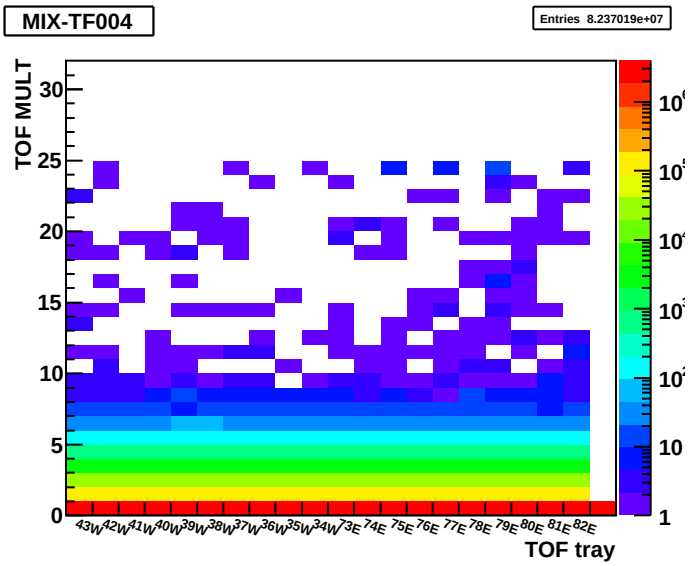

Endcap Jet Patches
Entries 1.176717e+07
JP ADC
JP ID

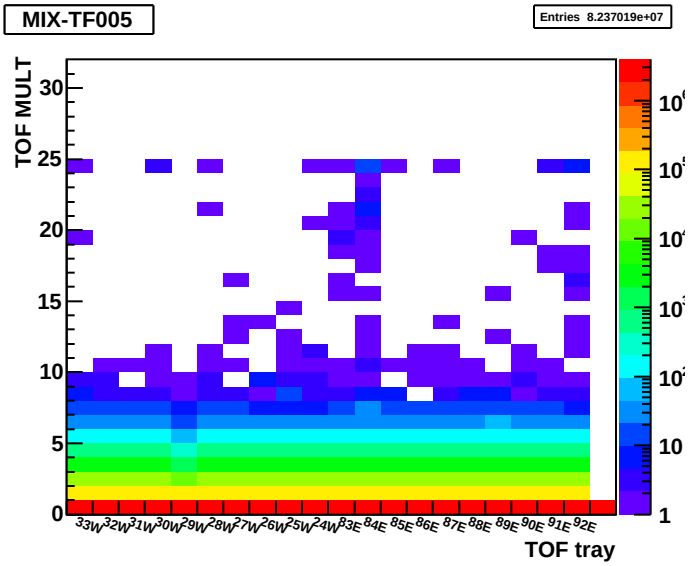

Hybrid Jet Patches
Entries 3922390
JP ADC
JP ID

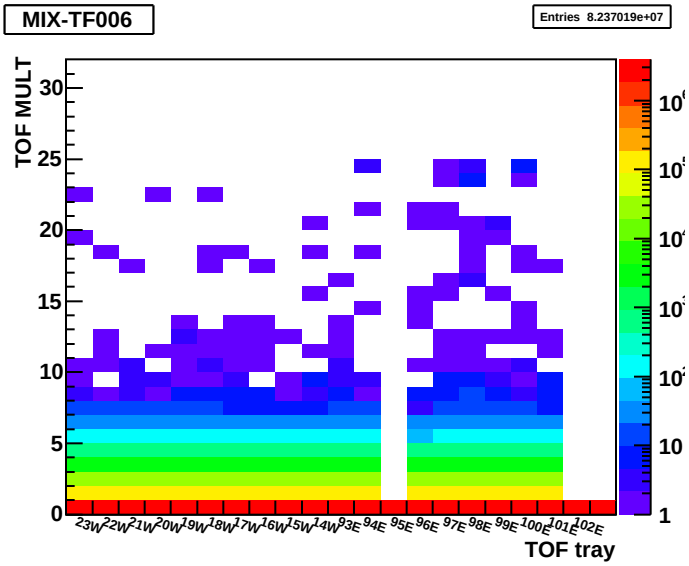

MIX-TF001
Entries 8.237019e+07
TOF MULT
TOF tray

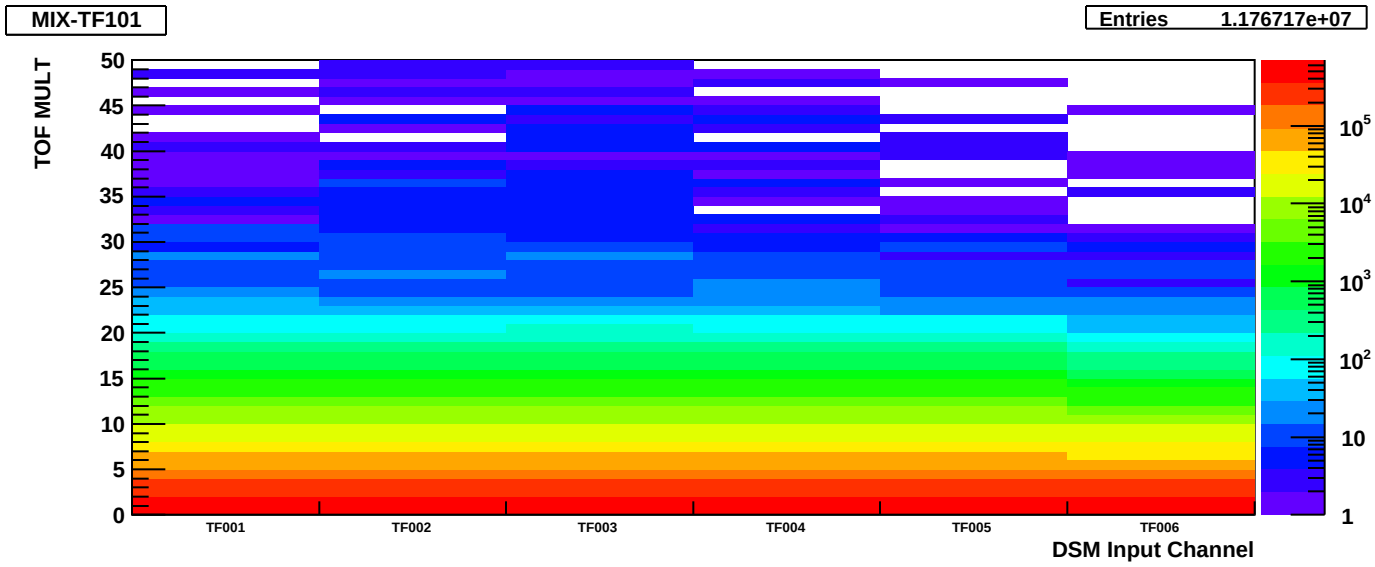

MIX-TF002
Entries 8.237019e+07
TOF MULT
TOF tray

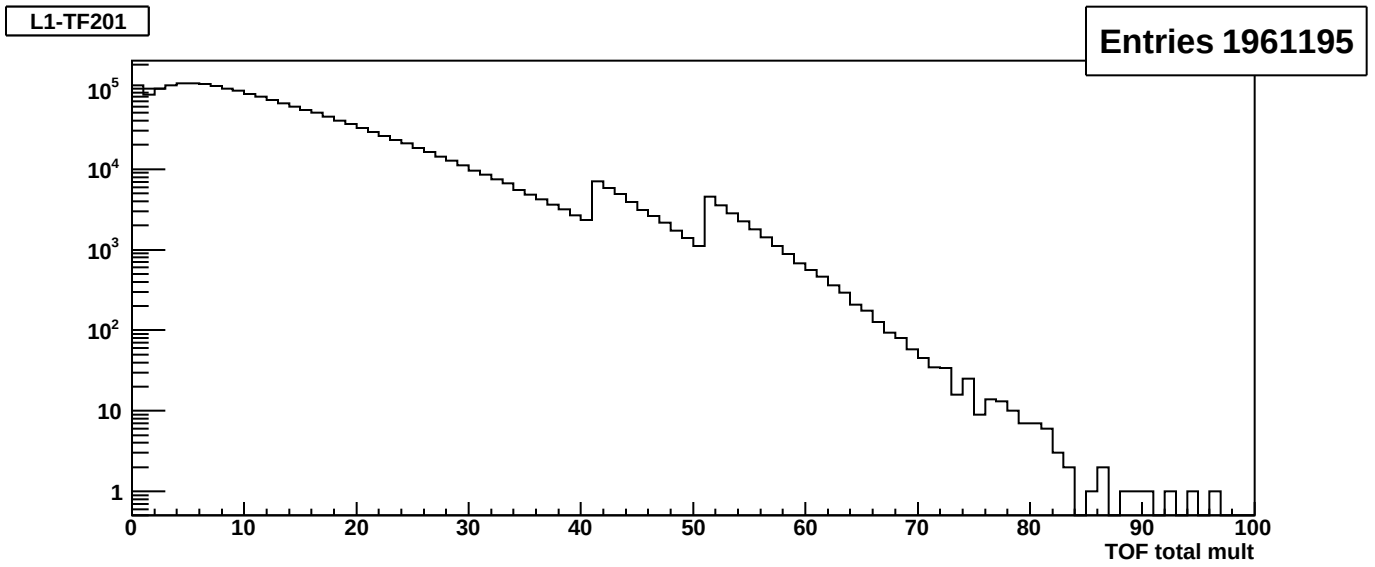

MIX-TF003
Entries 8.237019e+07
TOF MULT
TOF tray

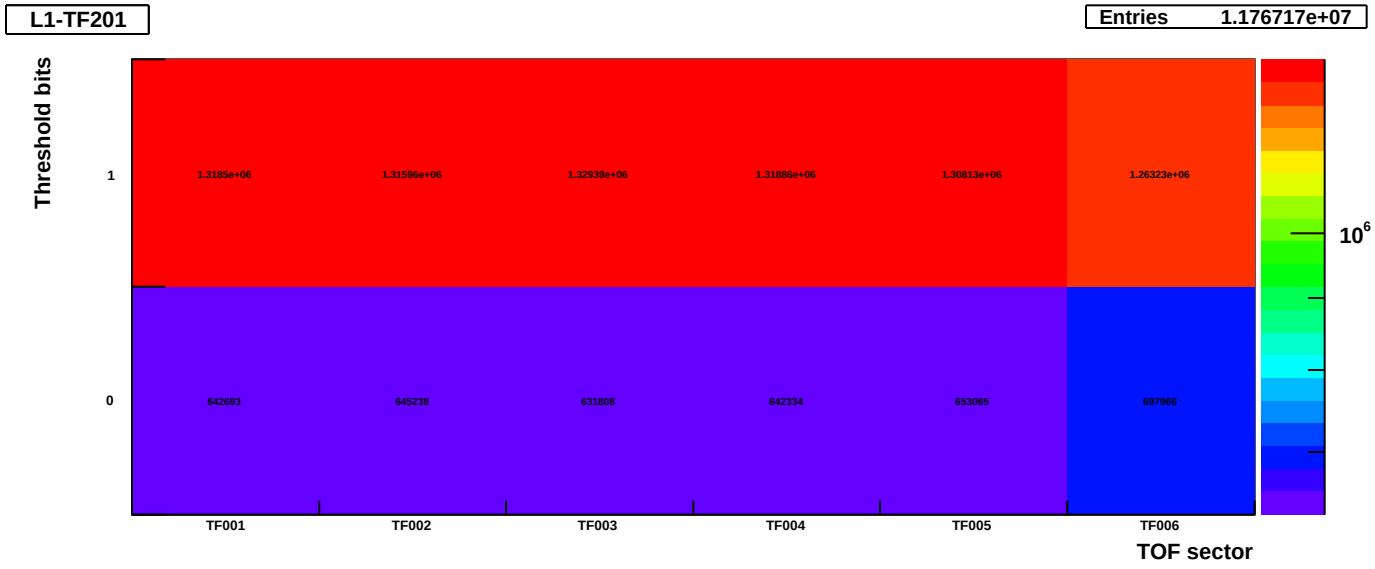

MIX-TF004
Entries 8.237019e+07
TOF MULT
TOF tray

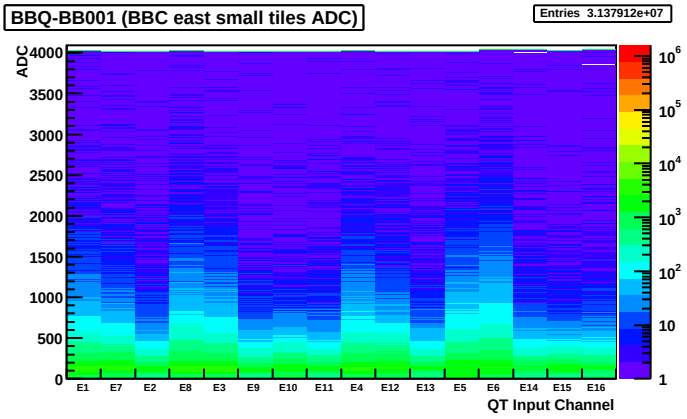

MIX-TF005
Entries 8.237019e+07
TOF MULT
TOF tray

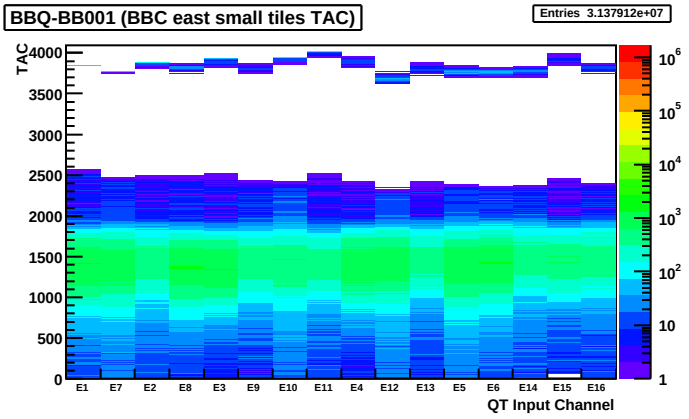

MIX-TF006
Entries 8.237019e+07
TOF MULT
TOF tray

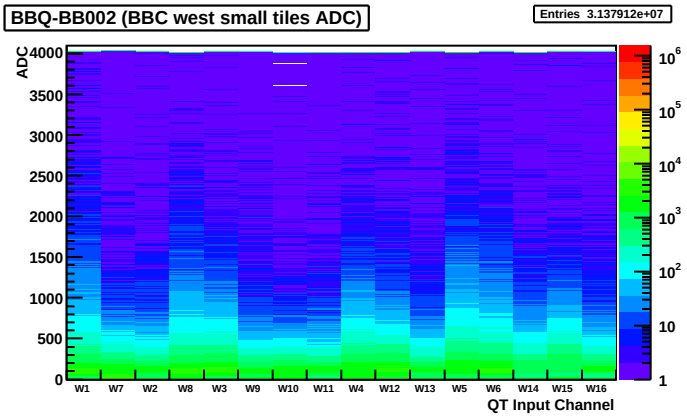

MIX-TF101
Entries 1.176717e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

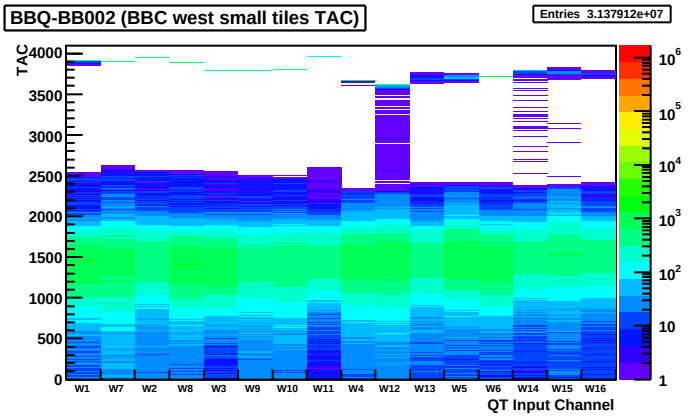

L1-TF201
Entries 1961195
TOF total mult

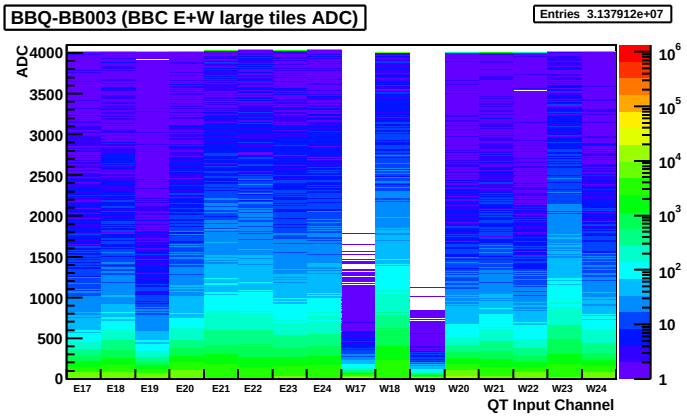

L1-TF201
Entries 1.176717e+07
Threshold bits
1.3185e+06
1.31596e+06
1.32939e+06
1.31886e+06
1.30813e+06
1.26323e+06
642693
645238
631808
642334
653065
697966
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

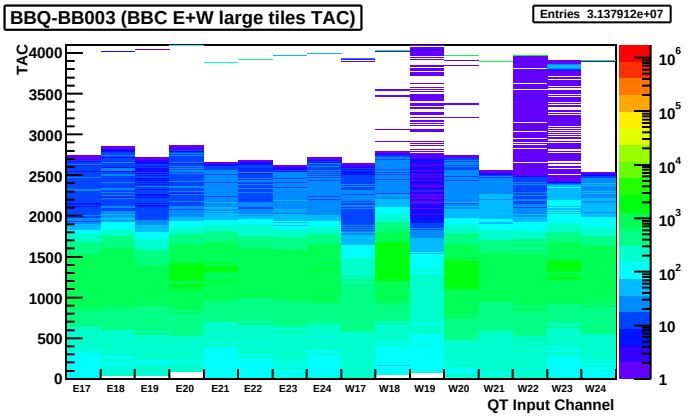

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.137912e+07
ADC
QT Input Channel

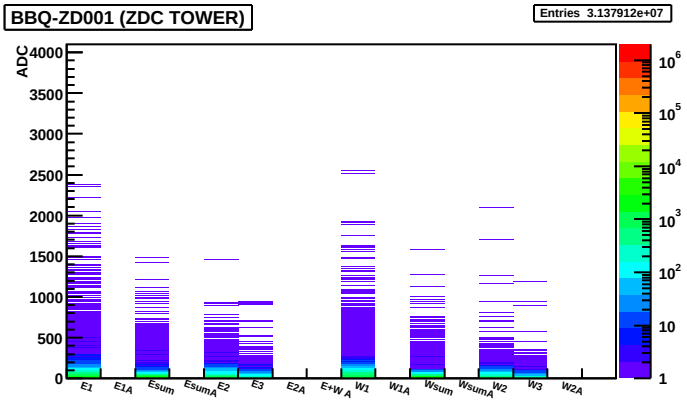

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.137912e+07
TAC
QT Input Channel

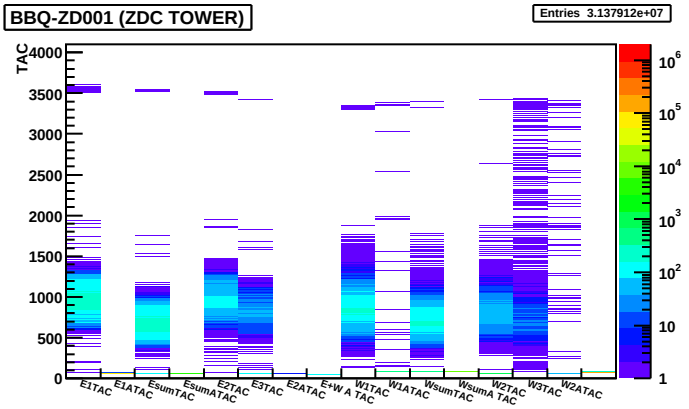

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.137912e+07
ADC
QT Input Channel

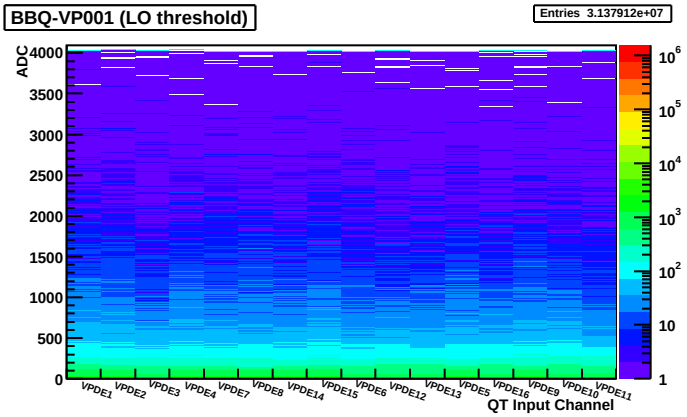

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.137912e+07
TAC
QT Input Channel

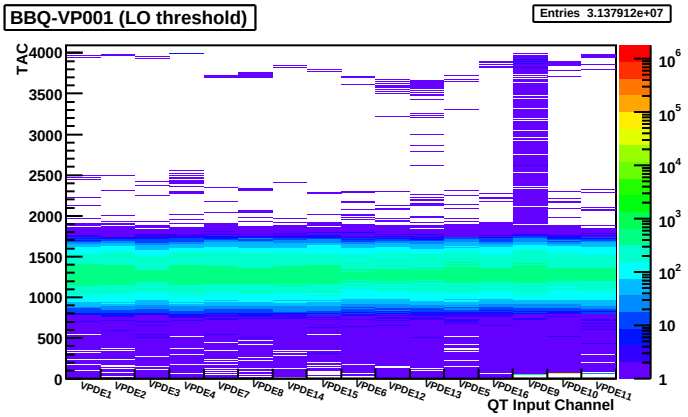

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.137912e+07
ADC
QT Input Channel

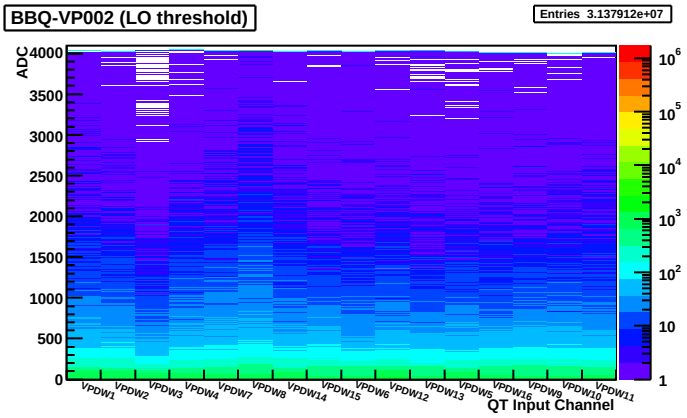

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.137912e+07
TAC
QT Input Channel

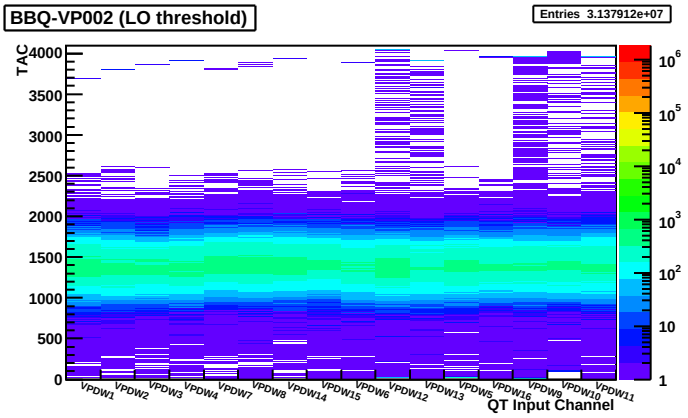

BBQ-ZD001 (ZDC TOWER)
Entries 3.137912e+07
ADC

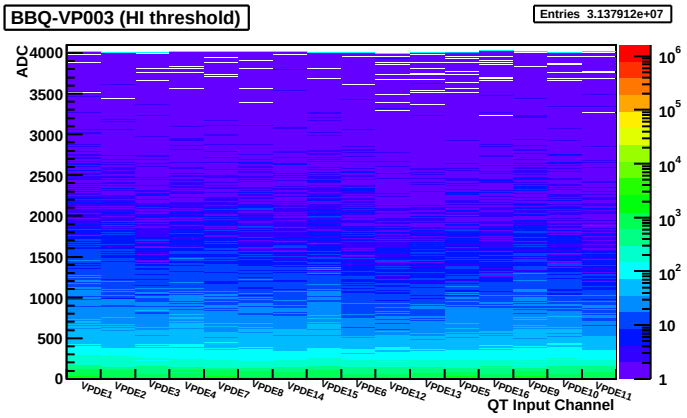

BBQ-ZD001 (ZDC TOWER)
Entries 3.137912e+07
TAC

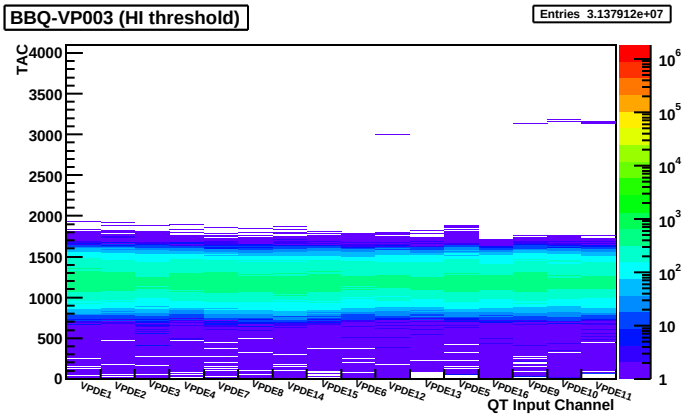

BBQ-VP001 (LO threshold)
Entries 3.137912e+07
ADC
QT Input Channel

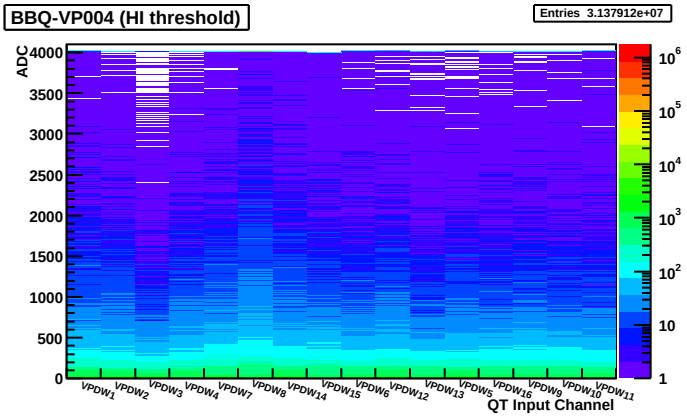

BBQ-VP001 (LO threshold)
Entries 3.137912e+07
TAC
QT Input Channel

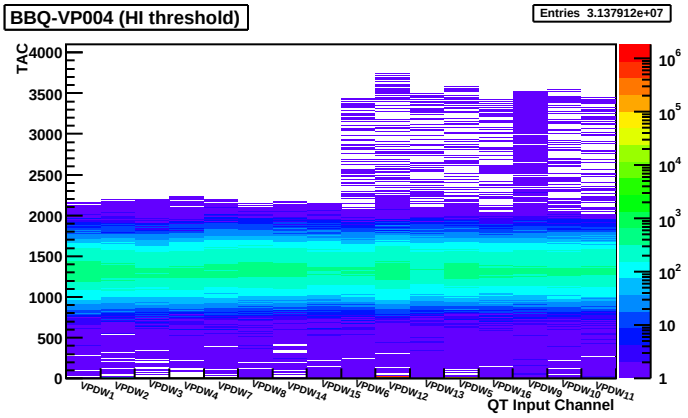

BBQ-VP002 (LO threshold)
Entries 3.137912e+07
ADC
QT Input Channel

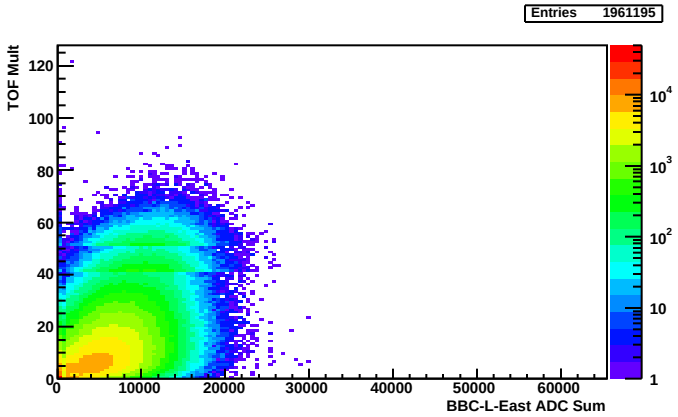

BBQ-VP002 (LO threshold)
Entries 3.137912e+07
TAC
QT Input Channel

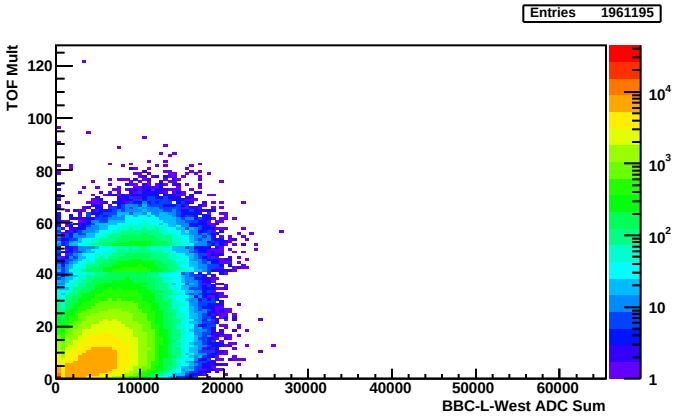

BBQ-VP003 (HI threshold)
Entries 3.137912e+07
ADC
QT Input Channel

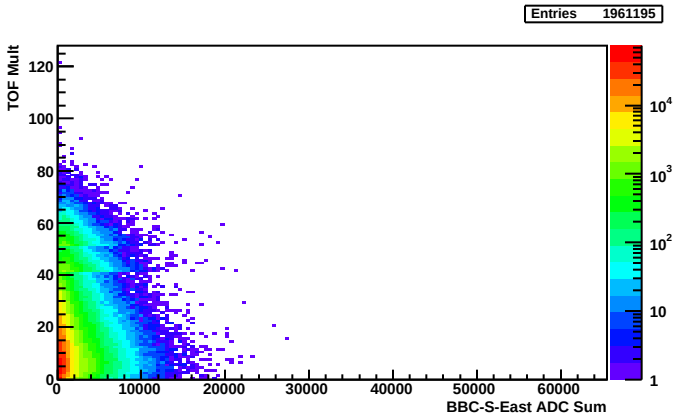

BBQ-VP003 (HI threshold)
Entries 3.137912e+07
TAC
QT Input Channel

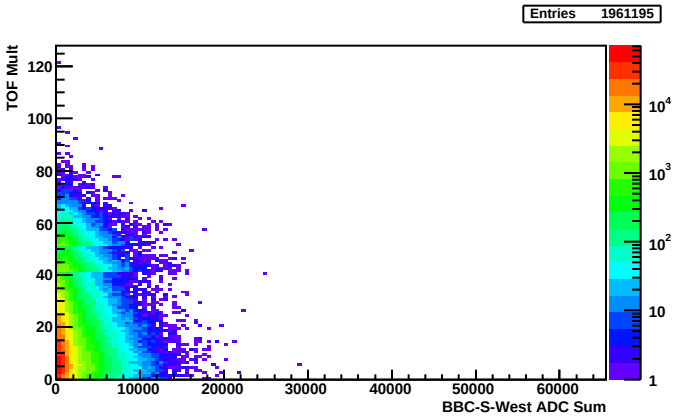

BBQ-VP004 (HI threshold)
Entries 3.137912e+07
ADC
QT Input Channel

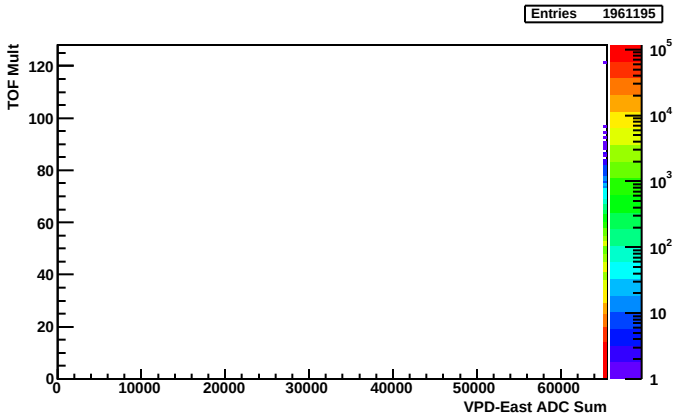

BBQ-VP004 (HI threshold)
Entries 3.137912e+07
TAC
QT Input Channel

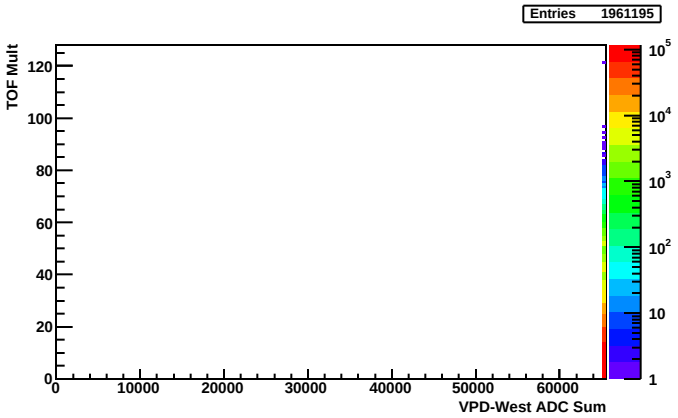

Entries 1961195
TOF Mult
BBC-L-East ADC Sum

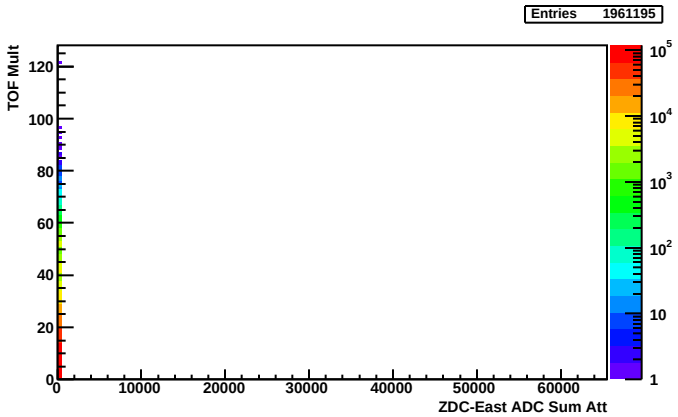

Entries 1961195
TOF Mult
BBC-L-West ADC Sum

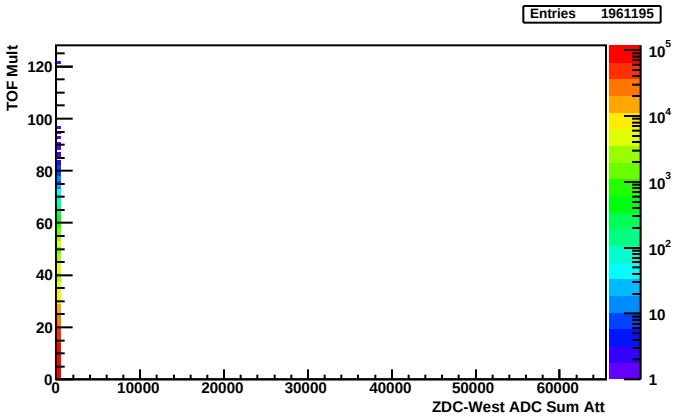

Entries 1961195
TOF Mult
BBC-S-East ADC Sum

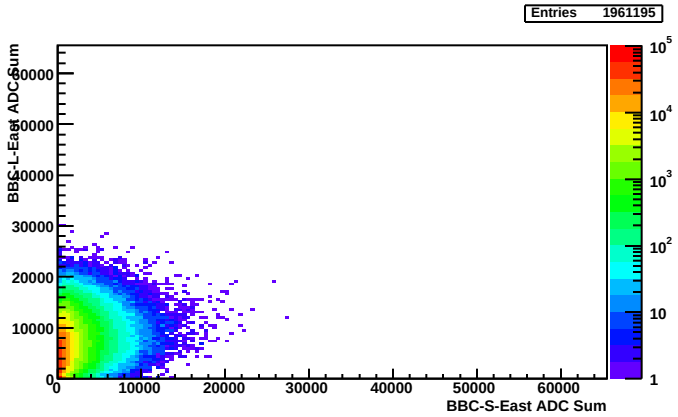

Entries 1961195
TOF Mult
BBC-S-West ADC Sum

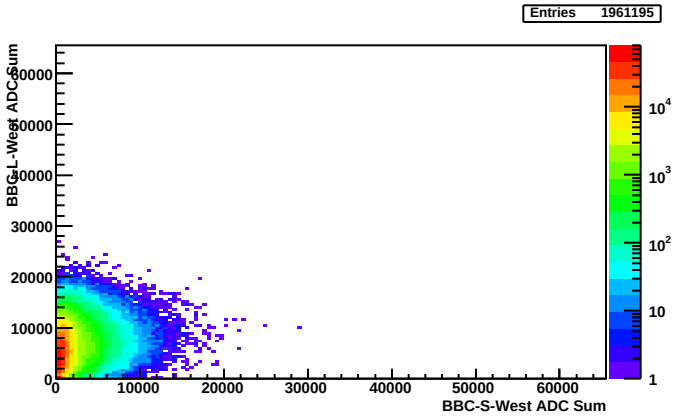

Entries 1961195
TOF Mult
VPD-East ADC Sum

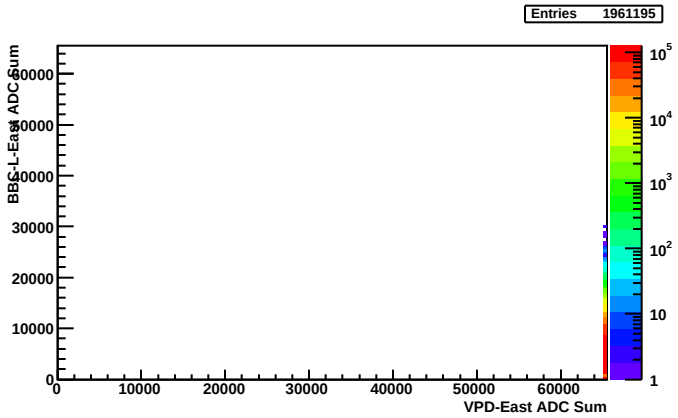

Entries 1961195
TOF Mult
VPD-West ADC Sum

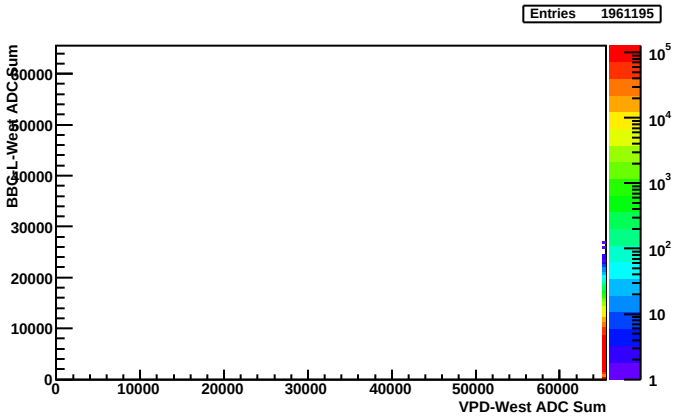

Entries 1961195
TOF Mult
ZDC-East ADC Sum Att

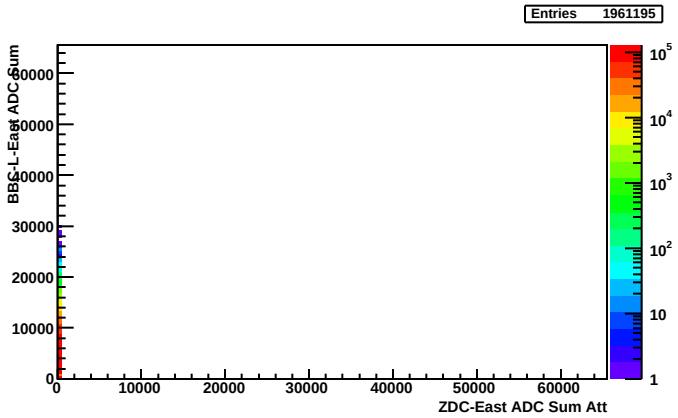

Entries 1961195
TOF Mult
ZDC-West ADC Sum Att

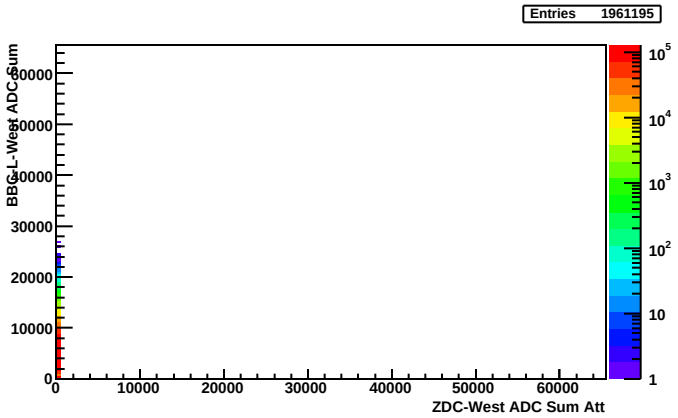

Entries 1961195
BBC-L-East ADC Sum
BBC-S-East ADC Sum

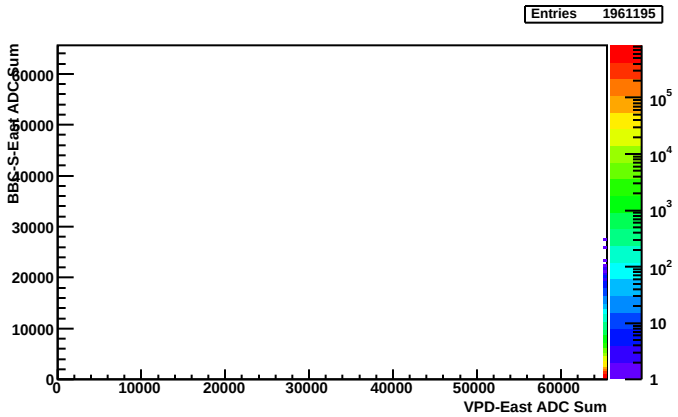

Entries 1961195
BBC-L-West ADC Sum
BBC-S-West ADC Sum

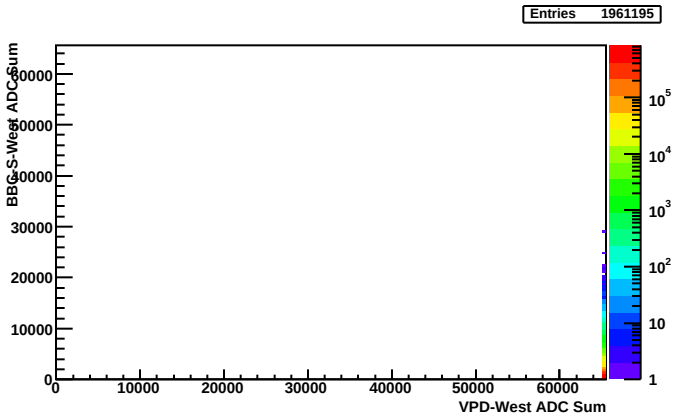

Entries 1961195
BBC-L-East ADC Sum
VPD-East ADC Sum

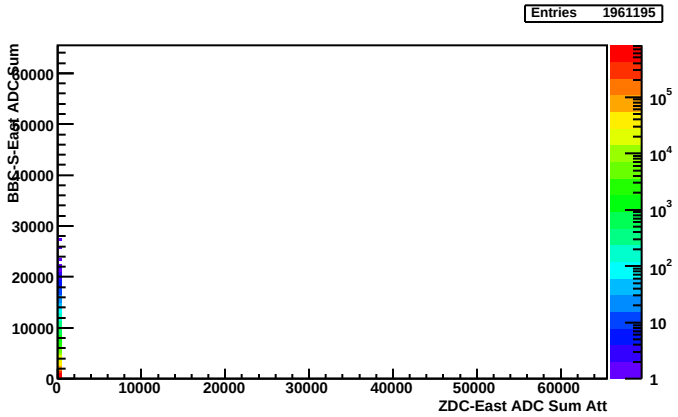

Entries 1961195
BBC-L-West ADC Sum
VPD-West ADC Sum

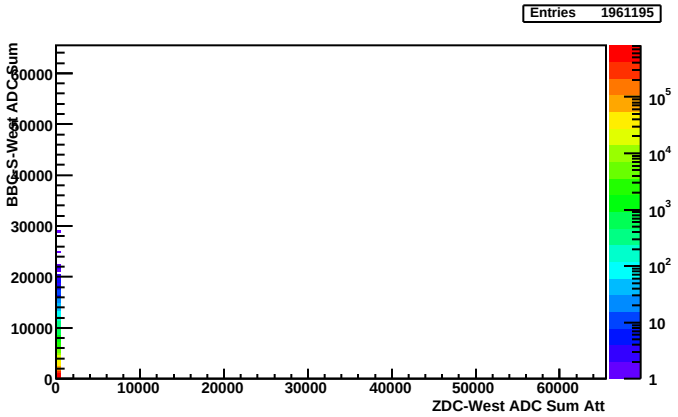

Entries 1961195
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

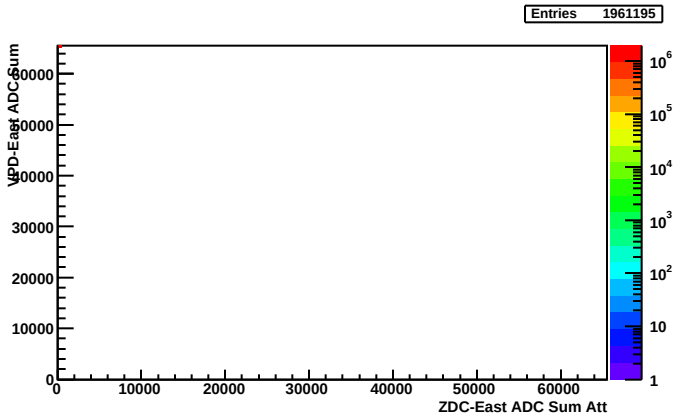

Entries 1961195
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

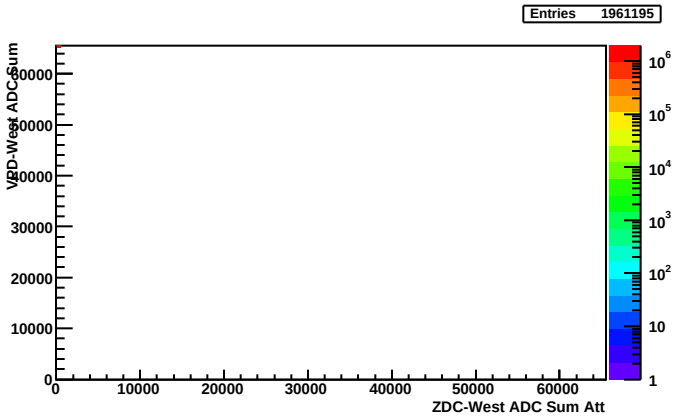

Entries 1961195
BBC-S-East ADC Sum
VPD-East ADC Sum

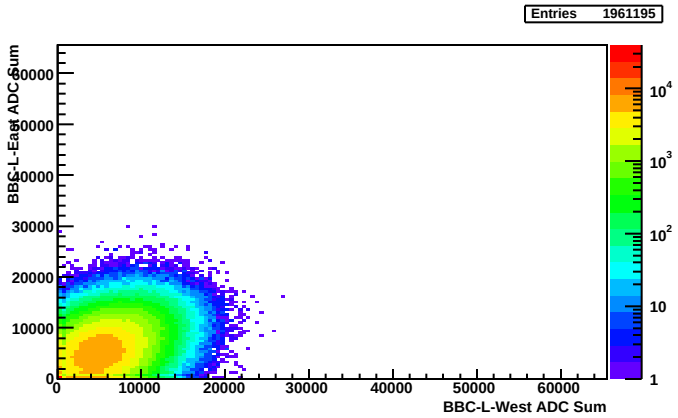

Entries 1961195
BBC-S-West ADC Sum
VPD-West ADC Sum

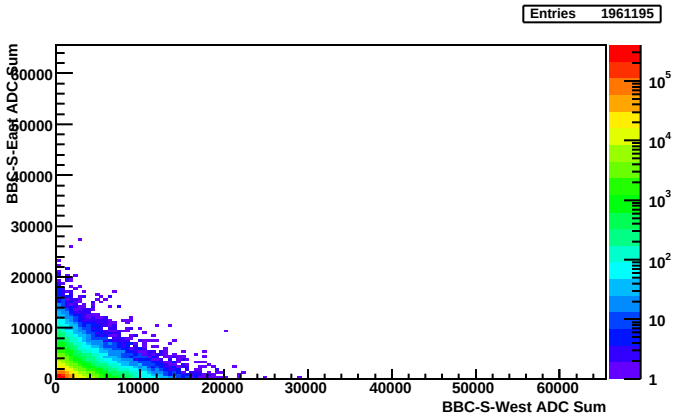

Entries 1961195
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

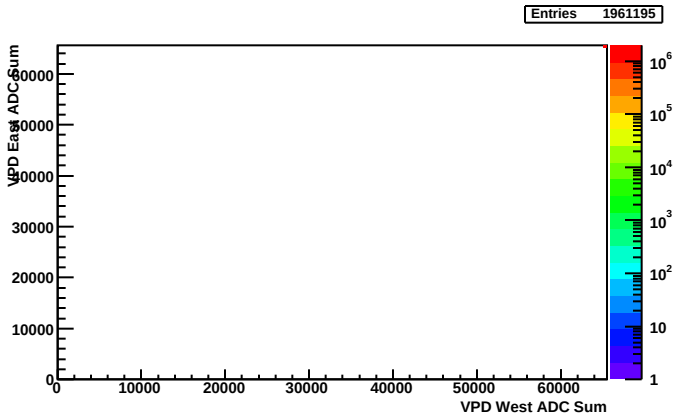

Entries 1961195
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

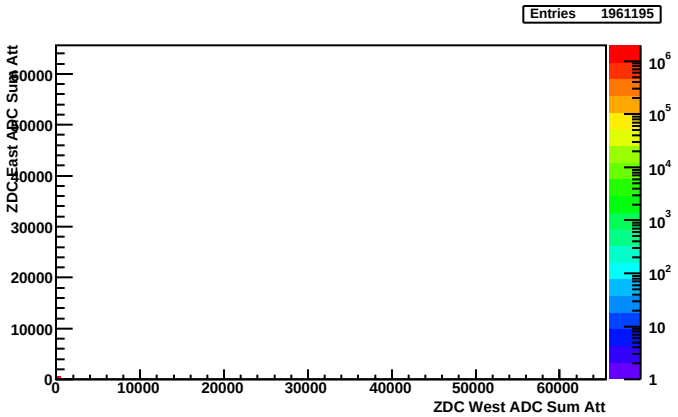

Entries 1961195
VPD-East ADC Sum
ZDC-East ADC Sum Att

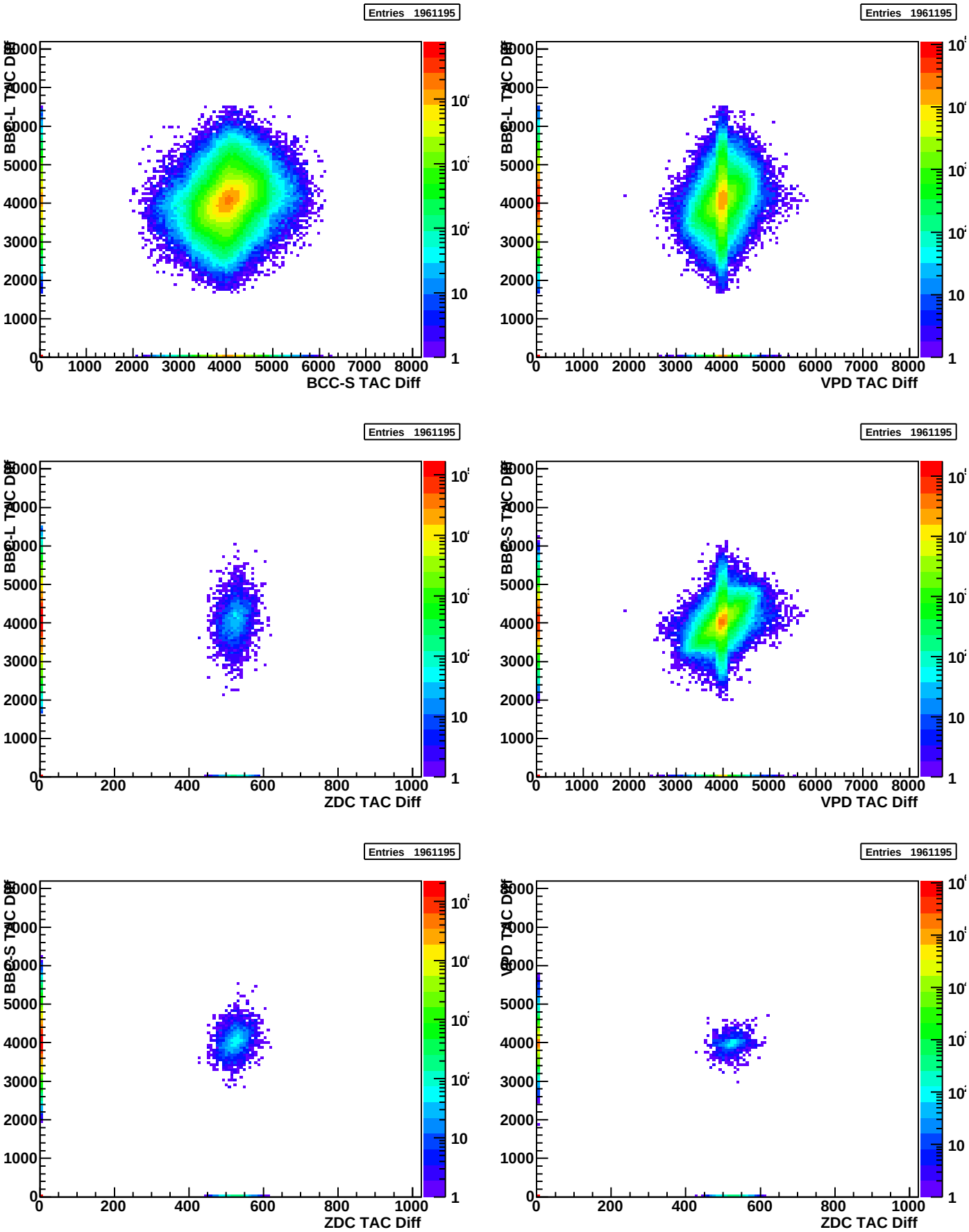

Entries 1961195
VPD-West ADC Sum
ZDC-West ADC Sum Att

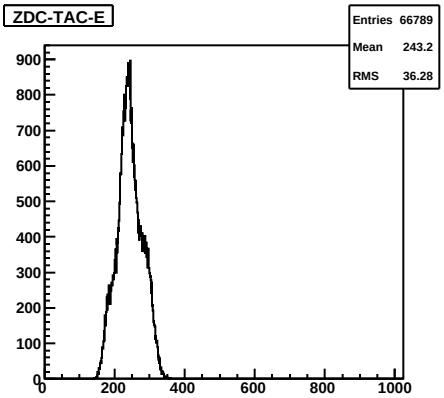

Entries 1961195
BBC-L-East ADC Sum
BBC-L-West ADC Sum

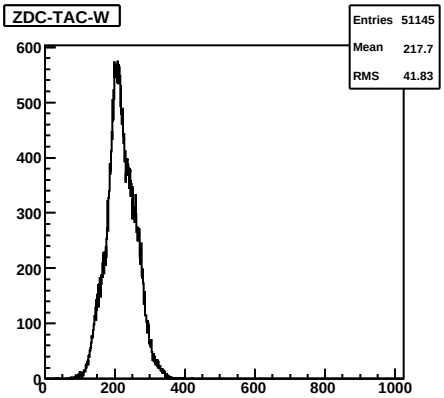

Entries 1961195
BBC-S-East ADC Sum
BBC-S-West ADC Sum

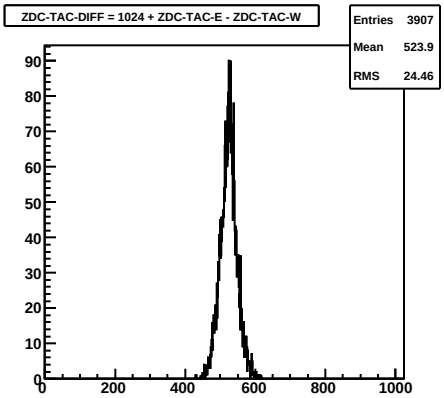

Entries 1961195
VPD East ADC Sum
VPD West ADC Sum

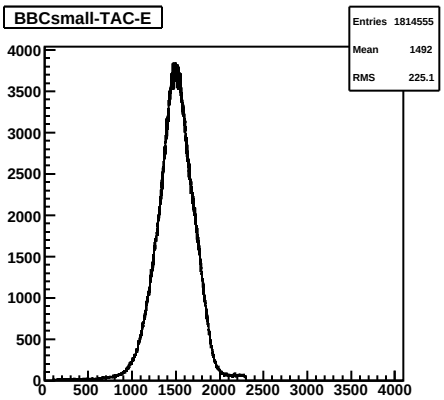

Entries 1961195
ZDC East ADC Sum Att
ZDC West ADC Sum Att

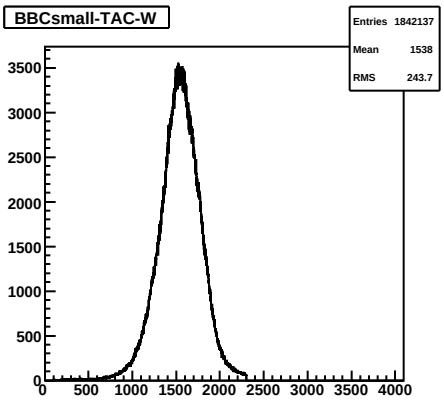

Entries 1961195
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1961195
BBC-L TAC Diff
VPD TAC Diff
Entries 1961195
BBC-L TAC Diff
ZDC TAC Diff
Entries 1961195
BBC-S TAC Diff
VPD TAC Diff
Entries 1961195
BBC-S TAC Diff
ZDC TAC Diff
Entries 1961195
VPD TAC Diff
ZDC TAC Diff

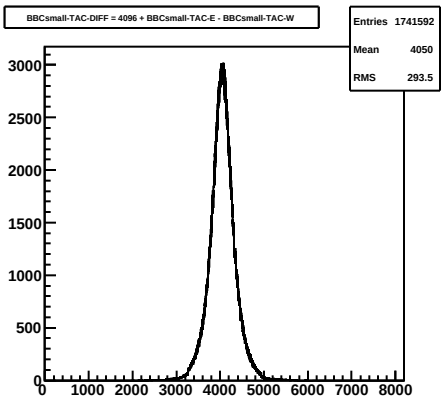

ZDC-TAC-E
Entries 66789
Mean 243.2
RMS 36.28

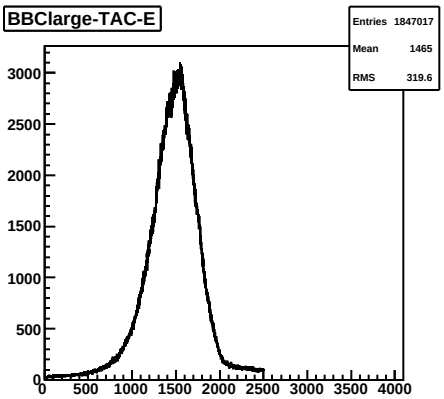

ZDC-TAC-W
Entries 51145
Mean 217.7
RMS 41.83

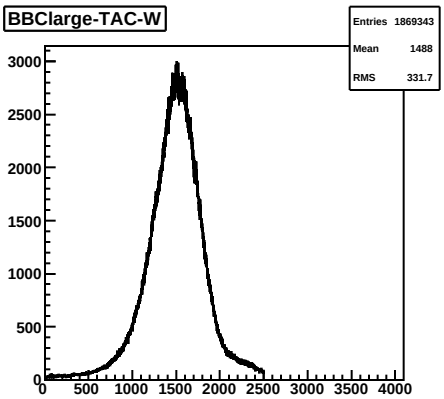

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3907
Mean 523.9
RMS 24.46

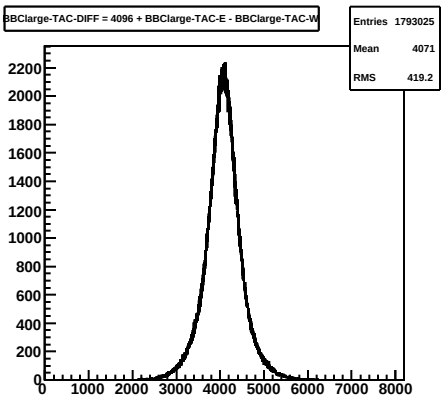

BBCsmall-TAC-E
Entries 1814555
Mean 1492
RMS 225.1

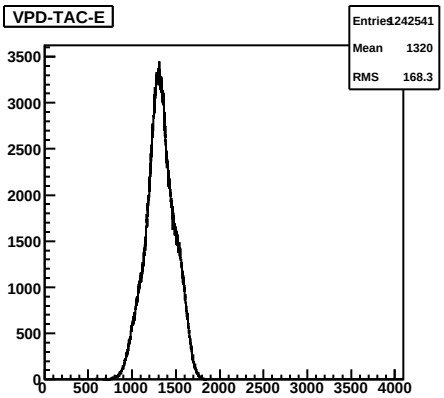

BBCsmall-TAC-W
Entries 1842137
Mean 1538
RMS 243.7

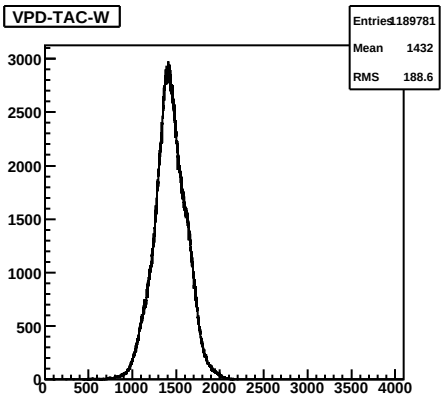

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1741592
Mean 4050
RMS 293.5

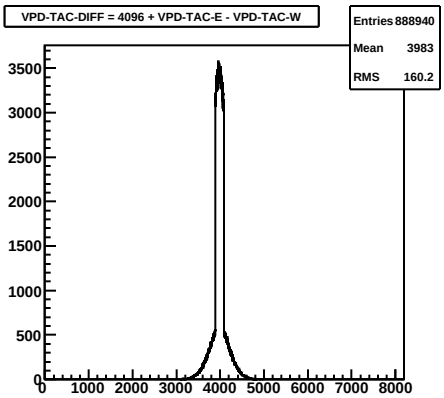

BBClarge-TAC-E
Entries 1847017
Mean 1465
RMS 319.6

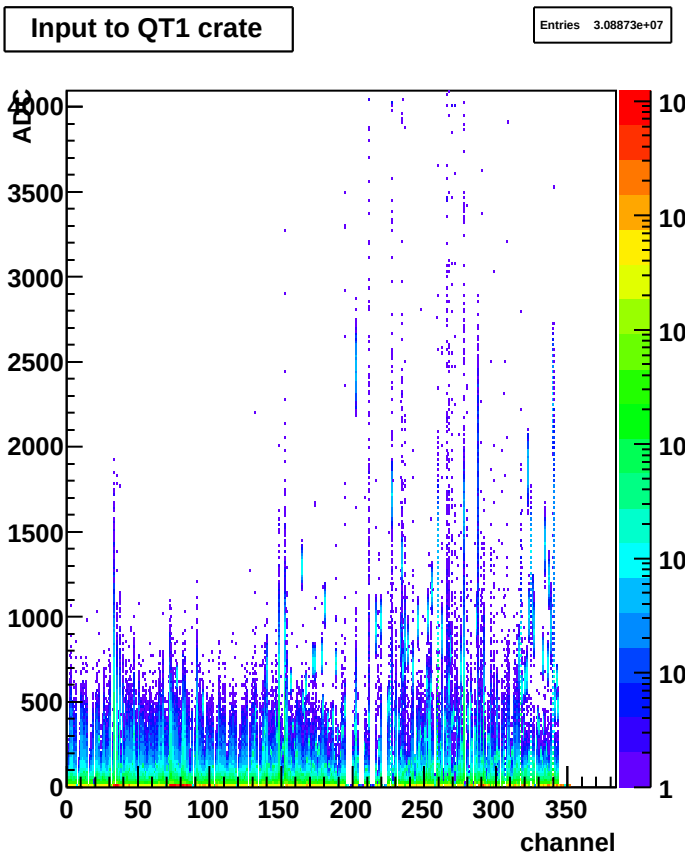

BBClarge-TAC-W
Entries 1869343
Mean 1488
RMS 331.7

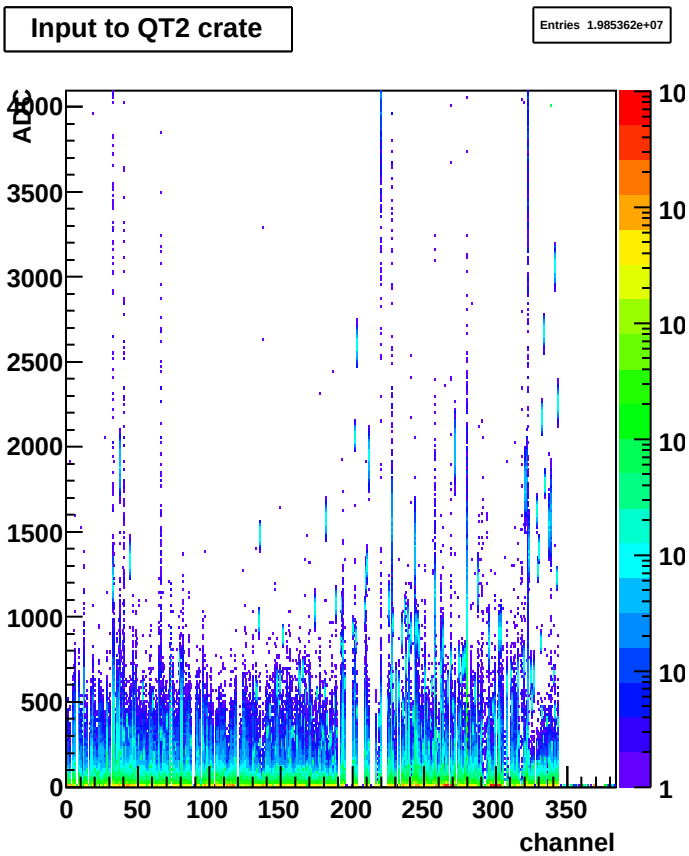

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1793025
Mean 4071
RMS 419.2

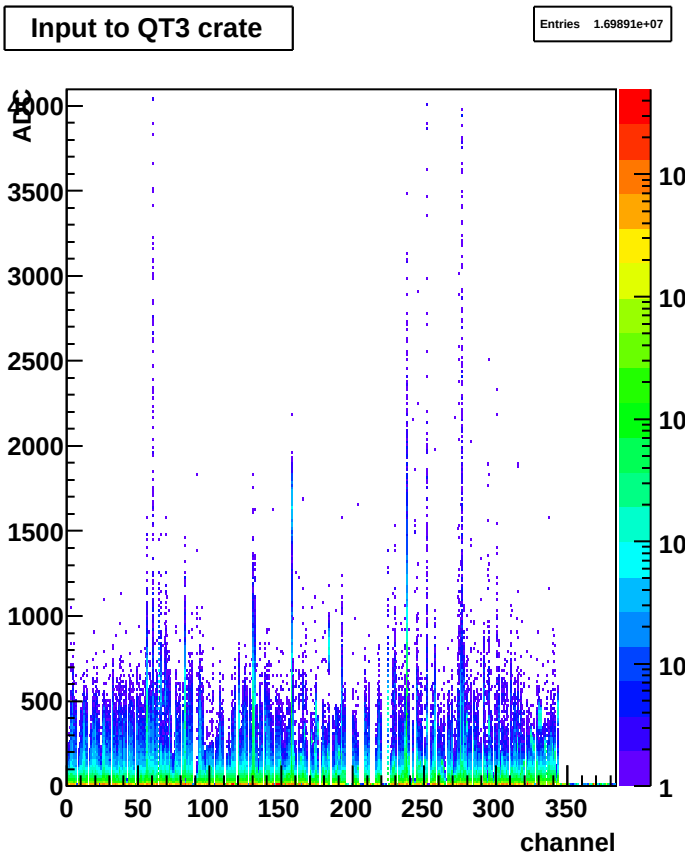

VPD-TAC-E
Entries 1242541
Mean 1320
RMS 168.3

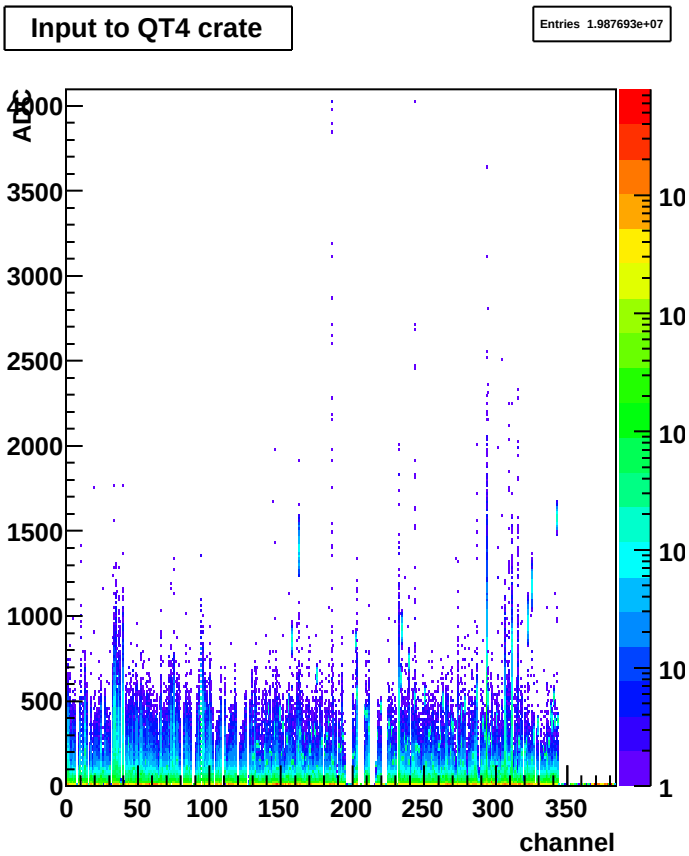

VPD-TAC-W
Entries 1189781
Mean 1432
RMS 188.6

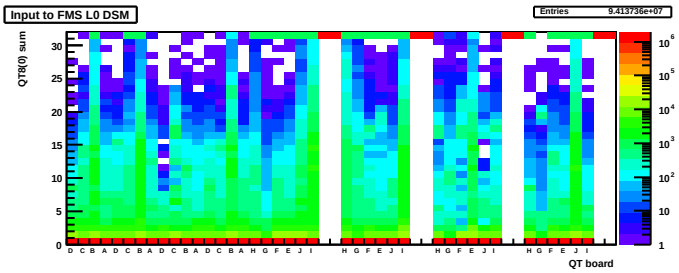

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 888940
Mean 3983
RMS 160.2

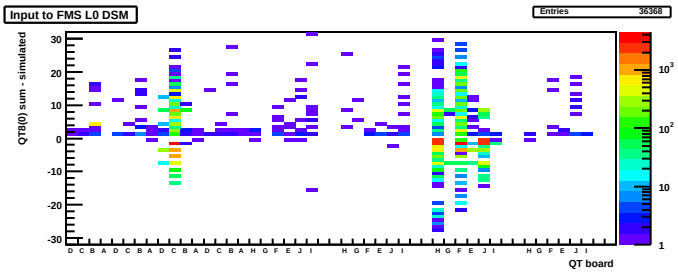

Input to QT1 crate
Entries 3.08873e+07
ADC
channel

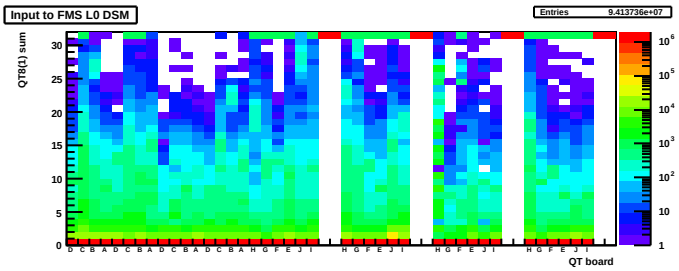

Input to QT2 crate
Entries 1.985362e+07
ADC
channel

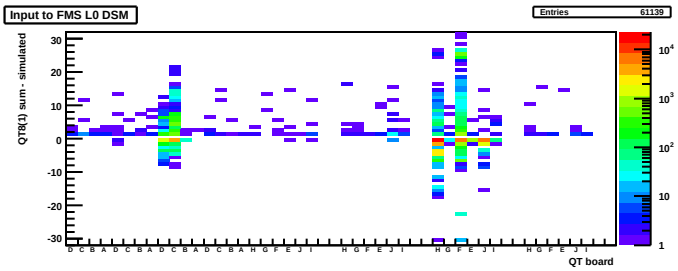

Input to QT3 crate
Entries 1.69891e+07
ADC
channel

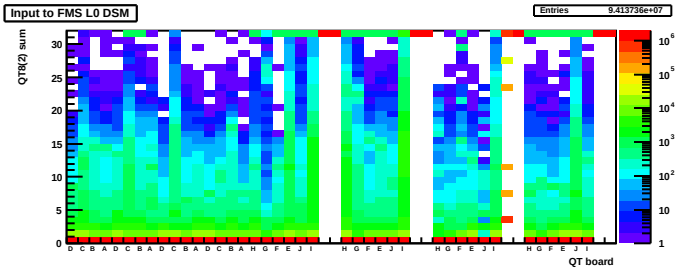

Input to QT4 crate
Entries 1.987693e+07
ADC
channel

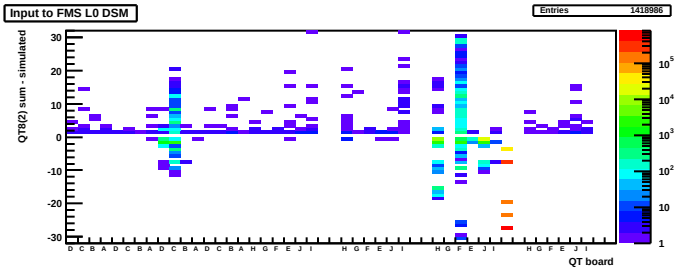

Input to FMS L0 DSM
Entries 9.413736e+07
QT8(0) sum
QT board

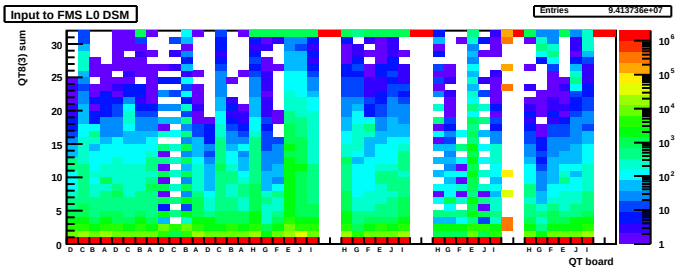

Input to FMS L0 DSM
Entries 36368
QT8(0) sum - simulated
QT board

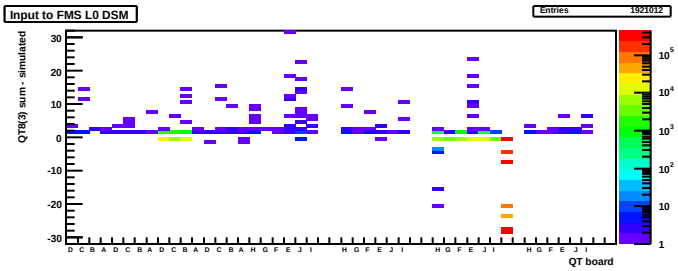

Input to FMS L0 DSM
Entries 9.413736e+07
QT8(1) sum
QT board

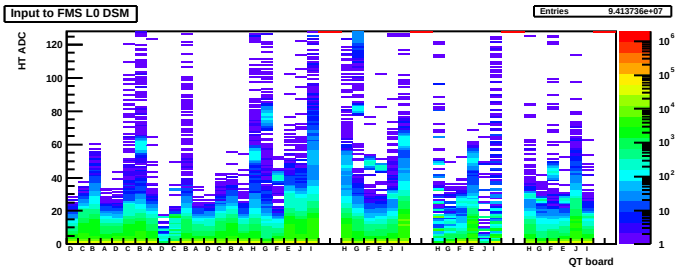

Input to FMS L0 DSM
Entries 61139
QT8(1) sum - simulated
QT board

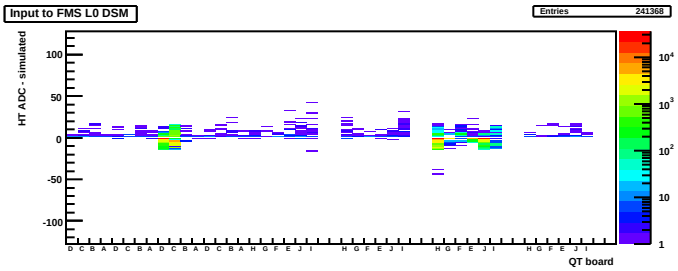

Input to FMS L0 DSM
Entries 9.413736e+07
QT8(2) sum
QT board

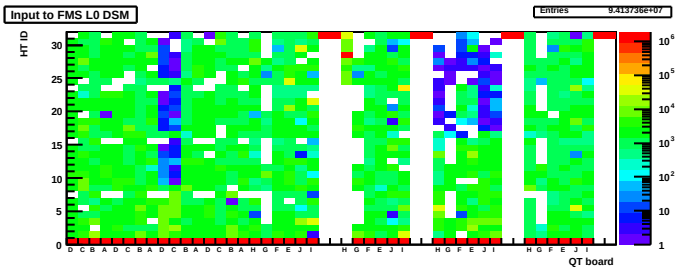

Input to FMS L0 DSM
Entries 1418986
QT8(2) sum - simulated
QT board

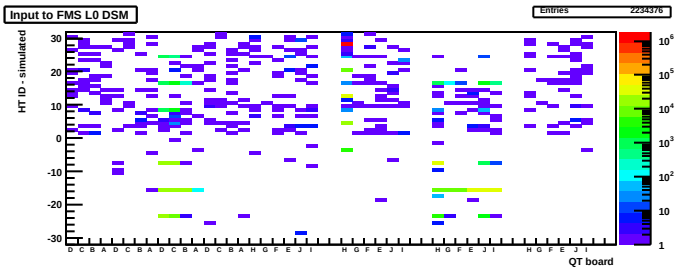

Input to FMS L0 DSM
Entries 9.413736e+07
QT8(3) sum
QT board

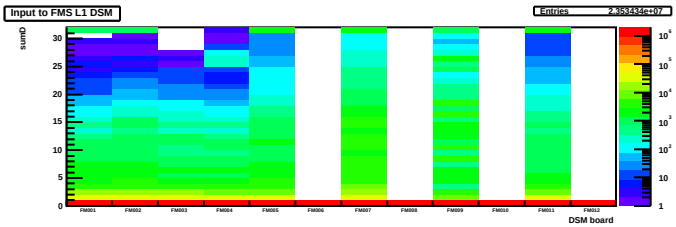

Input to FMS L0 DSM
Entries 1921012
QT8(3) sum - simulated
QT board

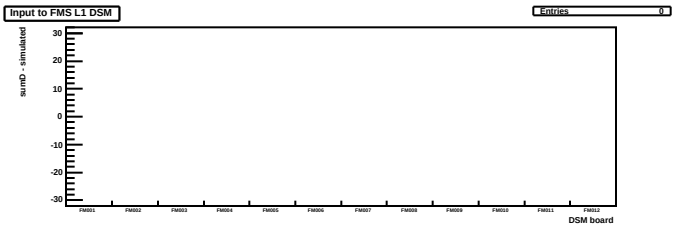

Input to FMS L0 DSM
Entries 9.413736e+07
HT ADC
QT board

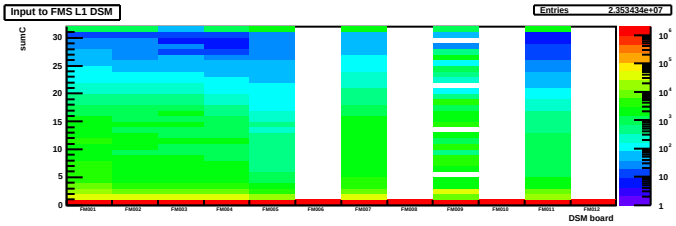

Input to FMS L0 DSM
Entries 241368
HT ADC - simulated
QT board

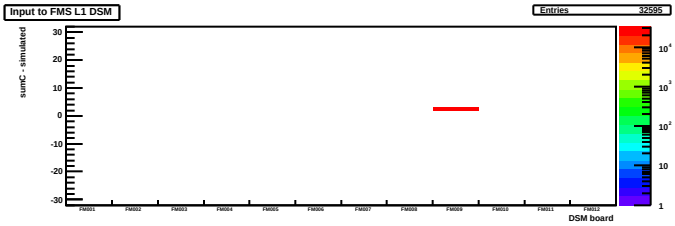

Input to FMS L0 DSM
Entries 9.413736e+07
HT ID
QT board

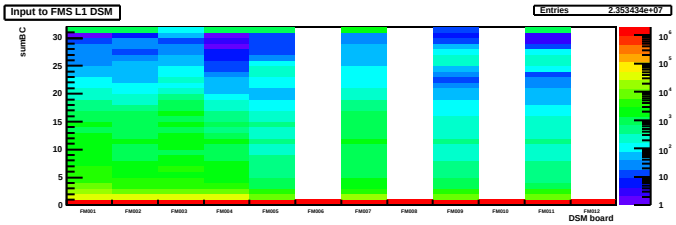

Input to FMS L0 DSM
Entries 2234376
HT ID - simulated
QT board

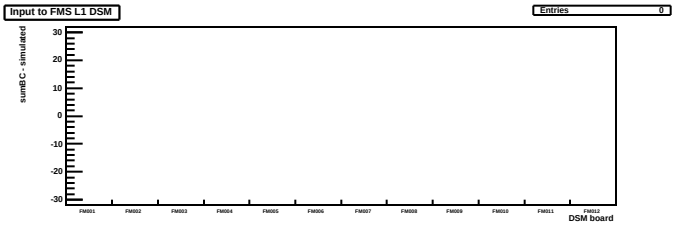

Input to FMS L1 DSM
Entries 2.353434e+07
sumD
DSM board

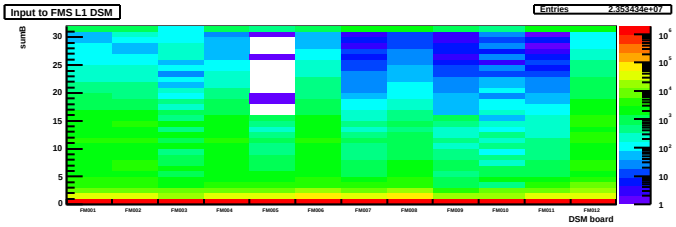

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

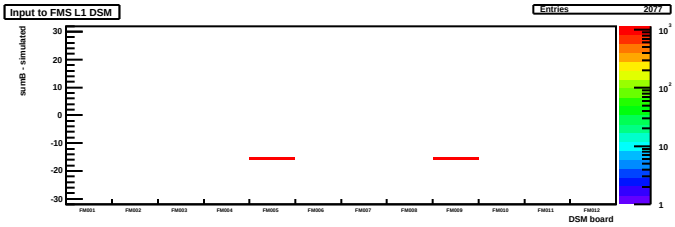

Input to FMS L1 DSM
Entries 2.353434e+07
sumC
DSM board

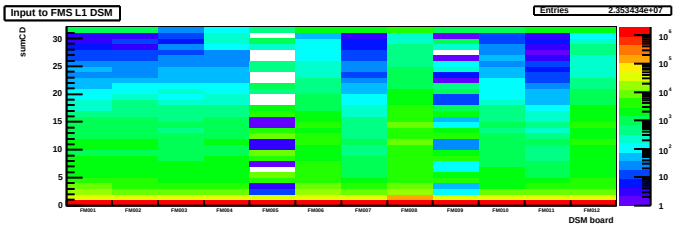

Input to FMS L1 DSM
Entries 32595
sumC - simulated
DSM board

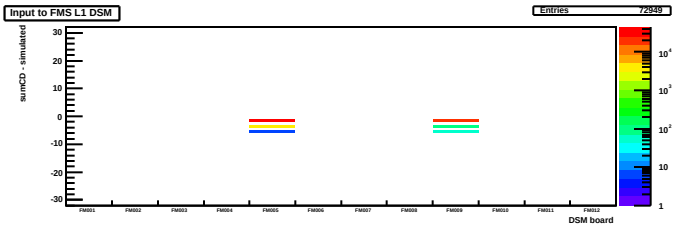

Input to FMS L1 DSM
Entries 2.353434e+07
sumBC
DSM board

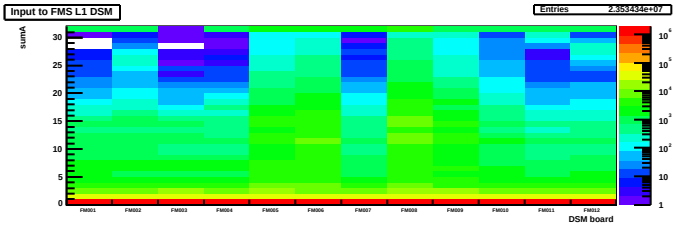

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

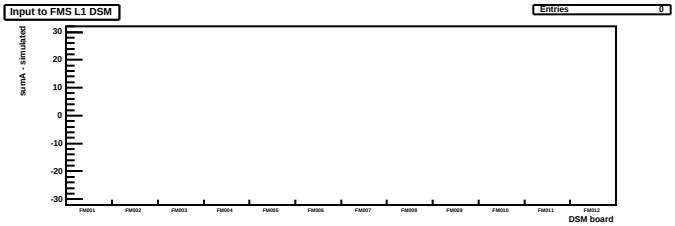

Input to FMS L1 DSM
Entries 2.353434e+07
sumB
DSM board

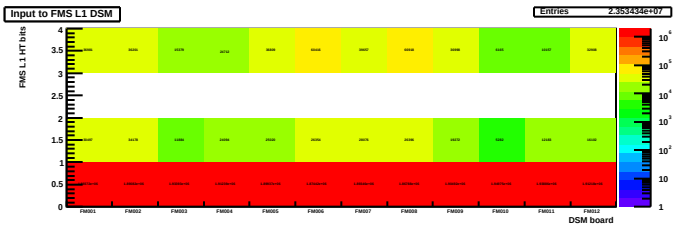

Input to FMS L1 DSM
Entries 2077
sumB - simulated
DSM board

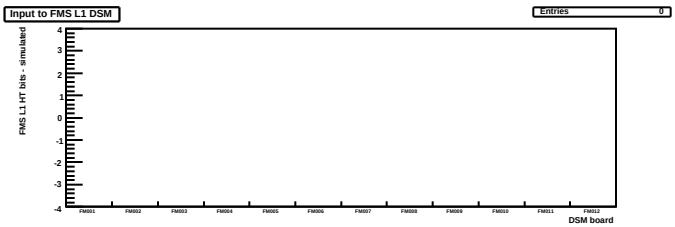

Input to FMS L1 DSM
Entries 2.353434e+07
sumCD
DSM board

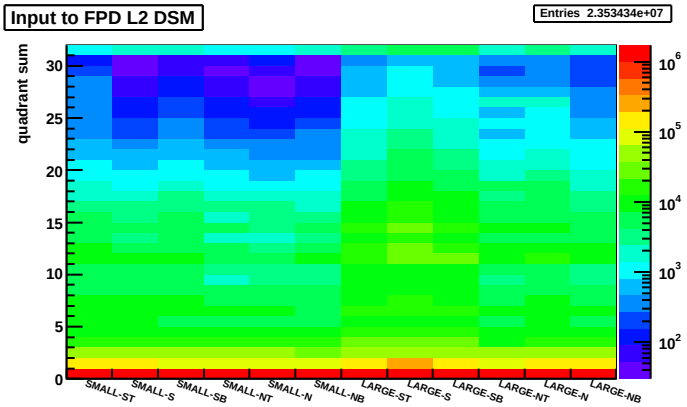

Input to FMS L1 DSM
Entries 72949
sumCD - simulated
DSM board

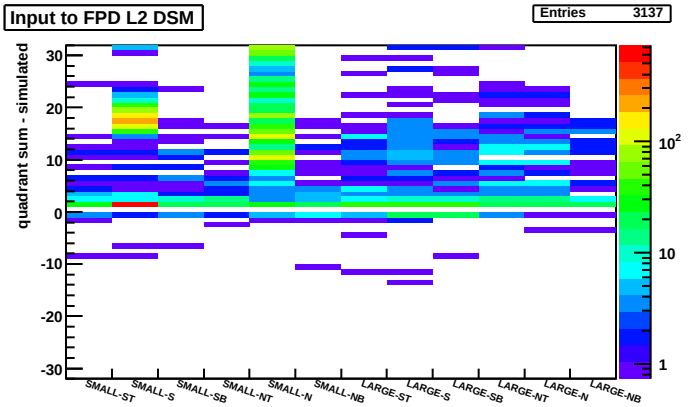

Input to FMS L1 DSM
Entries 2.353434e+07
sumA
DSM board

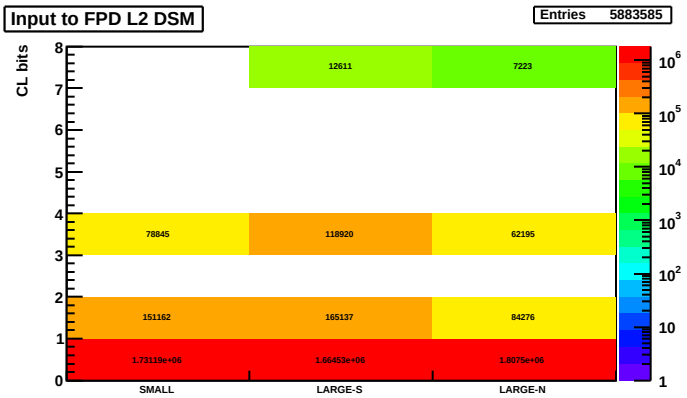

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

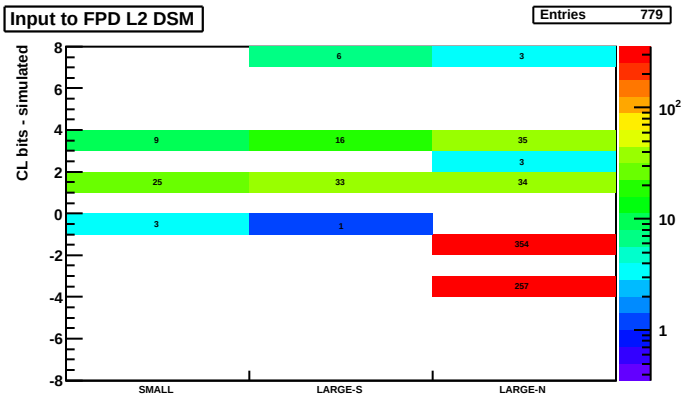

Input to FMS L1 DSM
Entries 2.353434e+07
FMS L1 HT bits
DSM board

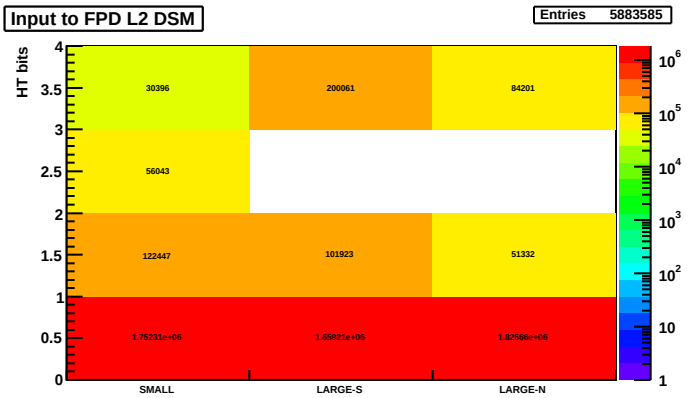

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

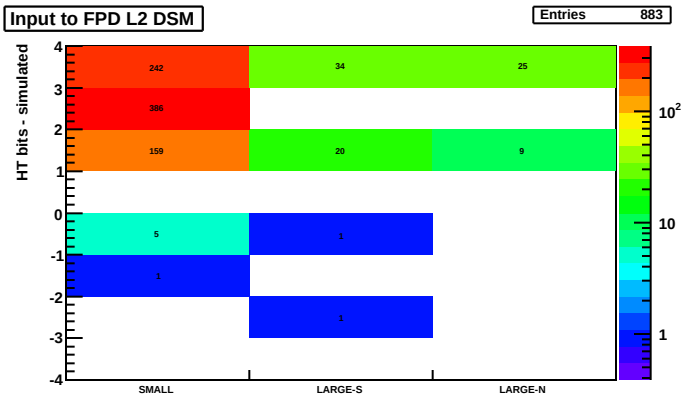

Input to FPD L2 DSM
Entries 2.353434e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

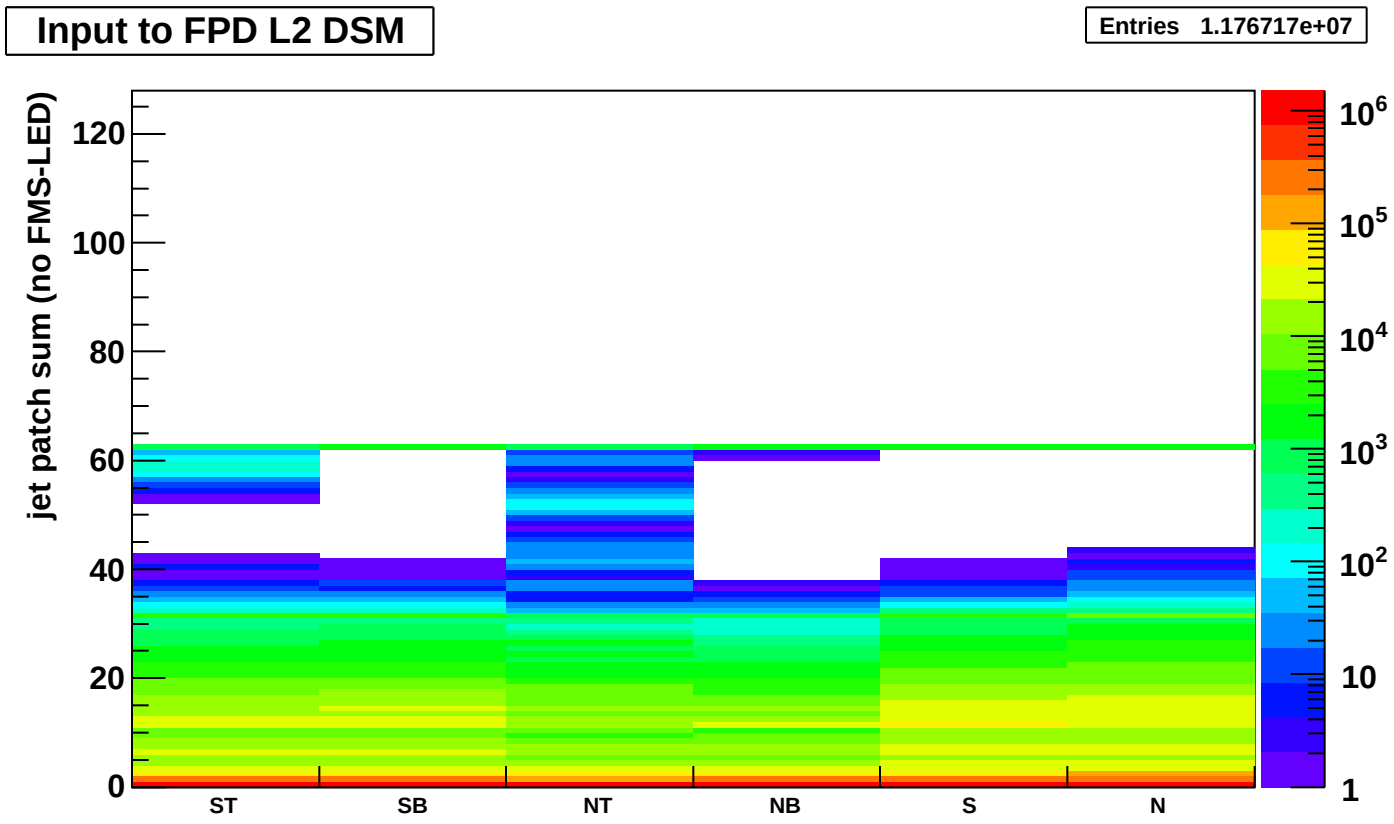

Input to FPD L2 DSM
Entries 3137
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

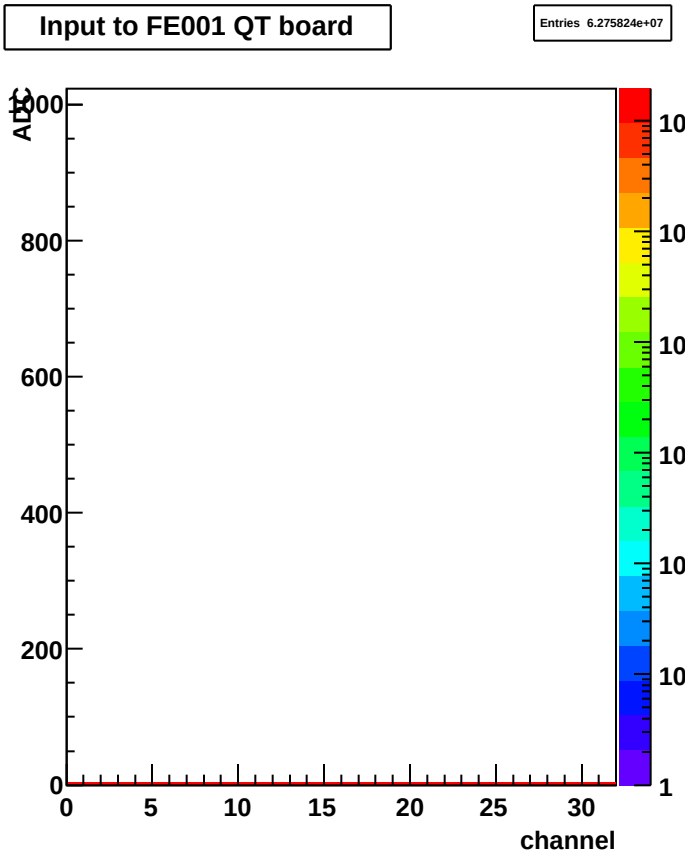

Input to FPD L2 DSM
Entries 5883585
CL bits
12611
7223
78845
118920
62195
151162
165137
84276
1.73119e+06
1.66453e+06
1.8075e+06
SMALL
LARGE-S
LARGE-N

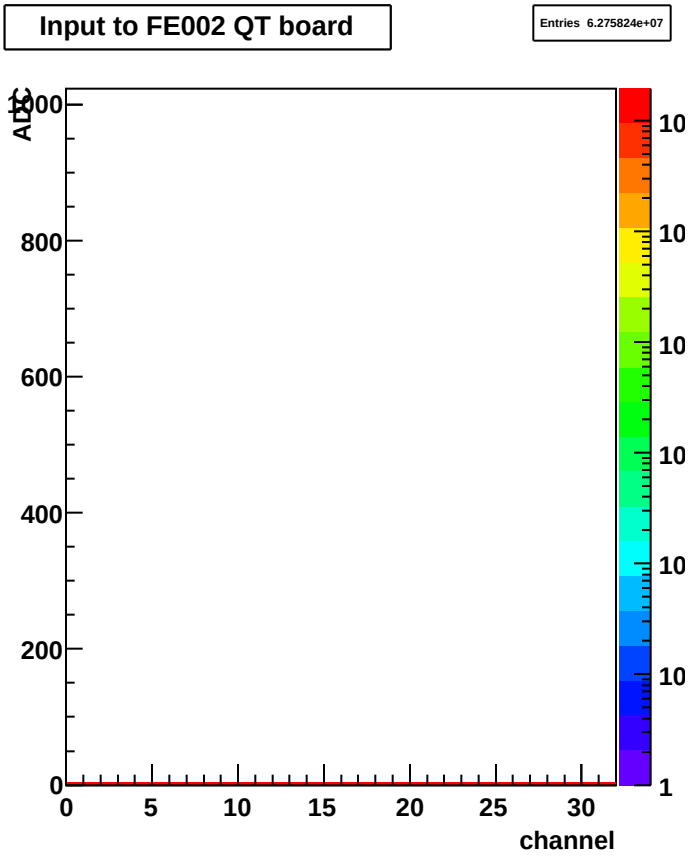

Input to FPD L2 DSM
Entries 779
CL bits - simulated
6
3
9
16
35
3
25
33
34
3
1
354
257
SMALL
LARGE-S
LARGE-N

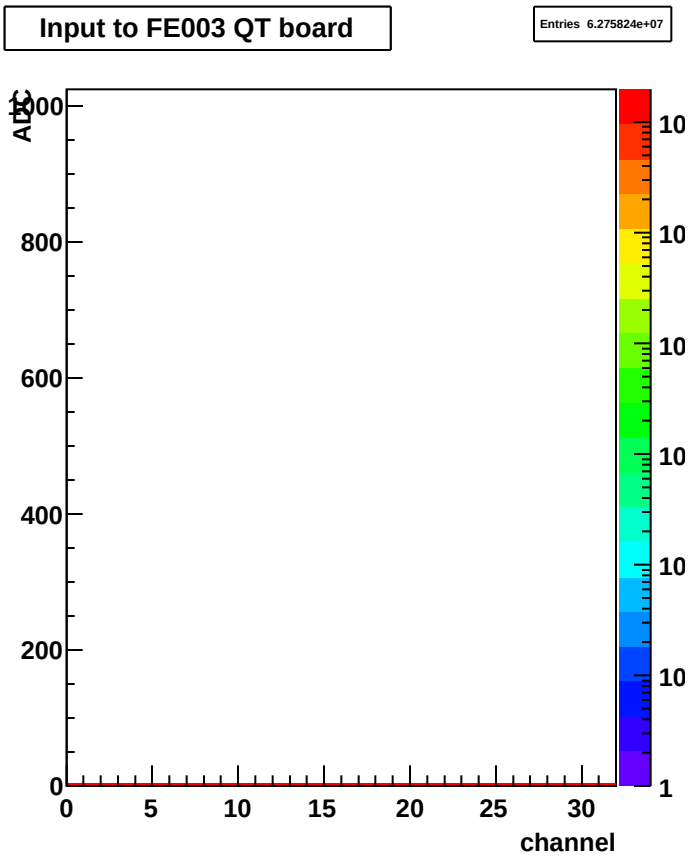

Input to FPD L2 DSM
Entries 5883585
HT bits
30396
200061
84201
56043
122447
101923
51332
1.75231e+06
1.65921e+06
1.82566e+06
SMALL
LARGE-S
LARGE-N

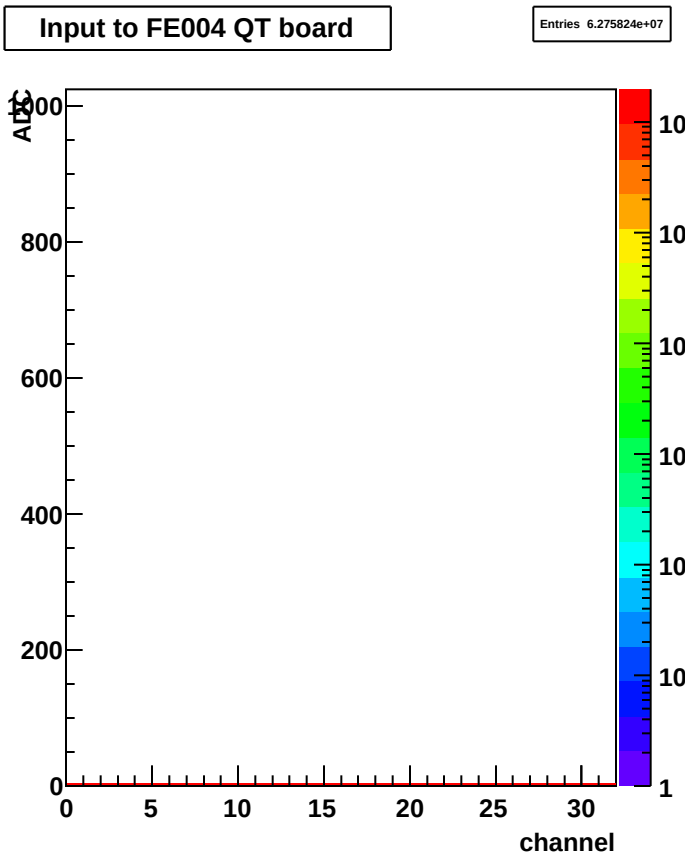

Input to FPD L2 DSM
Entries 883
HT bits - simulated
242
34
25
386
159
20
9
5
1
1
1
SMALL
LARGE-S
LARGE-N

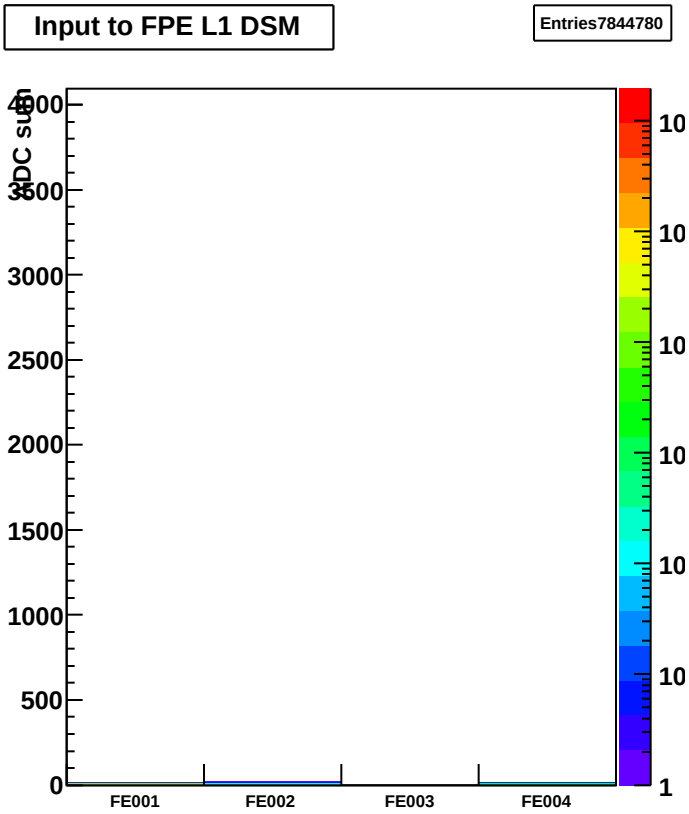

Input to FPD L2 DSM
Entries 1.176717e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

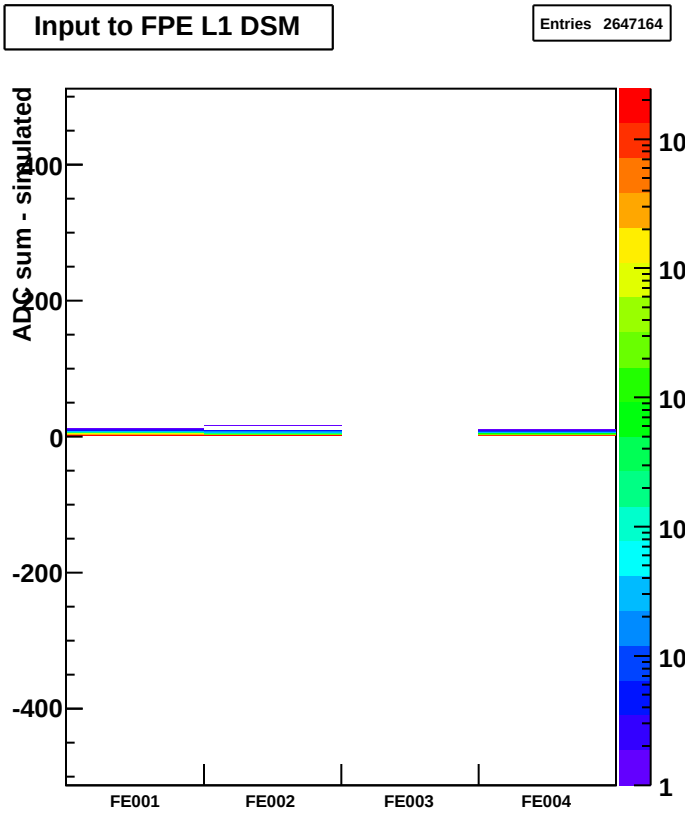

Input to FE001 QT board
Entries 6.275824e+07
ADC
channel

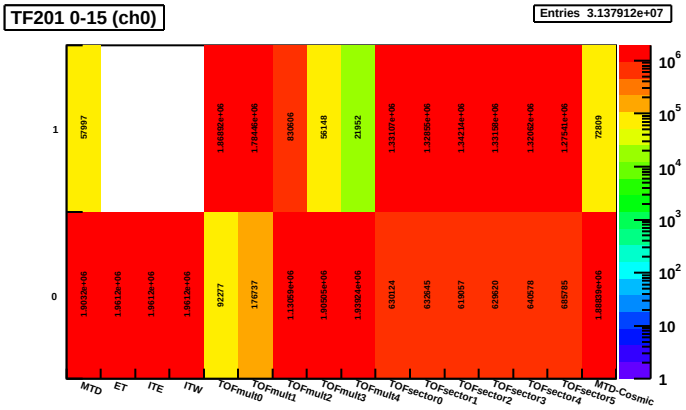

Input to FE002 QT board
Entries 6.275824e+07
ADC
channel

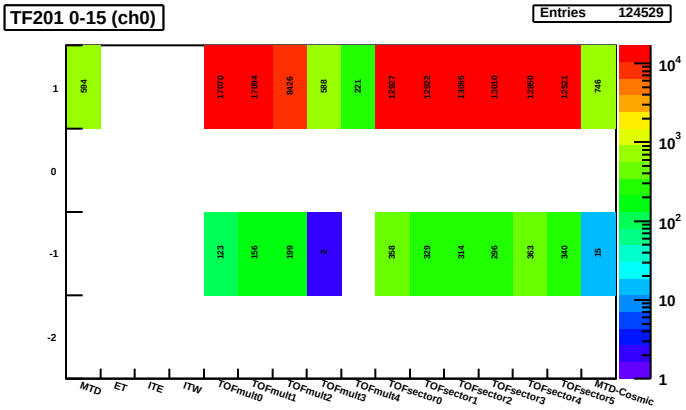

Input to FE003 QT board
Entries 6.275824e+07
ADC
channel

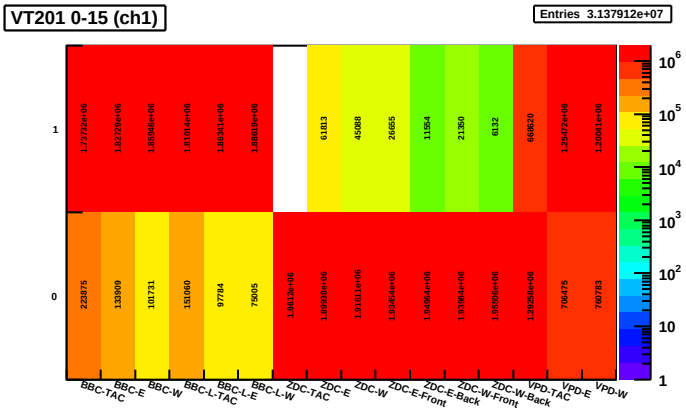

Input to FE004 QT board
Entries 6.275824e+07
ADC
channel


Input to FPE L1 DSM
Entries7844780
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 2647164
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.137912e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 124529
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
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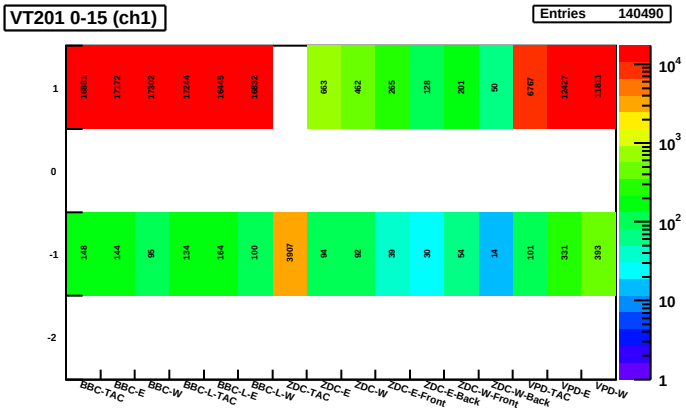
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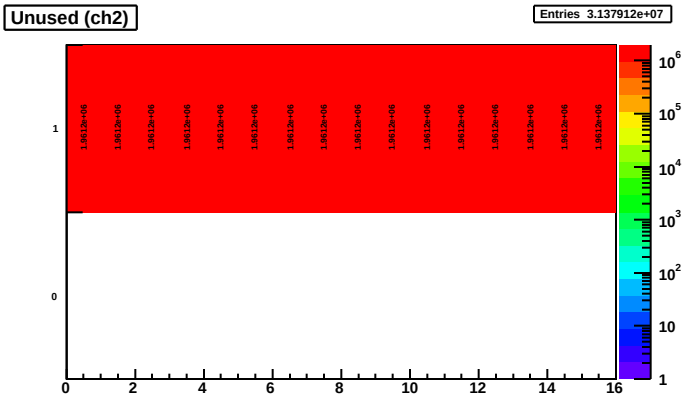
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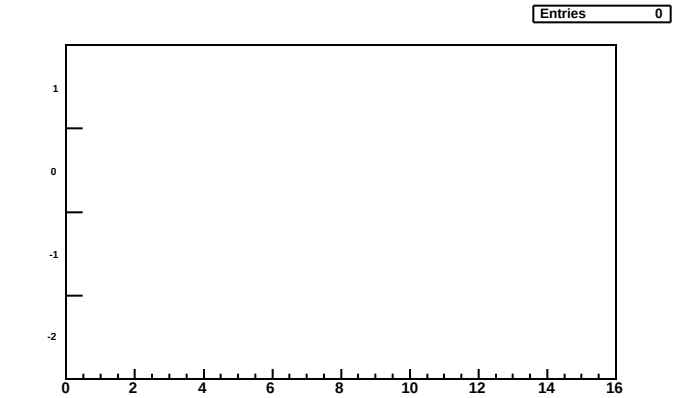
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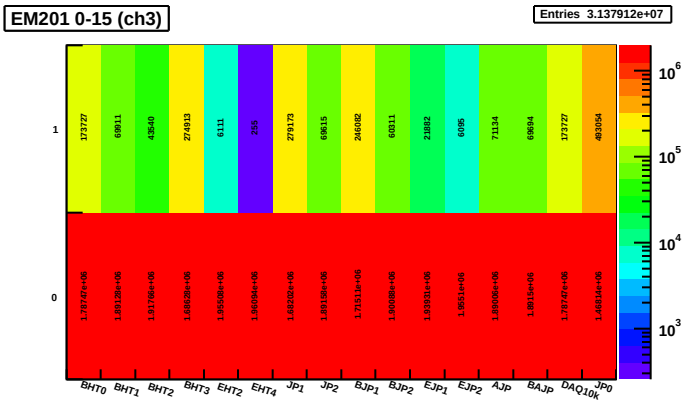
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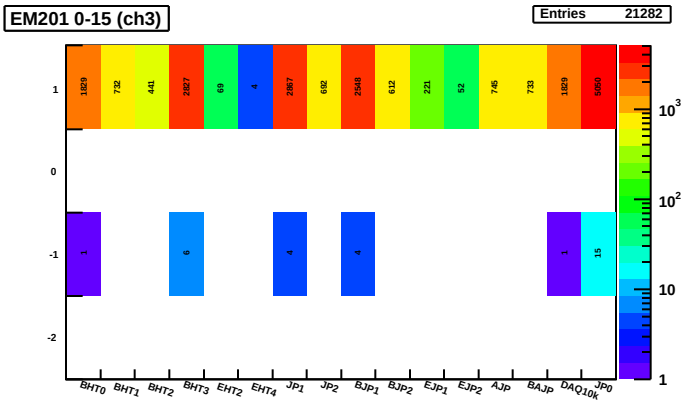
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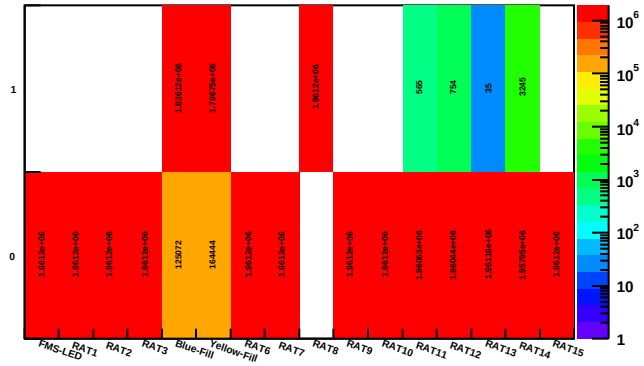
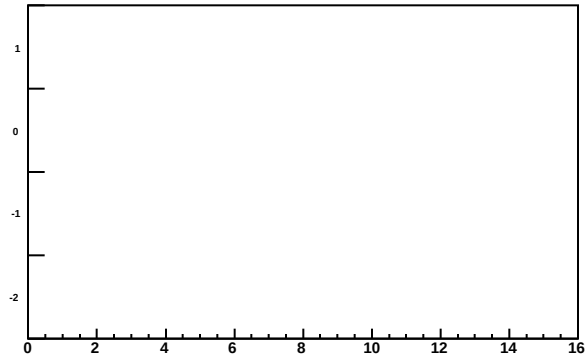
RAT board (ch4)





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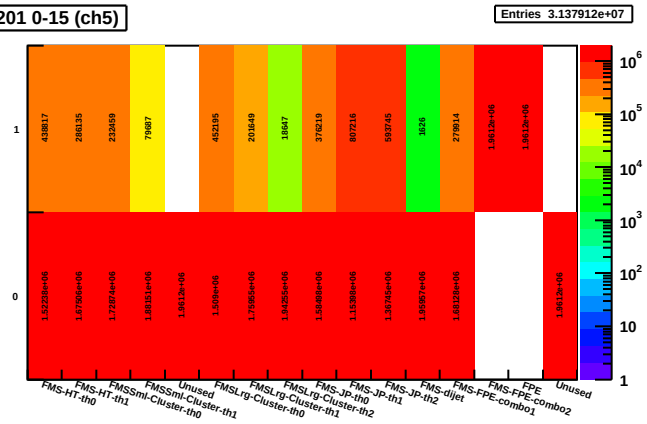

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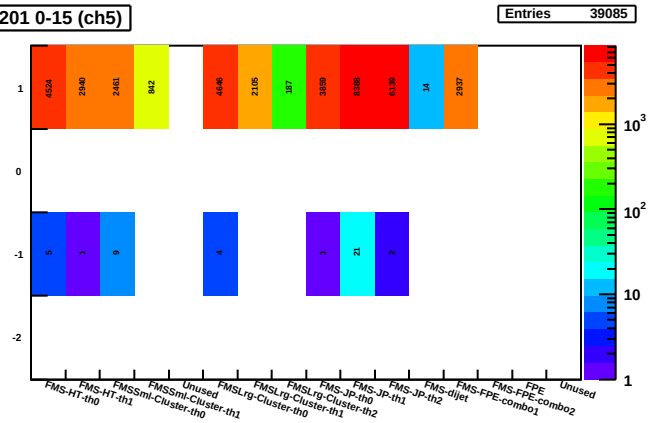
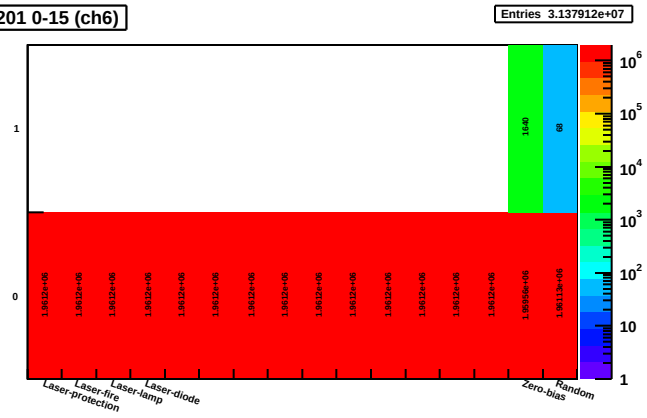
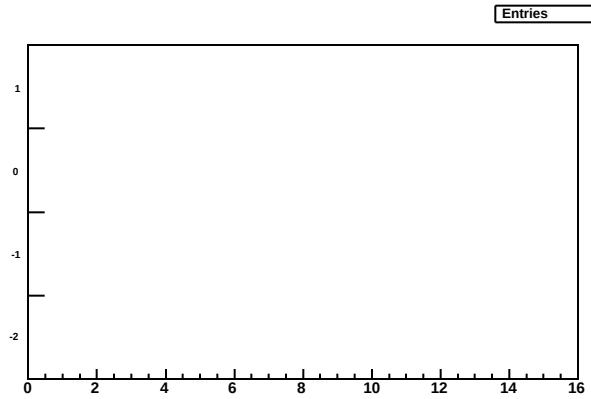
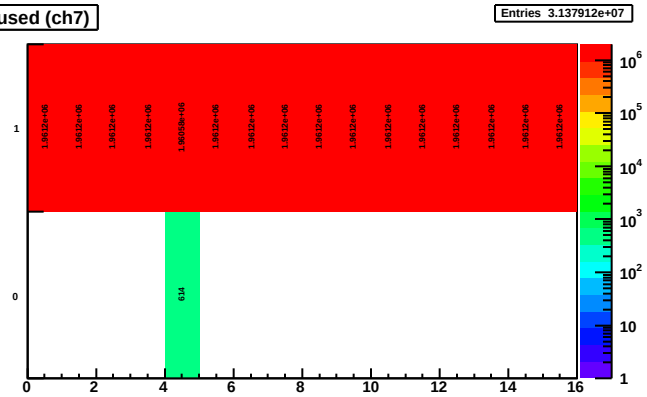
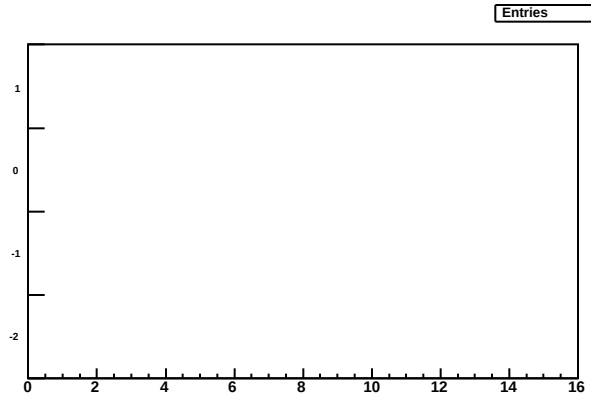


ST201 0-15 (ch6)





Unused (ch7)