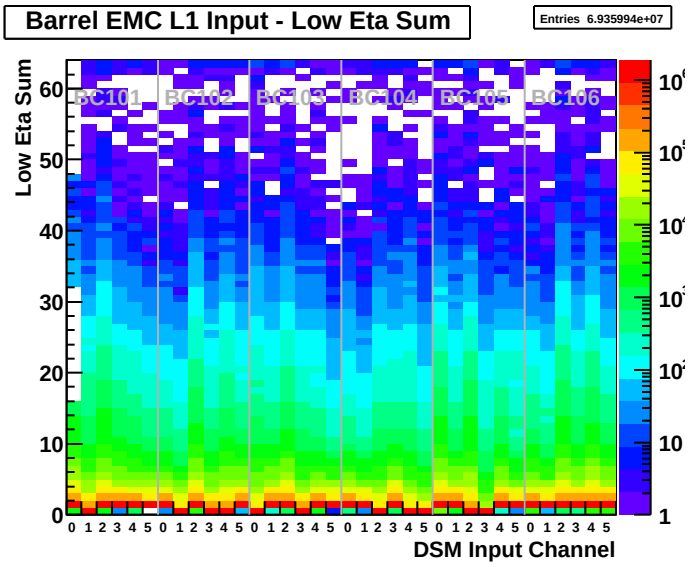
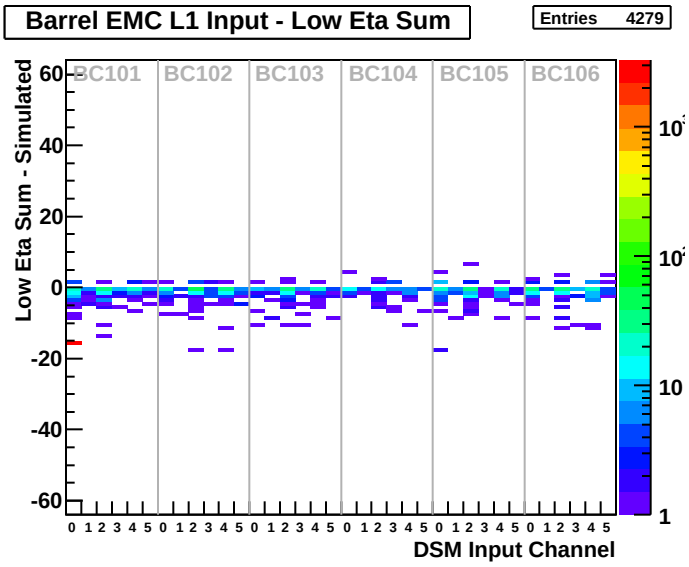
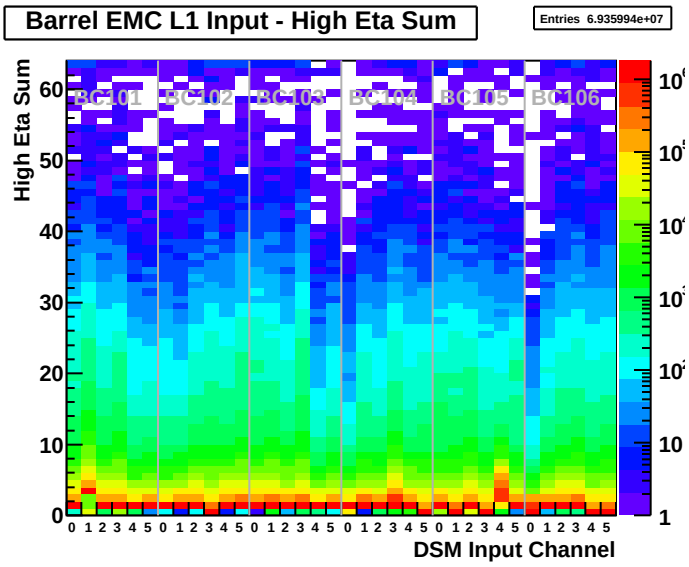
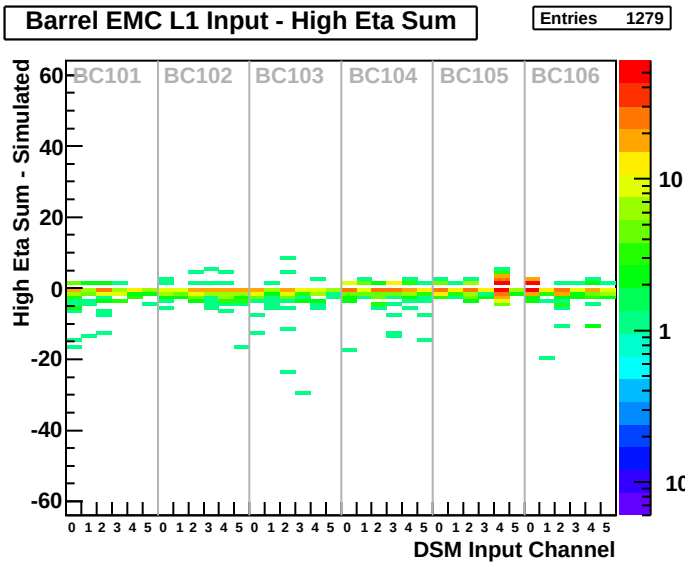
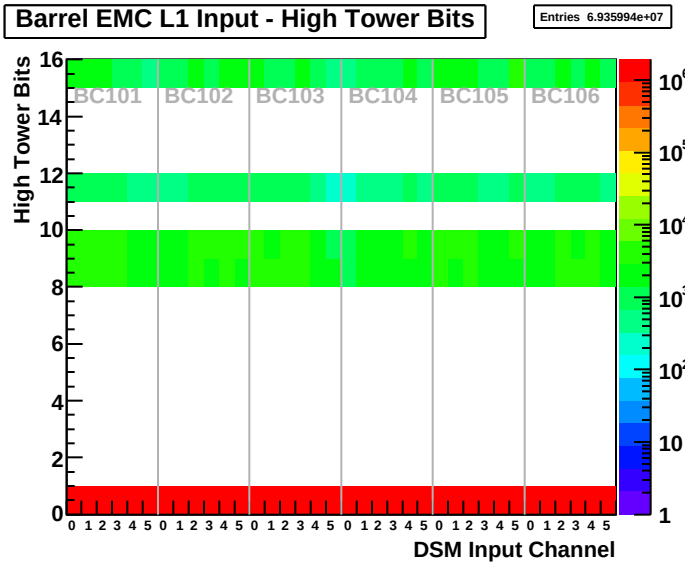
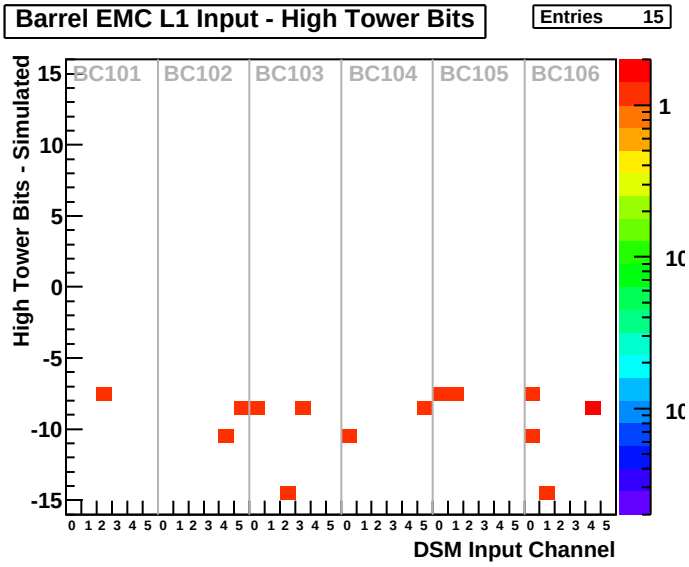

Barrel EMC L1 Input - Low Eta Sum
Entries 6.935994e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

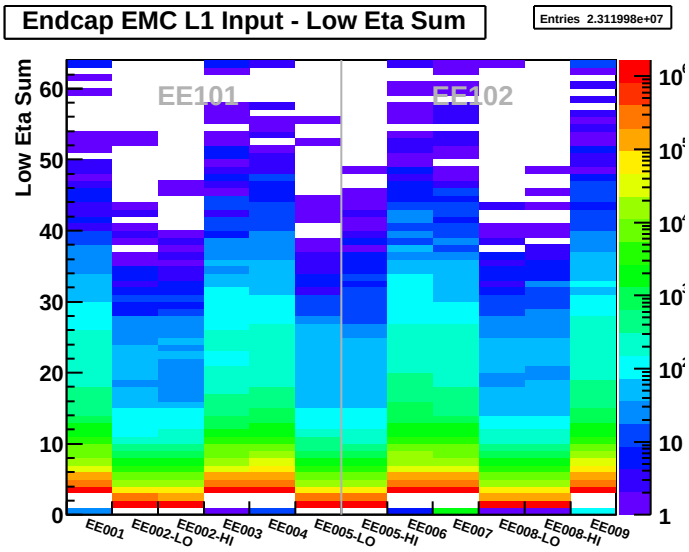

Barrel EMC L1 Input - Low Eta Sum
Entries 4279
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

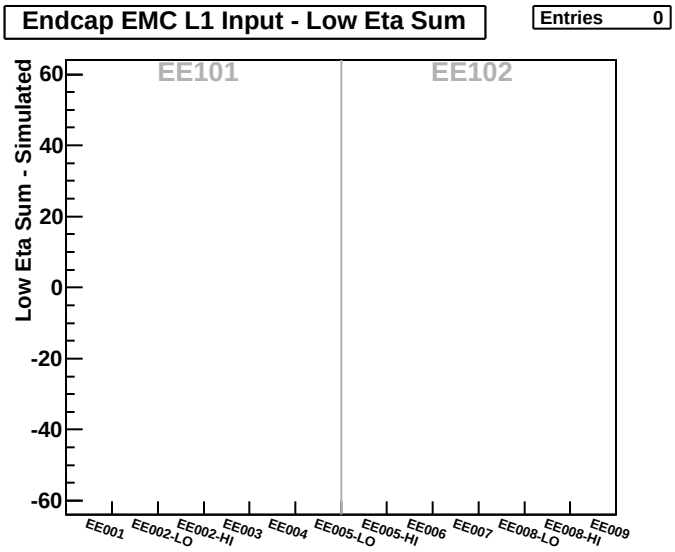

Barrel EMC L1 Input - High Eta Sum
Entries 6.935994e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

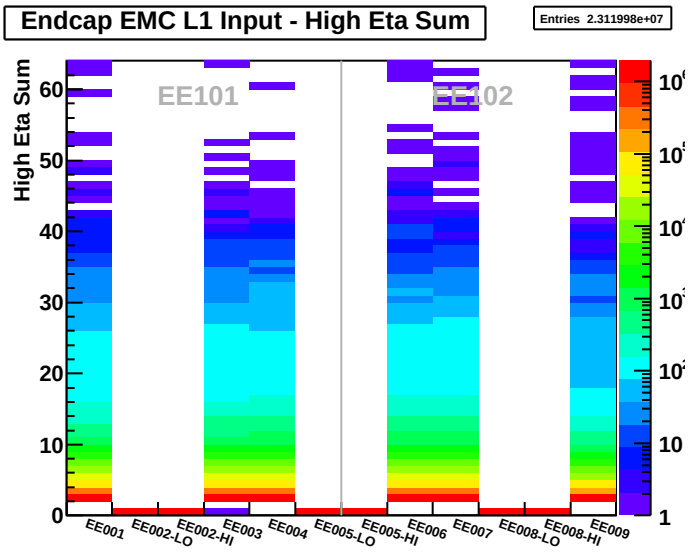

Barrel EMC L1 Input - High Eta Sum
Entries 1279
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

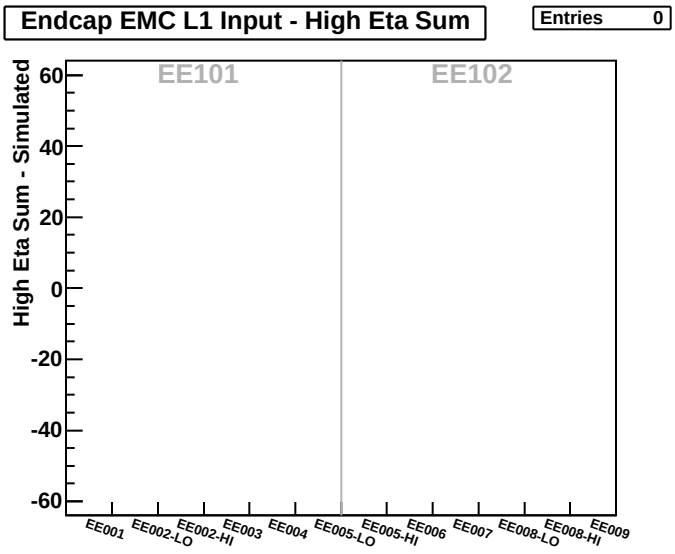

Barrel EMC L1 Input - High Tower Bits
Entries 6.935994e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

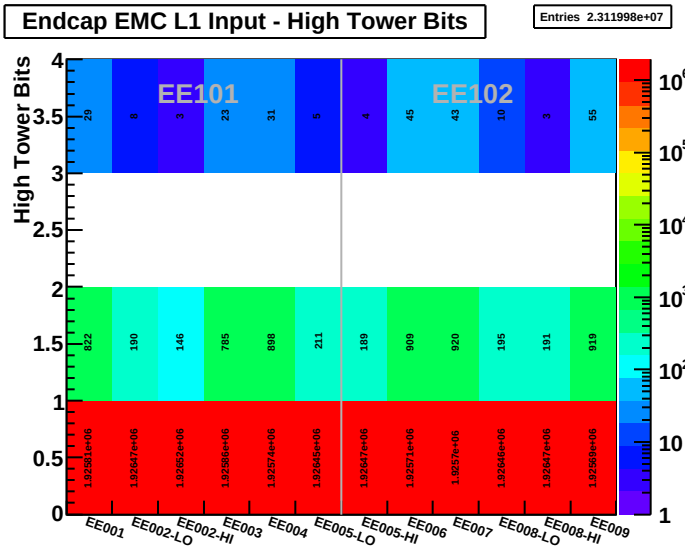

Barrel EMC L1 Input - High Tower Bits
Entries 15
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

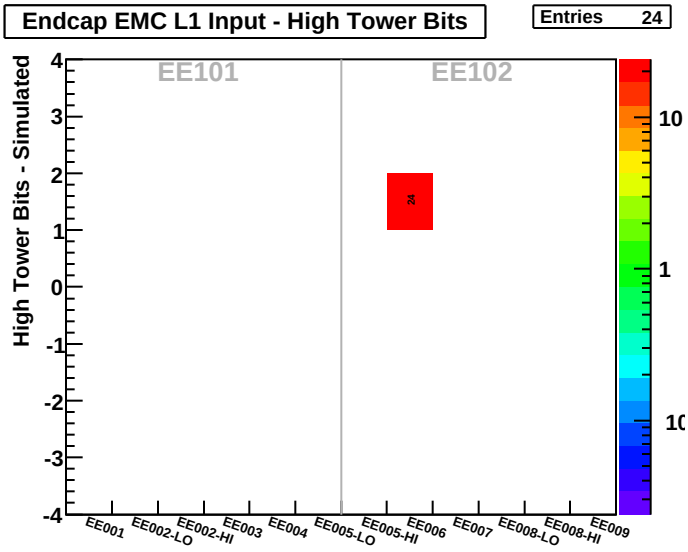

Endcap EMC L1 Input - Low Eta Sum
Entries 2.311998e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

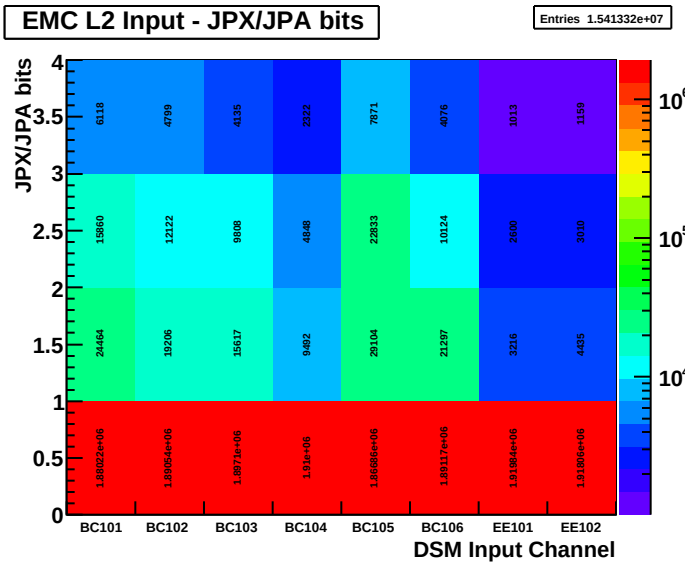

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

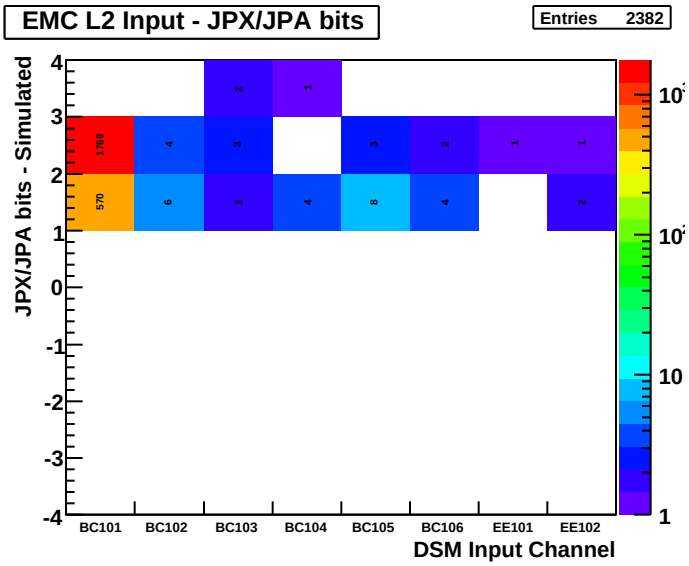

Endcap EMC L1 Input - High Eta Sum
Entries 2.311998e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

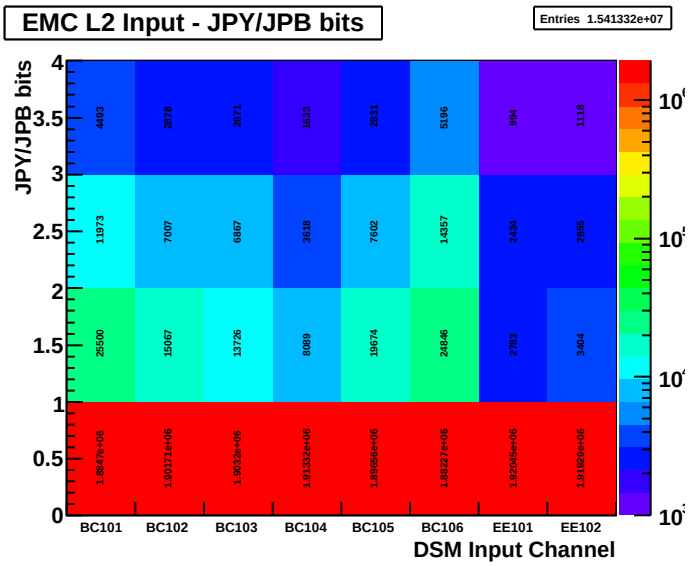

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

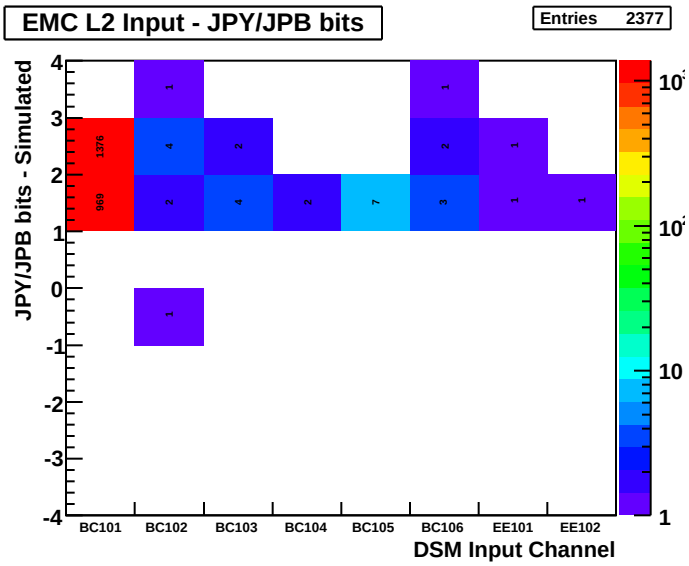

Endcap EMC L1 Input - High Tower Bits
Entries 2.311998e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

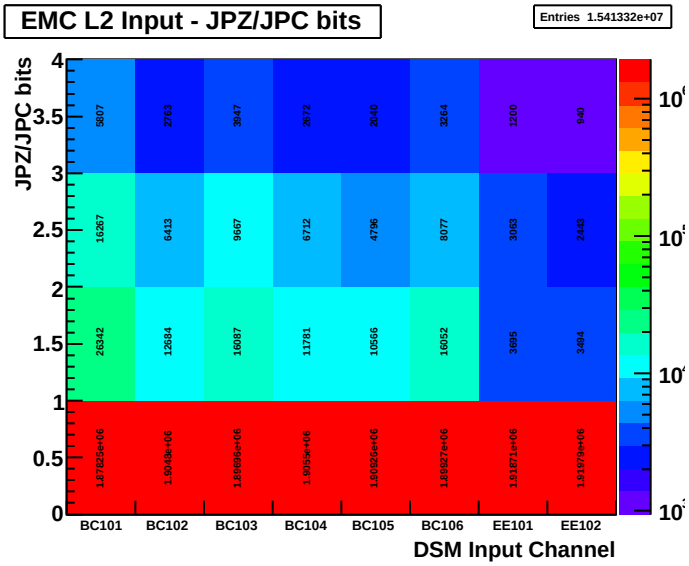

Endcap EMC L1 Input - High Tower Bits
Entries 24
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

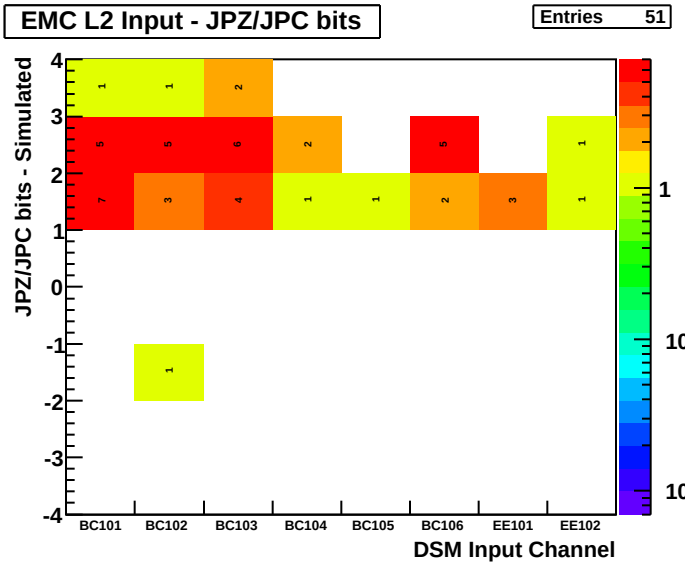

EMC L2 Input - JPX/JPA bits
Entries 1.541332e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

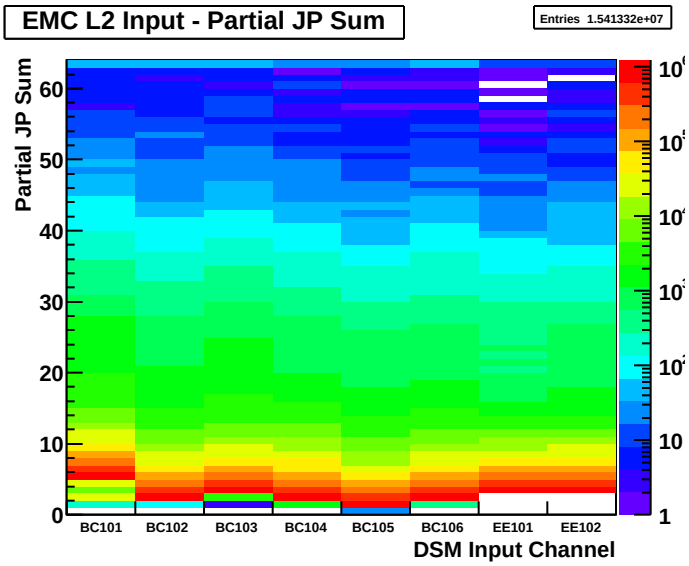

EMC L2 Input - JPX/JPA bits
Entries 2382
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

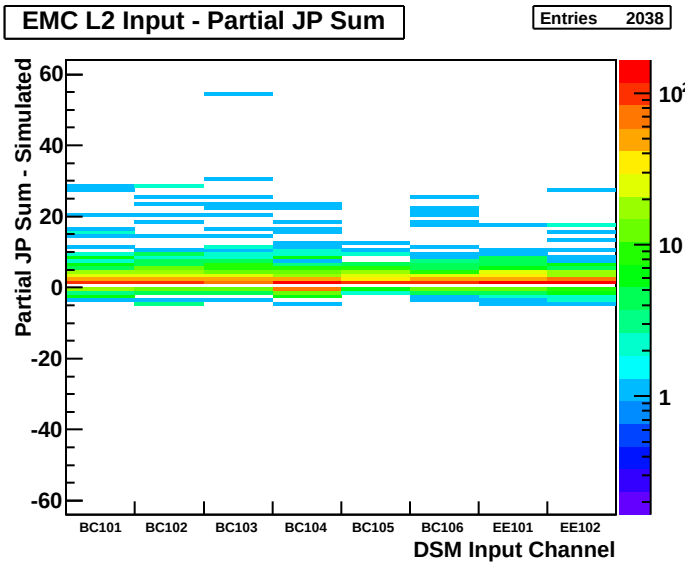

EMC L2 Input - JPY/JPB bits
Entries 1.541332e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

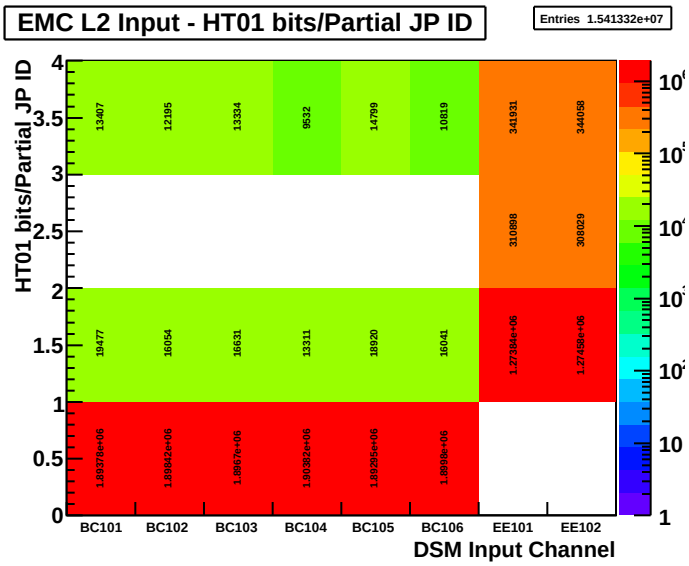

EMC L2 Input - JPY/JPB bits
Entries 2377
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

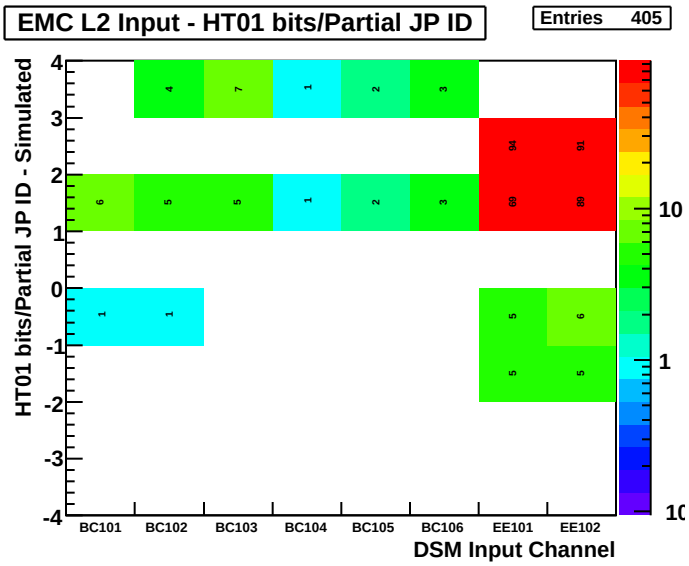

EMC L2 Input - JPZ/JPC bits
Entries 1.541332e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

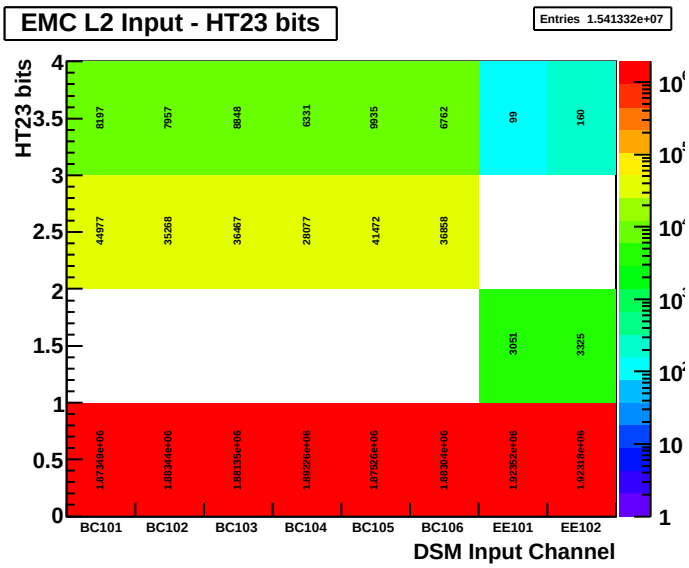

EMC L2 Input - JPZ/JPC bits
Entries 51
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

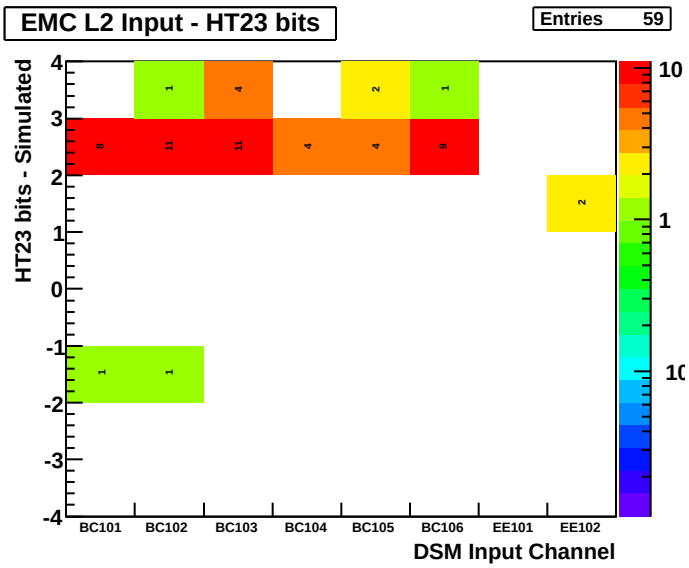

EMC L2 Input - Partial JP Sum
Entries 1.541332e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

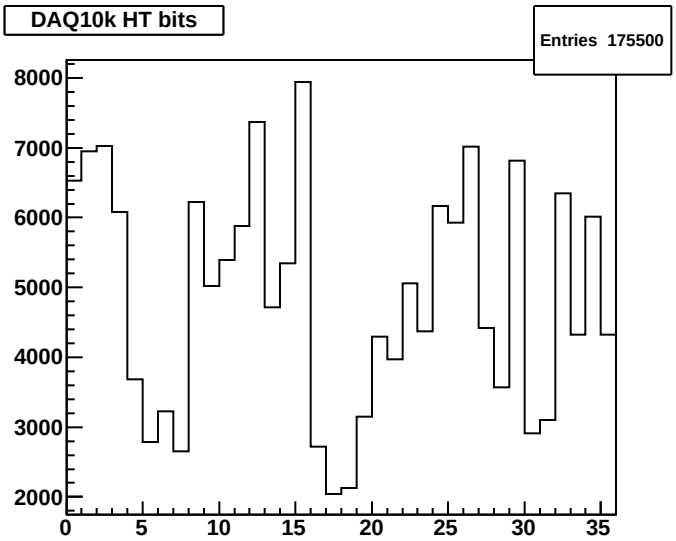

EMC L2 Input - Partial JP Sum
Entries 2038
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

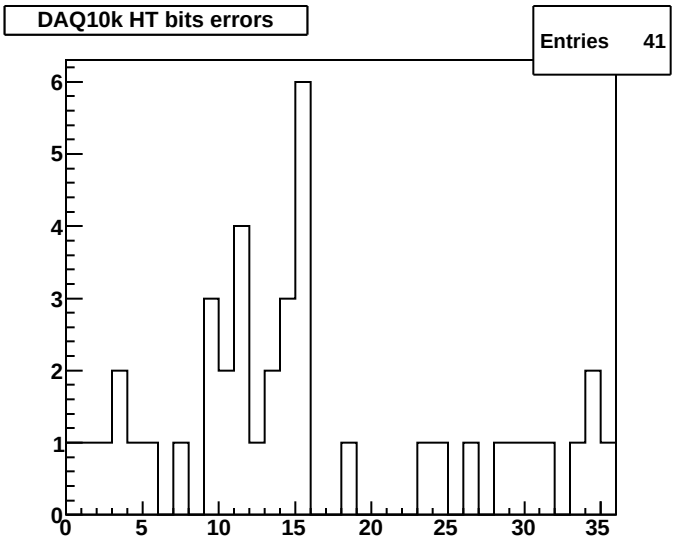

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.541332e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

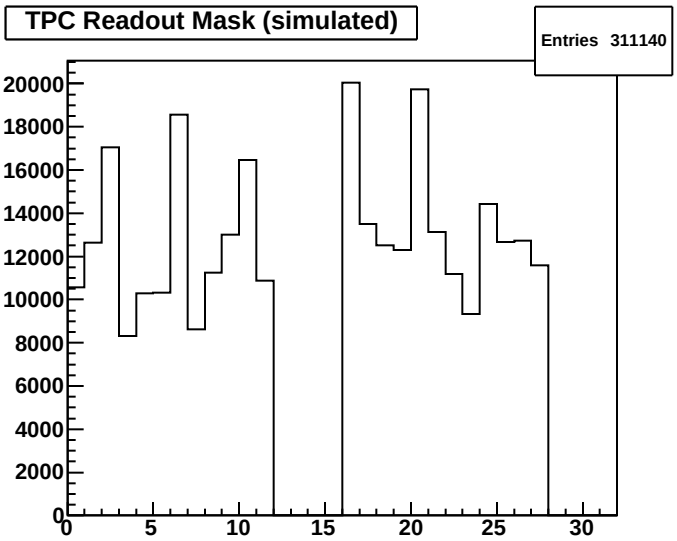

EMC L2 Input - HT01 bits/Partial JP ID
Entries 405
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

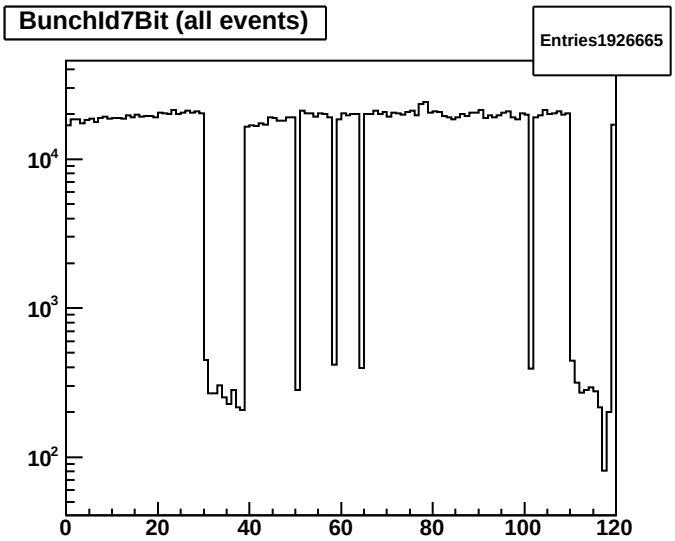

EMC L2 Input - HT23 bits
Entries 1.541332e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

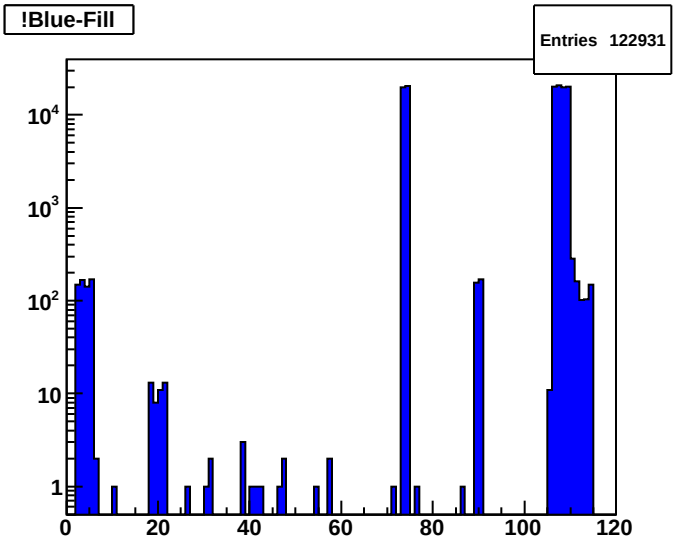

EMC L2 Input - HT23 bits
Entries 59
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

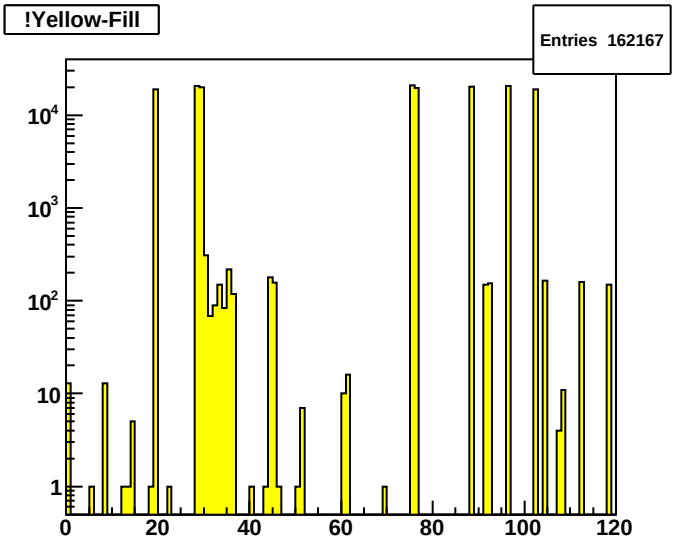

DAQ10k HT bits
Entries 175500

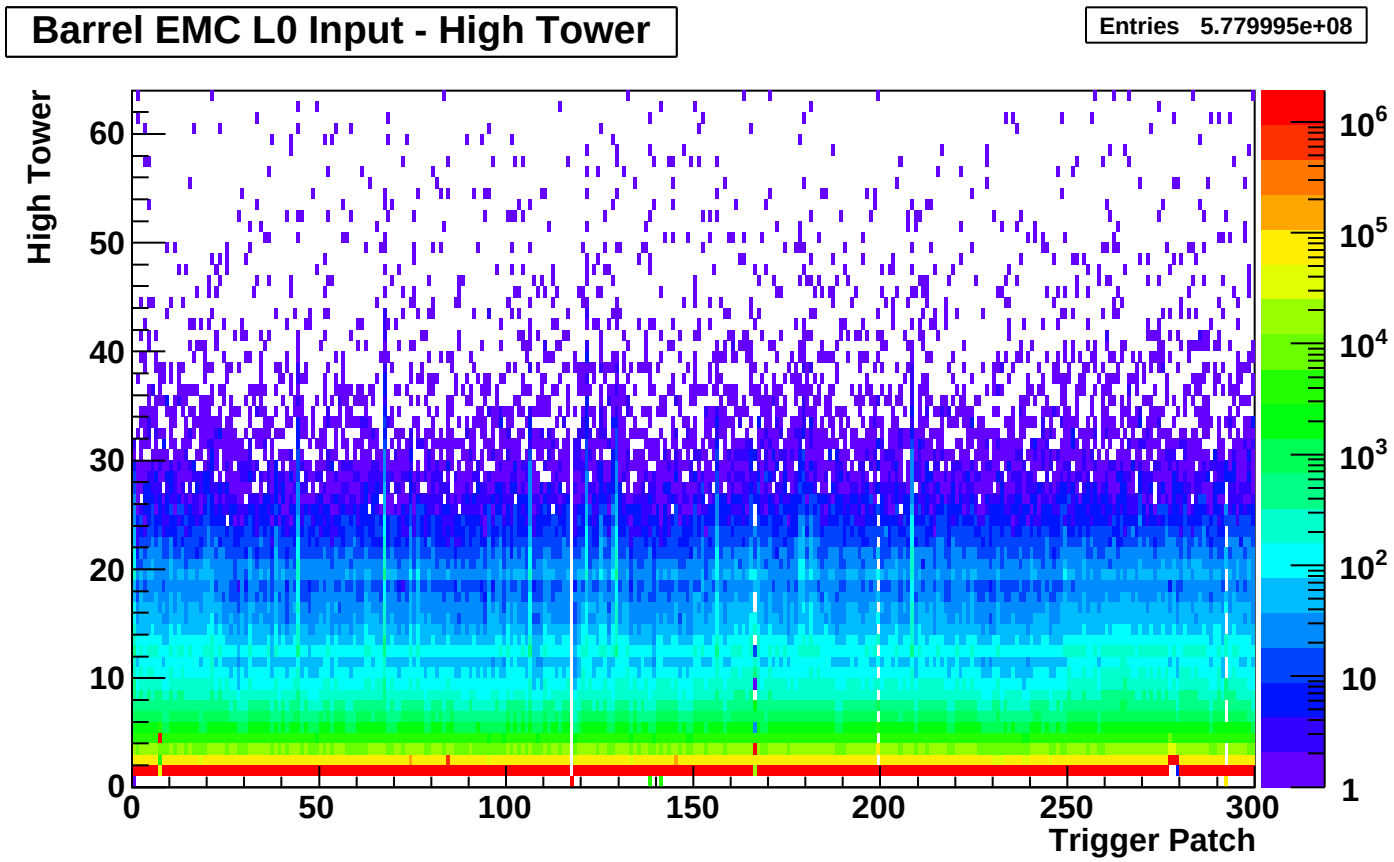

DAQ10k HT bits errors
Entries 41

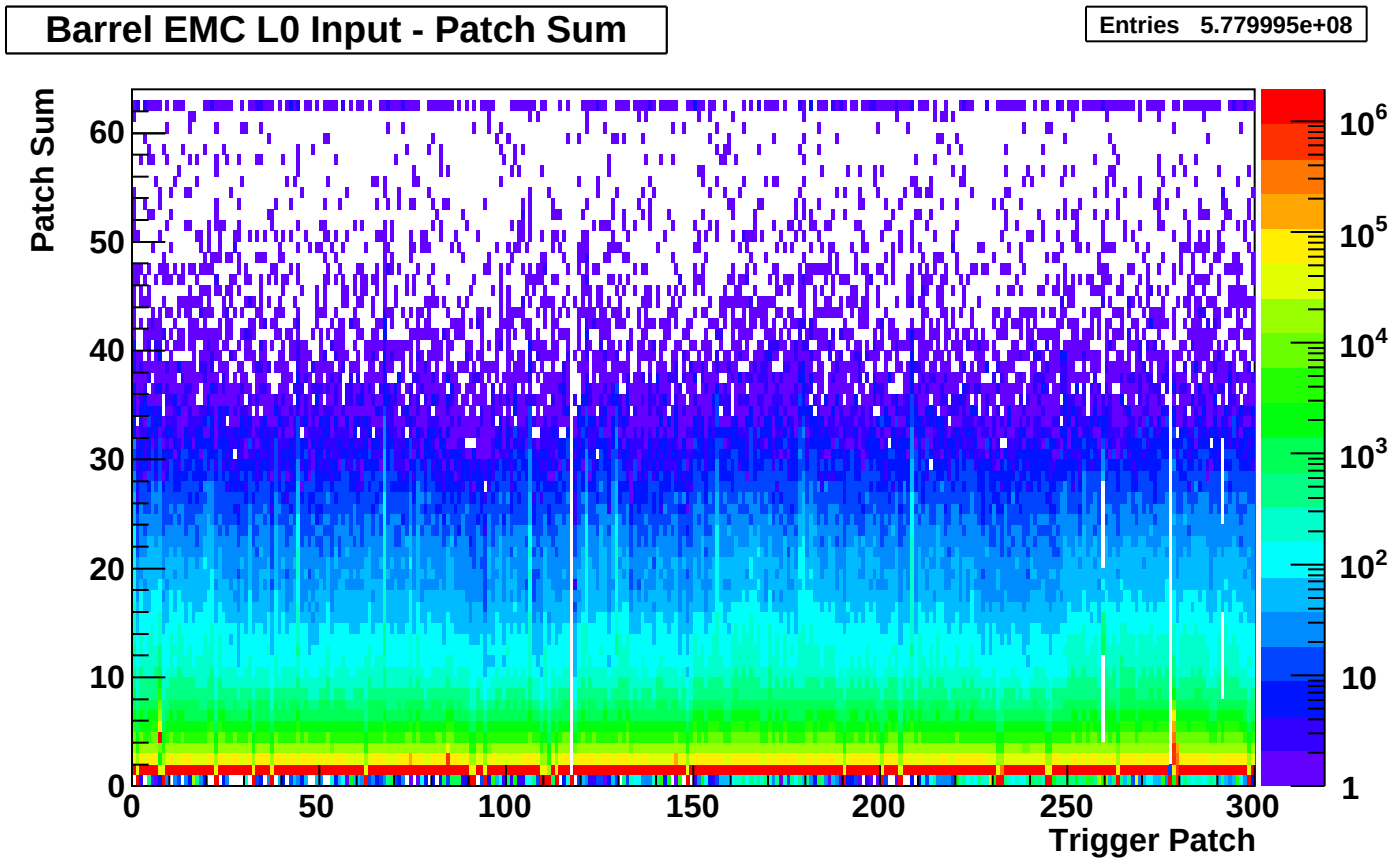

TPC Readout Mask (simulated)
Entries 311140

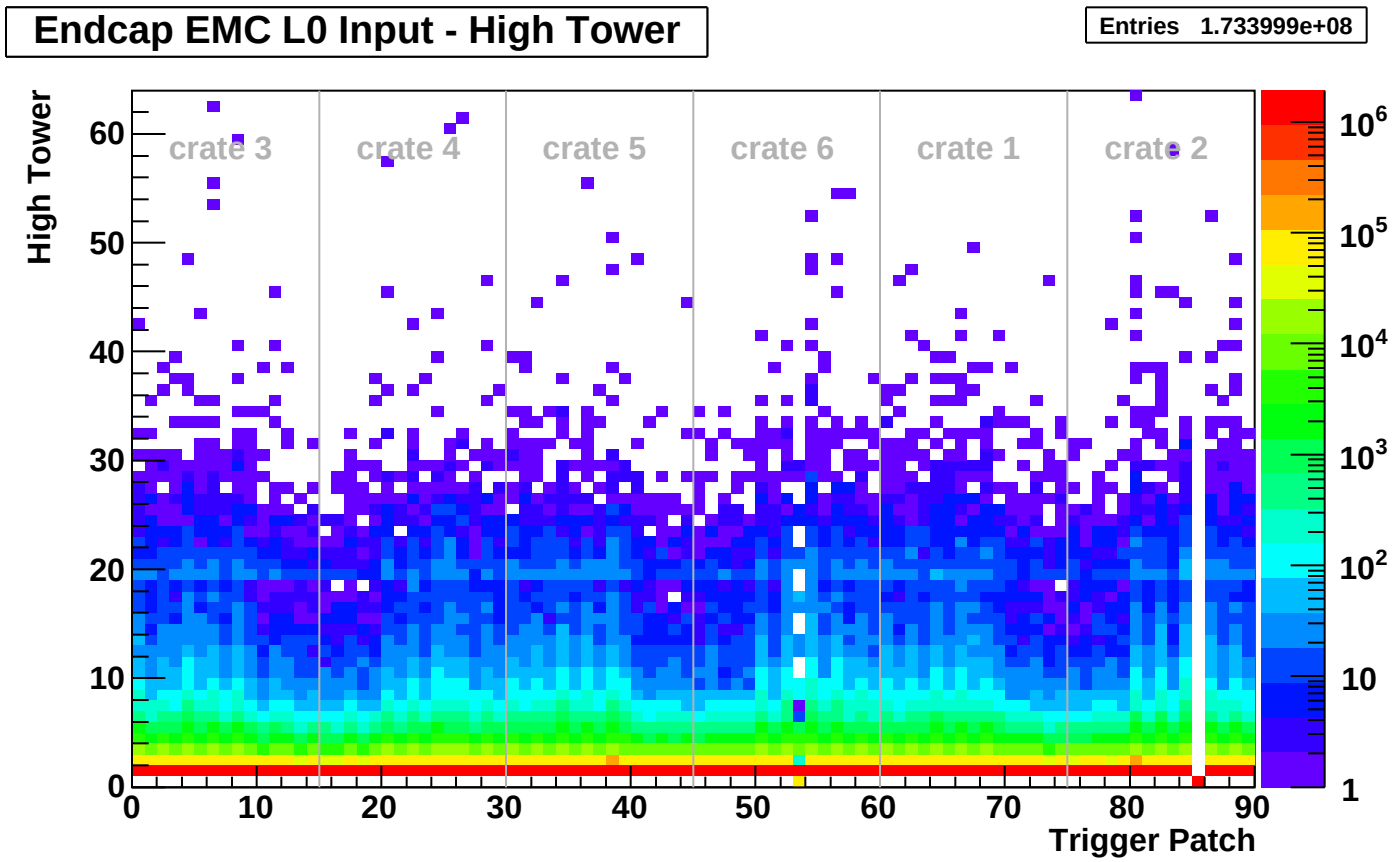

BunchId7Bit (all events)
Entries1926665

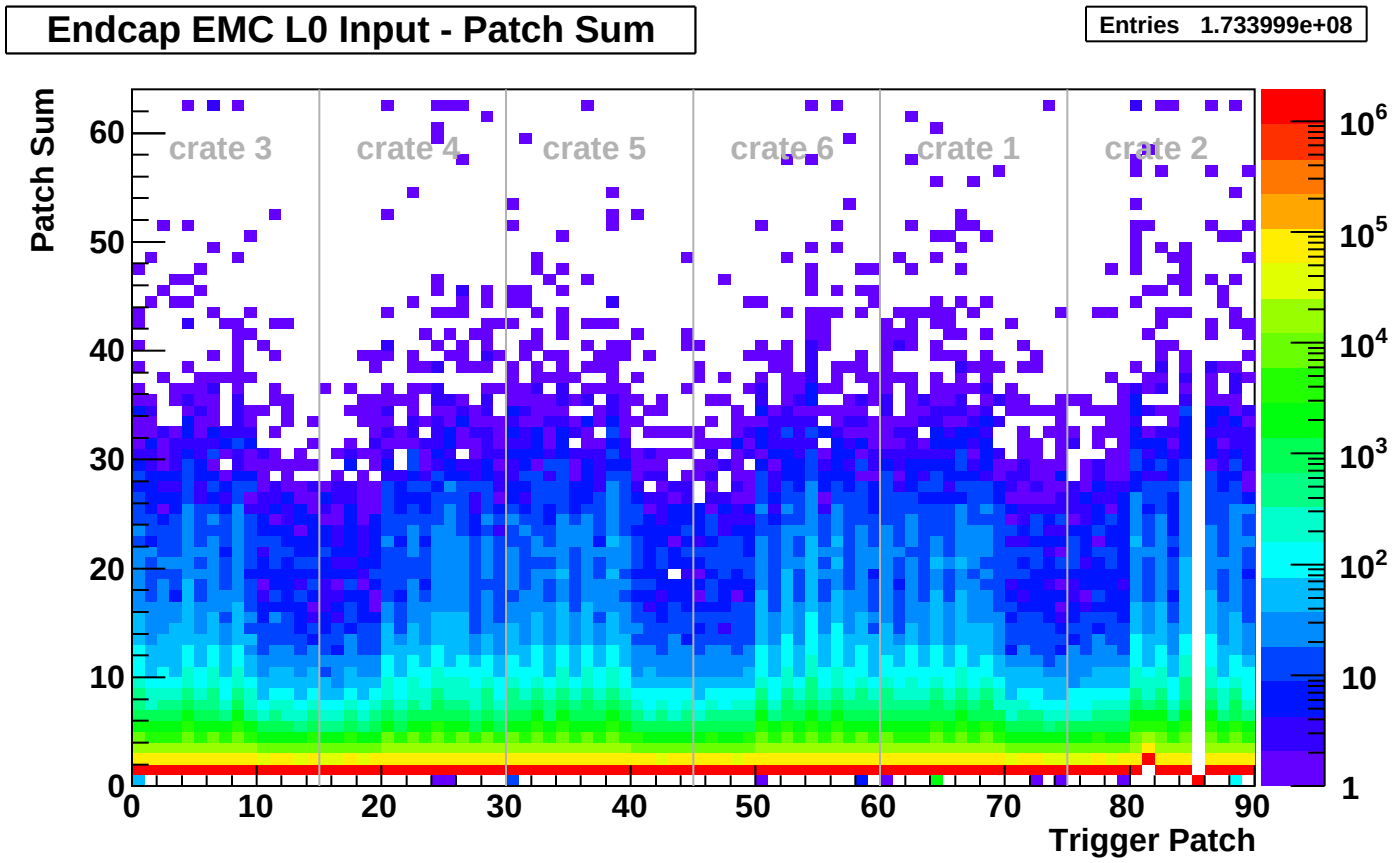

!Blue-Fill
Entries 122931

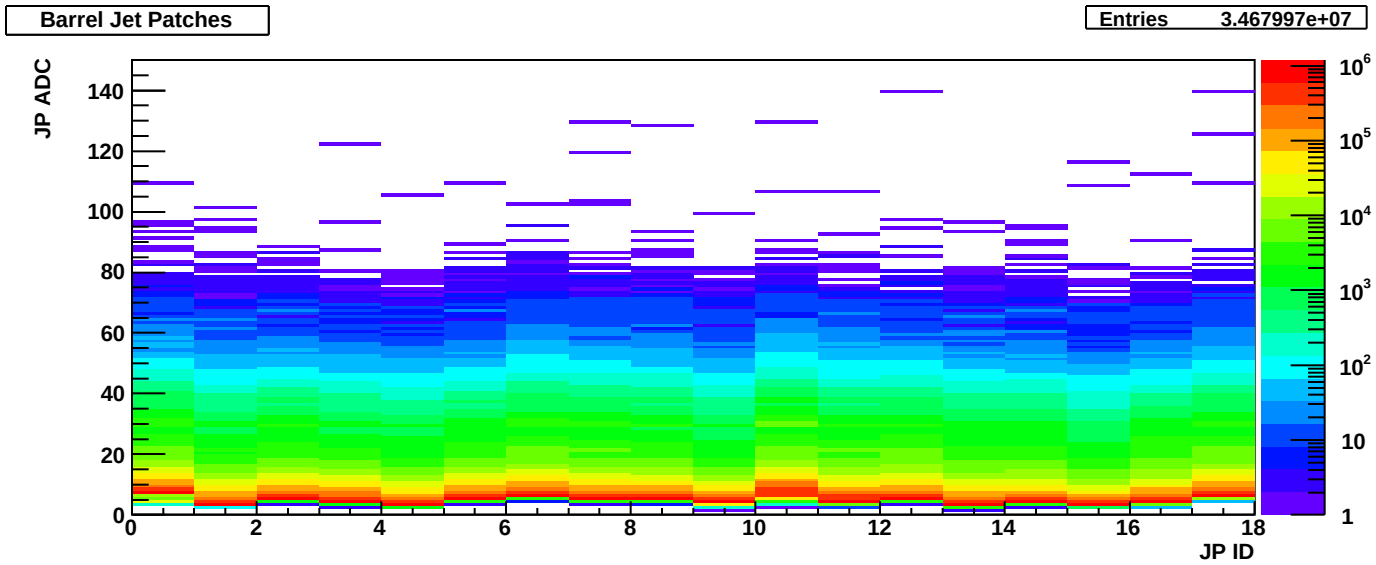

!Yellow-Fill
Entries 162167

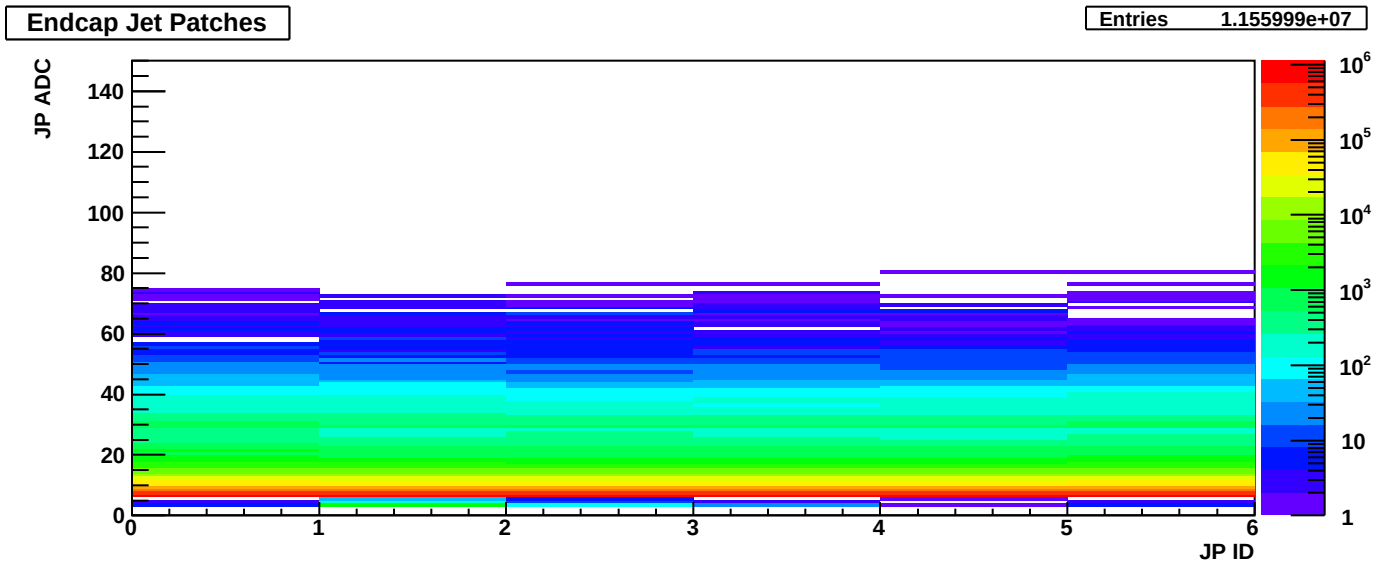

Barrel EMC L0 Input - High Tower
Entries 5.779995e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

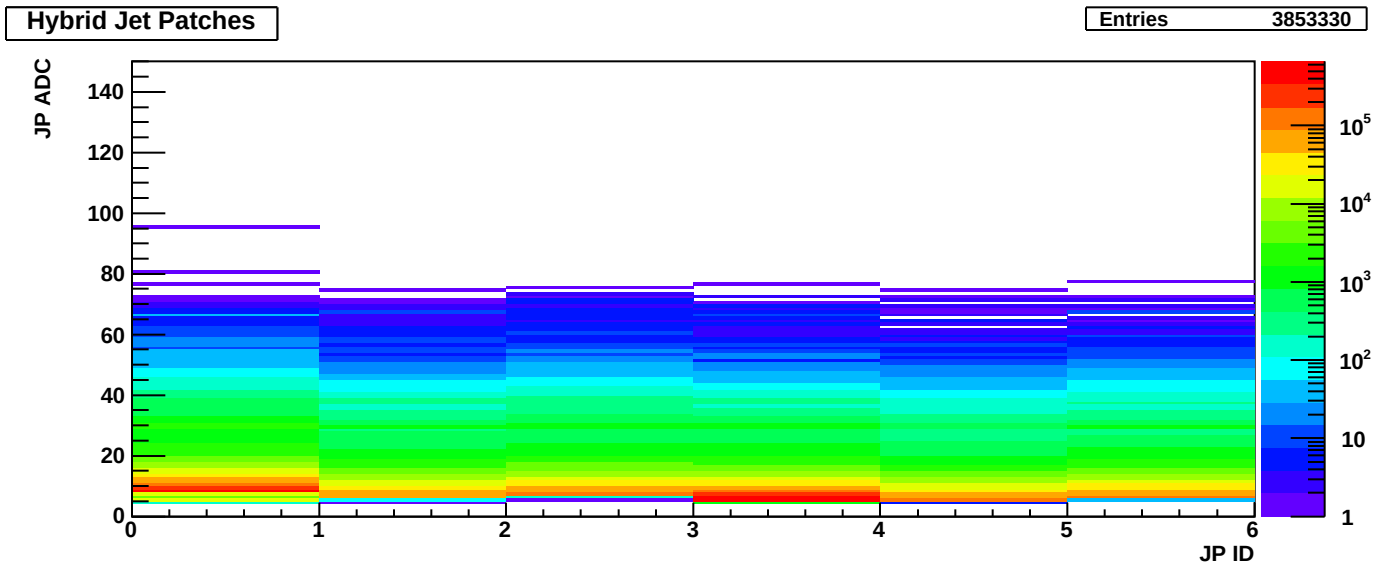

Barrel EMC L0 Input - Patch Sum
Entries 5.779995e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

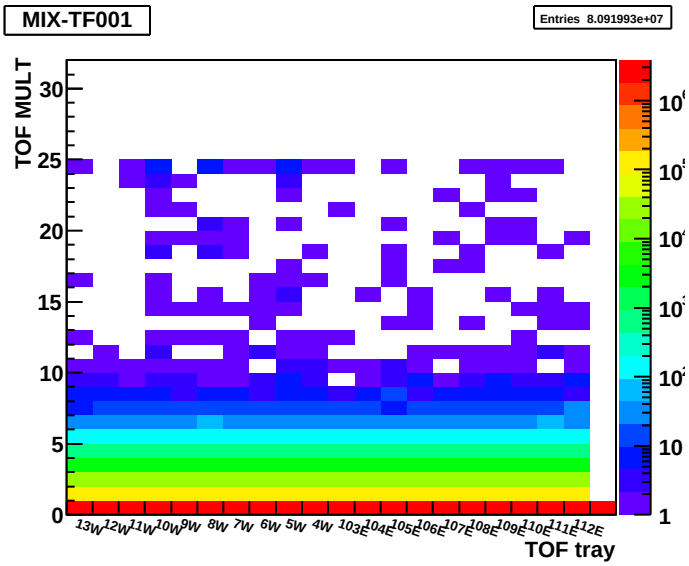

Endcap EMC L0 Input - High Tower
Entries 1.733999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

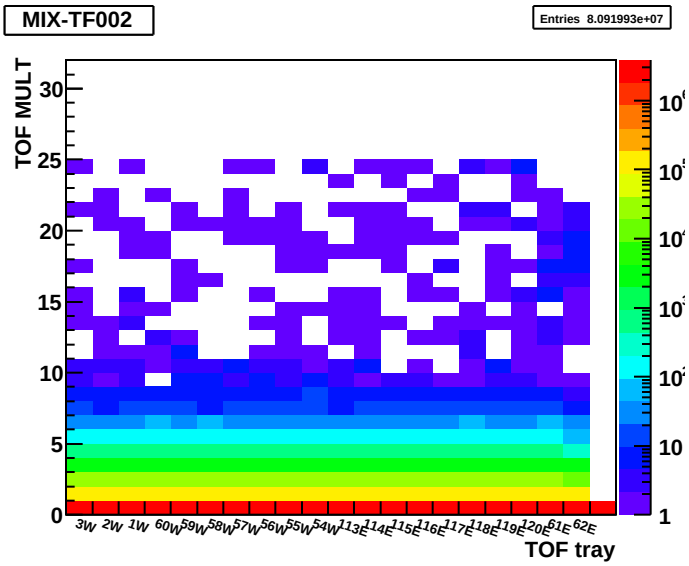

Endcap EMC L0 Input - Patch Sum
Entries 1.733999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

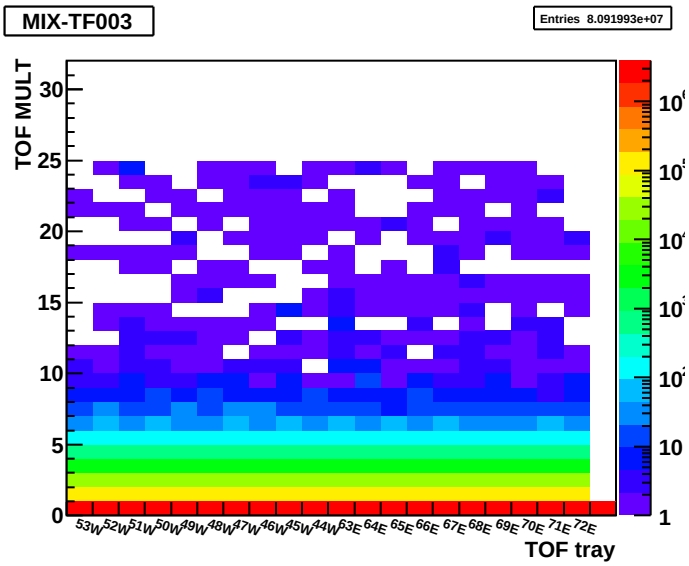

Barrel Jet Patches
Entries 3.467997e+07
JP ADC
JP ID

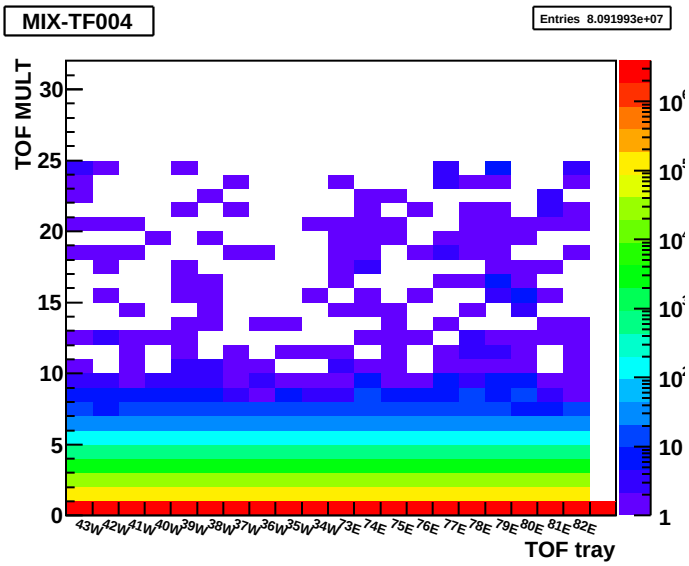

Endcap Jet Patches
Entries 1.155999e+07
JP ADC
JP ID

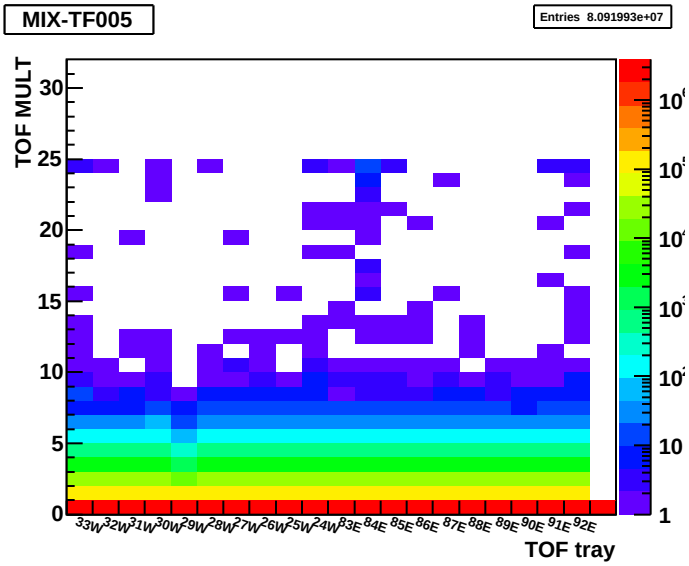

Hybrid Jet Patches
Entries 3853330
JP ADC
JP ID

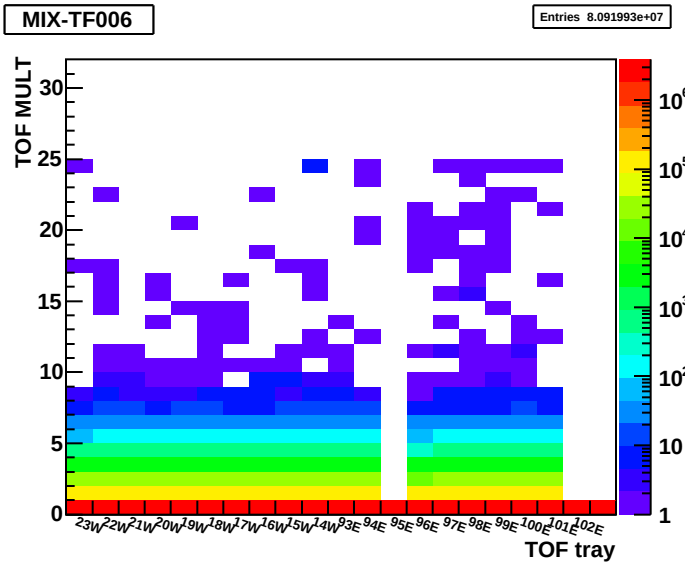

MIX-TF001
Entries 8.091993e+07
TOF MULT
TOF tray

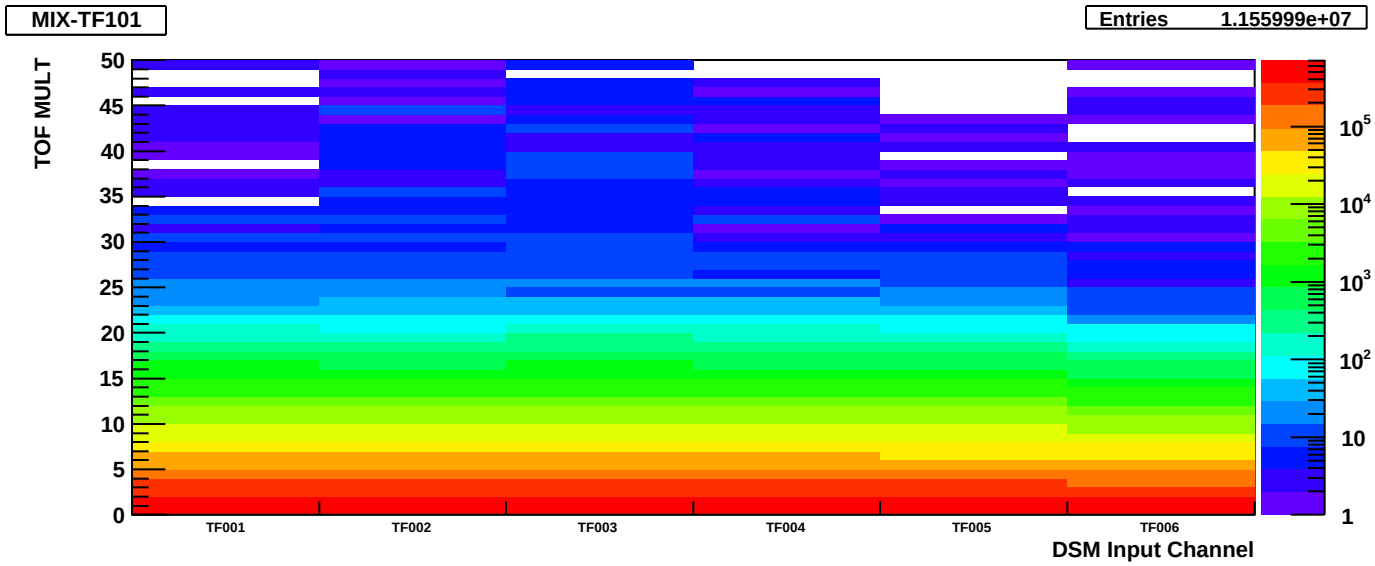

MIX-TF002
Entries 8.091993e+07
TOF MULT
TOF tray

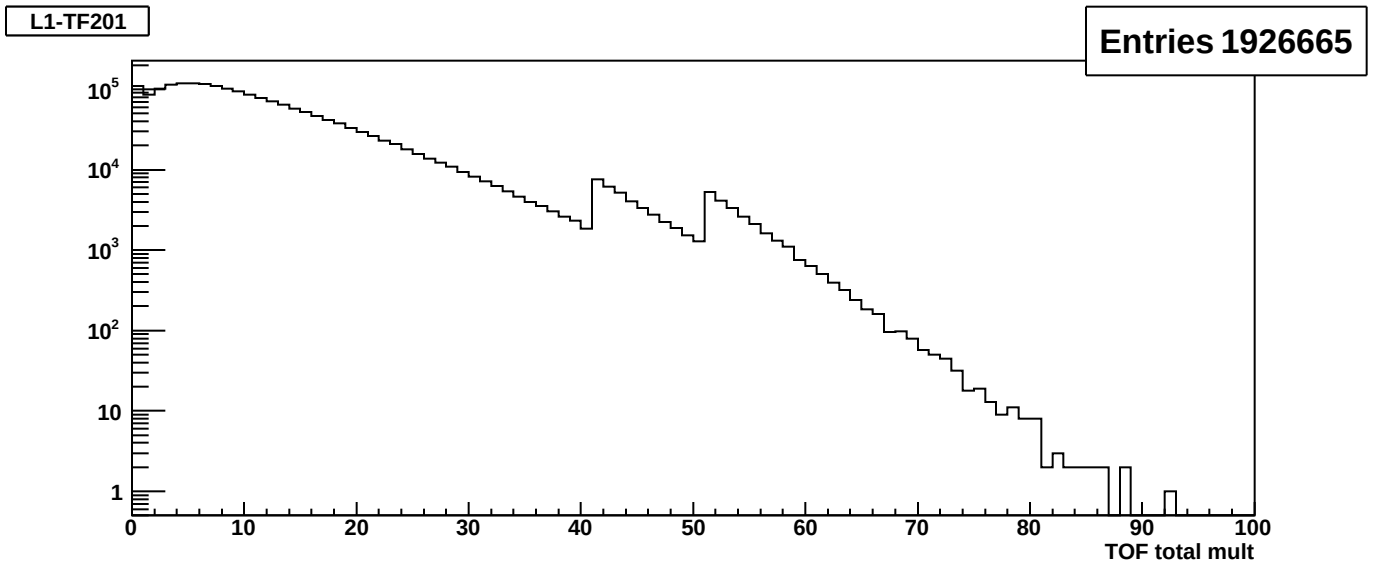

MIX-TF003
Entries 8.091993e+07
TOF MULT
TOF tray

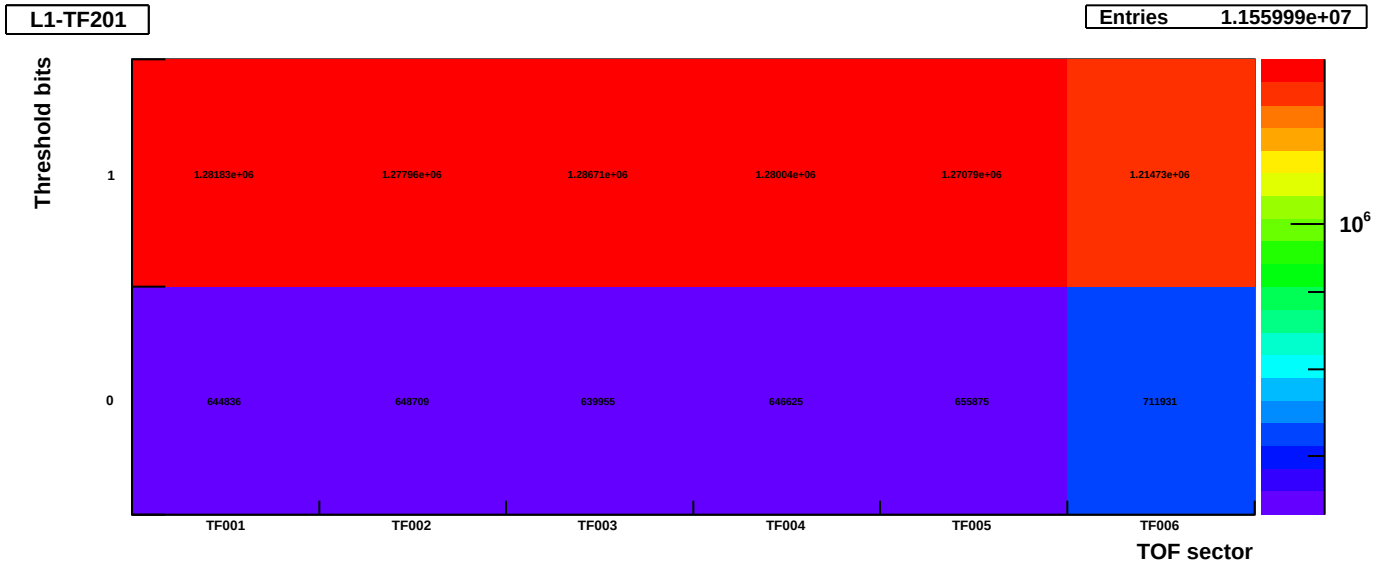

MIX-TF004
Entries 8.091993e+07
TOF MULT
TOF tray

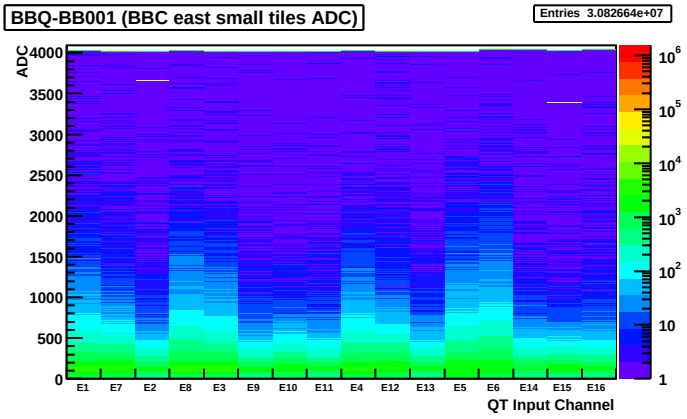

MIX-TF005
Entries 8.091993e+07
TOF MULT
TOF tray

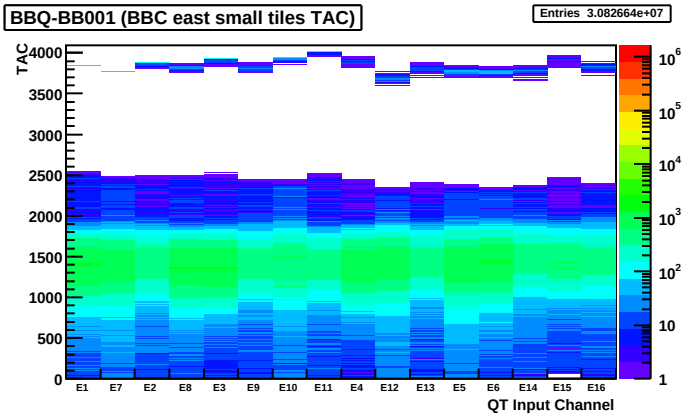

MIX-TF006
Entries 8.091993e+07
TOF MULT
TOF tray

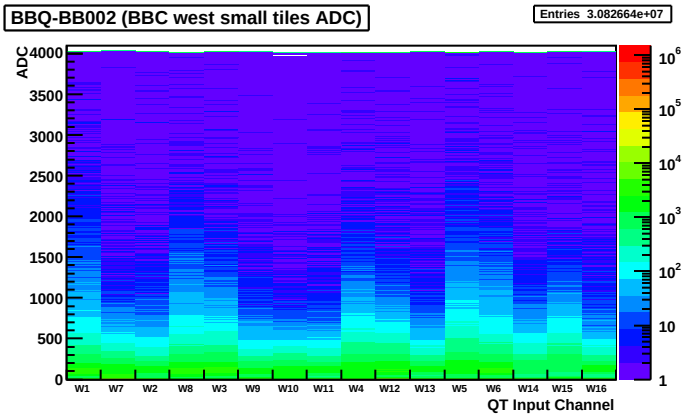

MIX-TF101
Entries 1.155999e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

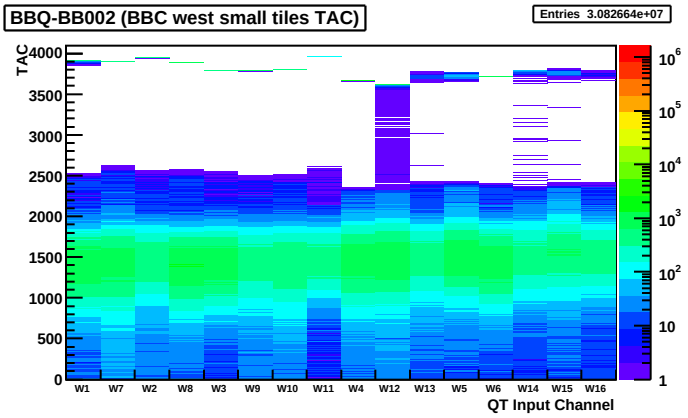

L1-TF201
Entries 1926665
TOF total mult

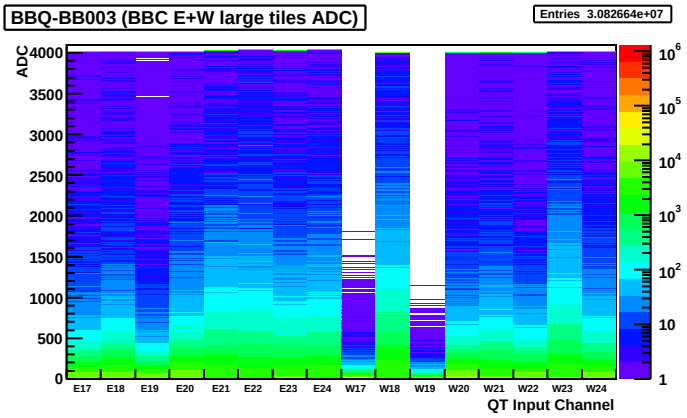

L1-TF201
Entries 1.155999e+07
Threshold bits
1.28183e+06
1.27796e+06
1.28671e+06
1.28004e+06
1.27079e+06
1.21473e+06
644836
648709
639955
646625
655875
711931
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

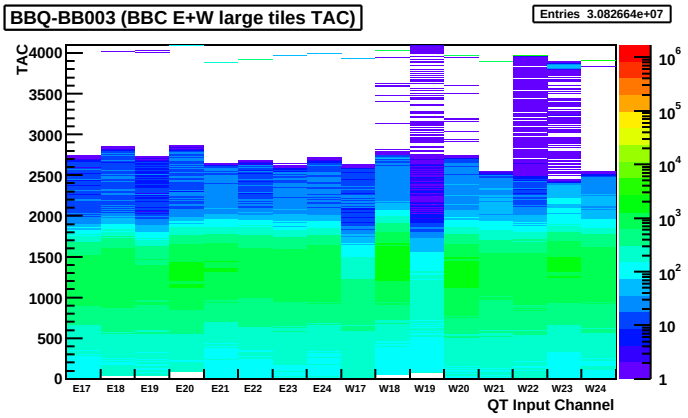

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.082664e+07
ADC
QT Input Channel

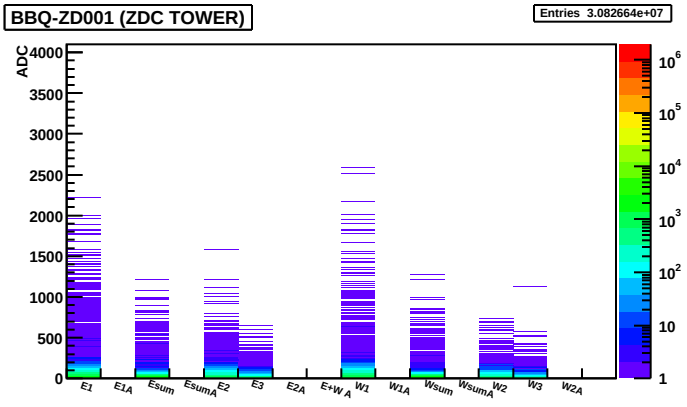

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.082664e+07
TAC
QT Input Channel

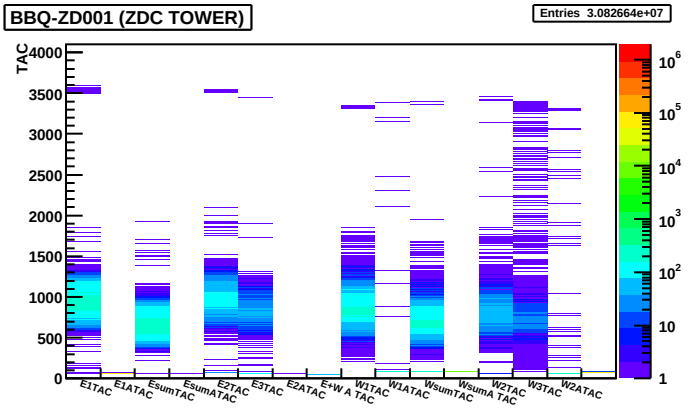

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.082664e+07
ADC
QT Input Channel

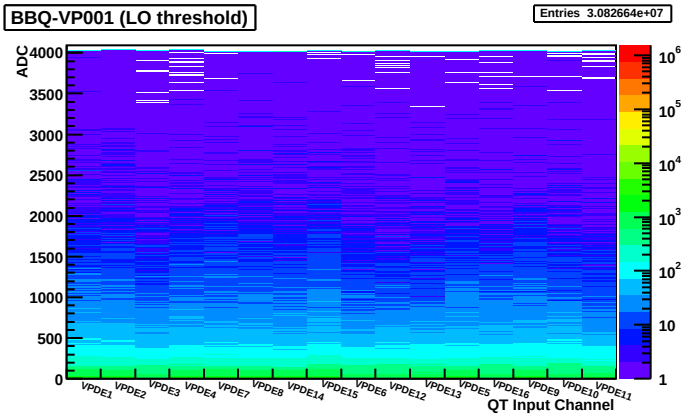

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.082664e+07
TAC
QT Input Channel

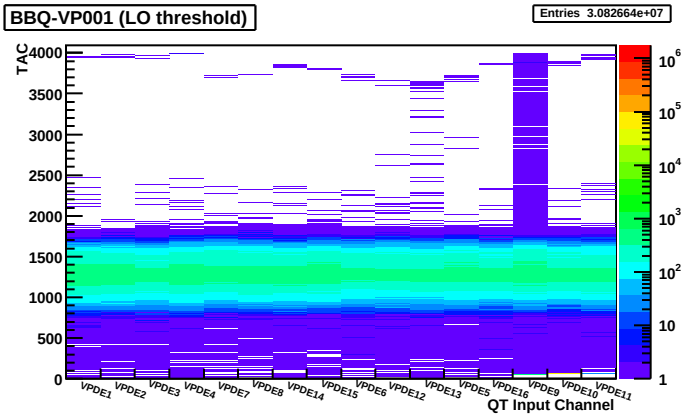

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.082664e+07
ADC
QT Input Channel

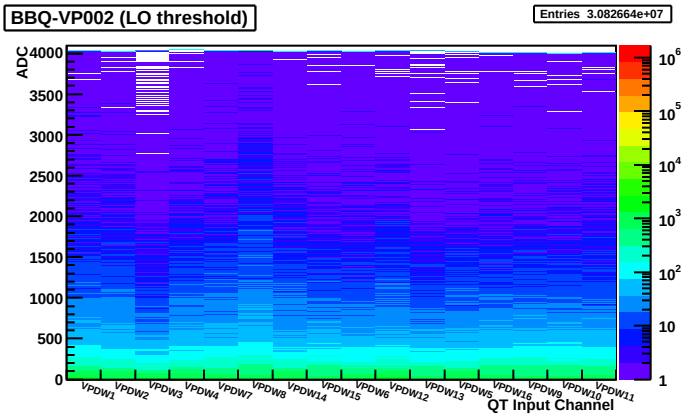

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.082664e+07
TAC
QT Input Channel

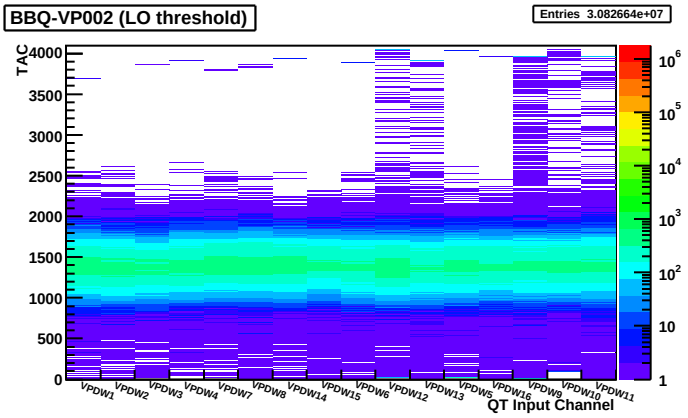

BBQ-ZD001 (ZDC TOWER)
Entries 3.082664e+07
ADC

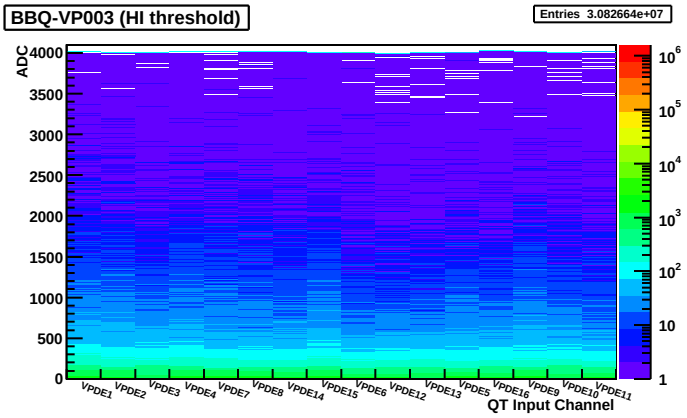

BBQ-ZD001 (ZDC TOWER)
Entries 3.082664e+07
TAC

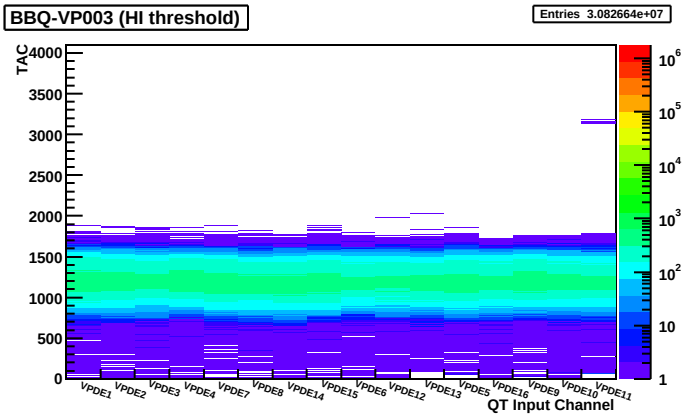

BBQ-VP001 (LO threshold)
Entries 3.082664e+07
ADC
QT Input Channel

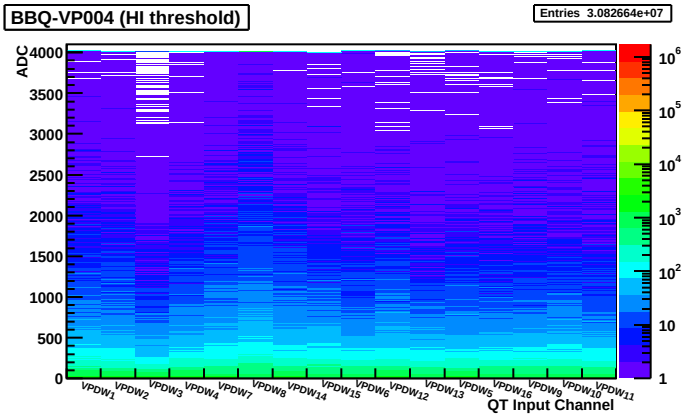

BBQ-VP001 (LO threshold)
Entries 3.082664e+07
TAC
QT Input Channel

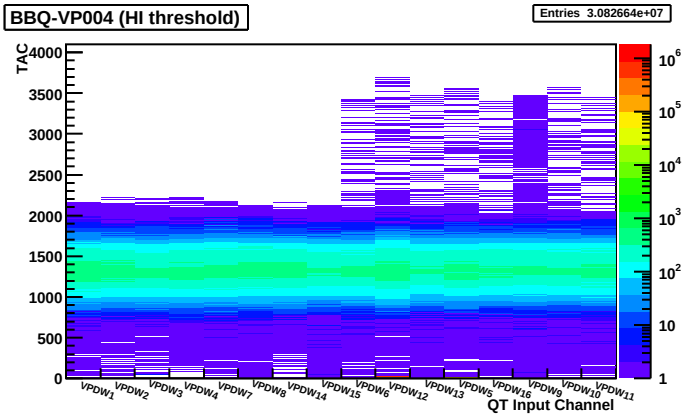

BBQ-VP002 (LO threshold)
Entries 3.082664e+07
ADC
QT Input Channel

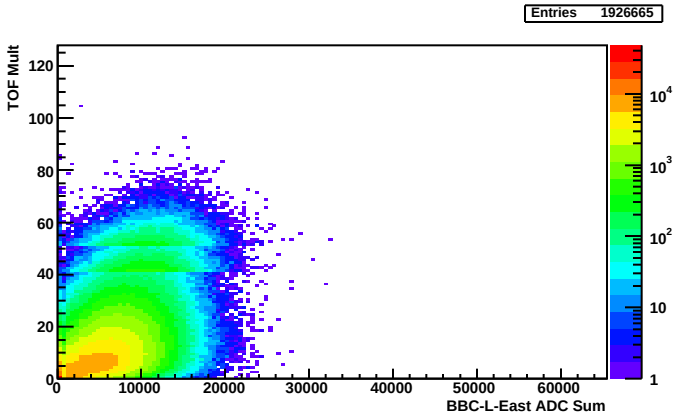

BBQ-VP002 (LO threshold)
Entries 3.082664e+07
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3.082664e+07
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3.082664e+07
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3.082664e+07
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3.082664e+07
TAC
QT Input Channel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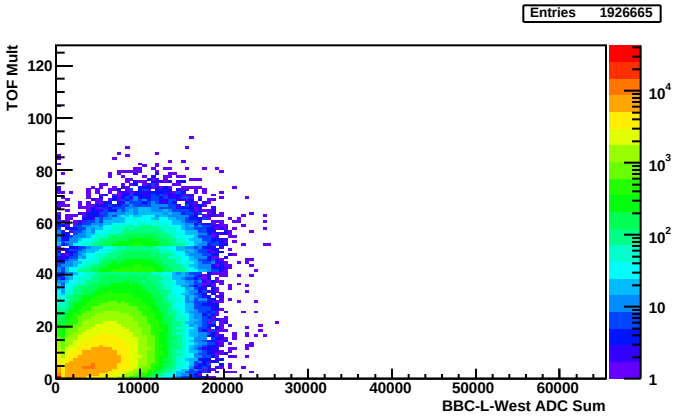
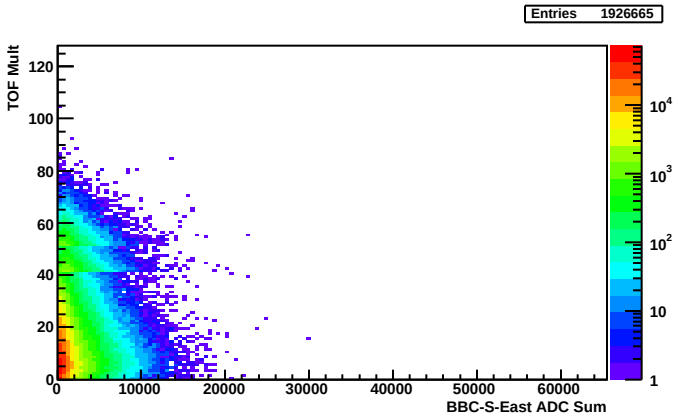
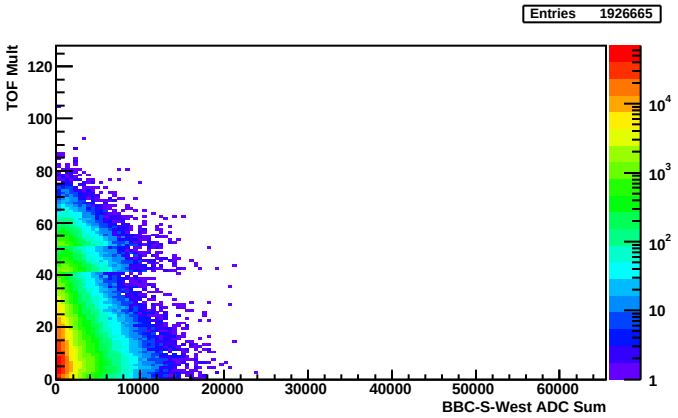
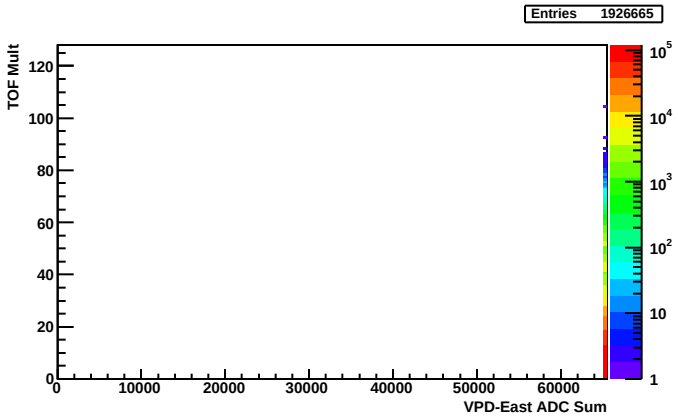
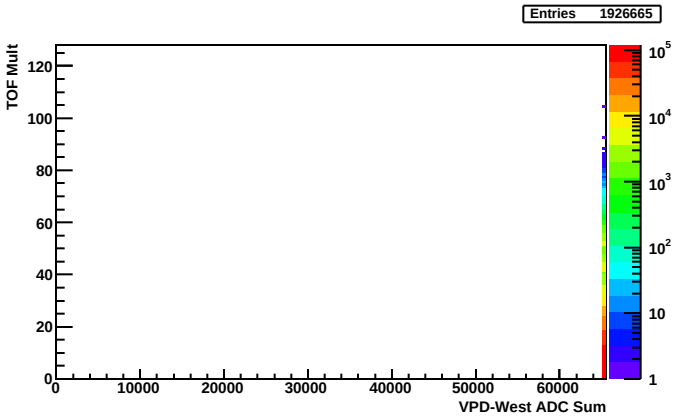
TOF Mult
BBC-L-East ADC Sum

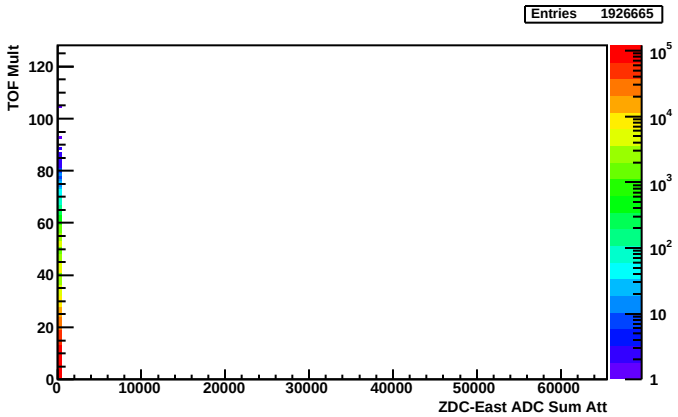

Entries 1926665
TOF Mult
BBC-L-West ADC Sum

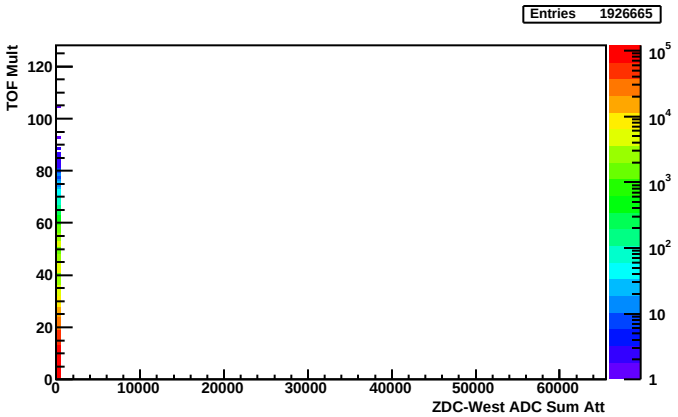

Entries 1926665
TOF Mult
BBC-S-East ADC Sum

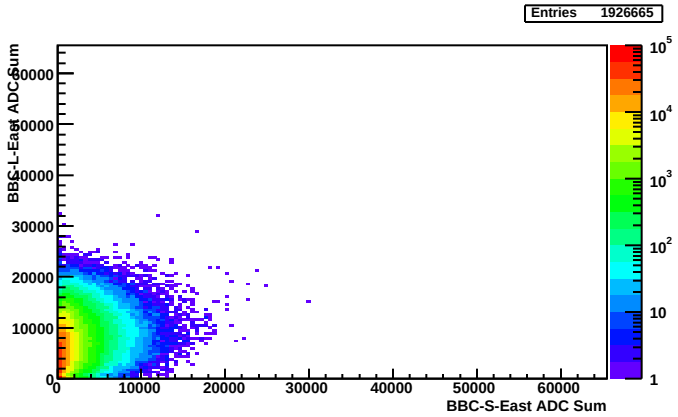

Entries 1926665
TOF Mult
BBC-S-West ADC Sum

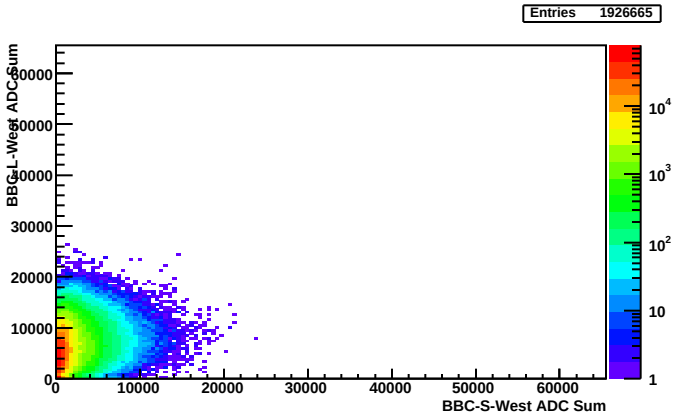

Entries 1926665
TOF Mult
VPD-East ADC Sum

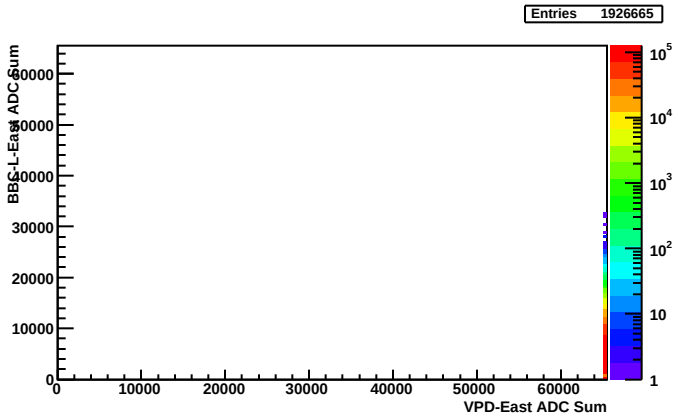

Entries 1926665
TOF Mult
VPD-West ADC Sum


Entries 1926665
TOF Mult
ZDC-East ADC Sum Att


Entries 1926665
TOF Mult
ZDC-West ADC Sum Att


Entries 1926665
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1926665
BBC-L-West ADC Sum
BBC-S-West ADC Sum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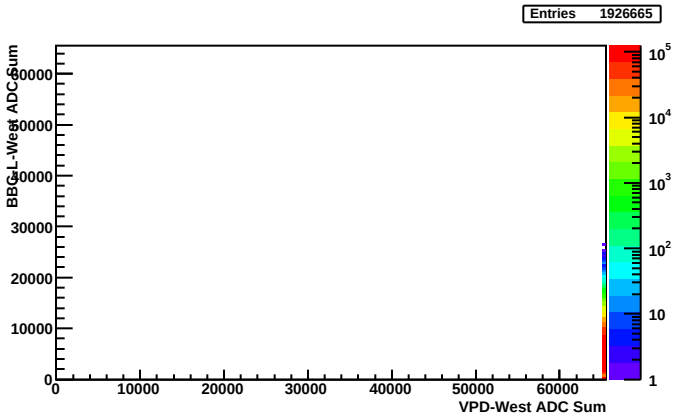
BBC-L-East ADC Sum
VPD-East ADC Sum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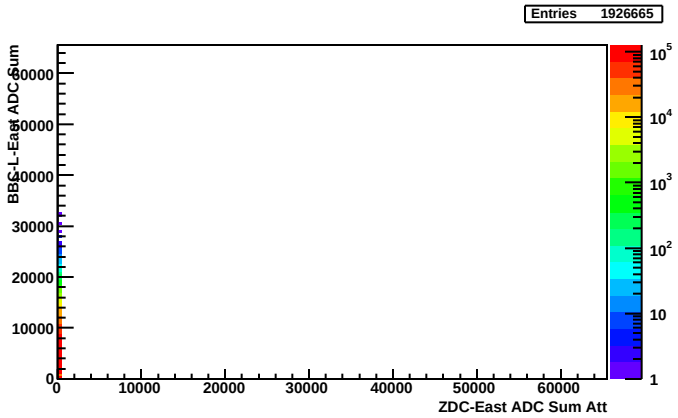
BBC-L-West ADC Sum
VPD-West ADC Sum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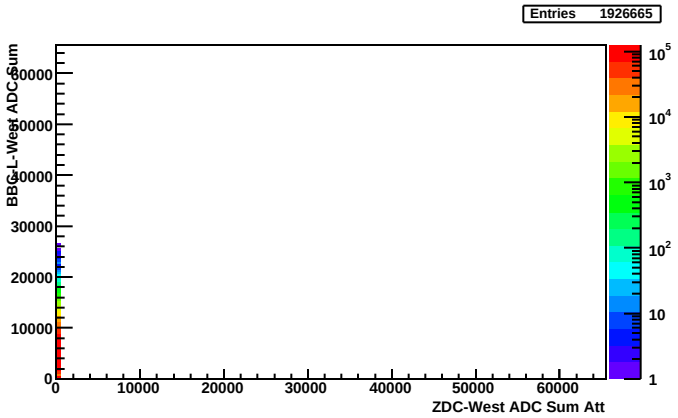
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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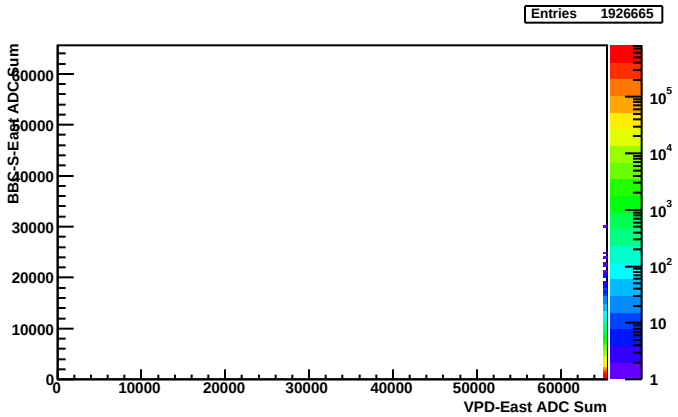
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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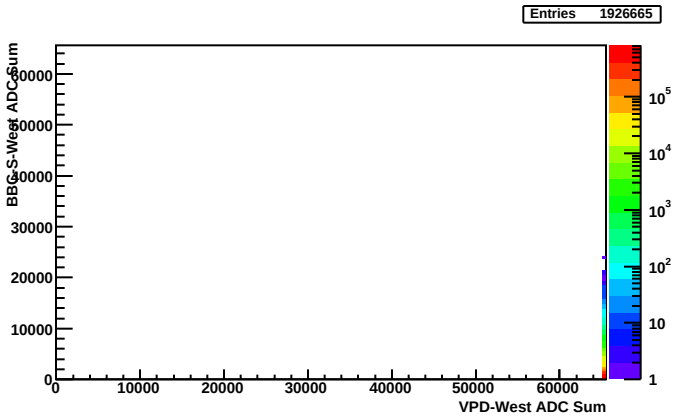
BBC-S-East ADC Sum
VPD-East ADC Sum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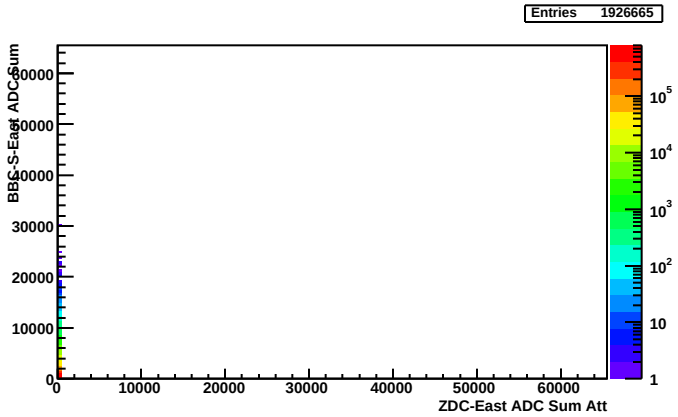
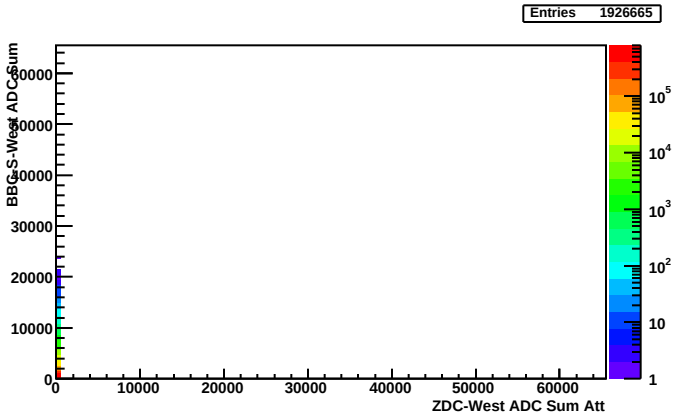
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1926665
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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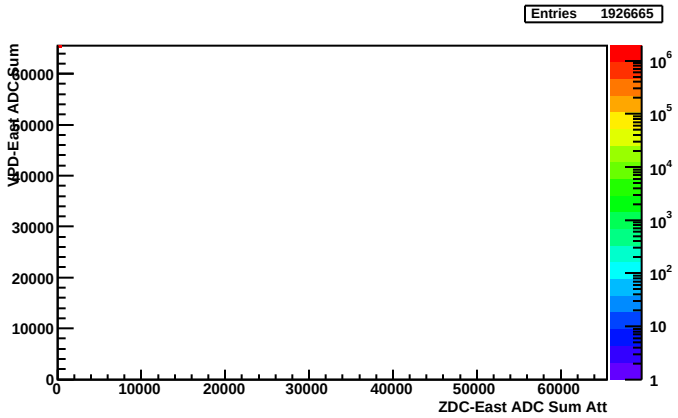
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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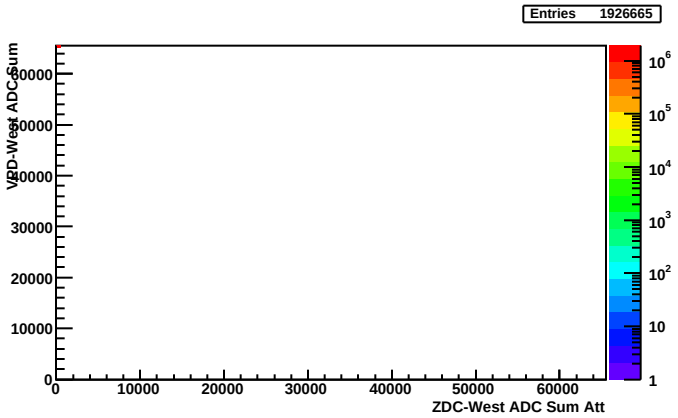
VPD-East ADC Sum
ZDC-East ADC Sum Att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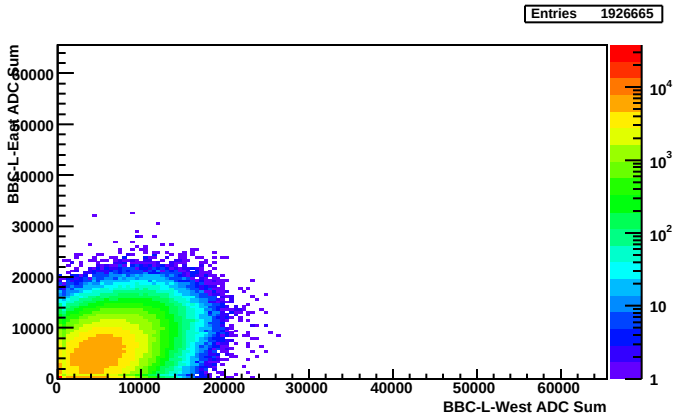
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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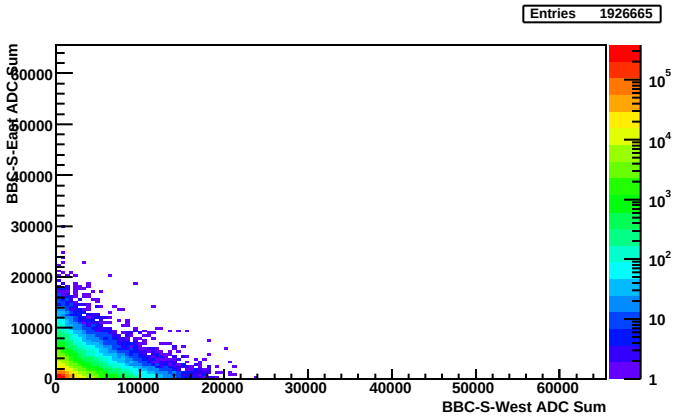
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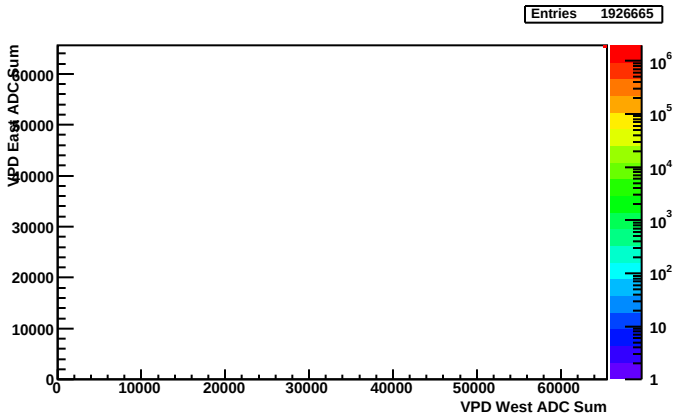
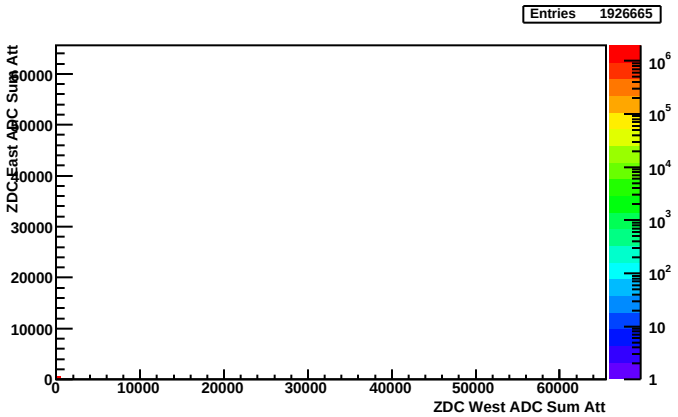
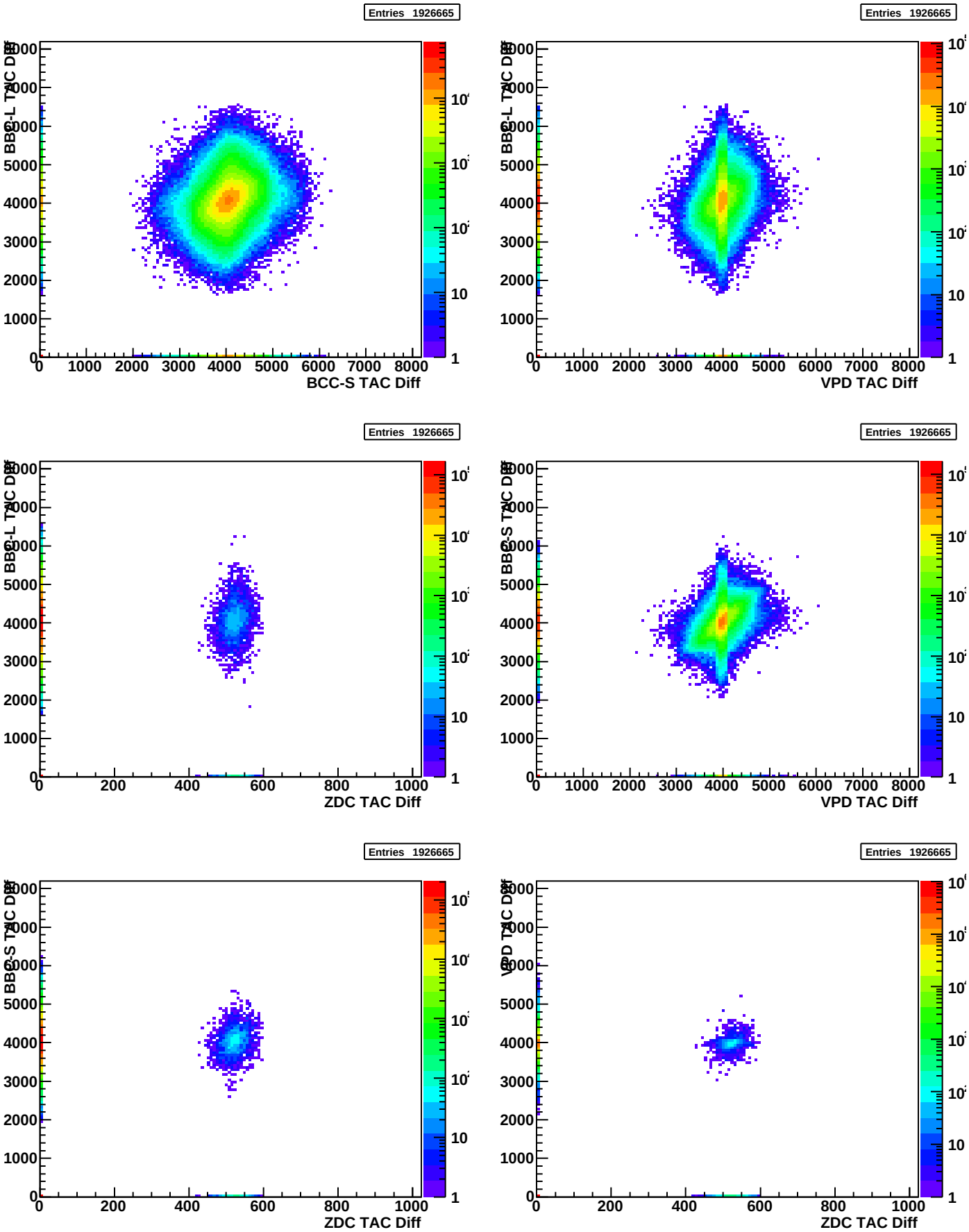
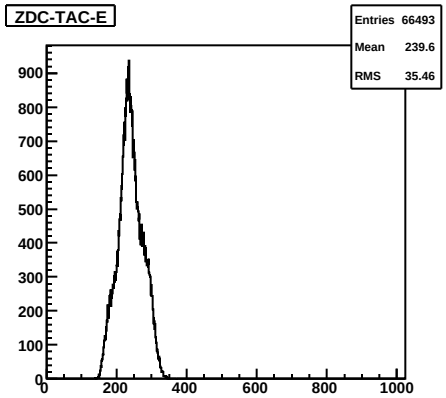
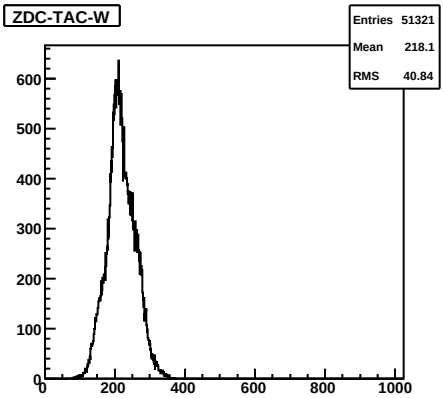
BBC-S-East ADC Sum
BBC-S-West ADC Sum

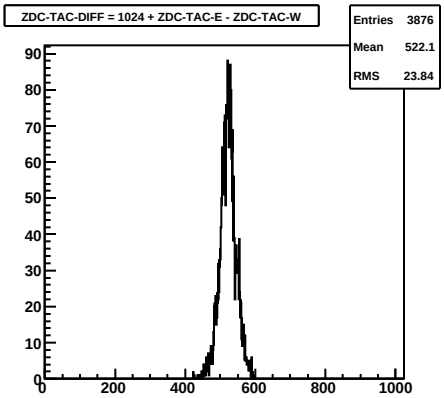

Entries 1926665
VPD East ADC Sum
VPD West ADC Sum

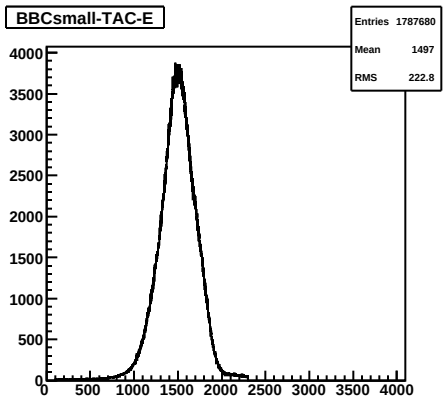

Entries 1926665
ZDC East ADC Sum Att
ZDC West ADC Sum Att

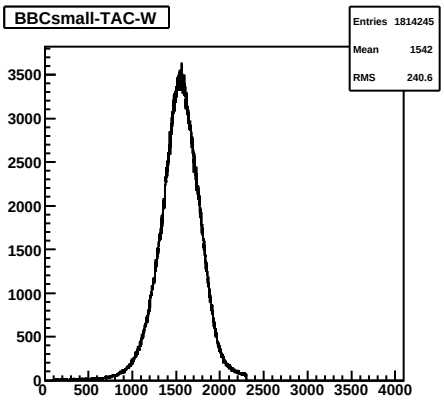

Entries 1926665
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1926665
BBC-L TAC Diff
VPD TAC Diff
Entries 1926665
BBC-L TAC Diff
ZDC TAC Diff
Entries 1926665
BBC-S TAC Diff
VPD TAC Diff
Entries 1926665
BBC-S TAC Diff
ZDC TAC Diff
Entries 1926665
VPD TAC Diff
ZDC TAC Diff

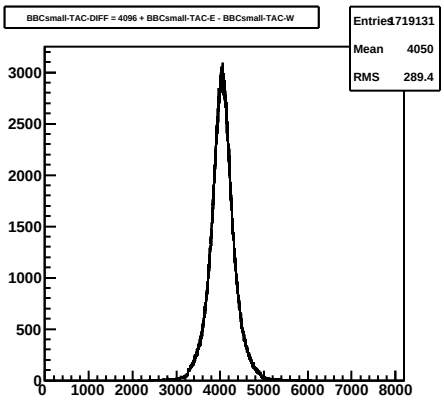

ZDC-TAC-E
Entries 66493
Mean 239.6
RMS 35.46

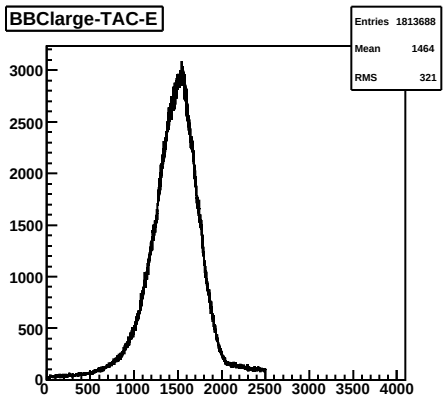

ZDC-TAC-W
Entries 51321
Mean 218.1
RMS 40.84

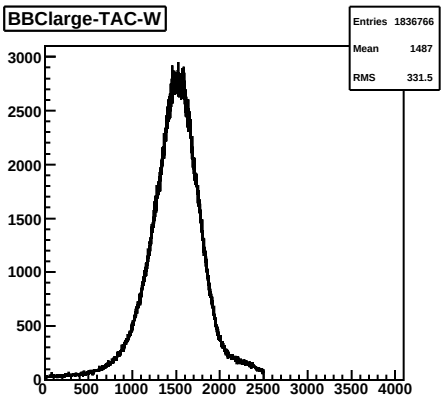

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3876
Mean 522.1
RMS 23.84

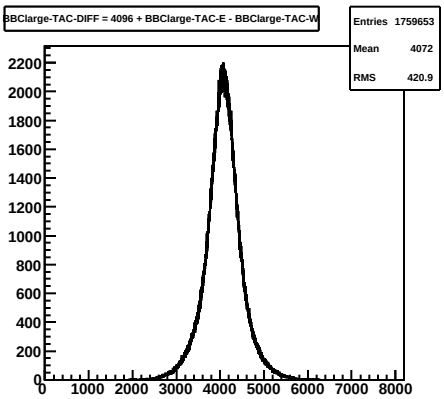

BBCsmall-TAC-E
Entries 1787680
Mean 1497
RMS 222.8

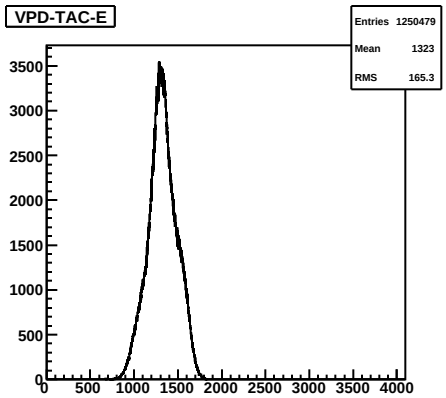

BBCsmall-TAC-W
Entries 1814245
Mean 1542
RMS 240.6

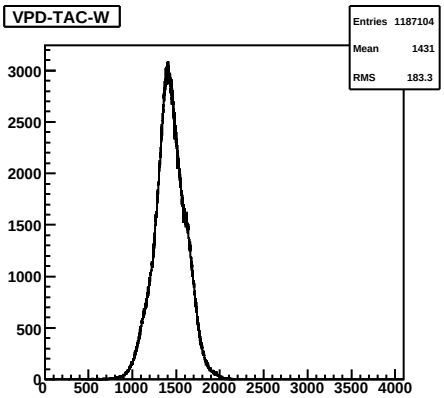

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1719131
Mean 4050
RMS 289.4

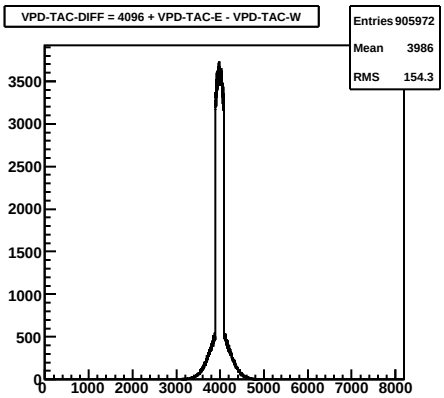

BBClarge-TAC-E
Entries 1813688
Mean 1464
RMS 321

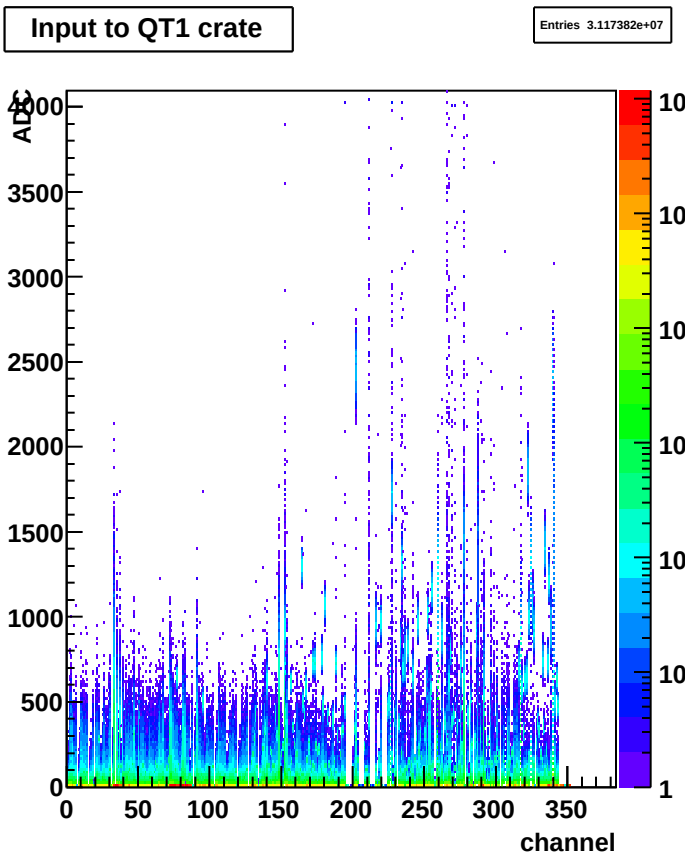

BBClarge-TAC-W
Entries 1836766
Mean 1487
RMS 331.5

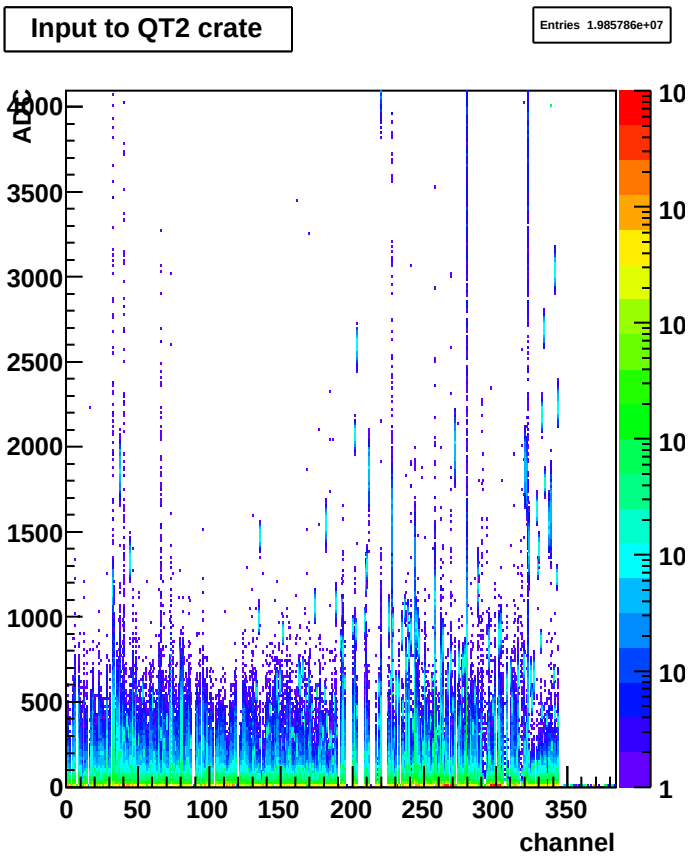

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1759653
Mean 4072
RMS 420.9

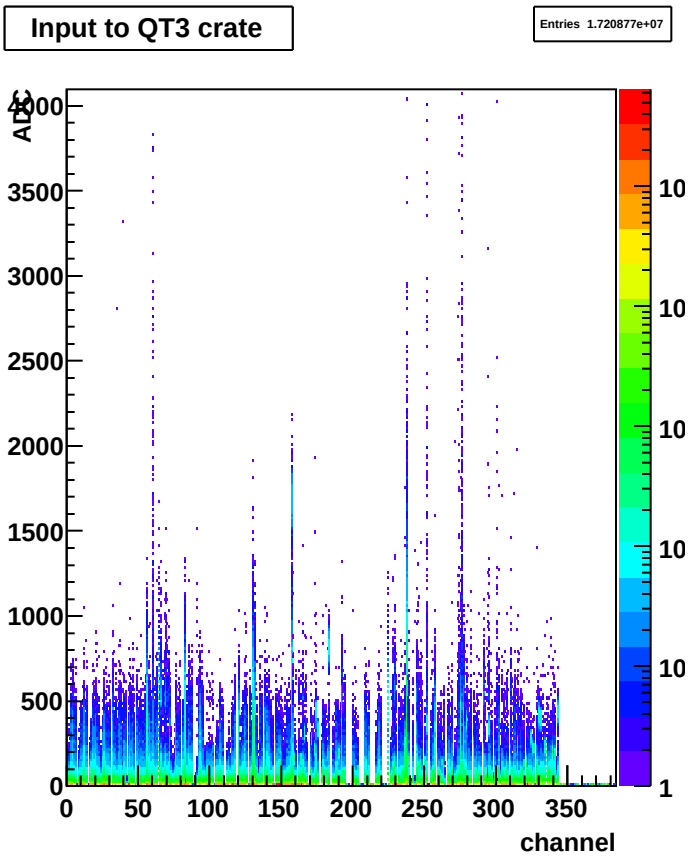

VPD-TAC-E
Entries 1250479
Mean 1323
RMS 165.3

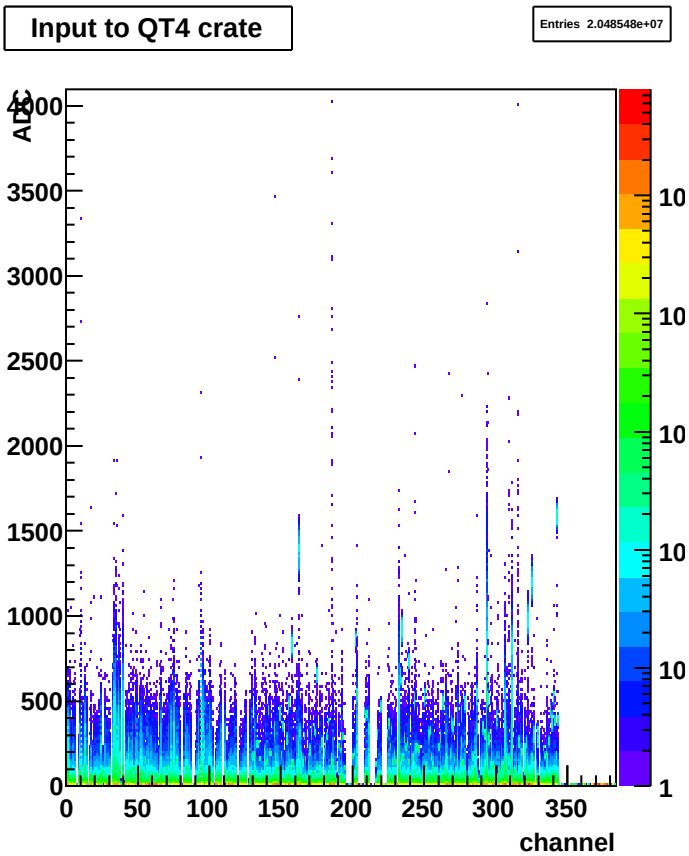

VPD-TAC-W
Entries 1187104
Mean 1431
RMS 183.3

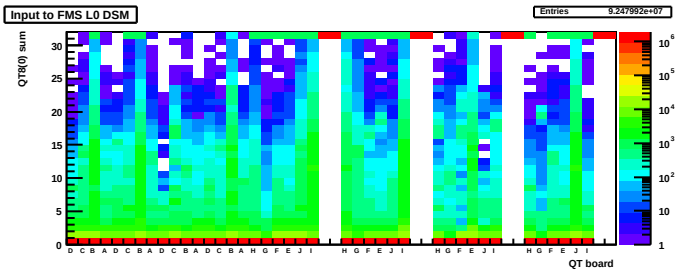

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 905972
Mean 3986
RMS 154.3

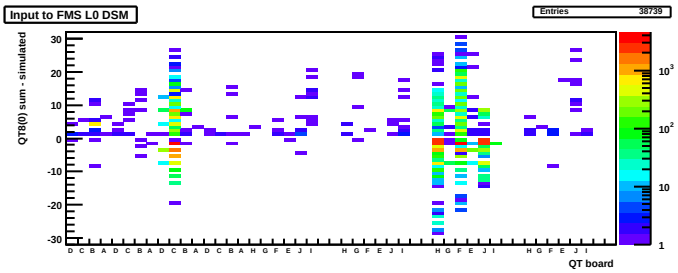

Input to QT1 crate
Entries 3.117382e+07
ADC
channel

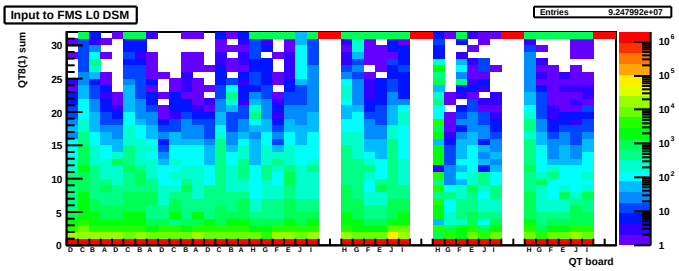

Input to QT2 crate
Entries 1.985786e+07
ADC
channel

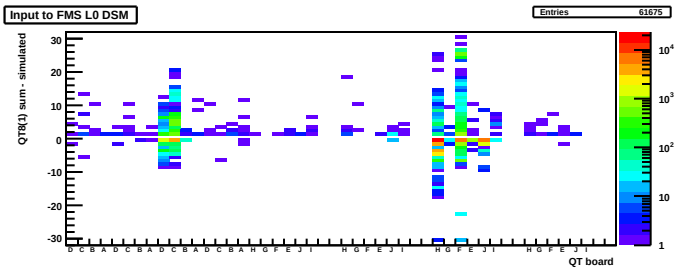

Input to QT3 crate
Entries 1.720877e+07
ADC
channel

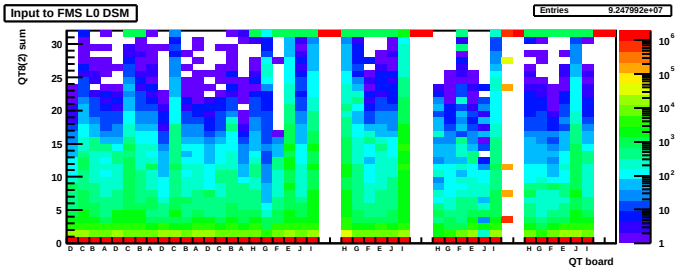

Input to QT4 crate
Entries 2.048548e+07
ADC
channel

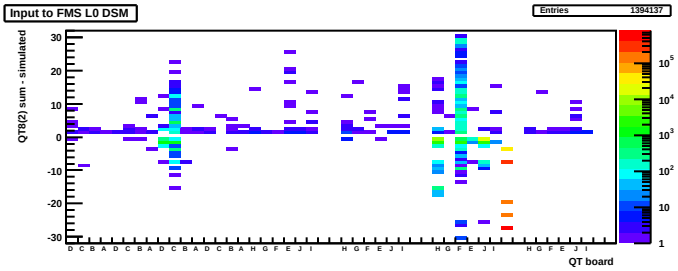

Input to FMS L0 DSM
Entries 9.247992e+07
QT8(0) sum
QT board

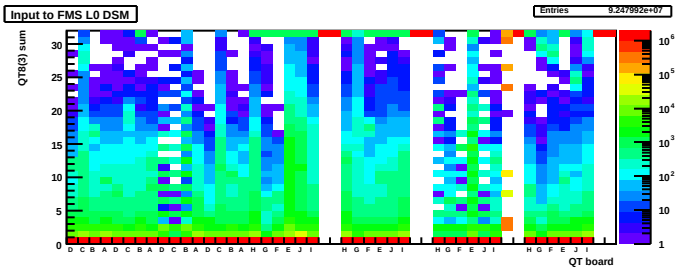

Input to FMS L0 DSM
Entries 38739
QT8(0) sum - simulated
QT board

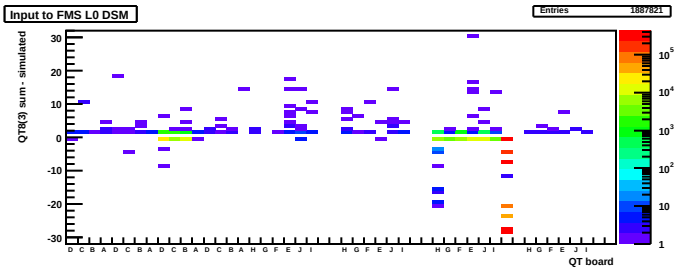

Input to FMS L0 DSM
Entries 9.247992e+07
QT8(1) sum
QT board

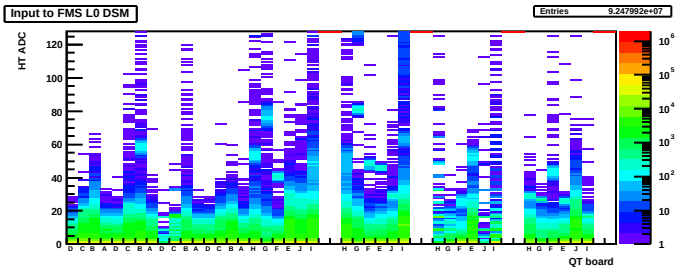

Input to FMS L0 DSM
Entries 61675
QT8(1) sum - simulated
QT board

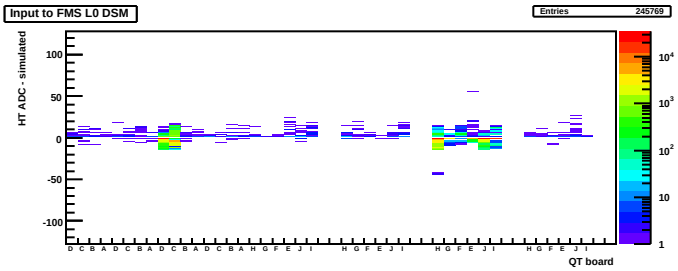

Input to FMS L0 DSM
Entries 9.247992e+07
QT8(2) sum
QT board

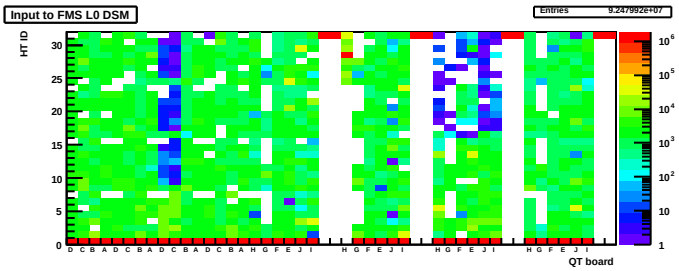

Input to FMS L0 DSM
Entries 1394137
QT8(2) sum - simulated
QT board

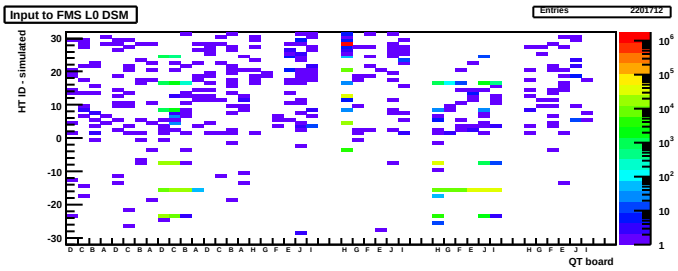

Input to FMS L0 DSM
Entries 9.247992e+07
QT8(3) sum
QT board

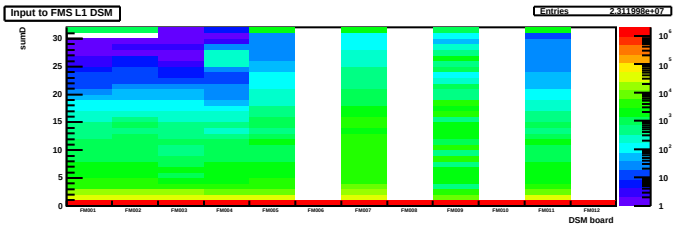

Input to FMS L0 DSM
Entries 1887821
QT8(3) sum - simulated
QT board

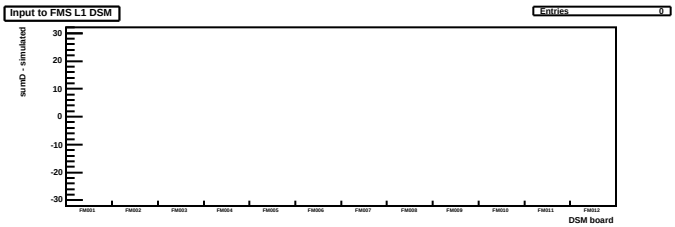

Input to FMS L0 DSM
Entries 9.247992e+07
HT ADC
QT board

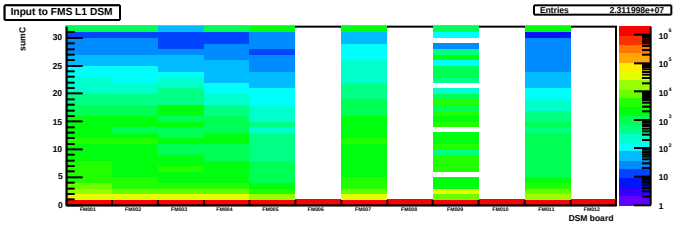

Input to FMS L0 DSM
Entries 245769
HT ADC - simulated
QT board

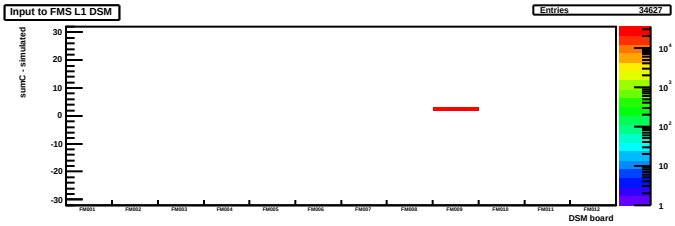

Input to FMS L0 DSM
Entries 9.247992e+07
HT ID
QT board

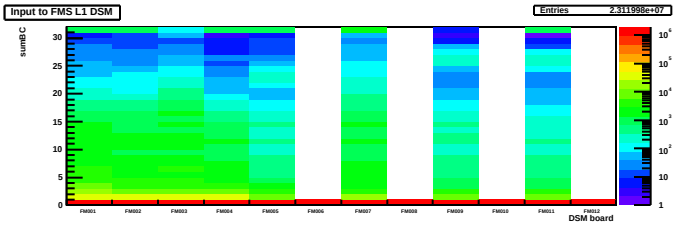

Input to FMS L0 DSM
Entries 2201712
HT ID - simulated
QT board

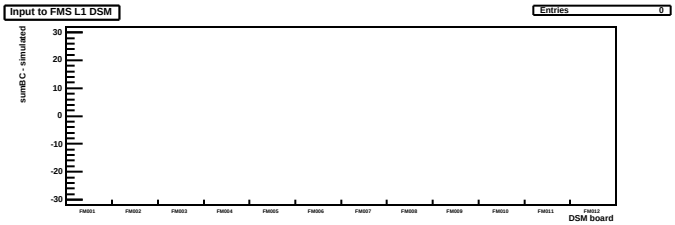

Input to FMS L1 DSM
Entries 2.311998e+07
sumD
DSM board

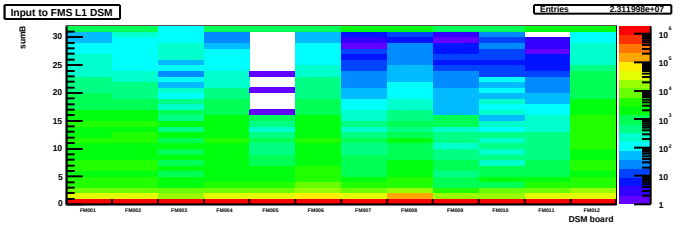

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

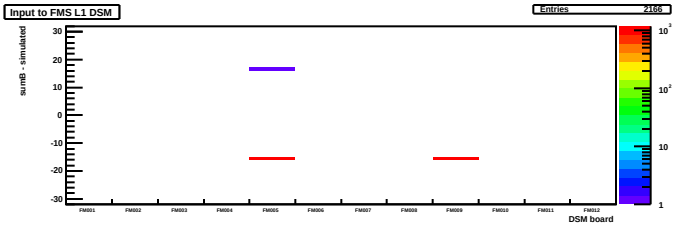

Input to FMS L1 DSM
Entries 2.311998e+07
sumC
DSM board

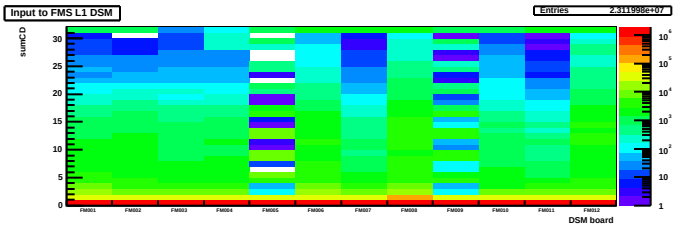

Input to FMS L1 DSM
Entries 34627
sumC - simulated
DSM board

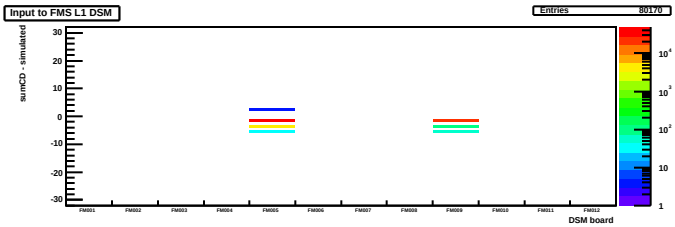

Input to FMS L1 DSM
Entries 2.311998e+07
sumBC
DSM board

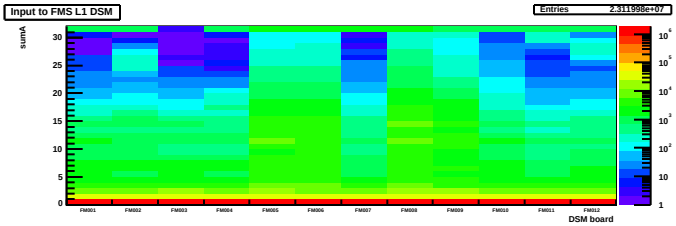

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

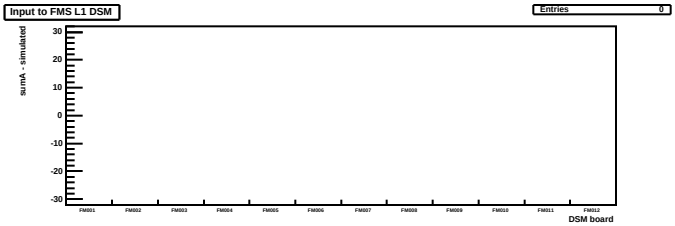

Input to FMS L1 DSM
Entries 2.311998e+07
sumB
DSM board

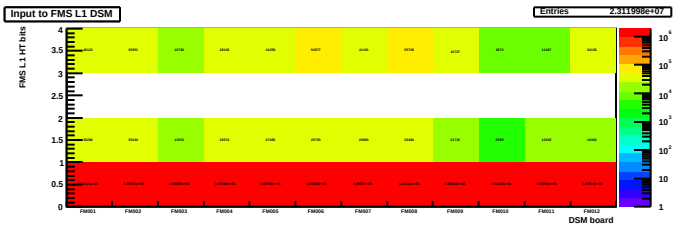

Input to FMS L1 DSM
Entries 2166
sumB - simulated
DSM board

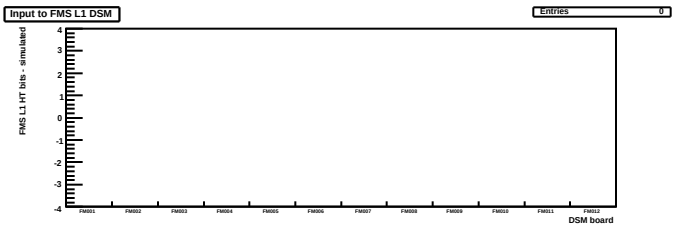

Input to FMS L1 DSM
Entries 2.311998e+07
sumCD
DSM board

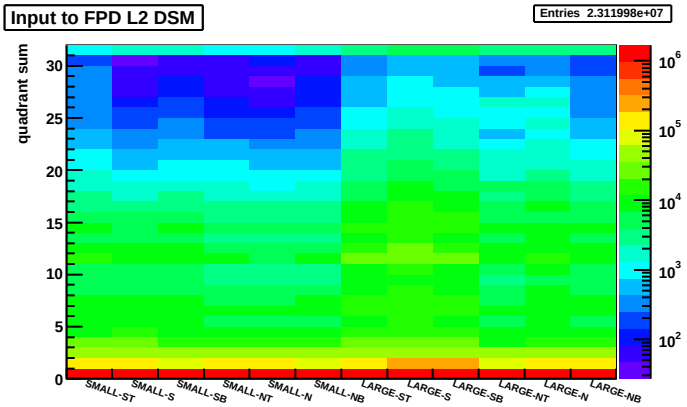

Input to FMS L1 DSM
Entries 80170
sumCD - simulated
DSM board

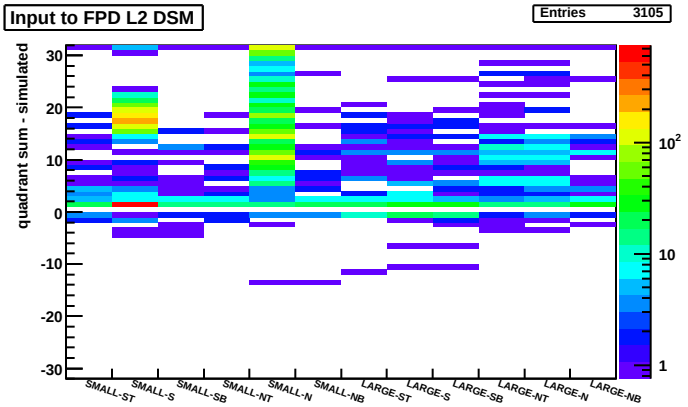

Input to FMS L1 DSM
Entries 2.311998e+07
sumA
DSM board

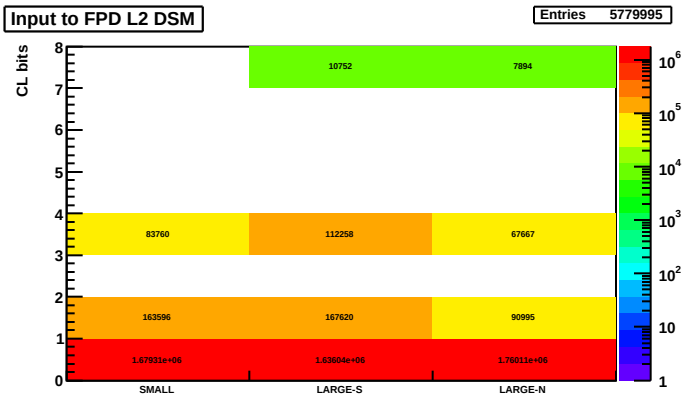

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

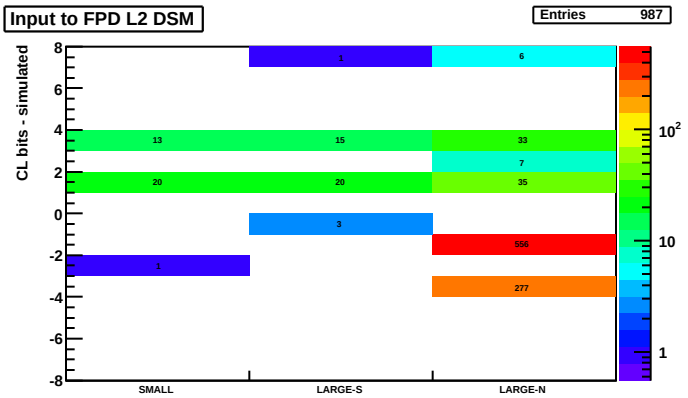

Input to FMS L1 DSM
Entries 2.311998e+07
FMS L1 HT bits
DSM board

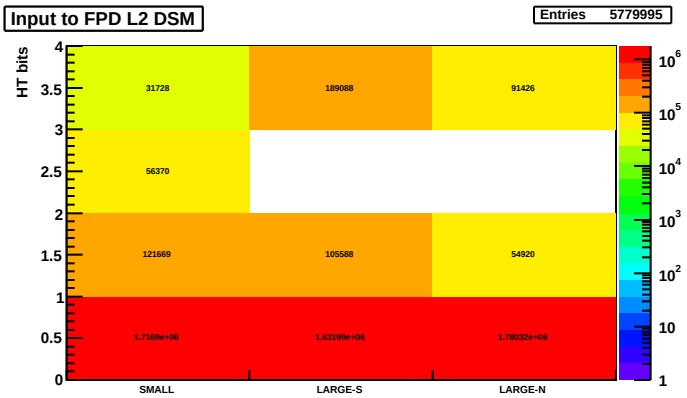

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

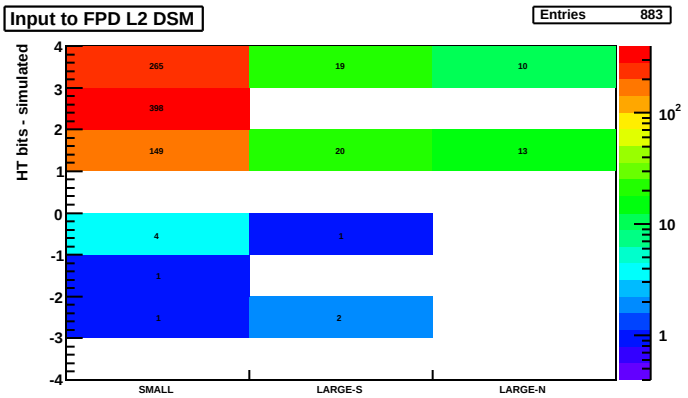

Input to FPD L2 DSM
Entries 2.311998e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

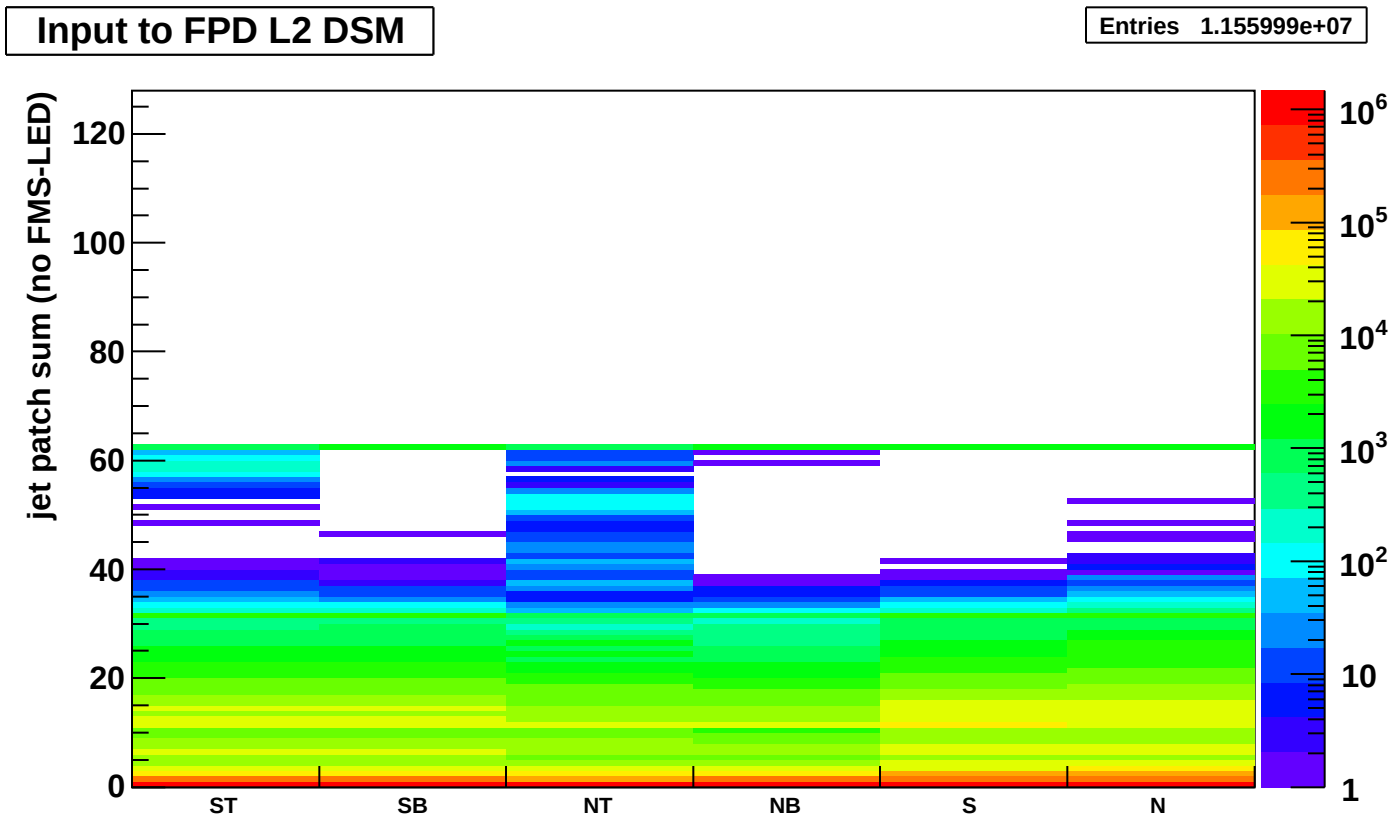

Input to FPD L2 DSM
Entries 3105
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

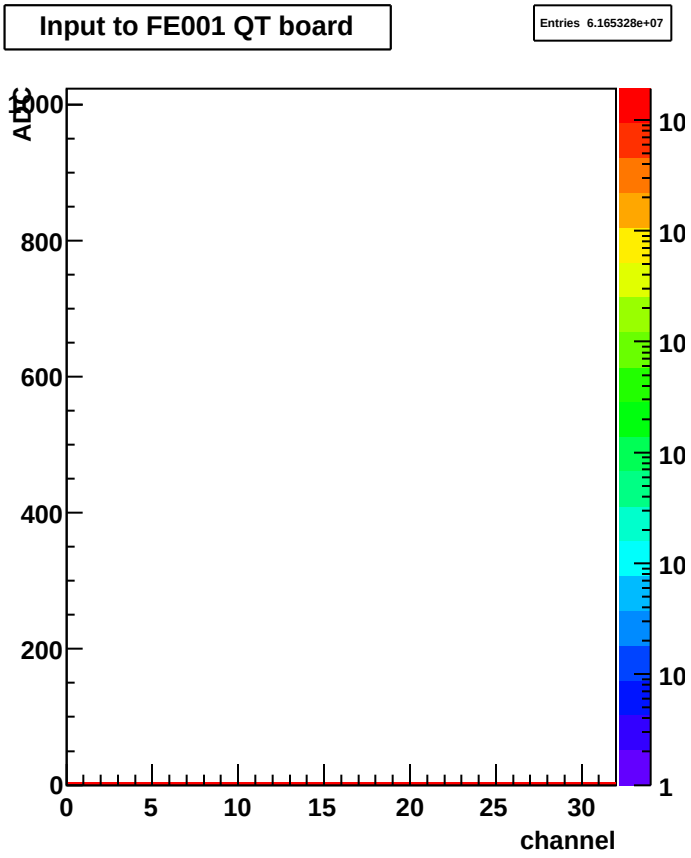

Input to FPD L2 DSM
Entries 5779995
CL bits
10752
7894
83760
112258
67667
163596
167620
90995
1.67931e+06
1.63604e+06
1.76011e+06
SMALL
LARGE-S
LARGE-N

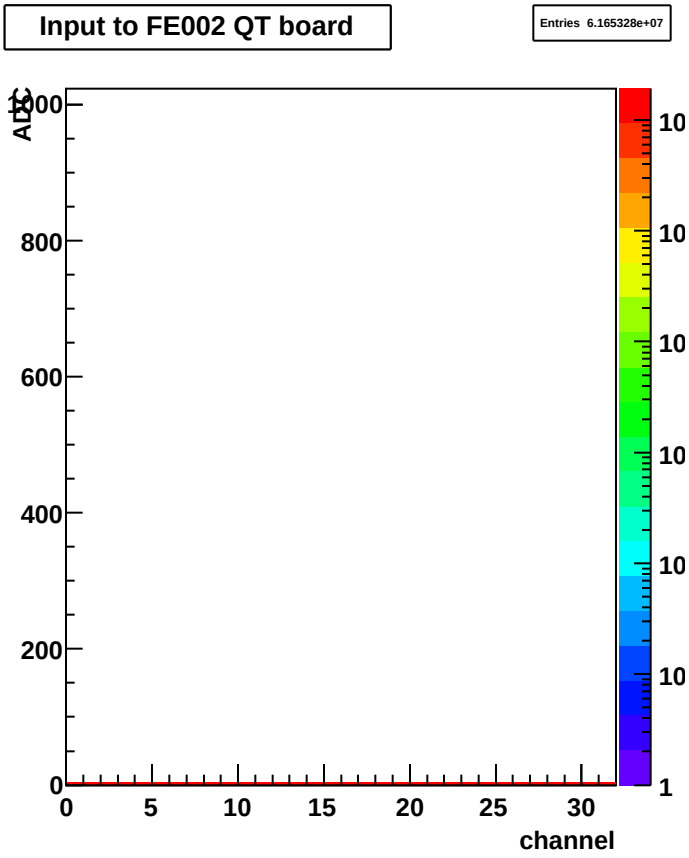

Input to FPD L2 DSM
Entries 987
CL bits - simulated
1
6
13
15
33
7
20
20
35
3
556
1
277
SMALL
LARGE-S
LARGE-N

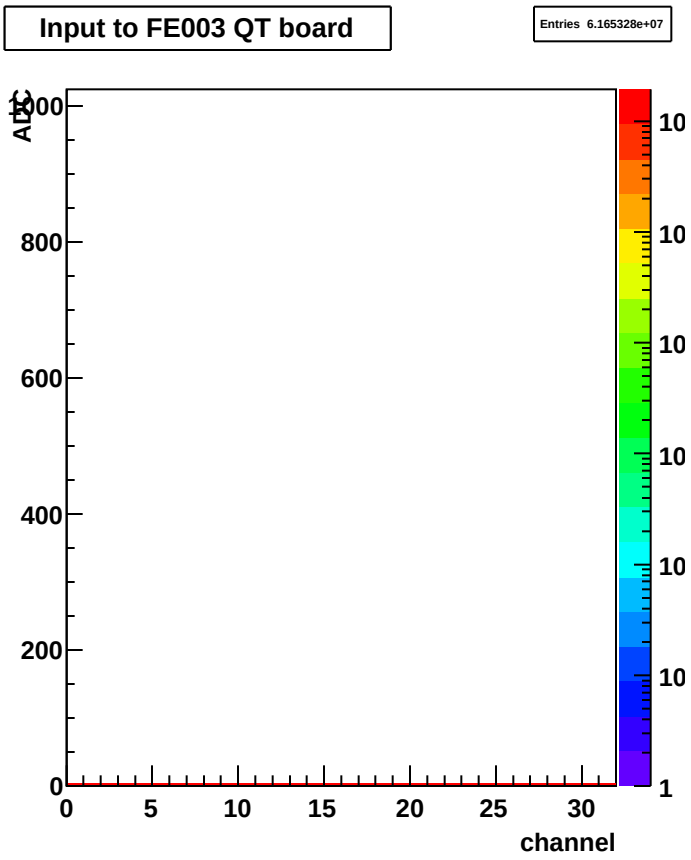

Input to FPD L2 DSM
Entries 5779995
HT bits
31728
189088
91426
56370
121669
105588
54920
1.7169e+06
1.63199e+06
1.78032e+06
SMALL
LARGE-S
LARGE-N

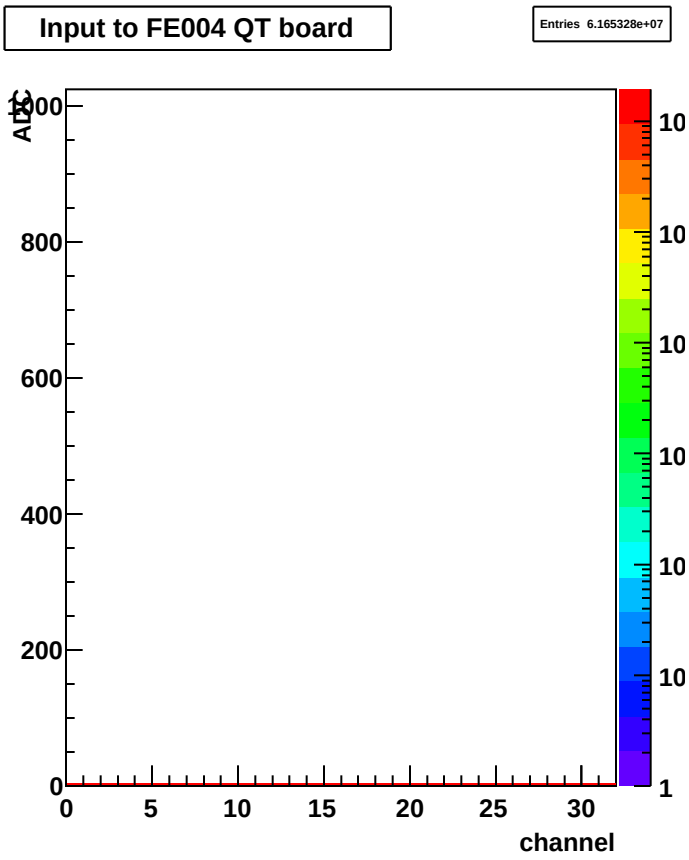

Input to FPD L2 DSM
Entries 883
HT bits - simulated
265
19
10
398
149
20
13
4
1
1
1
2
SMALL
LARGE-S
LARGE-N

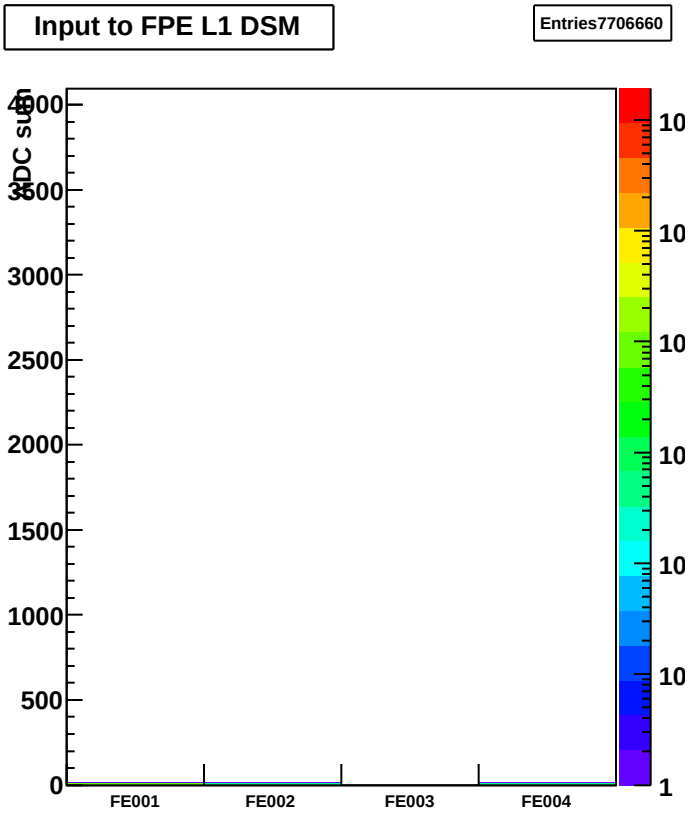

Input to FPD L2 DSM
Entries 1.155999e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

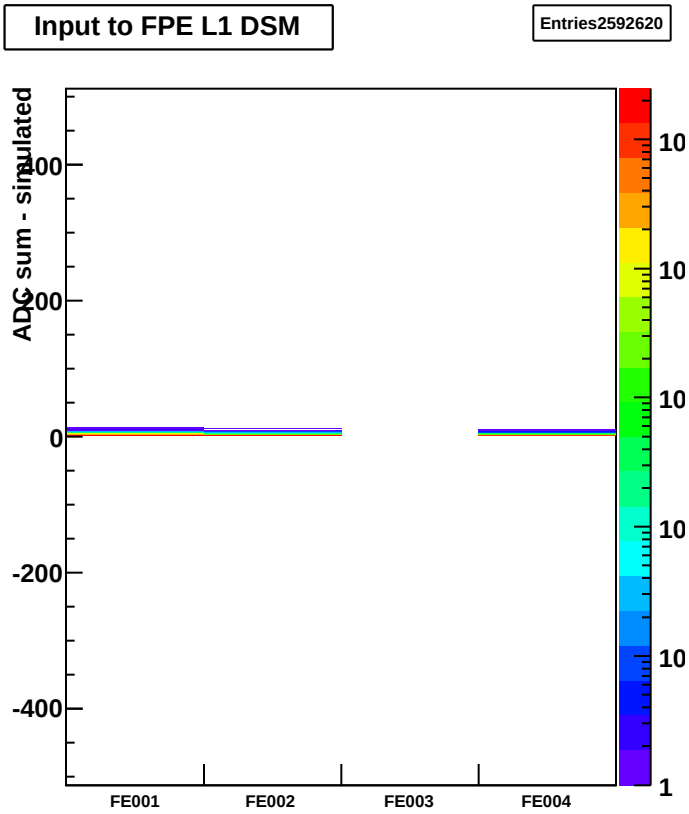

Input to FE001 QT board
Entries 6.165328e+07
ADC
channel


Input to FE002 QT board
Entries 6.165328e+07
ADC
channel

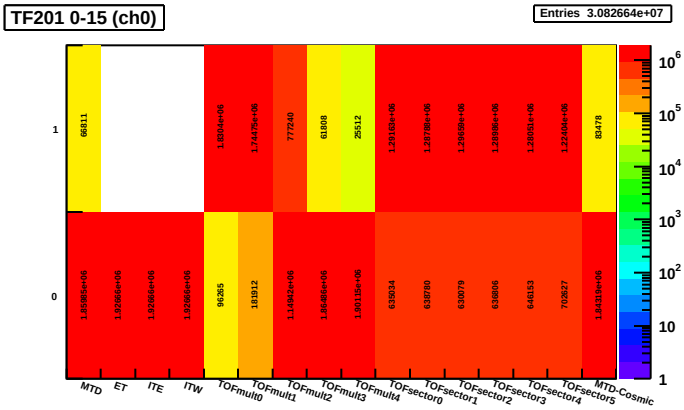

Input to FE003 QT board
Entries 6.165328e+07
ADC
channel


Input to FE004 QT board
Entries 6.165328e+07
ADC
channel

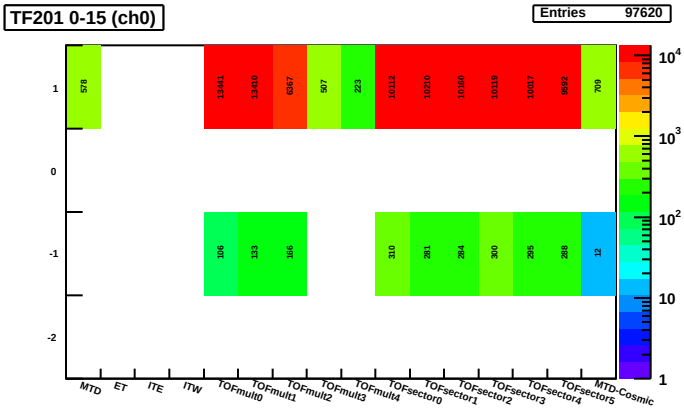

Input to FPE L1 DSM
Entries7706660
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2592620
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

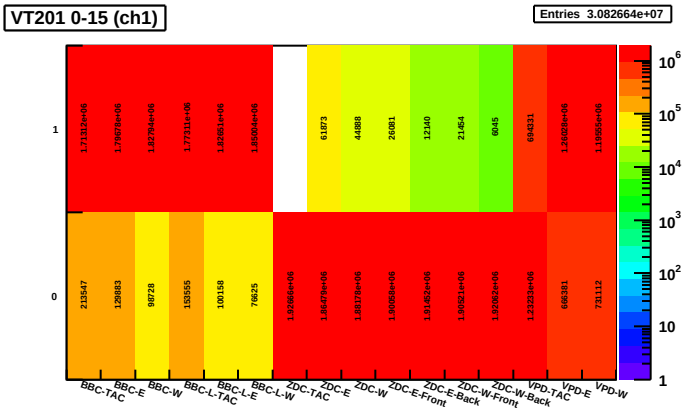
TF201 0-15 (ch0)



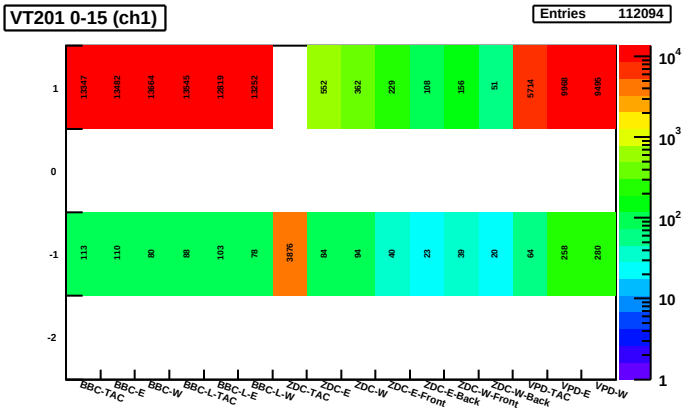
TF201 0-15 (ch0)



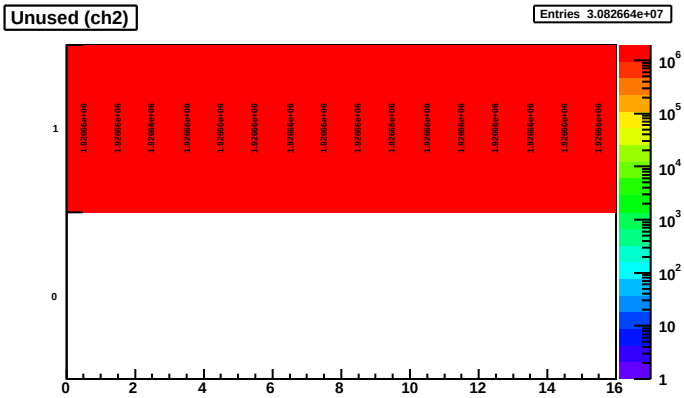
VT201 0-15 (ch1)

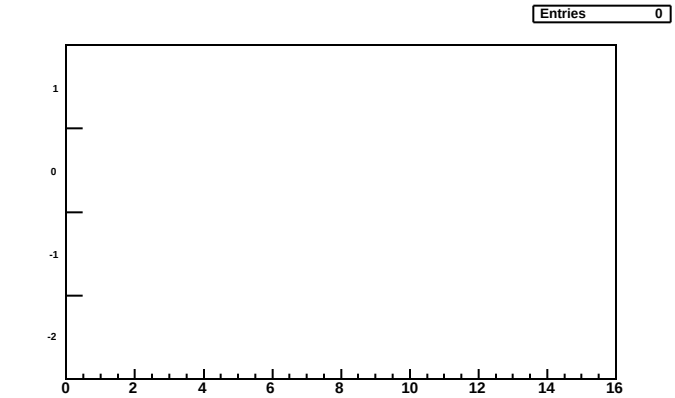


VT201 0-15 (ch1)



Unused (ch2)





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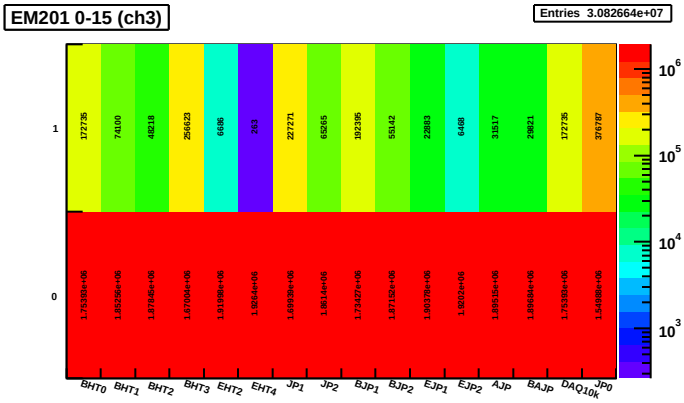

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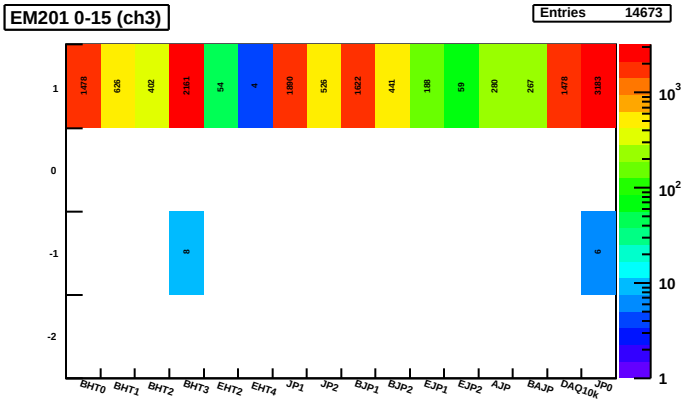
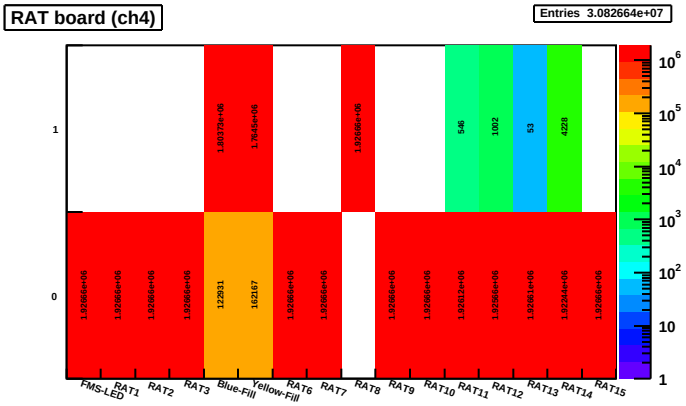
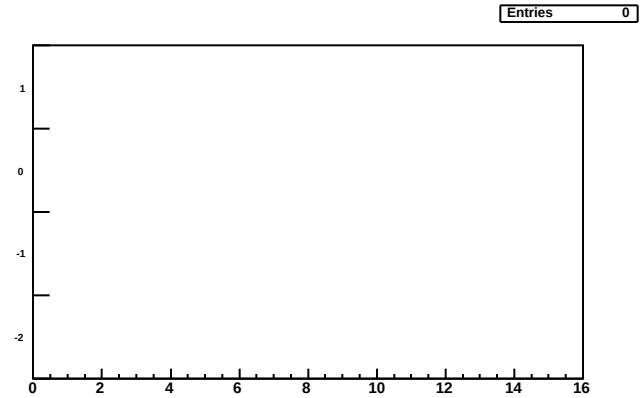


RAT board (ch4)





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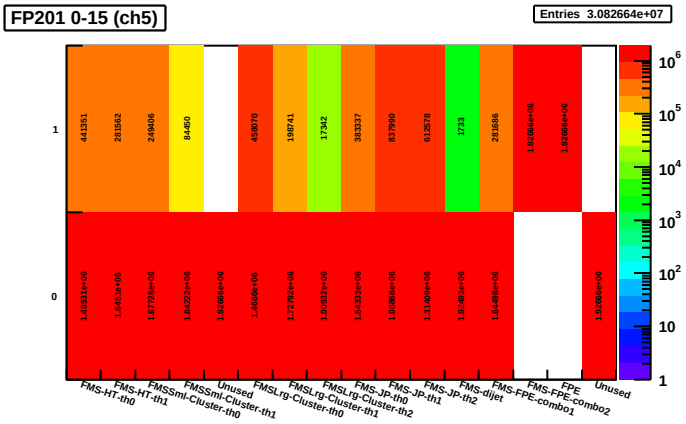

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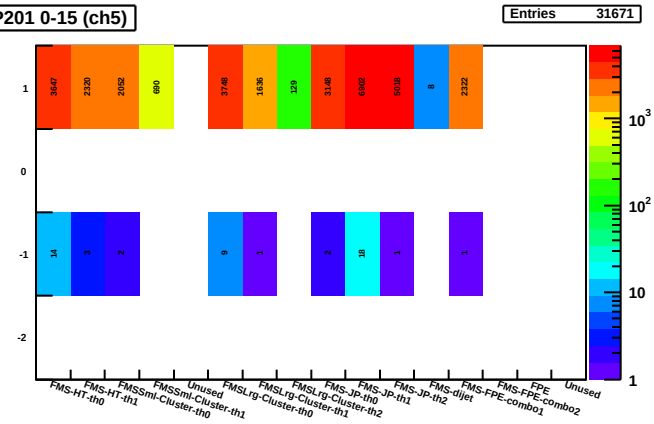

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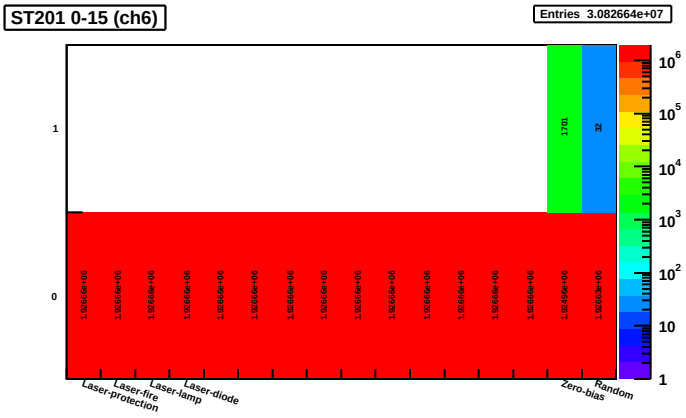

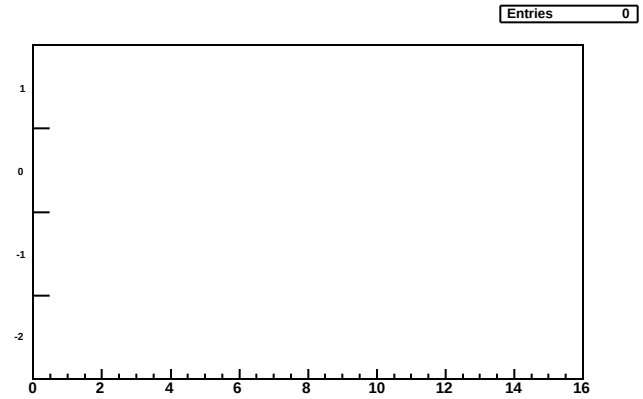
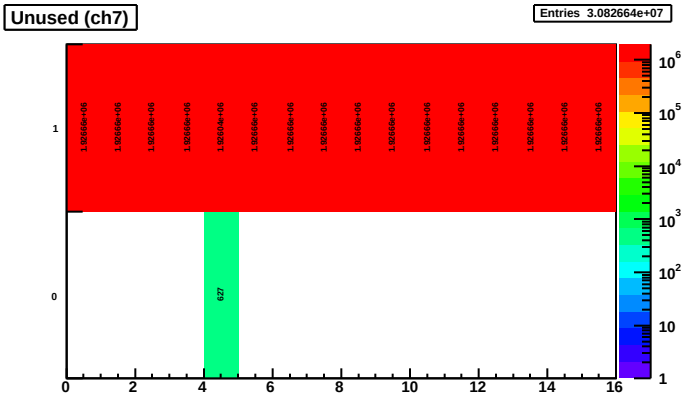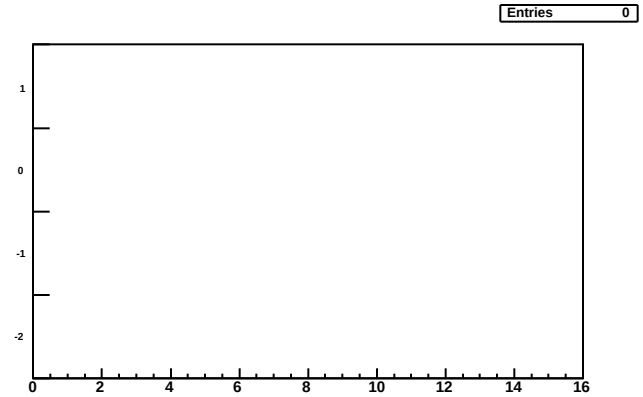
Unused (ch7)