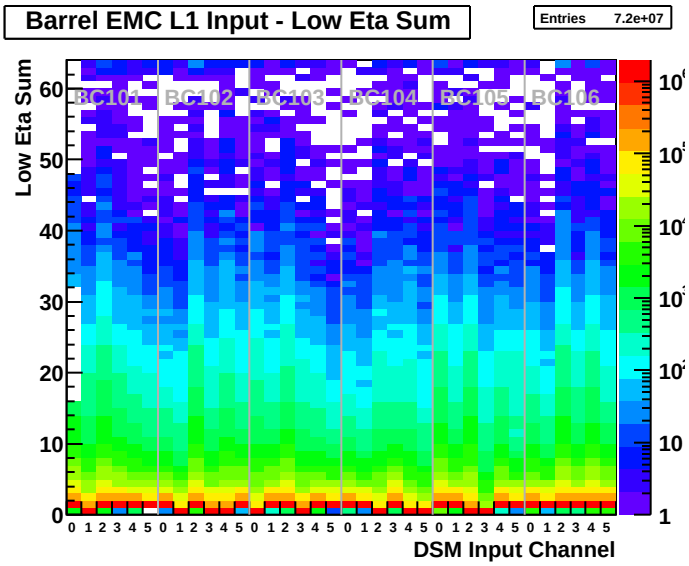
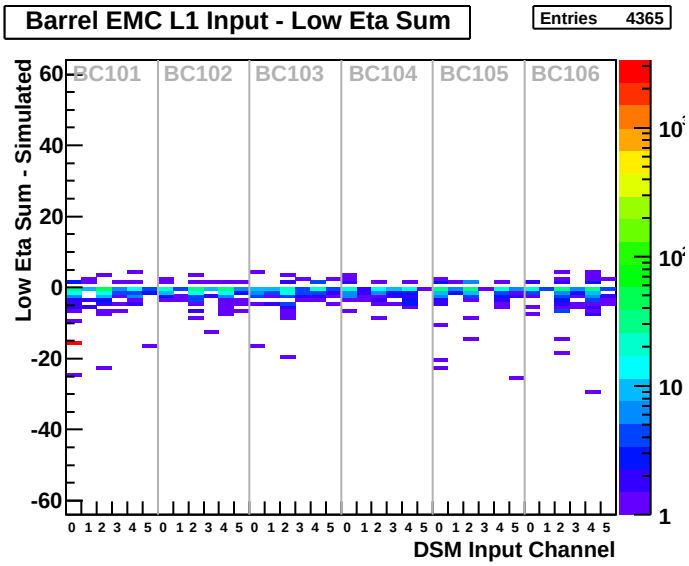
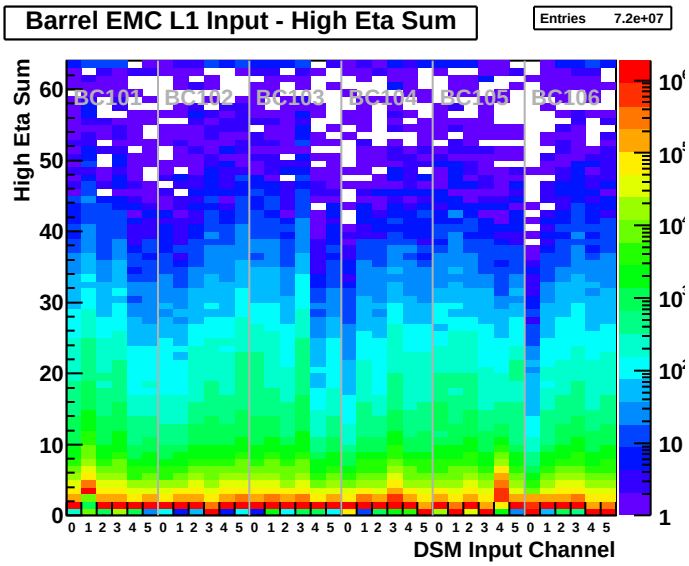
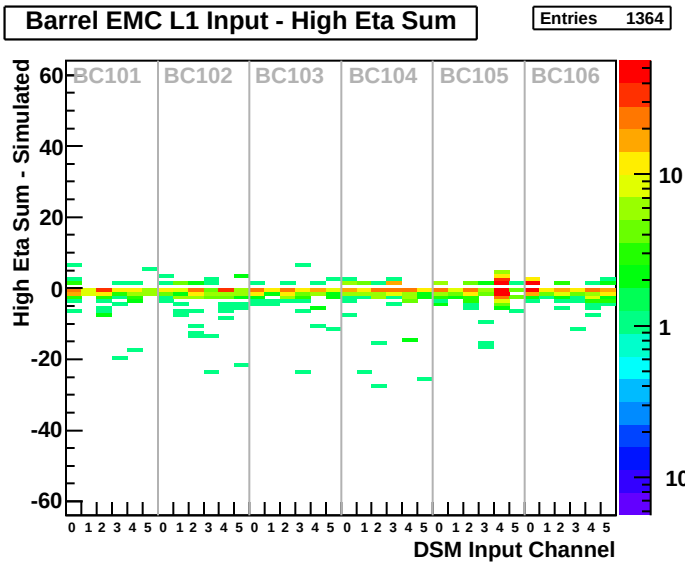
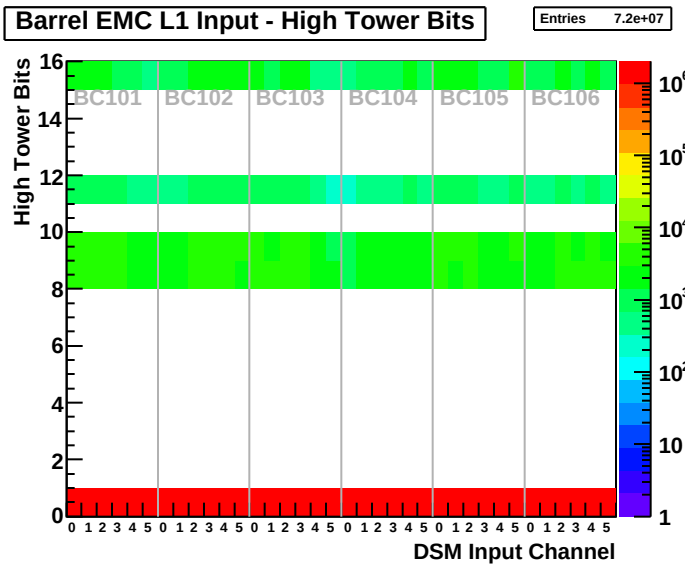
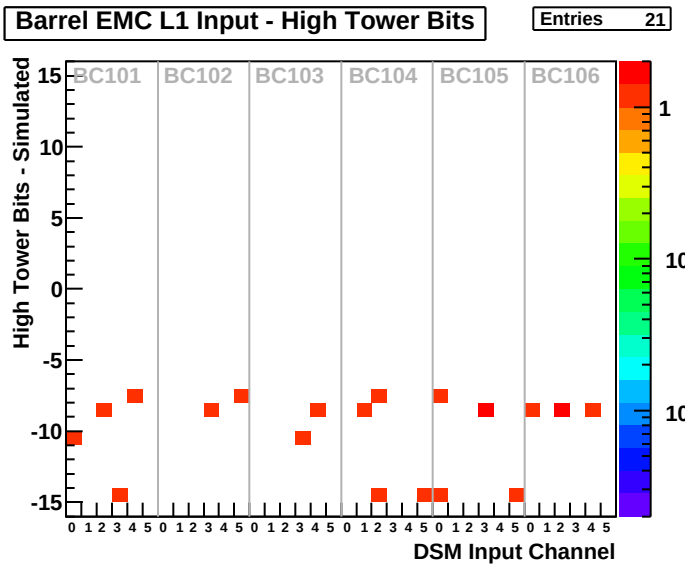

Barrel EMC L1 Input - Low Eta Sum
Entries 7.2e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

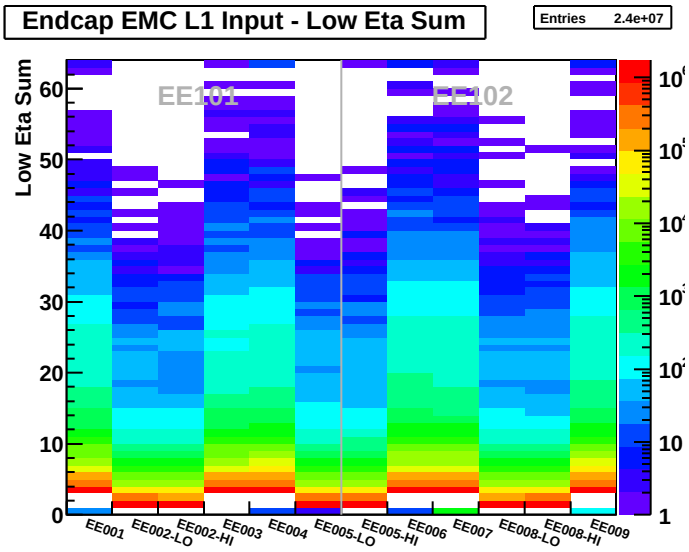

Barrel EMC L1 Input - Low Eta Sum
Entries 4365
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

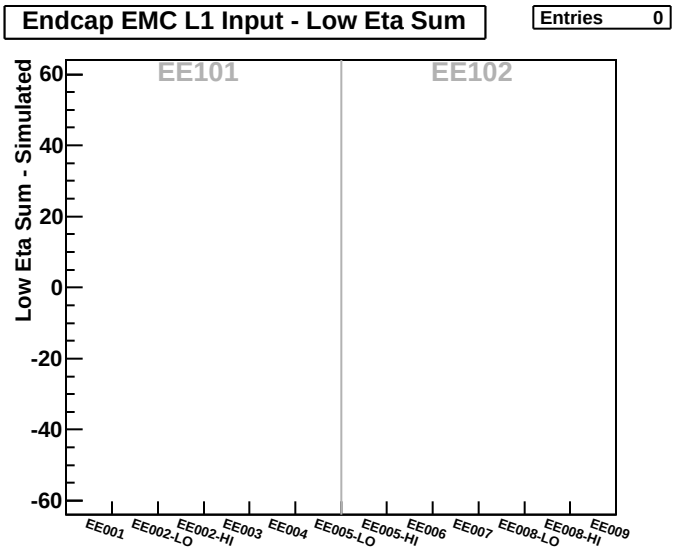

Barrel EMC L1 Input - High Eta Sum
Entries 7.2e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

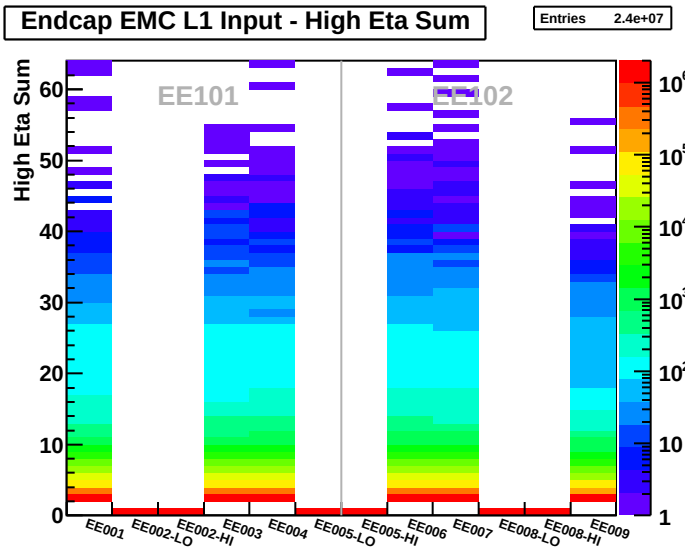

Barrel EMC L1 Input - High Eta Sum
Entries 1364
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

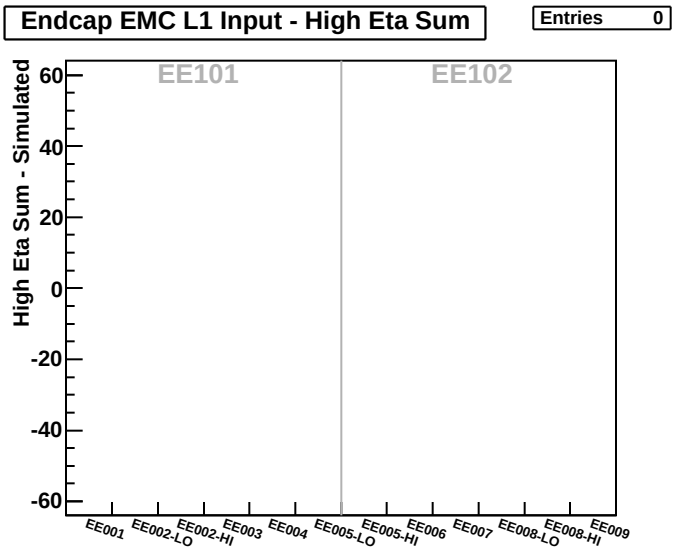

Barrel EMC L1 Input - High Tower Bits
Entries 7.2e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

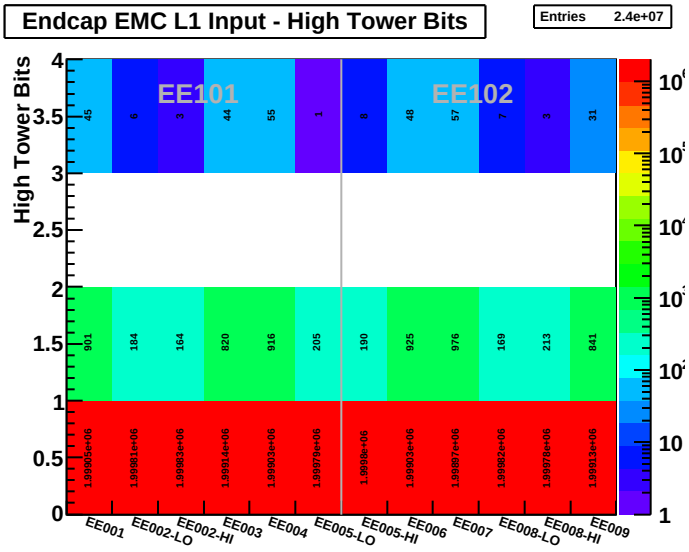

Barrel EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

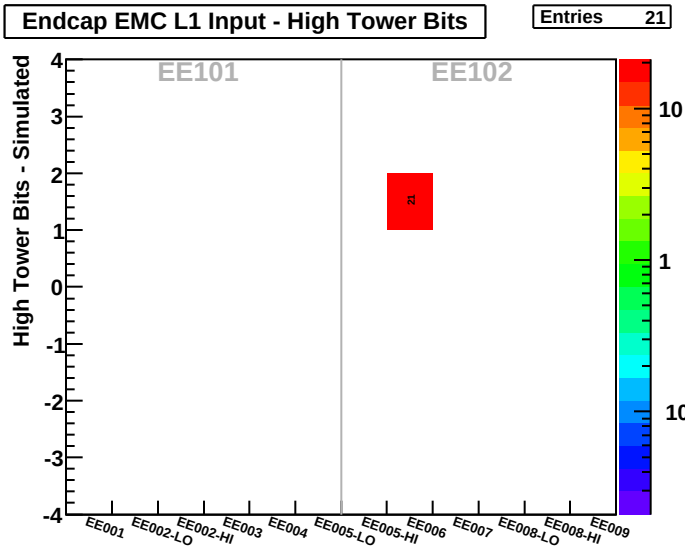

Endcap EMC L1 Input - Low Eta Sum
Entries 2.4e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

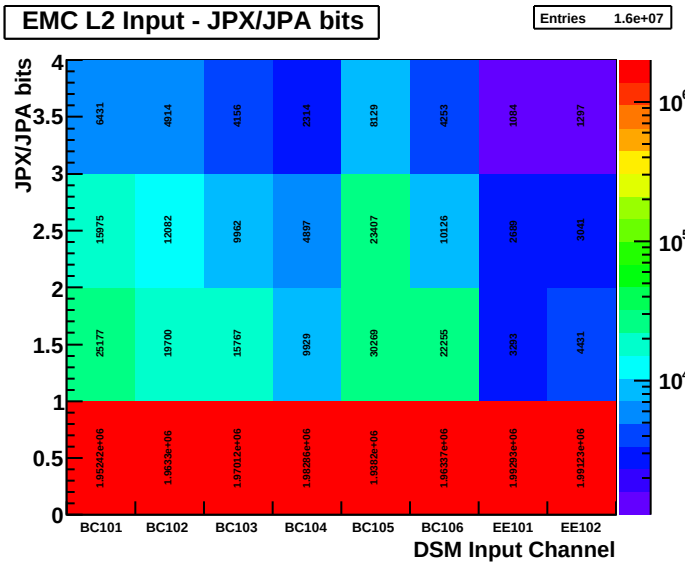

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

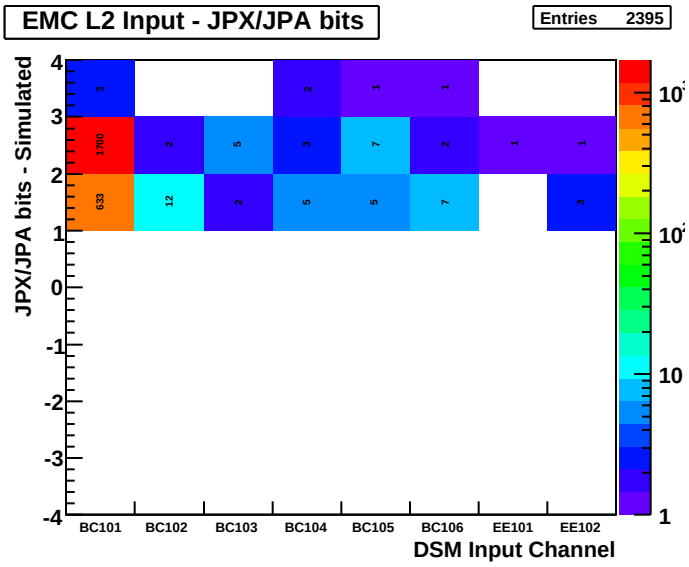

Endcap EMC L1 Input - High Eta Sum
Entries 2.4e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

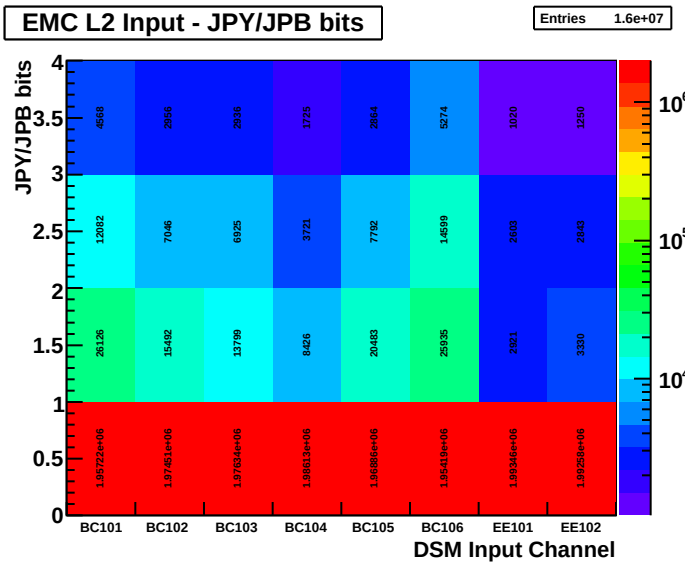

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

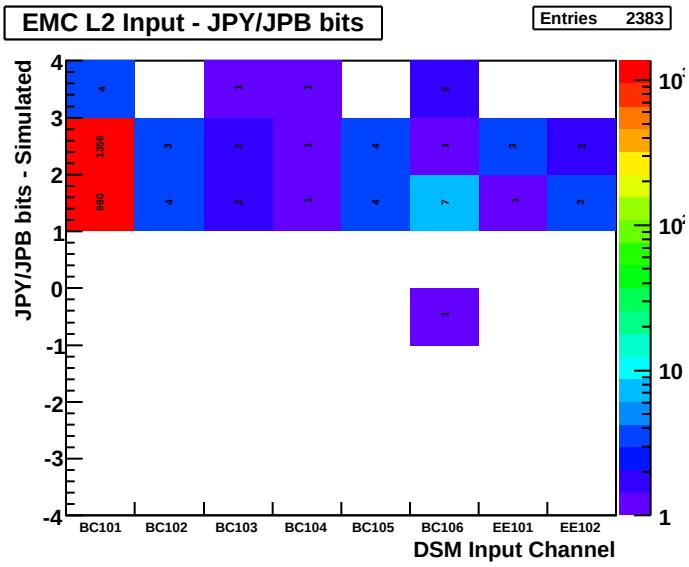

Endcap EMC L1 Input - High Tower Bits
Entries 2.4e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

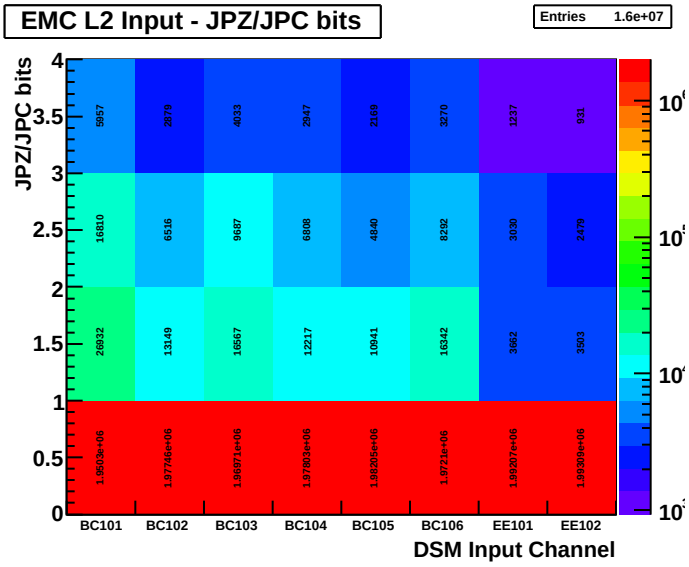

Endcap EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

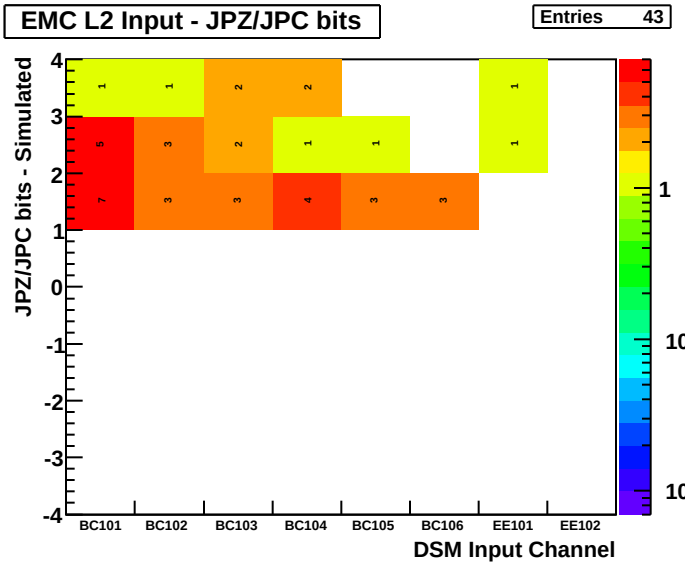

EMC L2 Input - JPX/JPA bits
Entries 1.6e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

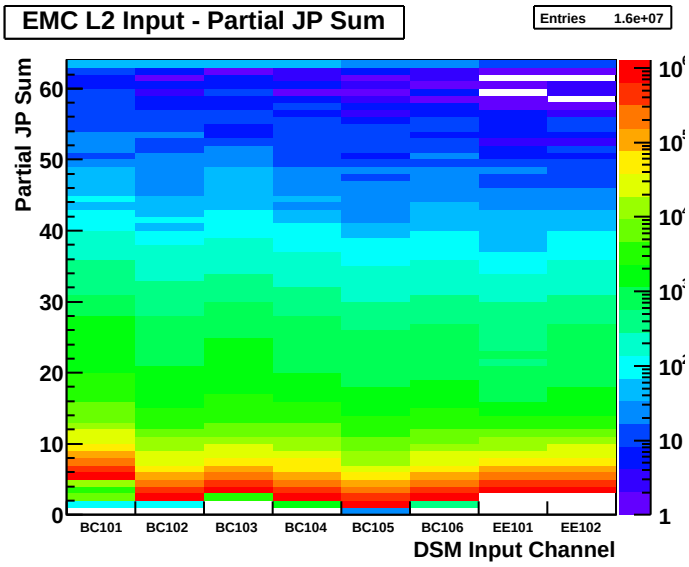

EMC L2 Input - JPX/JPA bits
Entries 2395
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

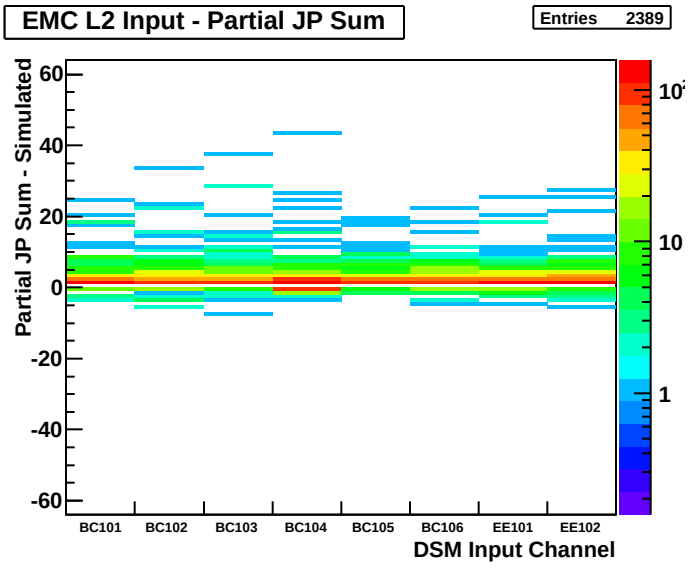

EMC L2 Input - JPY/JPB bits
Entries 1.6e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

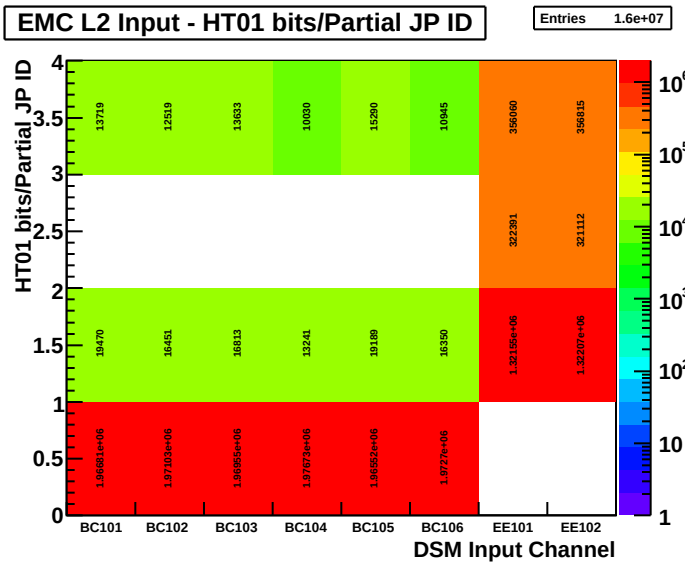

EMC L2 Input - JPY/JPB bits
Entries 2383
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

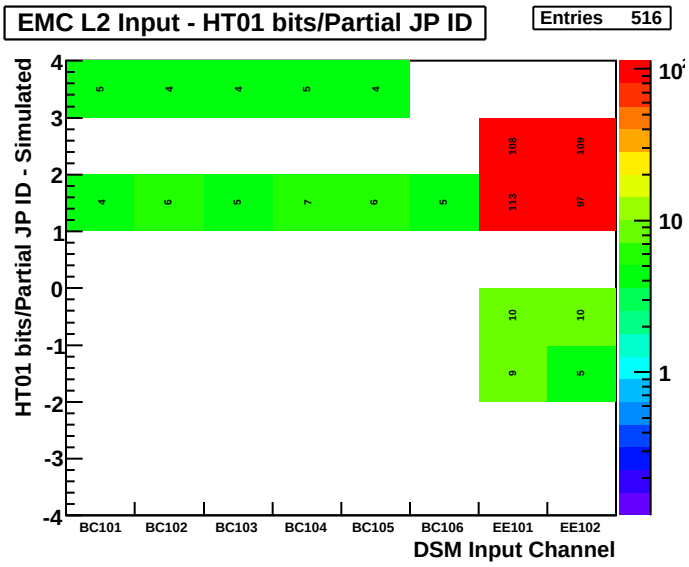

EMC L2 Input - JPZ/JPC bits
Entries 1.6e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

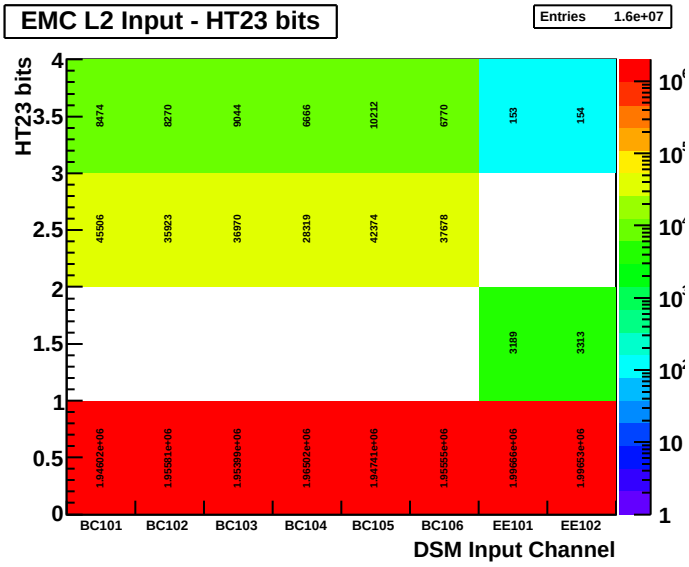

EMC L2 Input - JPZ/JPC bits
Entries 43
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

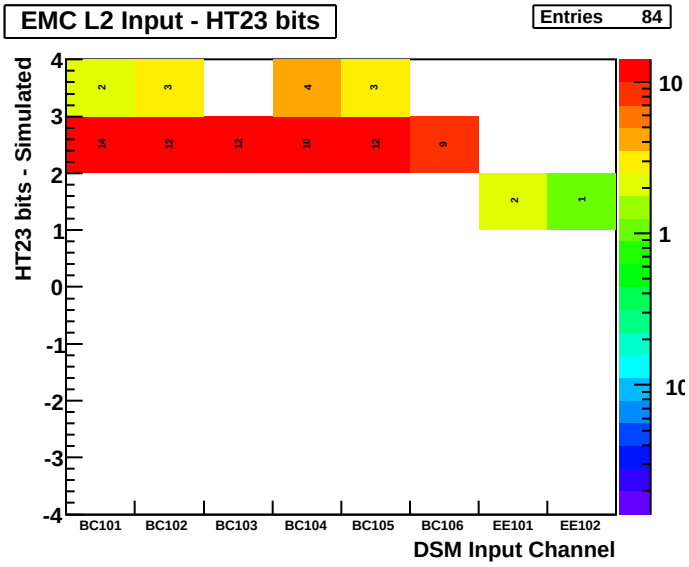

EMC L2 Input - Partial JP Sum
Entries 1.6e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

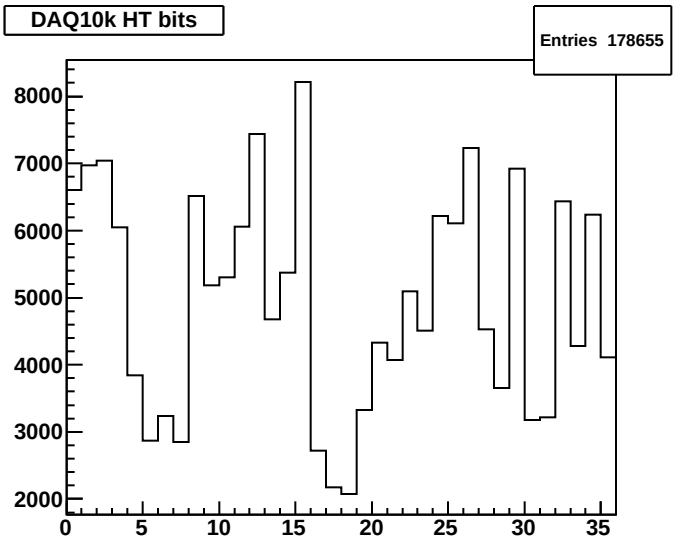

EMC L2 Input - Partial JP Sum
Entries 2389
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

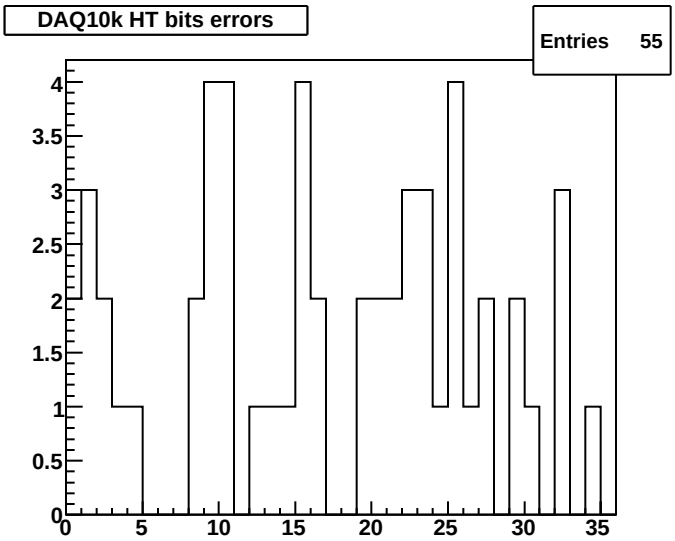

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.6e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

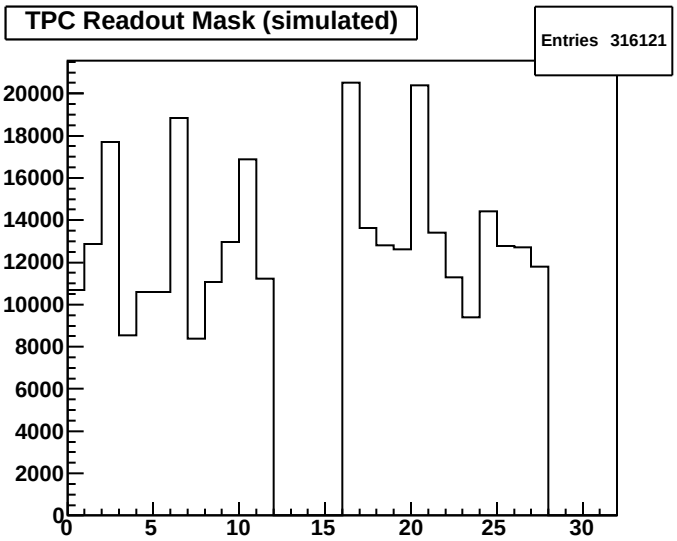

EMC L2 Input - HT01 bits/Partial JP ID
Entries 516
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

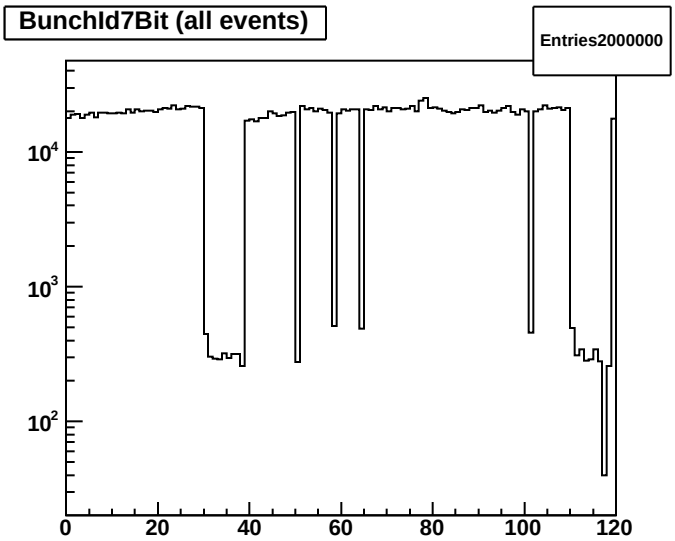

EMC L2 Input - HT23 bits
Entries 1.6e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

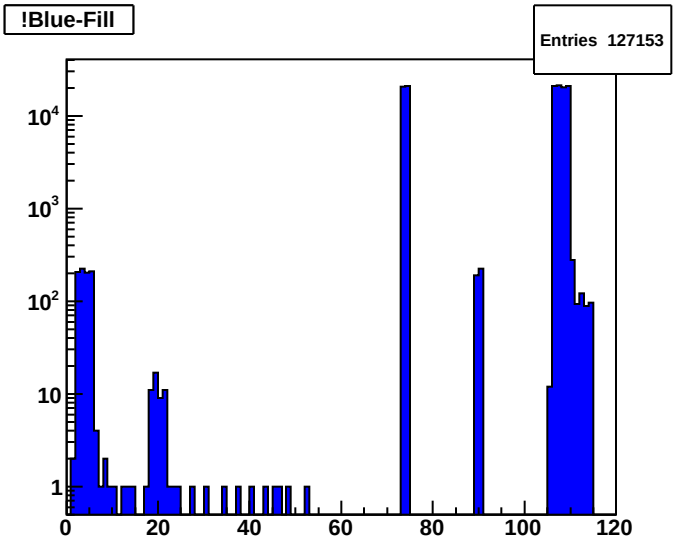

EMC L2 Input - HT23 bits
Entries 84
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

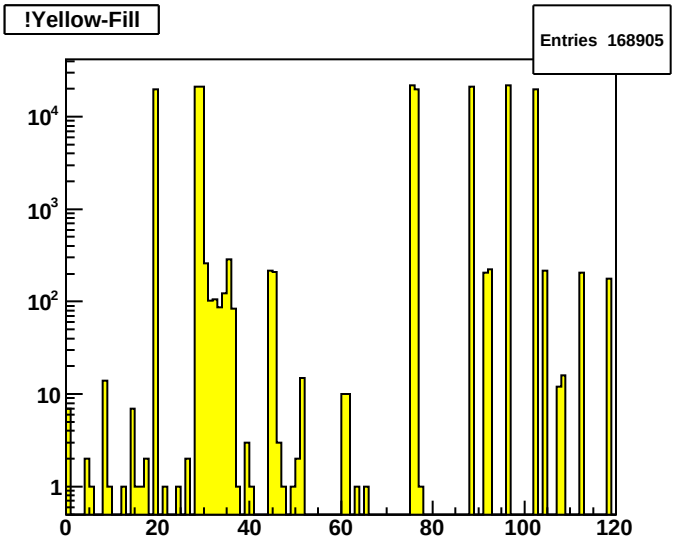

DAQ10k HT bits
Entries 178655

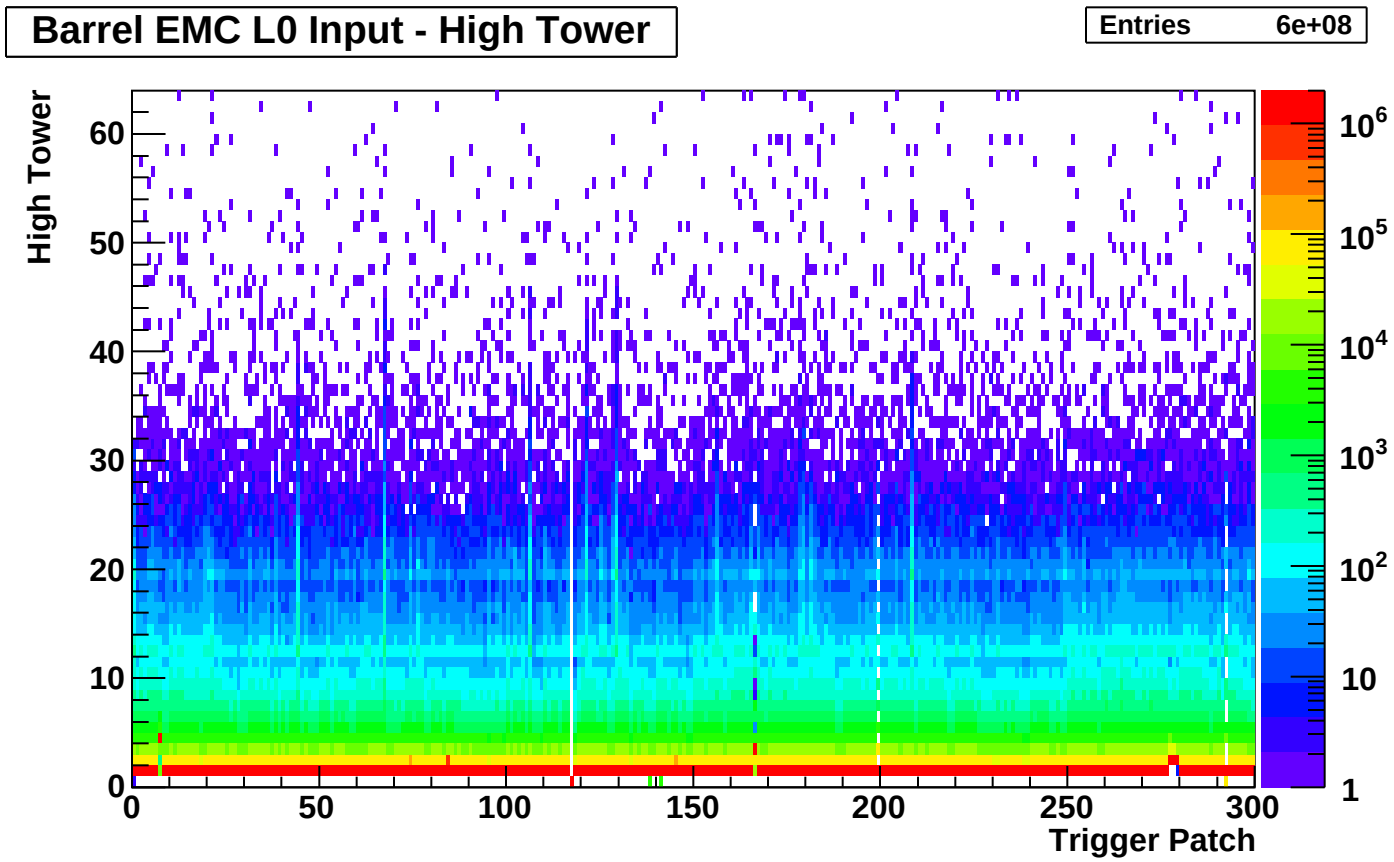

DAQ10k HT bits errors
Entries 55

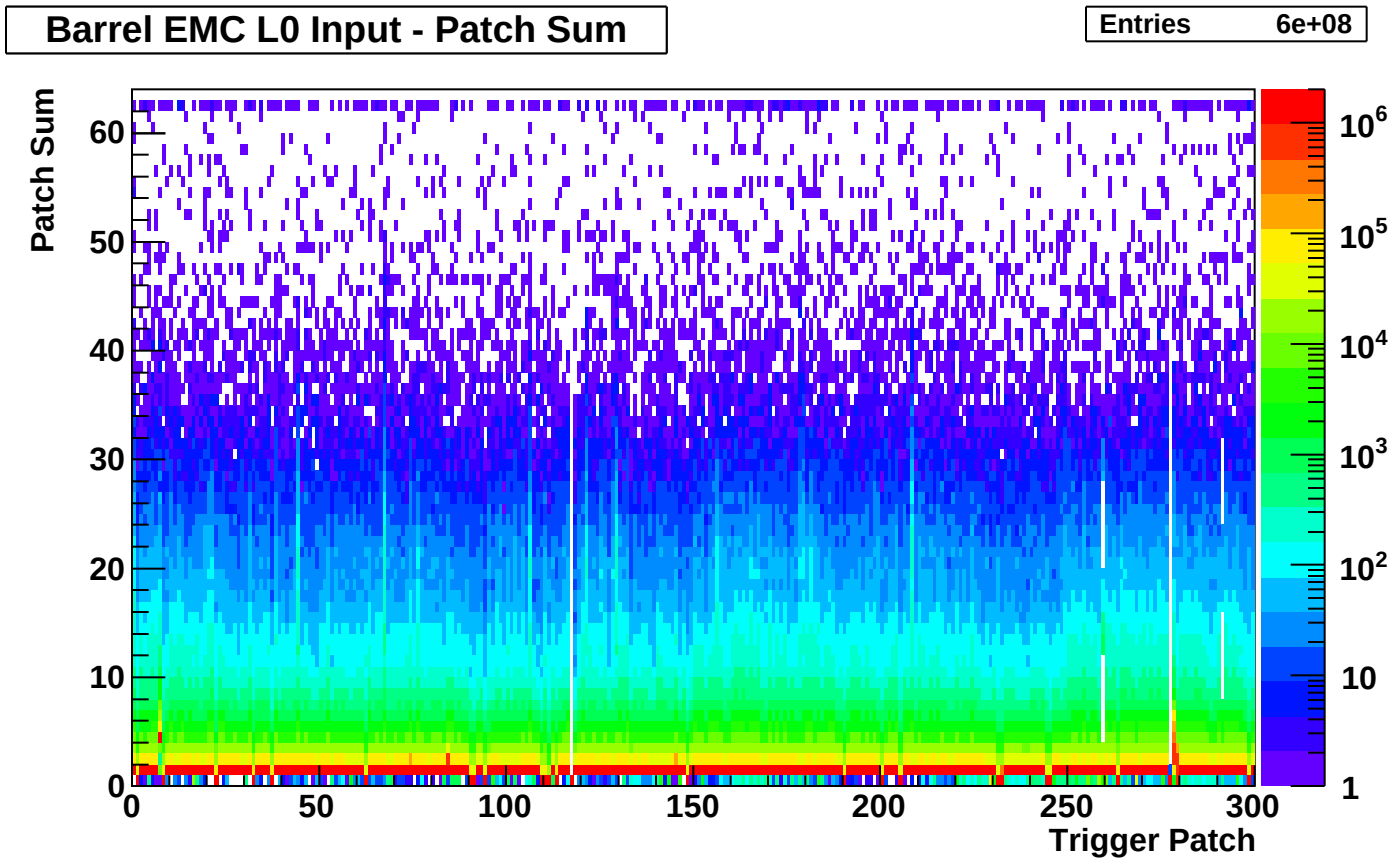

TPC Readout Mask (simulated)
Entries 316121

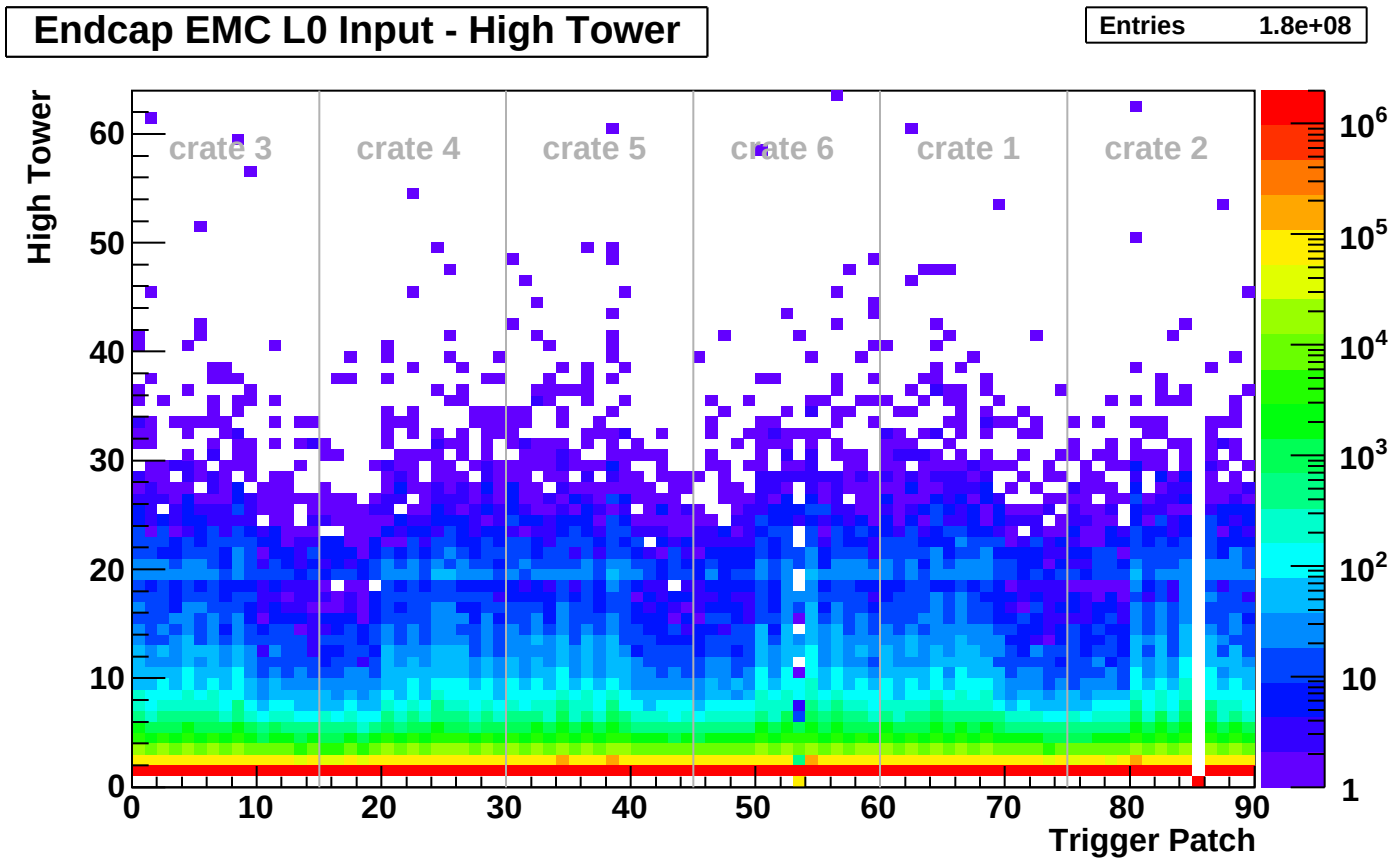

BunchId7Bit (all events)
Entries2000000

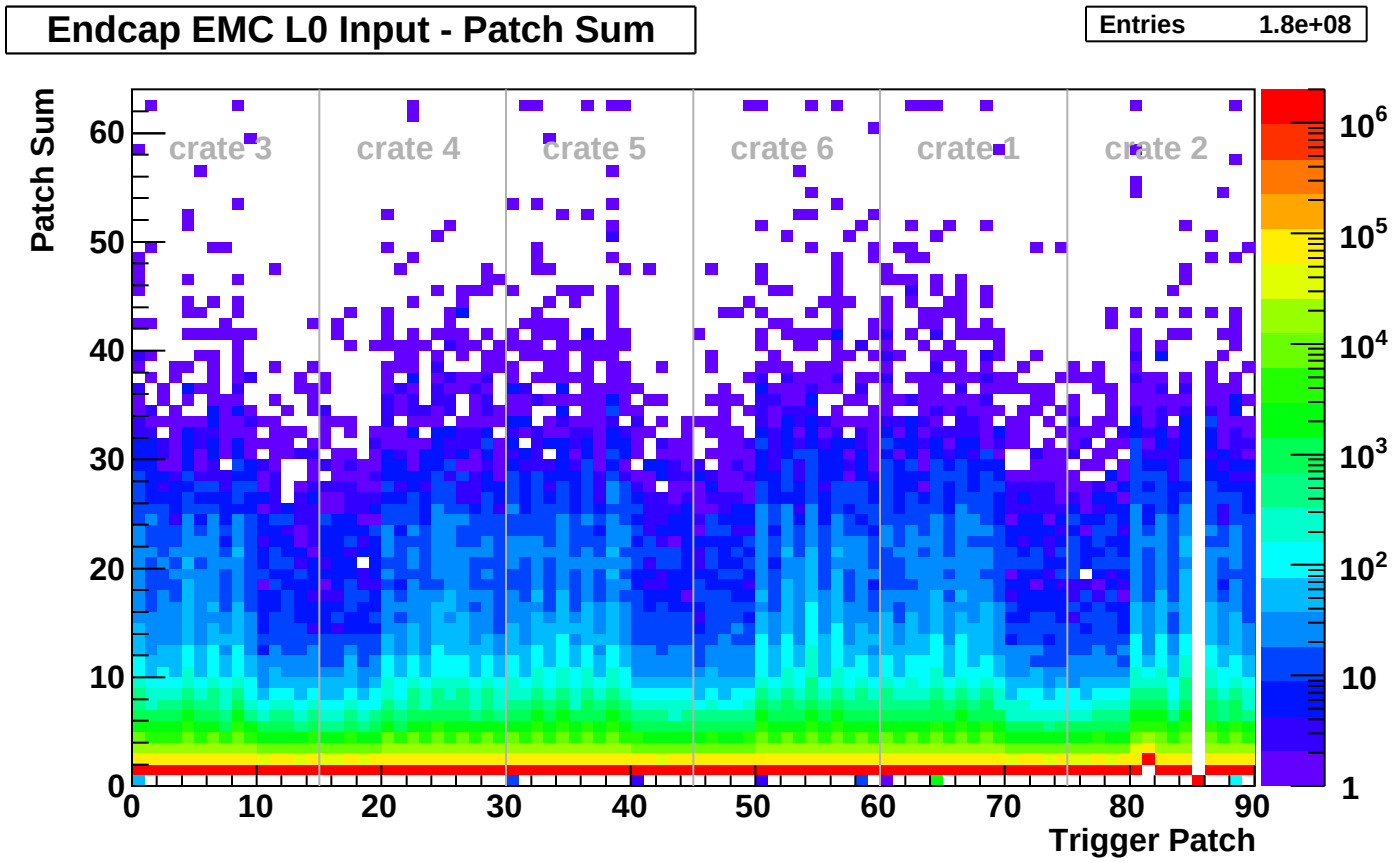

!Blue-Fill
Entries 127153

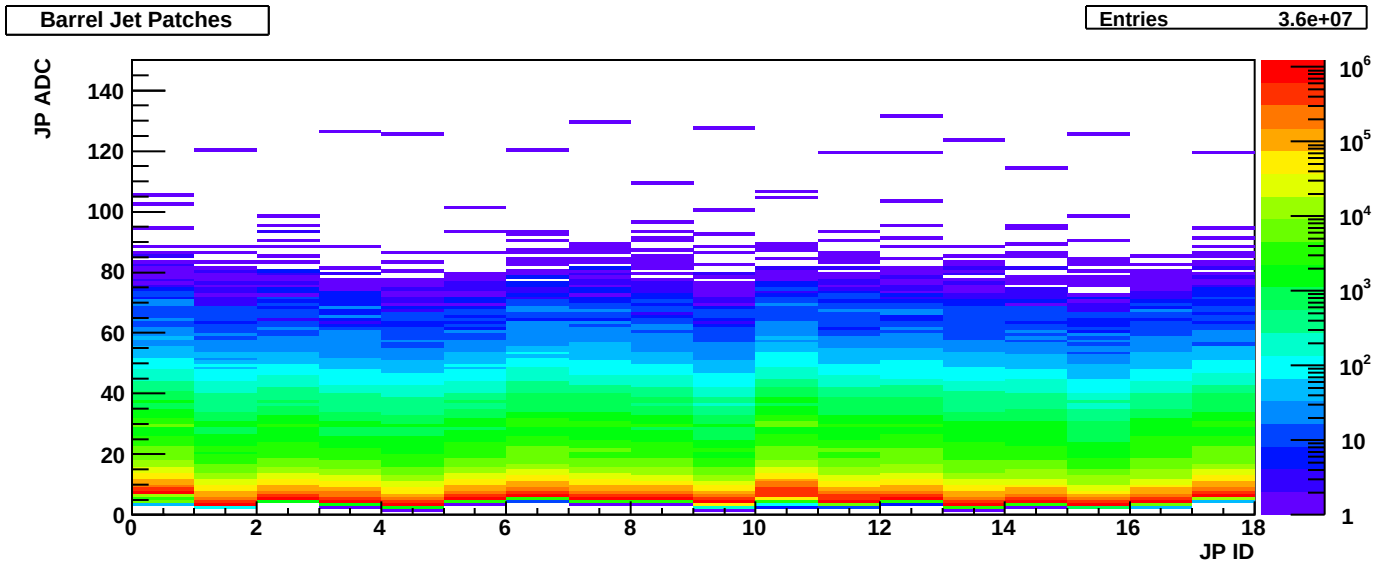

!Yellow-Fill
Entries 168905

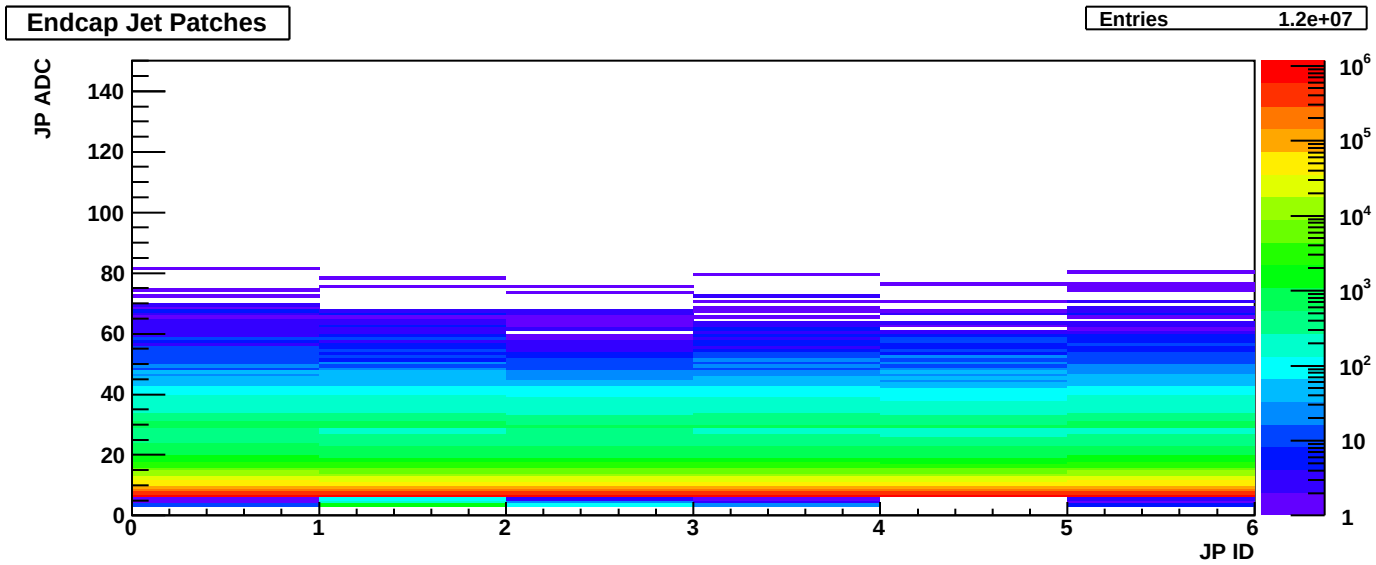

Barrel EMC L0 Input - High Tower
Entries 6e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

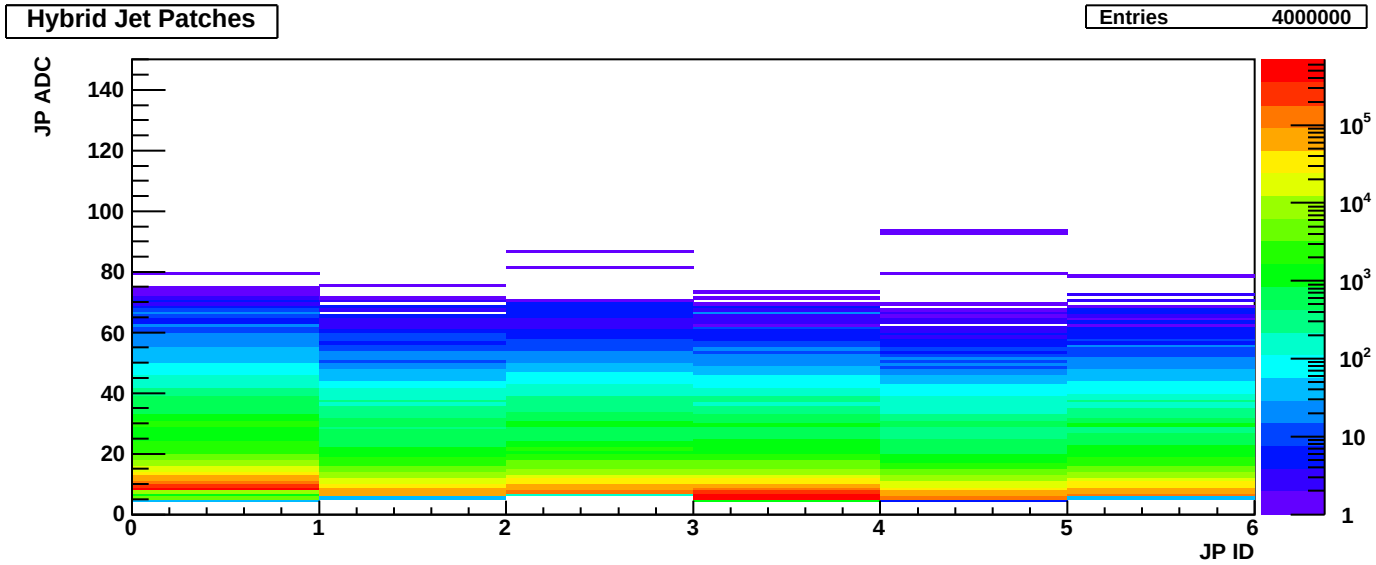

Barrel EMC L0 Input - Patch Sum
Entries 6e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

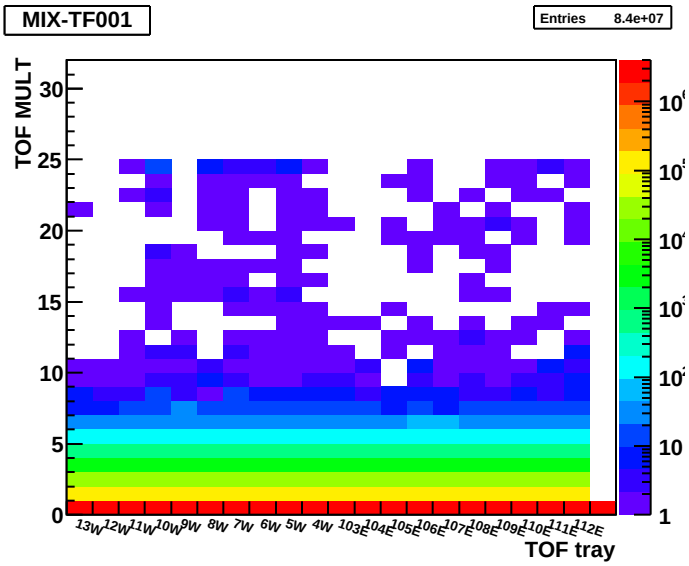

Endcap EMC L0 Input - High Tower
Entries 1.8e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

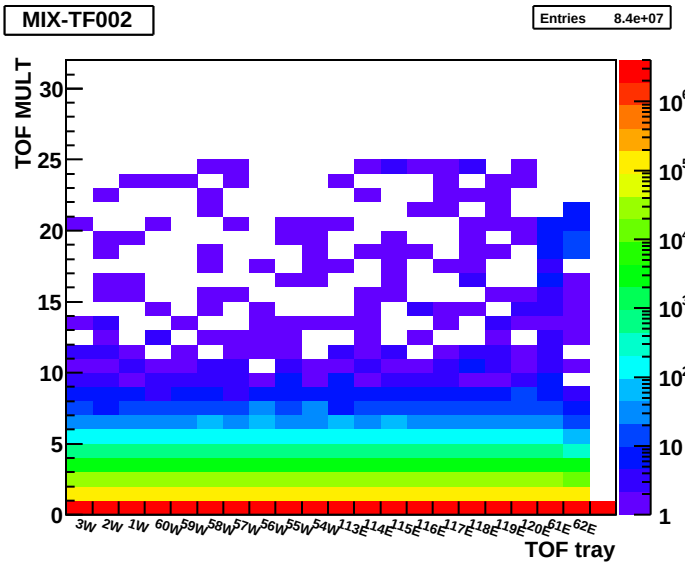

Endcap EMC L0 Input - Patch Sum
Entries 1.8e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

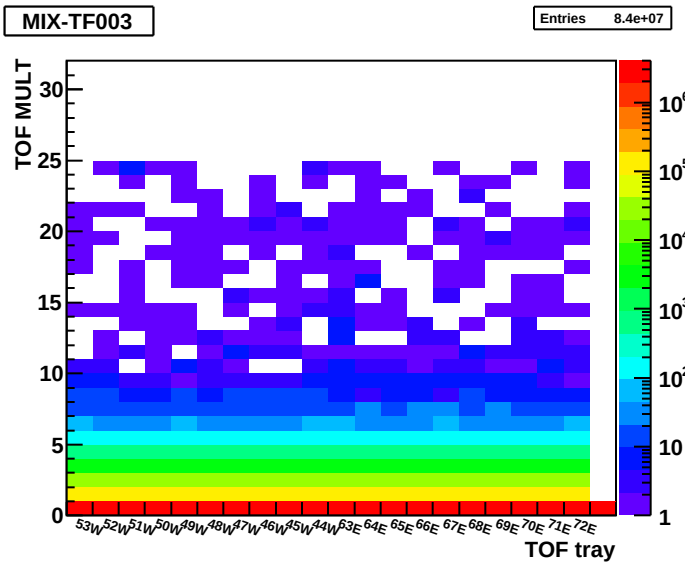

Barrel Jet Patches
Entries 3.6e+07
JP ADC
JP ID

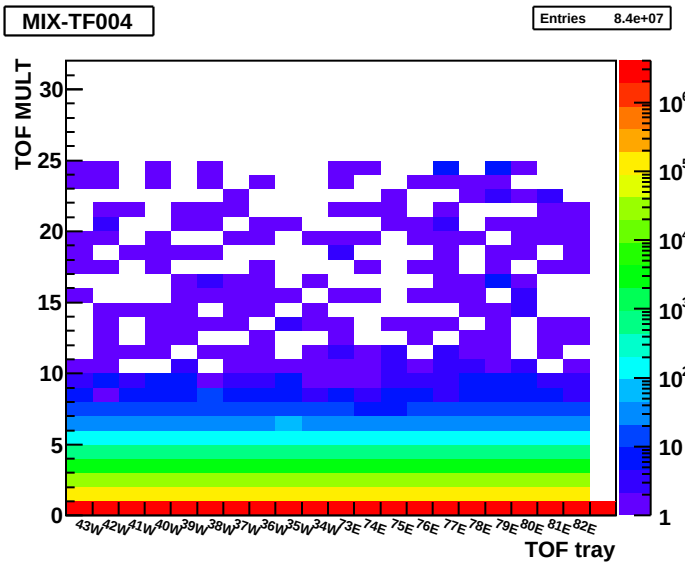

Endcap Jet Patches
Entries 1.2e+07
JP ADC
JP ID

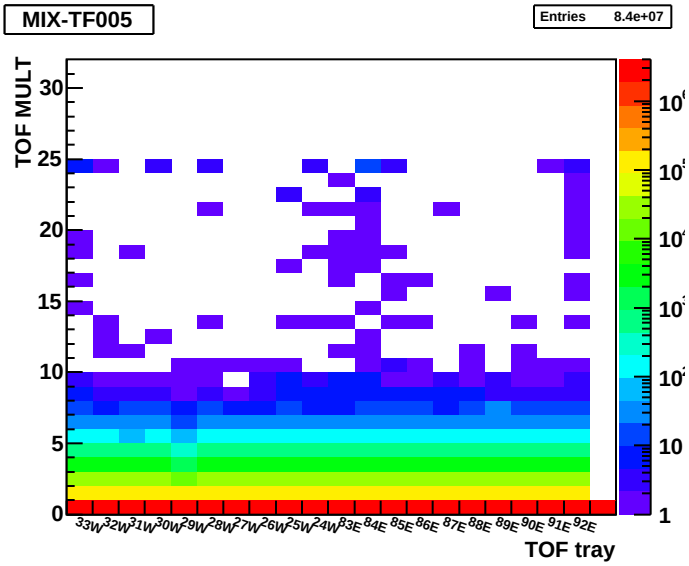

Hybrid Jet Patches
Entries 4000000
JP ADC
JP ID

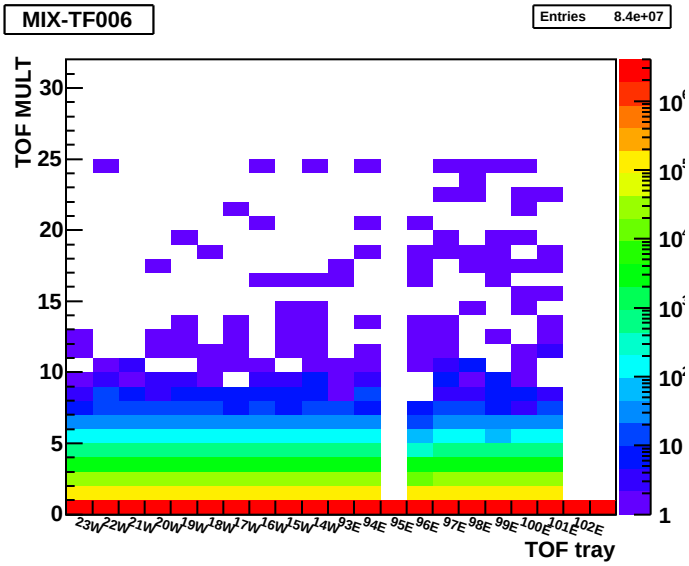

MIX-TF001
Entries 8.4e+07
TOF MULT
TOF tray

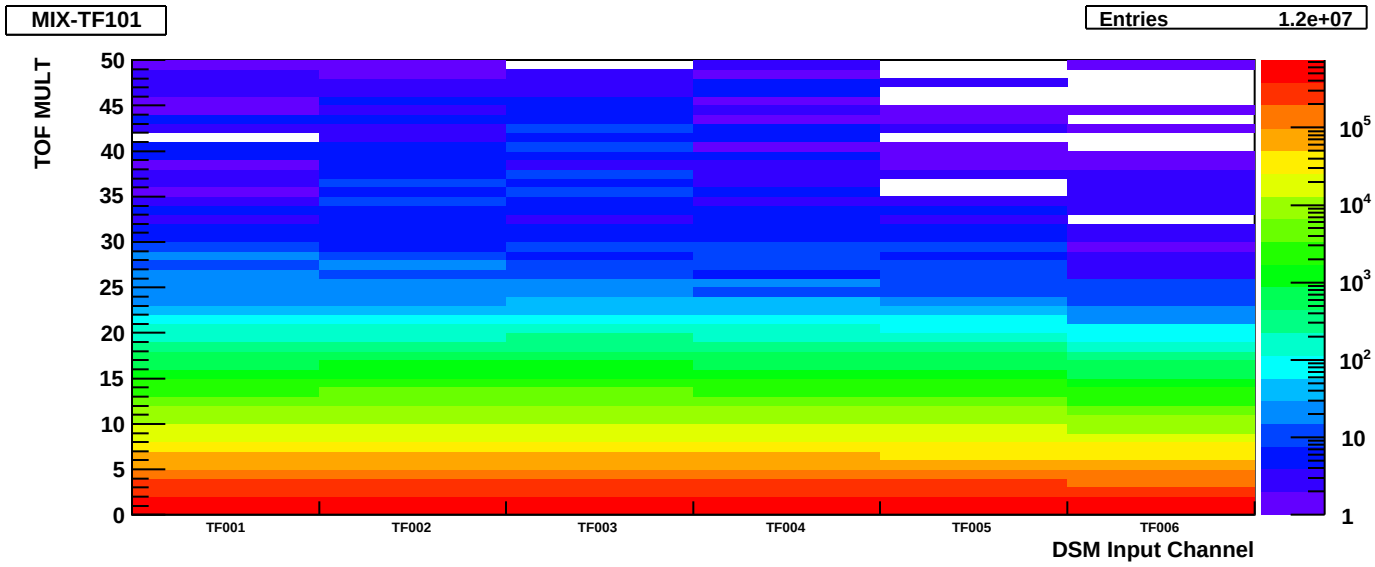

MIX-TF002
Entries 8.4e+07
TOF MULT
TOF tray

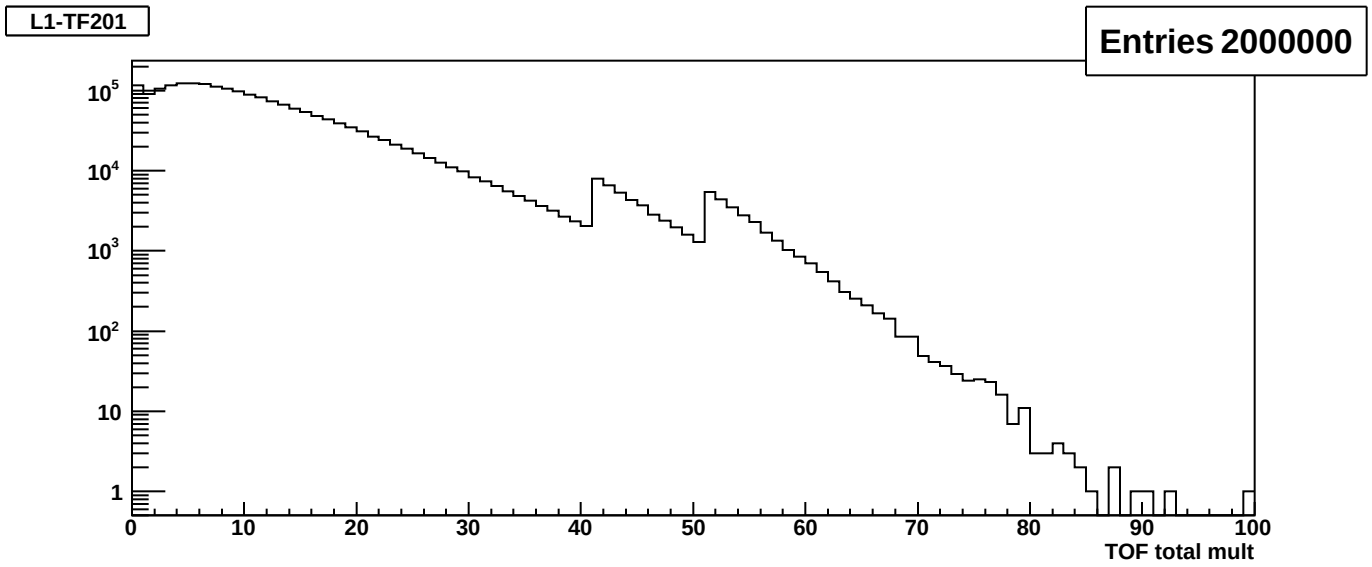

MIX-TF003
Entries 8.4e+07
TOF MULT
TOF tray

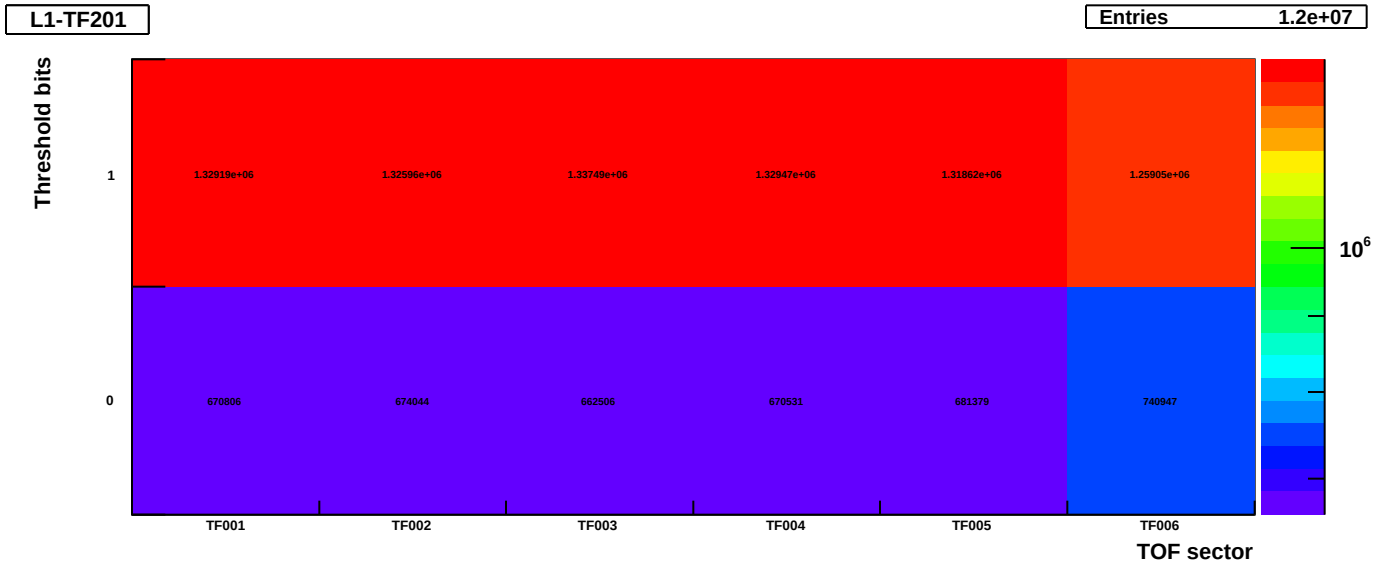

MIX-TF004
Entries 8.4e+07
TOF MULT
TOF tray

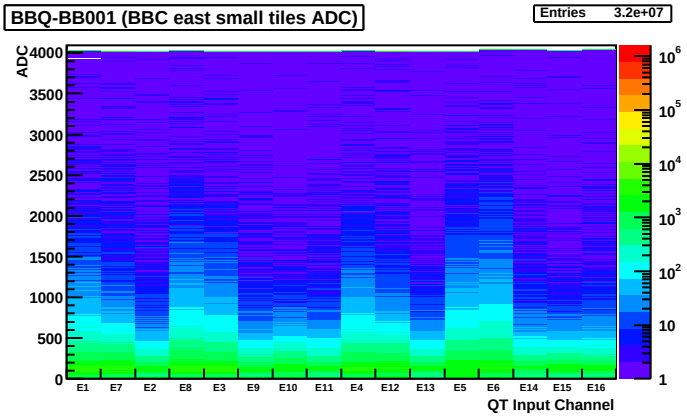

MIX-TF005
Entries 8.4e+07
TOF MULT
TOF tray

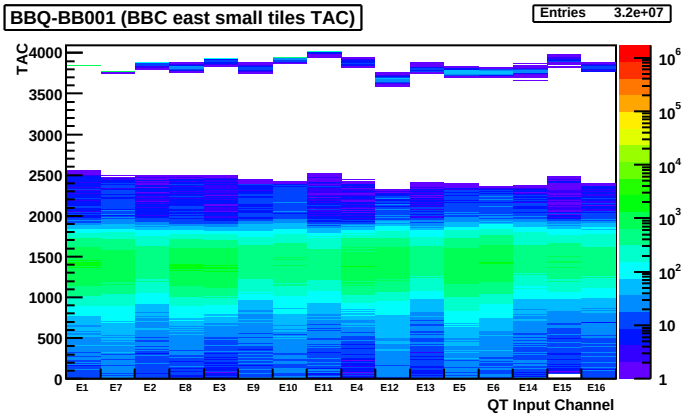

MIX-TF006
Entries 8.4e+07
TOF MULT
TOF tray

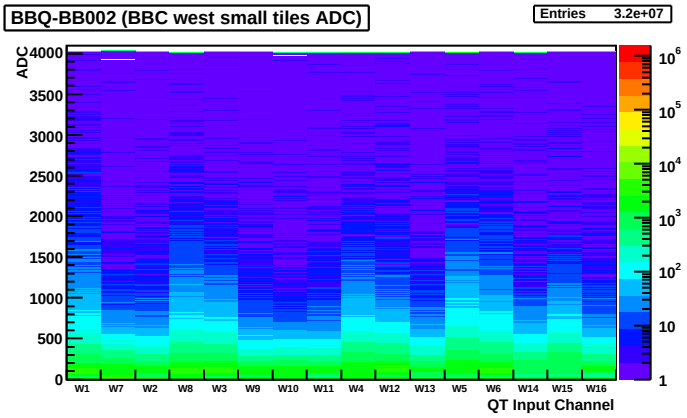

MIX-TF101
Entries 1.2e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

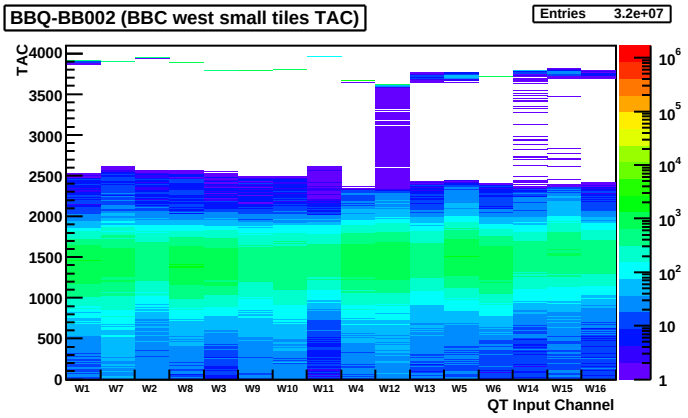

L1-TF201
Entries 2000000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

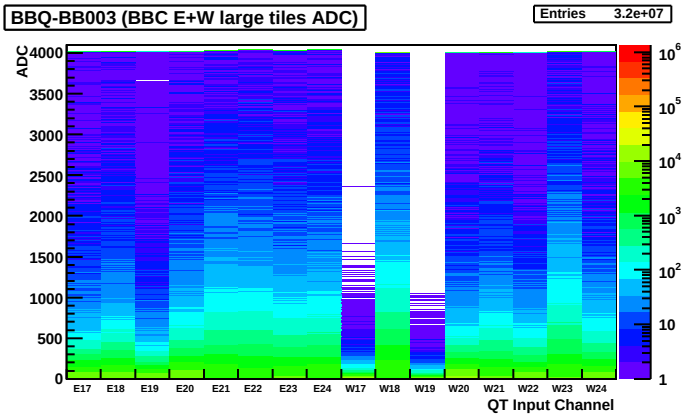

L1-TF201
Entries 1.2e+07
Threshold bits
1
0
1.32919e+06
1.32596e+06
1.33749e+06
1.32947e+06
1.31862e+06
1.25905e+06
670806
674044
662506
670531
681379
740947
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

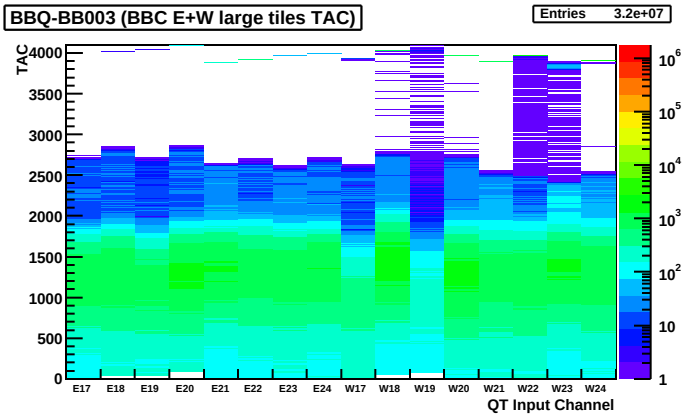

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

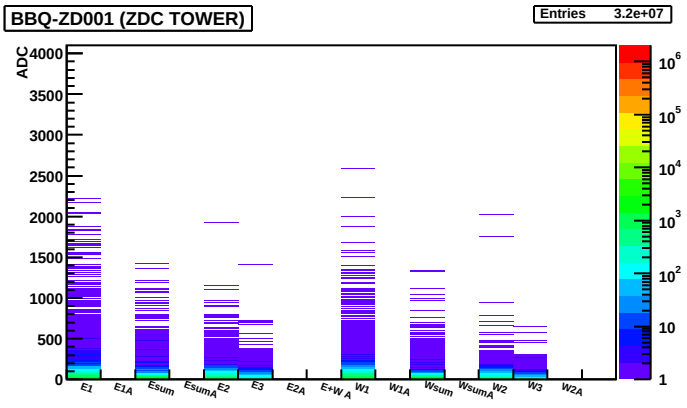

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

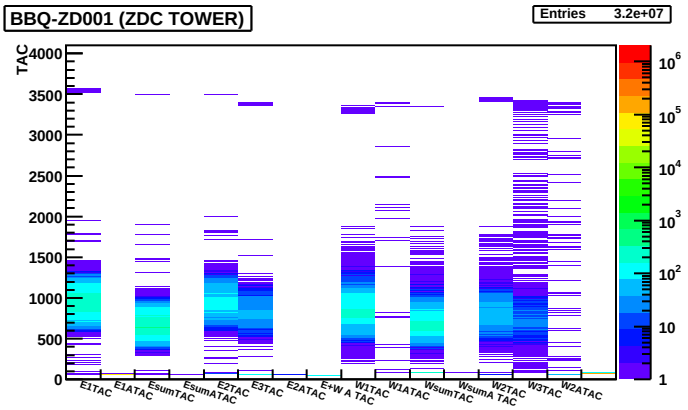

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

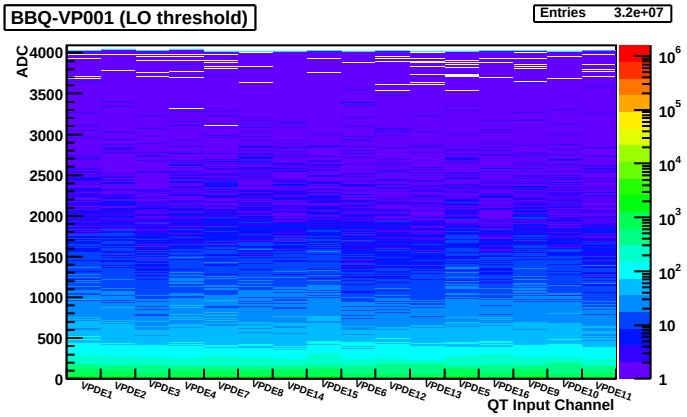

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

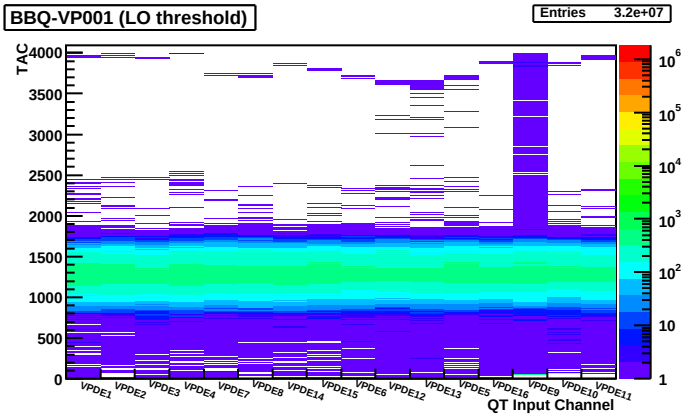

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

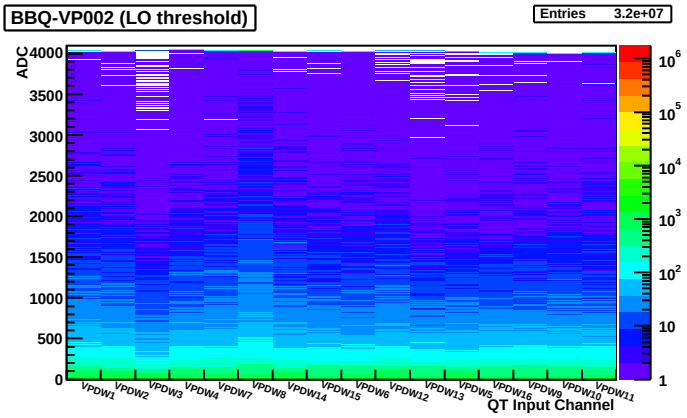

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

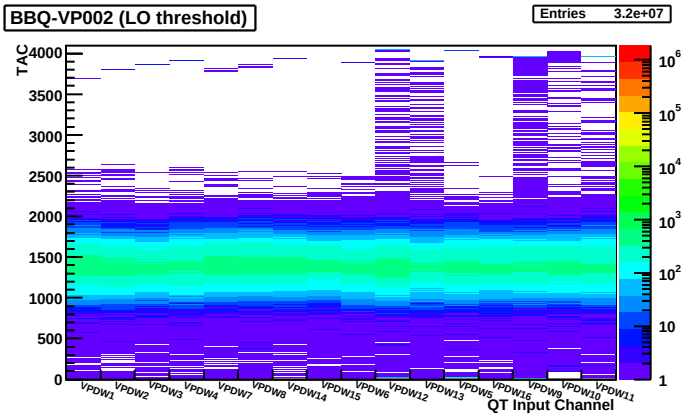

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
ADC

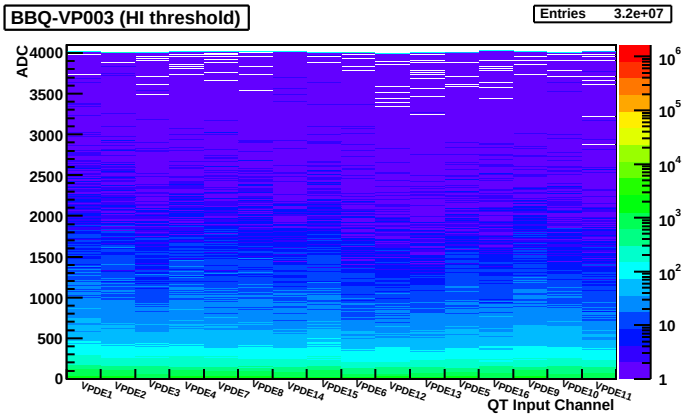

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
TAC

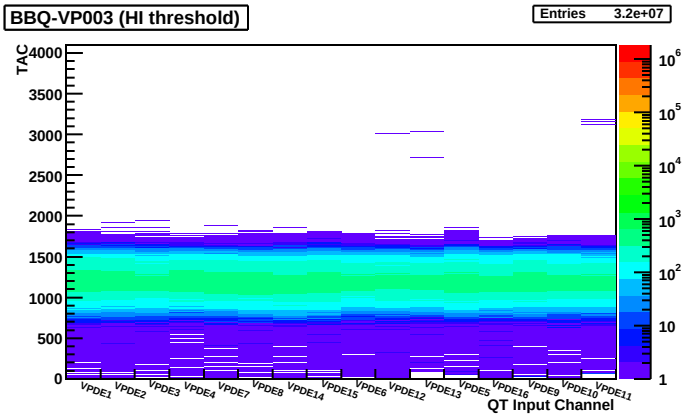

BBQ-VP001 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

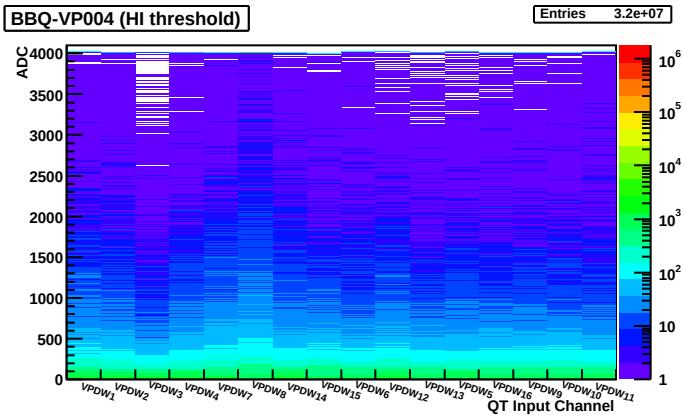

BBQ-VP001 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

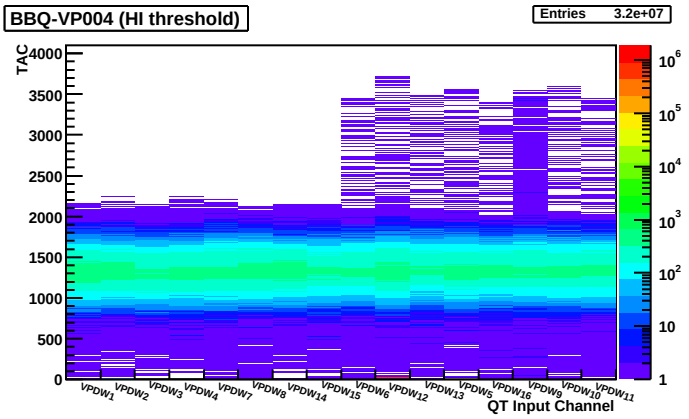

BBQ-VP002 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

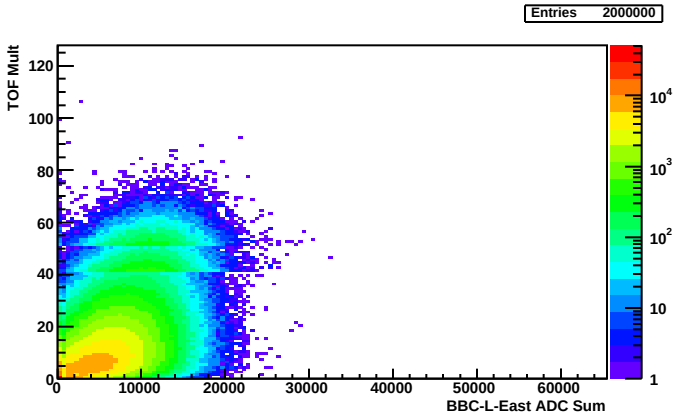

BBQ-VP002 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

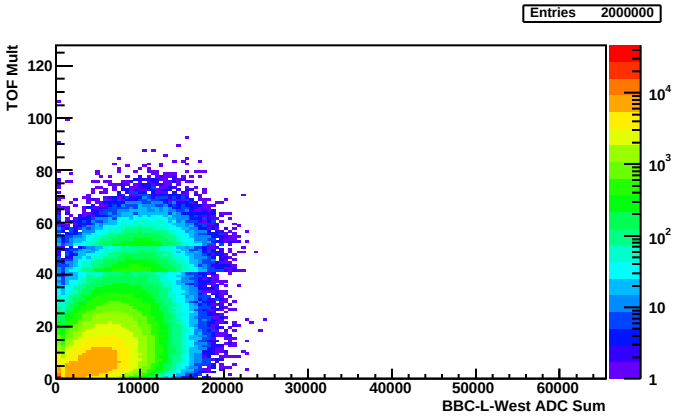

BBQ-VP003 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

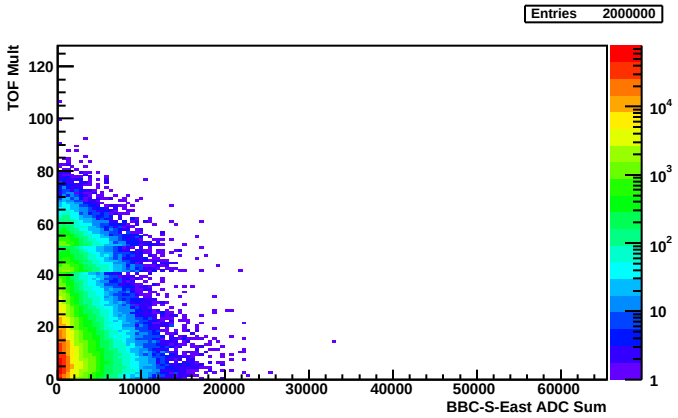

BBQ-VP003 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

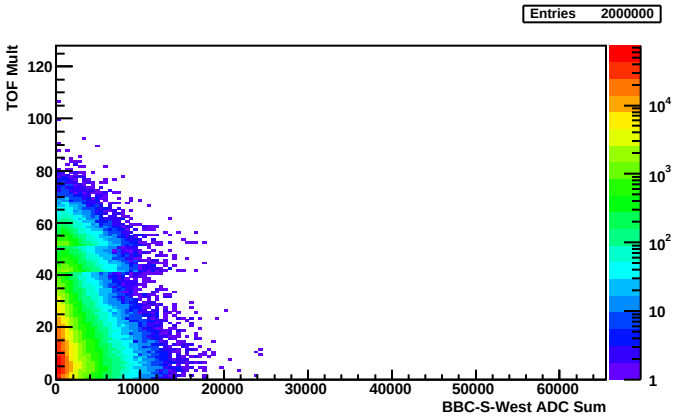

BBQ-VP004 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

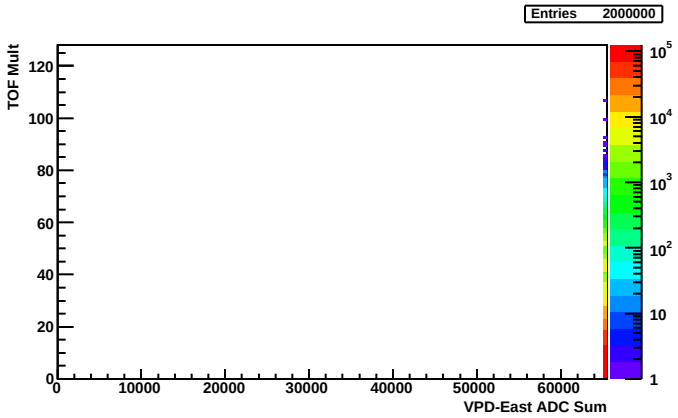

BBQ-VP004 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

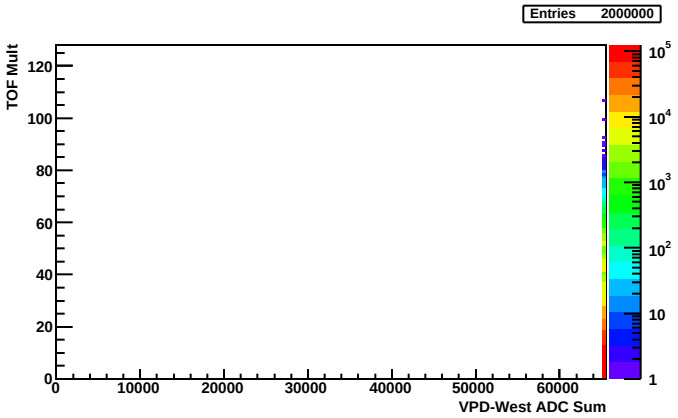

Entries 2000000
TOF Mult
BBC-L-East ADC Sum

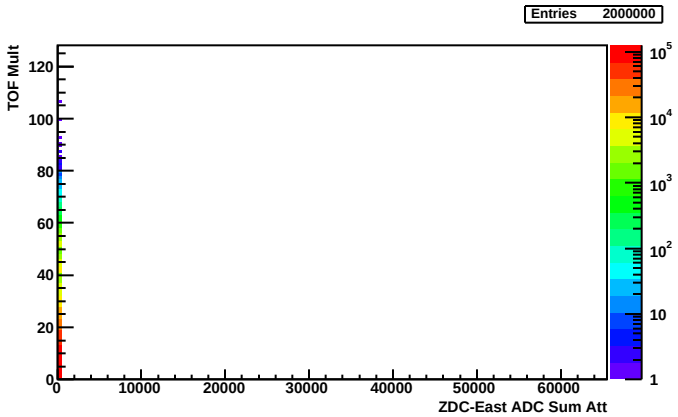

Entries 2000000
TOF Mult
BBC-L-West ADC Sum

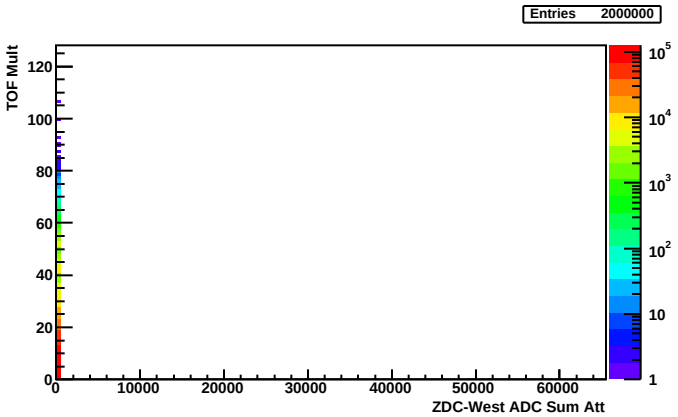

Entries 2000000
TOF Mult
BBC-S-East ADC Sum

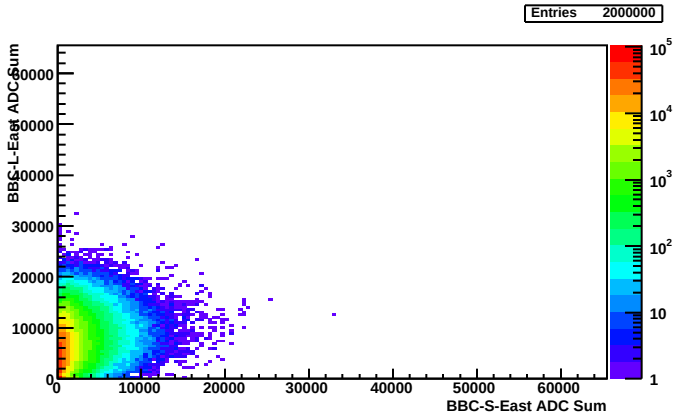

Entries 2000000
TOF Mult
BBC-S-West ADC Sum

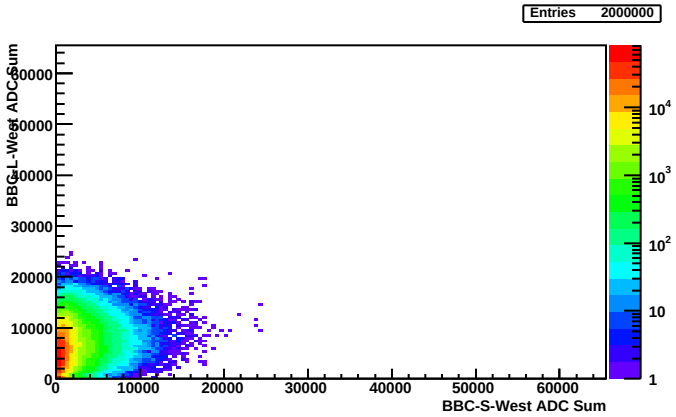

Entries 2000000
TOF Mult
VPD-East ADC Sum

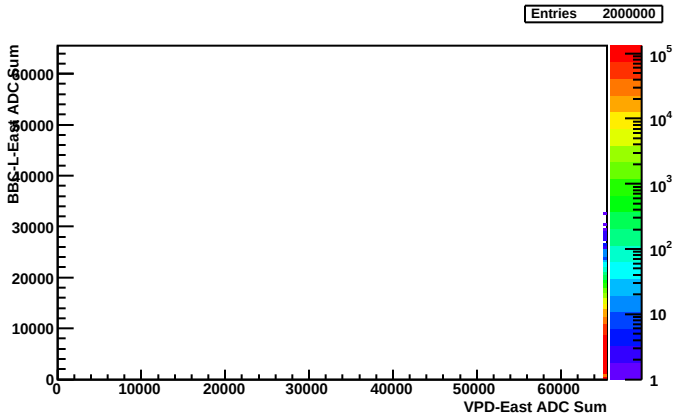

Entries 2000000
TOF Mult
VPD-West ADC Sum

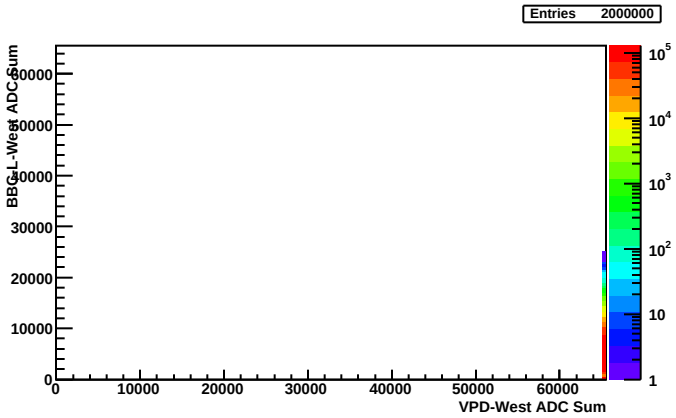

Entries 2000000
TOF Mult
ZDC-East ADC Sum Att

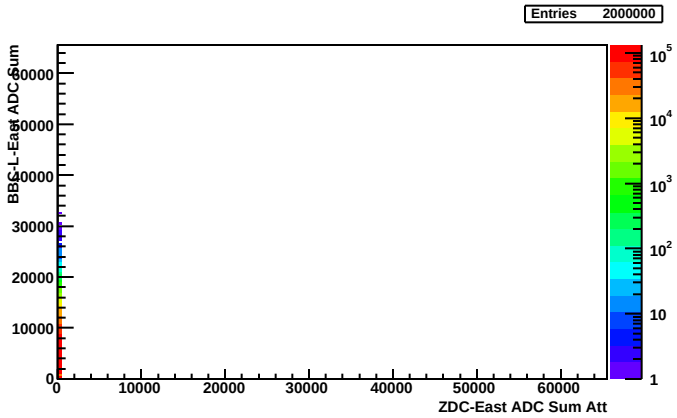

Entries 2000000
TOF Mult
ZDC-West ADC Sum Att

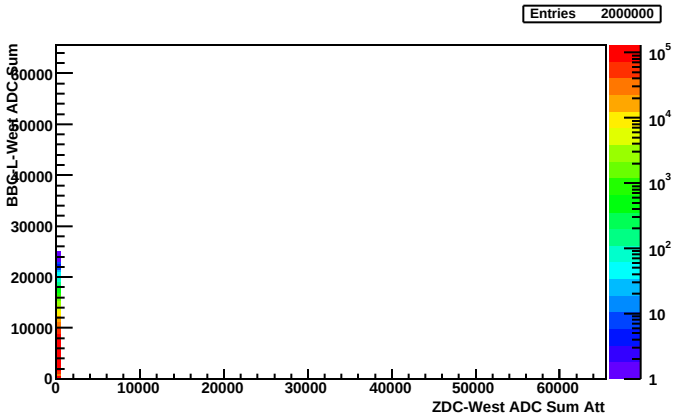

Entries 2000000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

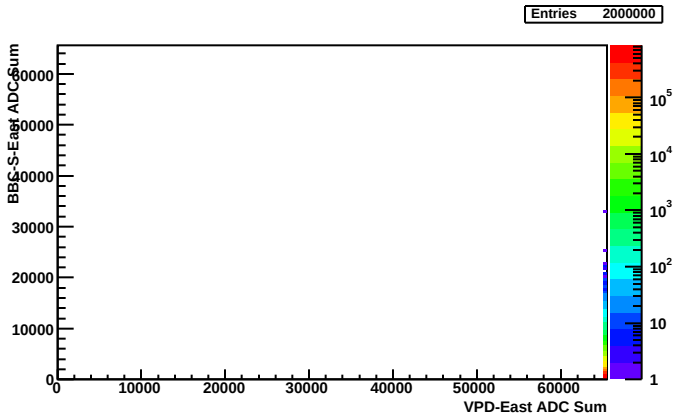

Entries 2000000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

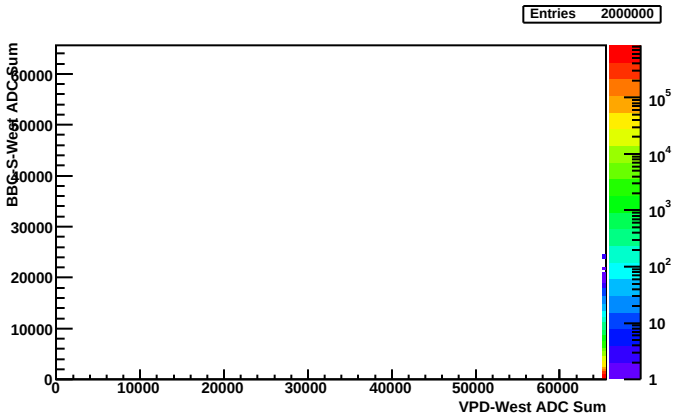

Entries 2000000
BBC-L-East ADC Sum
VPD-East ADC Sum

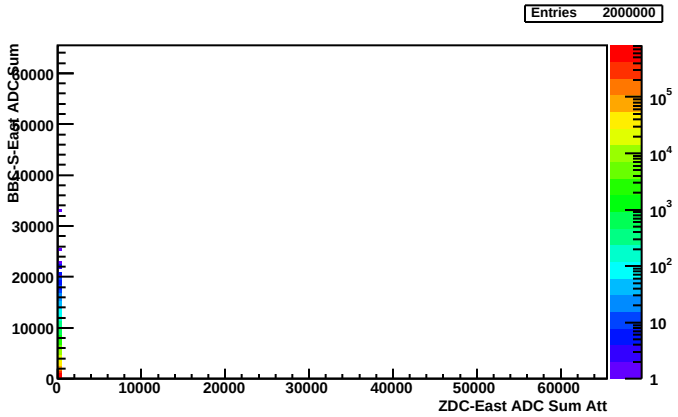

Entries 2000000
BBC-L-West ADC Sum
VPD-West ADC Sum

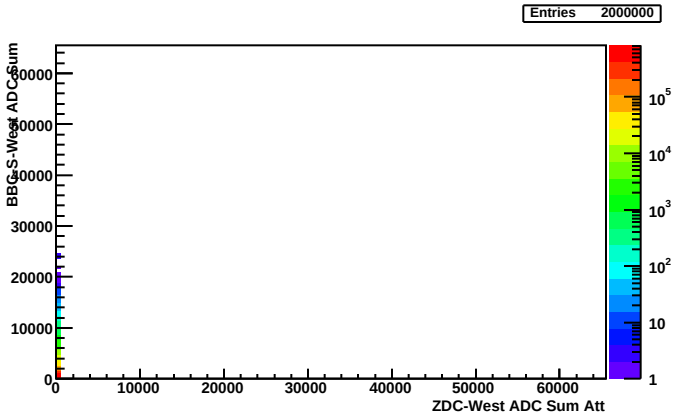

Entries 2000000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

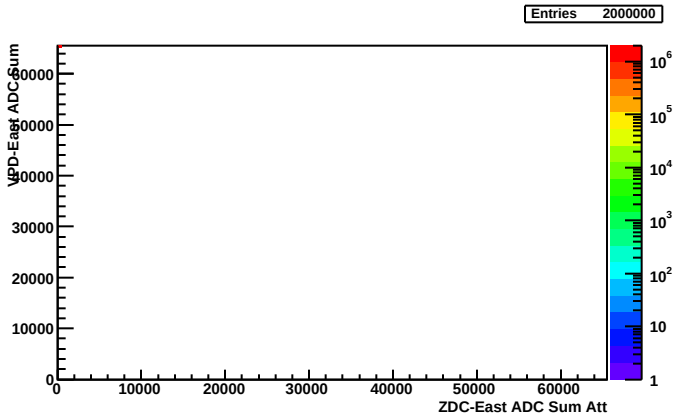

Entries 2000000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

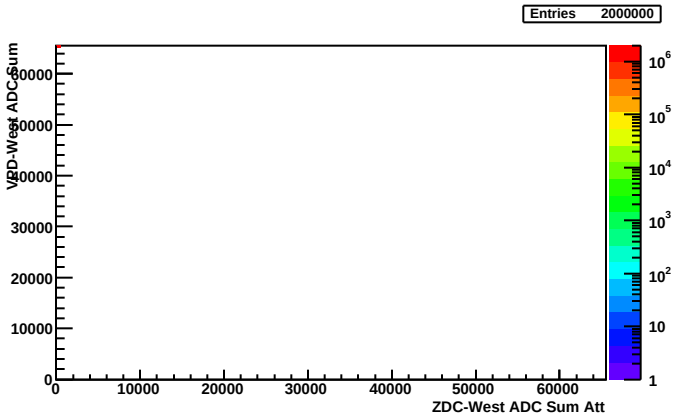

Entries 2000000
BBC-S-East ADC Sum
VPD-East ADC Sum

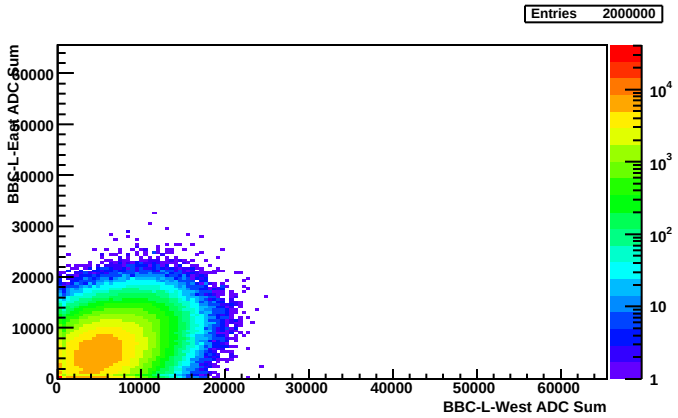

Entries 2000000
BBC-S-West ADC Sum
VPD-West ADC Sum

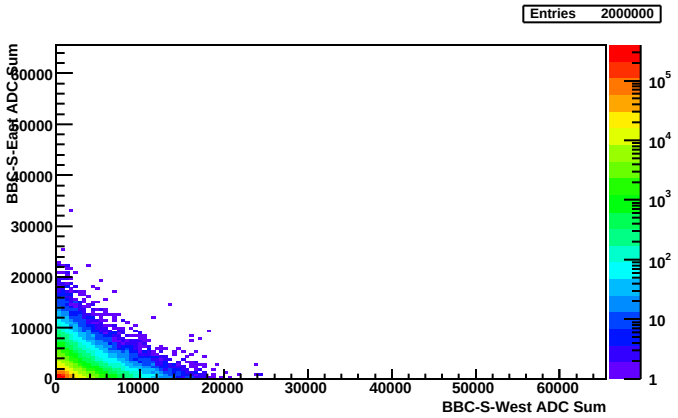

Entries 2000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2000000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2000000
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 2000000
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 2000000
BBC-L-East ADC Sum
BBC-L-West ADC Sum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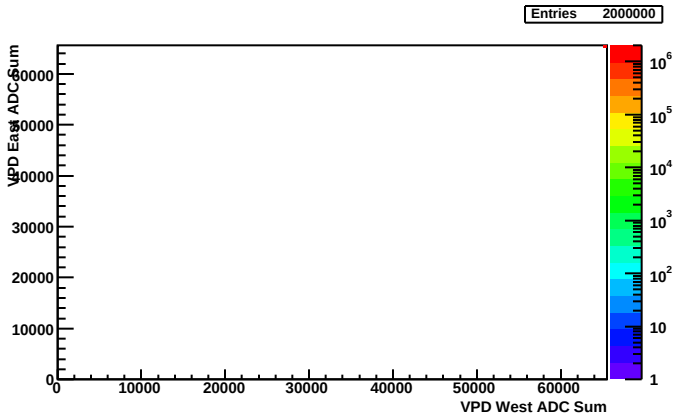
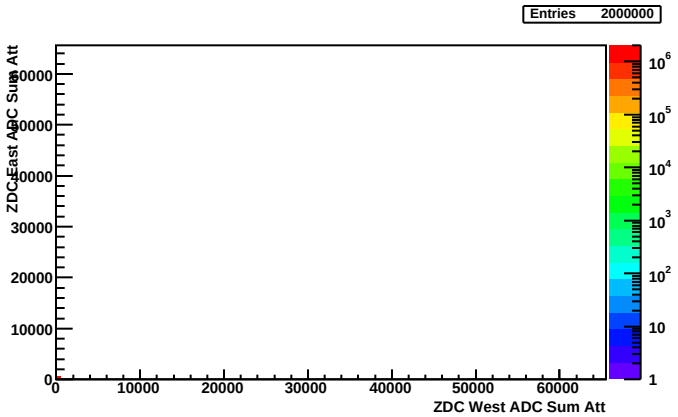
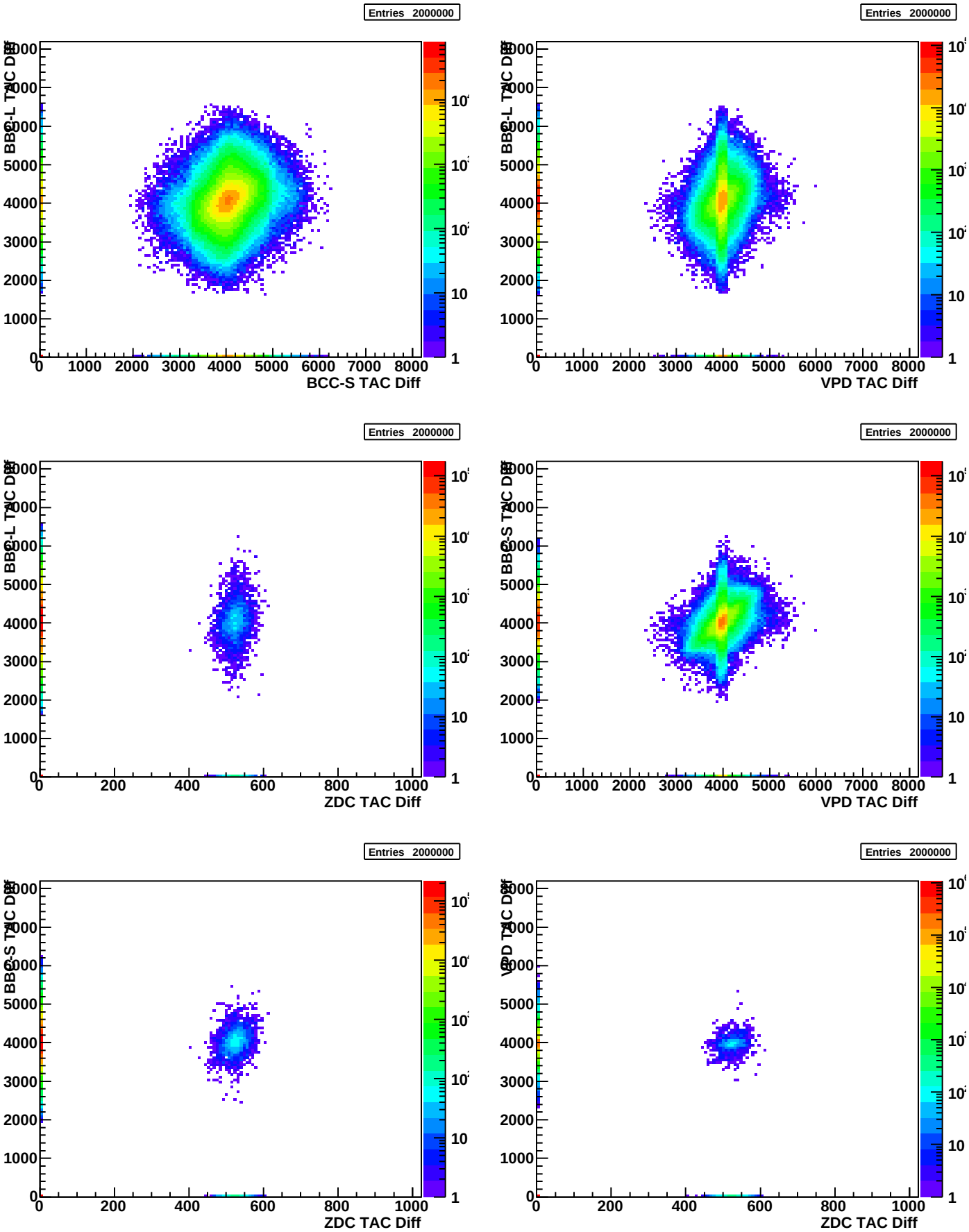
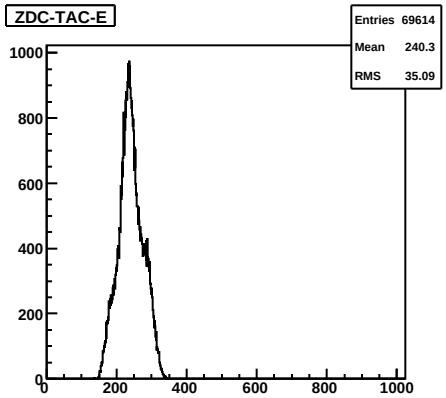
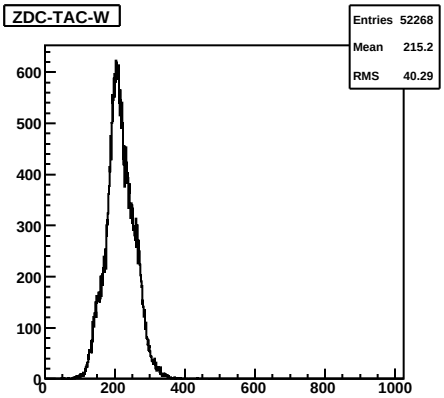
BBC-S-East ADC Sum
BBC-S-West ADC Sum

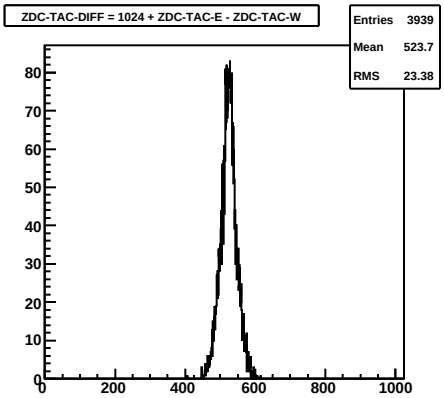

Entries 2000000
VPD East ADC Sum
VPD West ADC Sum

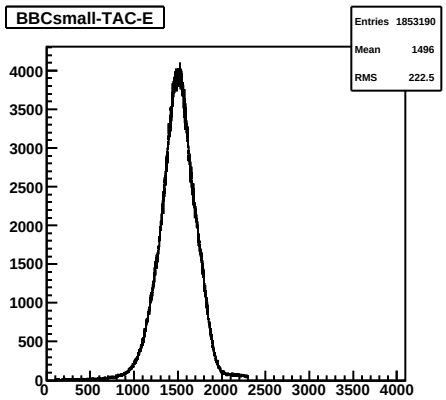

Entries 2000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

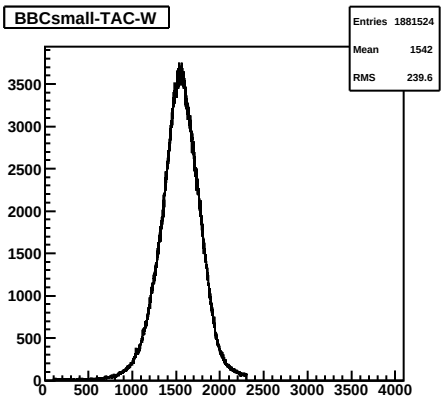

Entries 2000000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2000000
BBC-L TAC Diff
VPD TAC Diff
Entries 2000000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2000000
BBC-S TAC Diff
VPD TAC Diff
Entries 2000000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2000000
VPD TAC Diff
ZDC TAC Diff

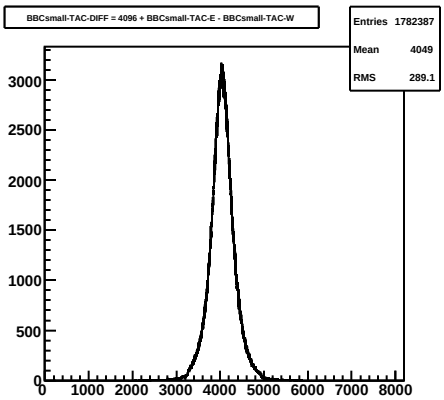

ZDC-TAC-E
Entries 69614
Mean 240.3
RMS 35.09

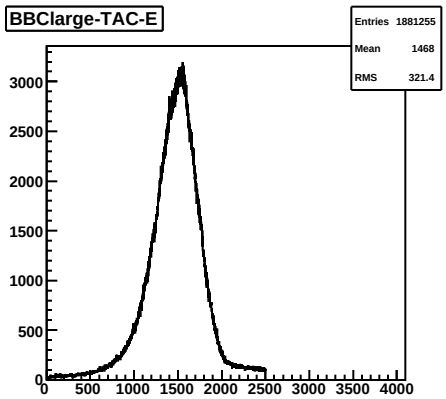

ZDC-TAC-W
Entries 52268
Mean 215.2
RMS 40.29

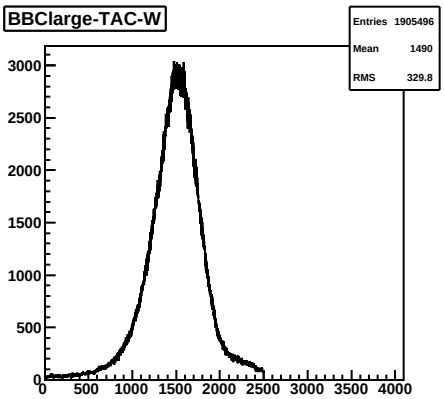

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3939
Mean 523.7
RMS 23.38

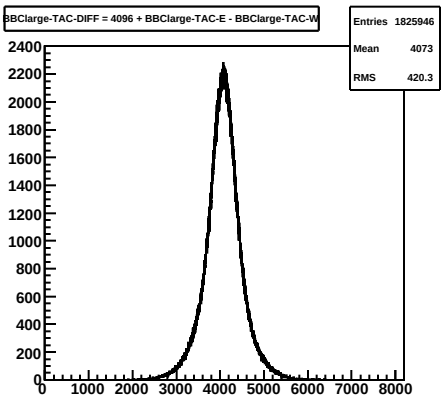

BBCsmall-TAC-E
Entries 1853190
Mean 1496
RMS 222.5

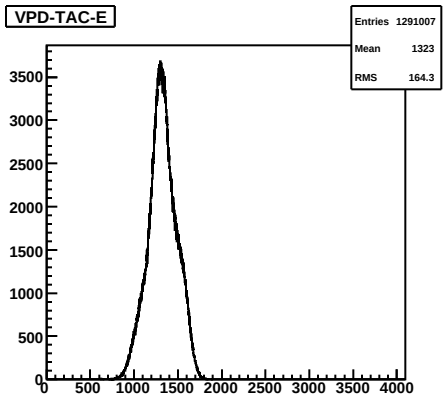

BBCsmall-TAC-W
Entries 1881524
Mean 1542
RMS 239.6

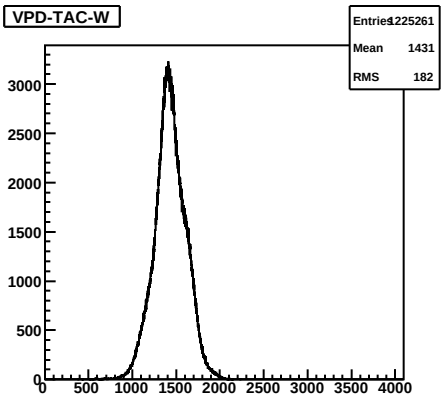

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1782387
Mean 4049
RMS 289.1

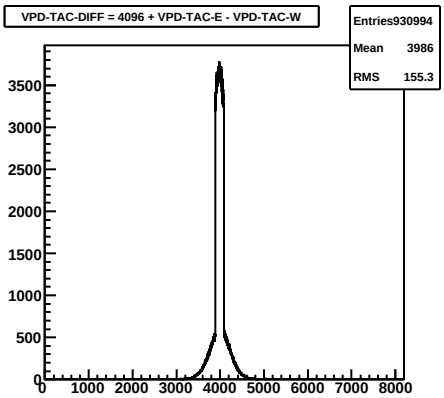

BBClarge-TAC-E
Entries 1881255
Mean 1468
RMS 321.4

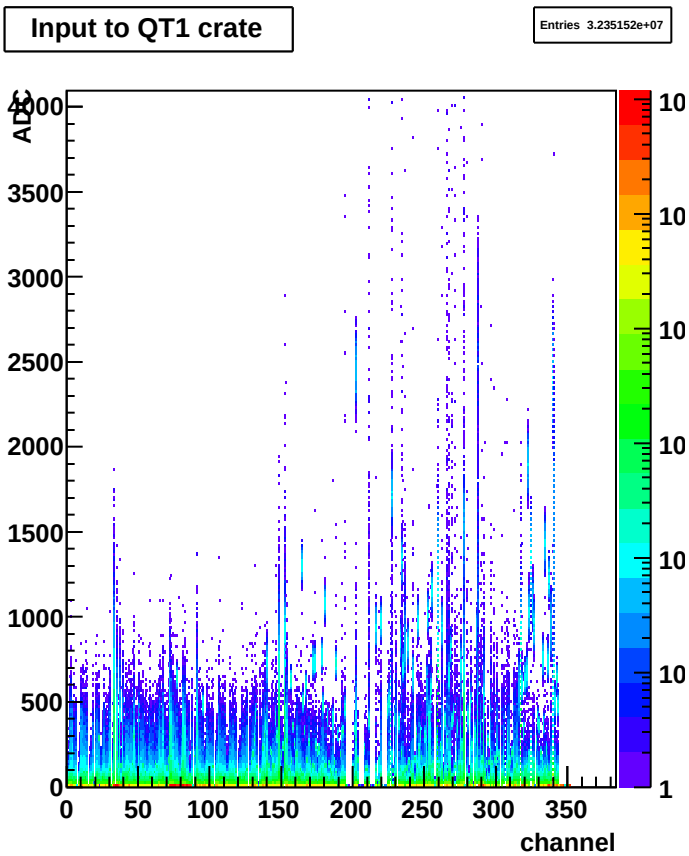

BBClarge-TAC-W
Entries 1905496
Mean 1490
RMS 329.8

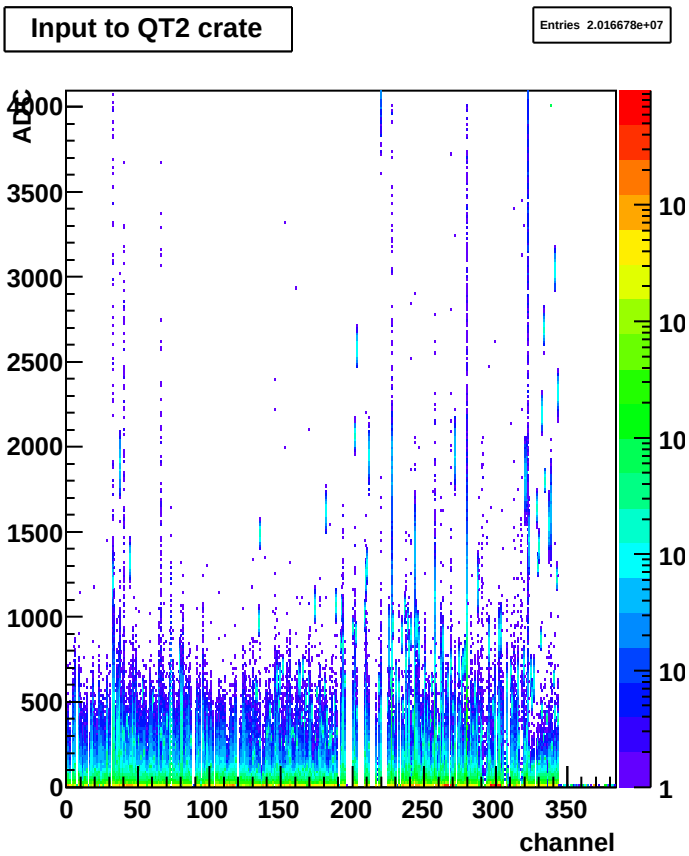

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1825946
Mean 4073
RMS 420.3

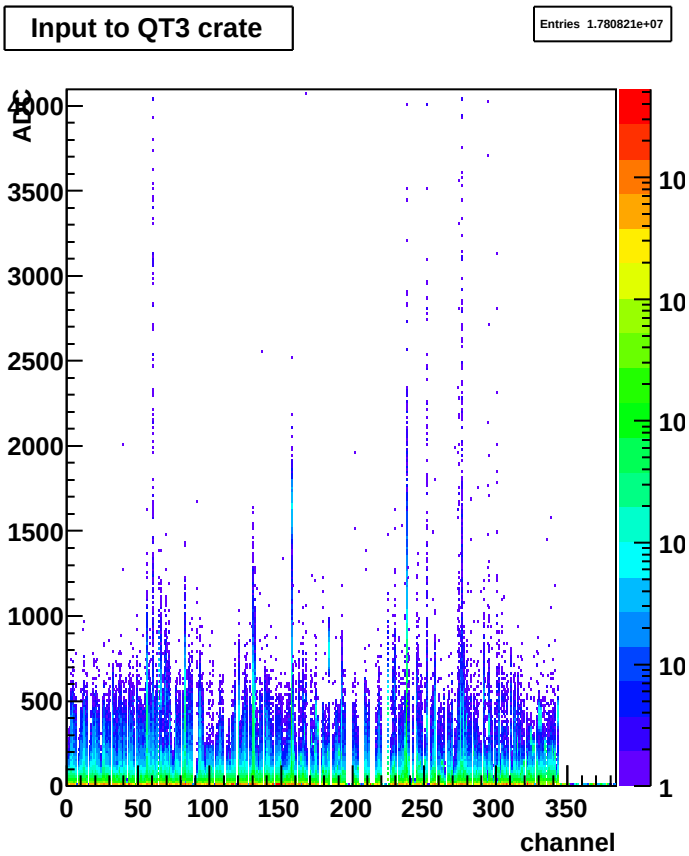

VPD-TAC-E
Entries 1291007
Mean 1323
RMS 164.3

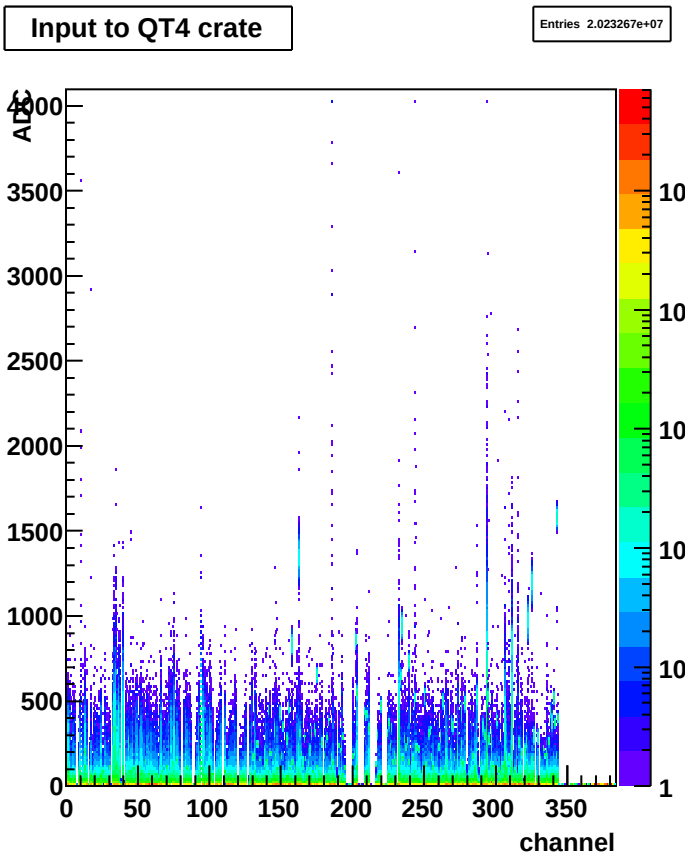

VPD-TAC-W
Entries 1225261
Mean 1431
RMS 182

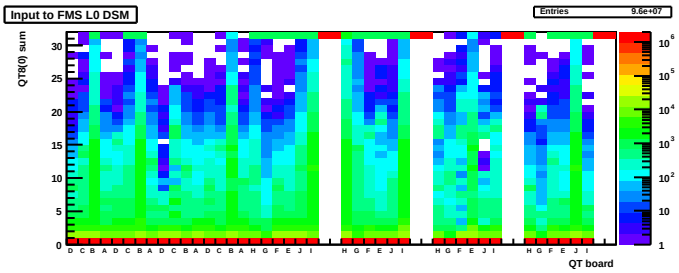

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 930994
Mean 3986
RMS 155.3

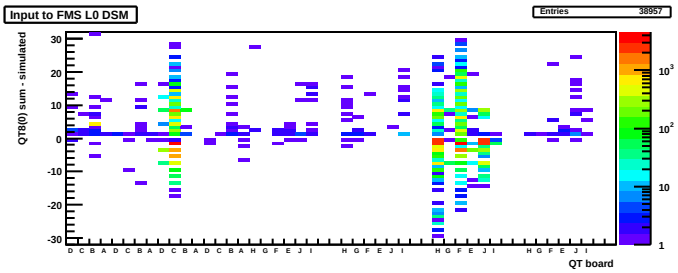

Input to QT1 crate
Entries 3.235152e+07
ADC
channel

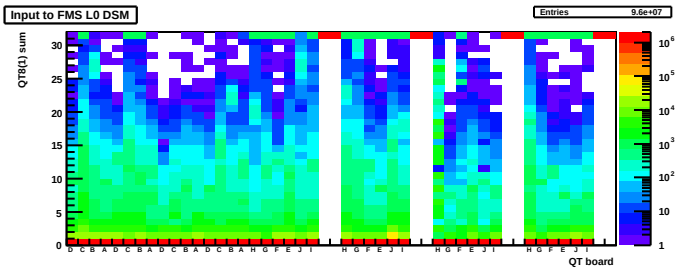

Input to QT2 crate
Entries 2.016678e+07
ADC
channel

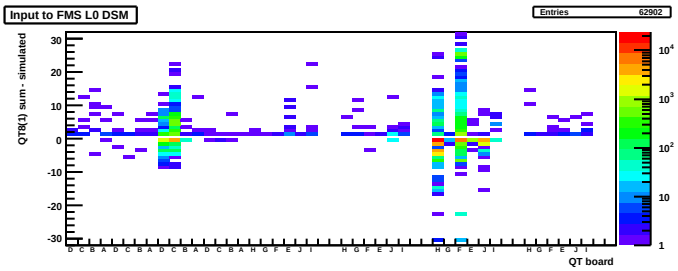

Input to QT3 crate
Entries 1.780821e+07
ADC
channel

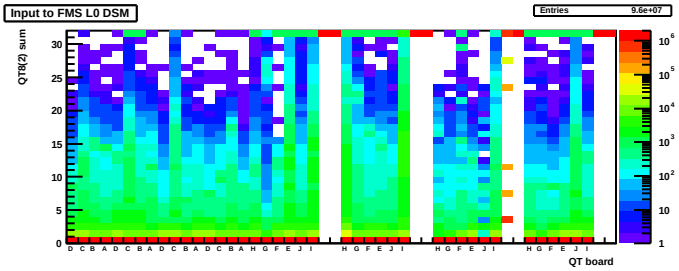

Input to QT4 crate
Entries 2.023267e+07
ADC
channel

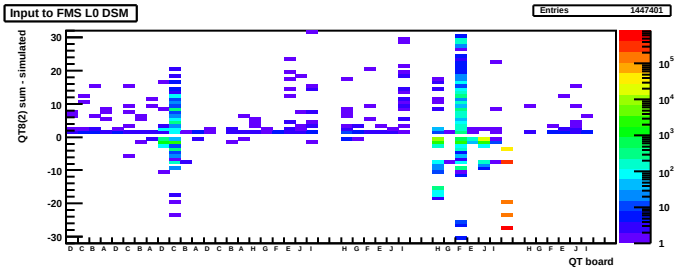

Input to FMS L0 DSM
Entries 9.6e+07
QT8(0) sum
QT board

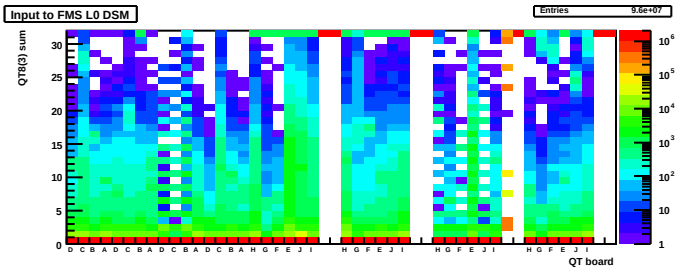

Input to FMS L0 DSM
Entries 38957
QT8(0) sum - simulated
QT board

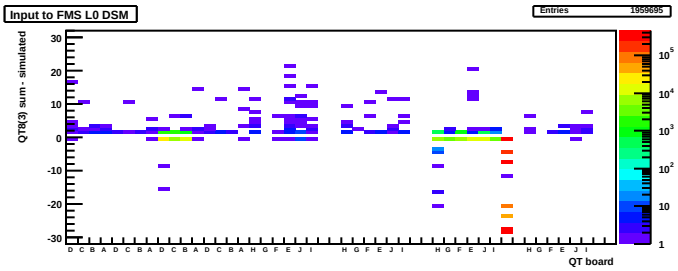

Input to FMS L0 DSM
Entries 9.6e+07
QT8(1) sum
QT board

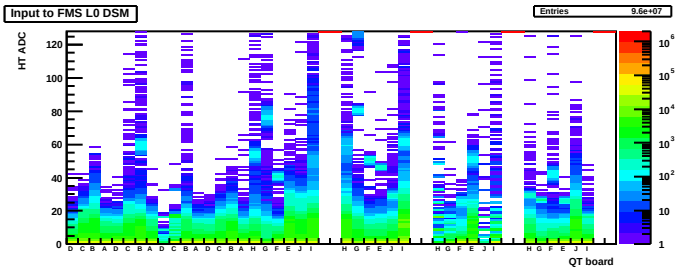

Input to FMS L0 DSM
Entries 62902
QT8(1) sum - simulated
QT board

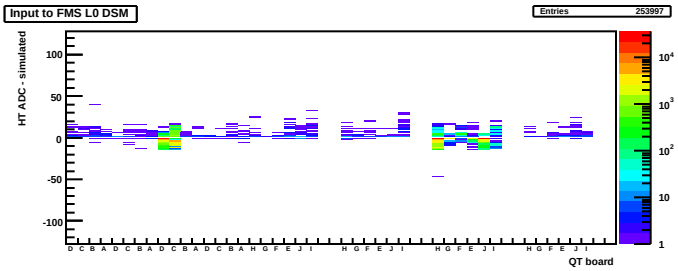

Input to FMS L0 DSM
Entries 9.6e+07
QT8(2) sum
QT board

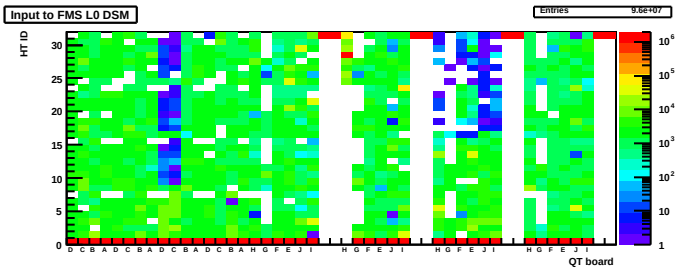

Input to FMS L0 DSM
Entries 1447401
QT8(2) sum - simulated
QT board

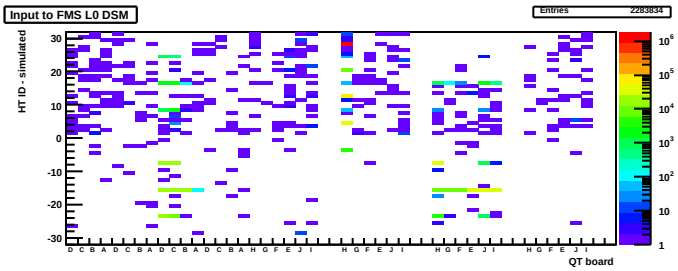

Input to FMS L0 DSM
Entries 9.6e+07
QT8(3) sum
QT board

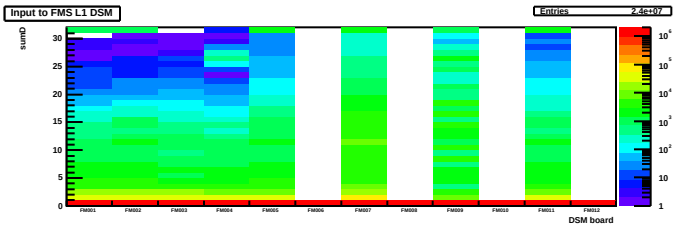

Input to FMS L0 DSM
Entries 1959695
QT8(3) sum - simulated
QT board

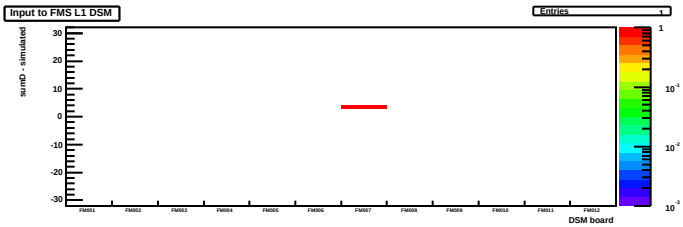

Input to FMS L0 DSM
Entries 9.6e+07
HT ADC
QT board

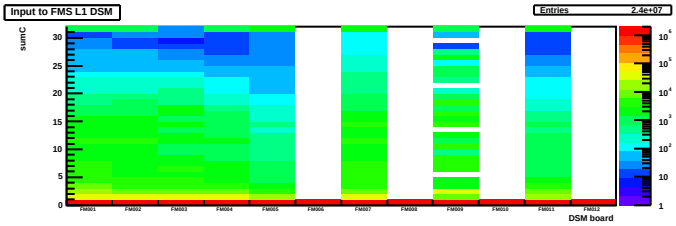

Input to FMS L0 DSM
Entries 253997
HT ADC - simulated
QT board

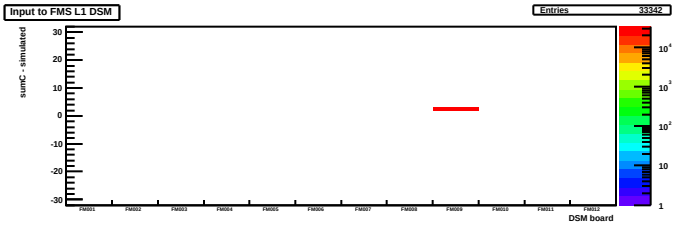

Input to FMS L0 DSM
Entries 9.6e+07
HT ID
QT board

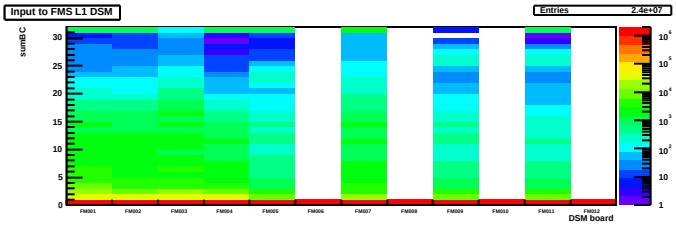

Input to FMS L0 DSM
Entries 2283834
HT ID - simulated
QT board

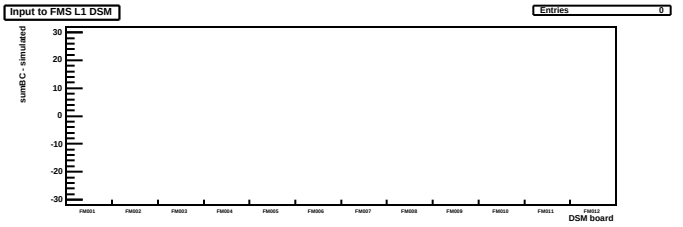

Input to FMS L1 DSM
Entries 2.4e+07
sumD
DSM board

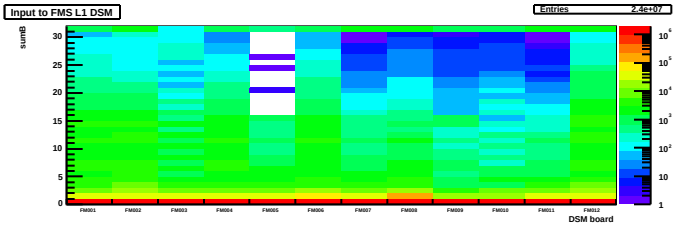

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

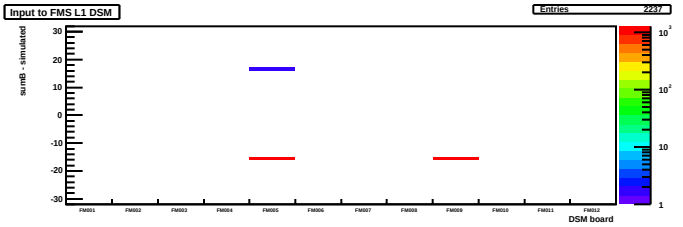

Input to FMS L1 DSM
Entries 2.4e+07
sumC
DSM board

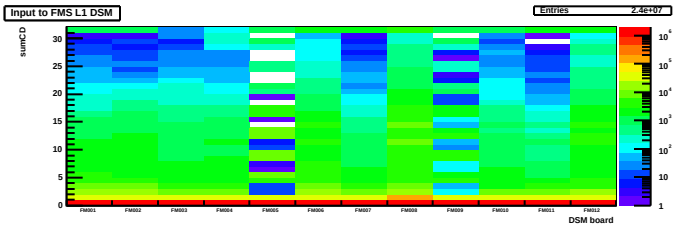

Input to FMS L1 DSM
Entries 33342
sumC - simulated
DSM board

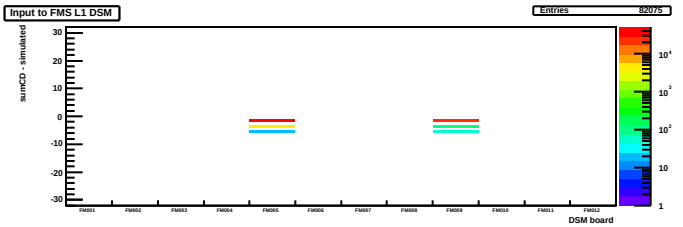

Input to FMS L1 DSM
Entries 2.4e+07
sumBC
DSM board

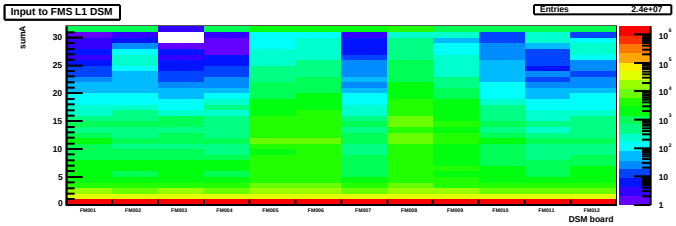

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

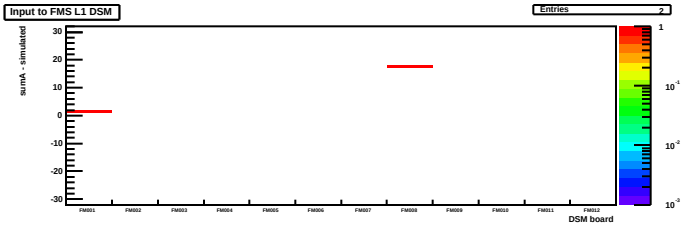

Input to FMS L1 DSM
Entries 2.4e+07
sumB
DSM board

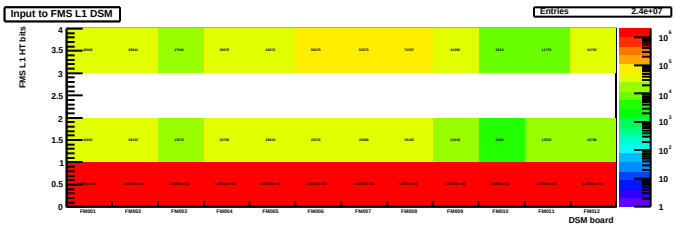

Input to FMS L1 DSM
Entries 2237
sumB - simulated
DSM board

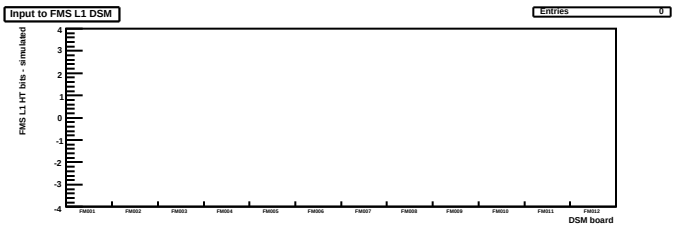

Input to FMS L1 DSM
Entries 2.4e+07
sumCD
DSM board

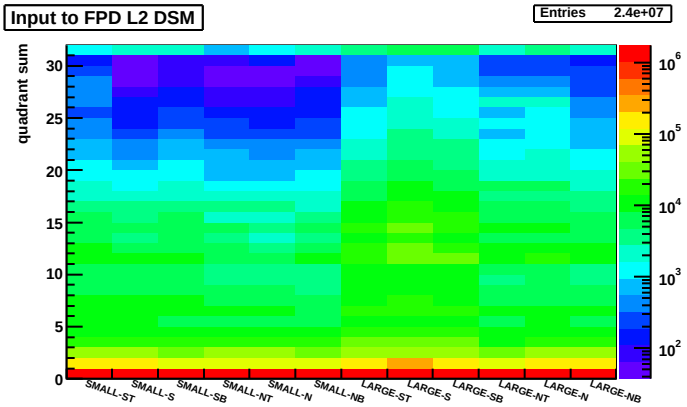

Input to FMS L1 DSM
Entries 82075
sumCD - simulated
DSM board

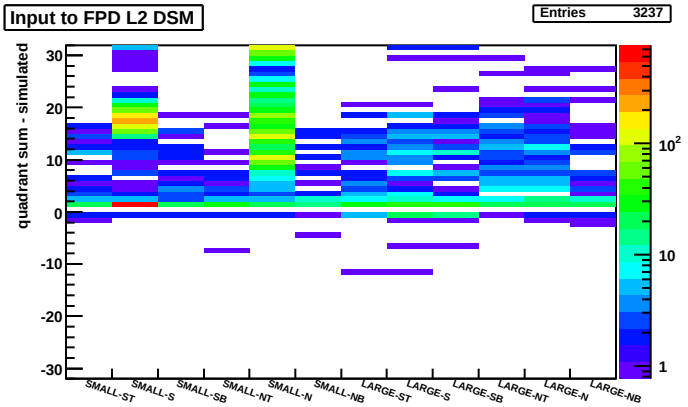

Input to FMS L1 DSM
Entries 2.4e+07
sumA
DSM board

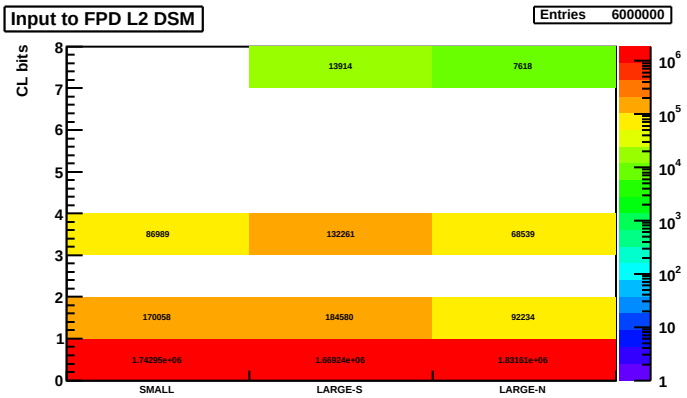

Input to FMS L1 DSM
Entries 2
sumA - simulated
DSM board

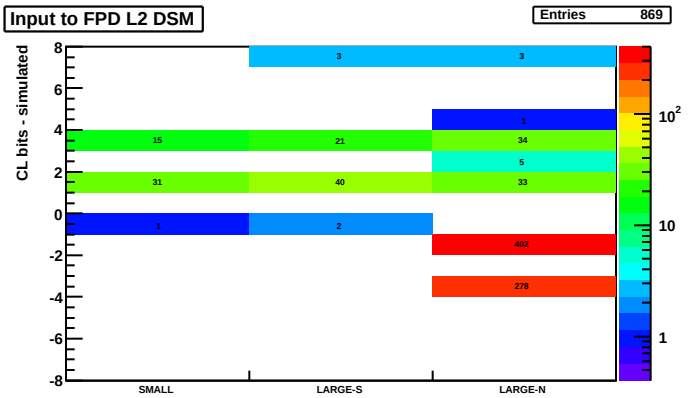

Input to FMS L1 DSM
Entries 2.4e+07
FMS L1 HT bits
DSM board

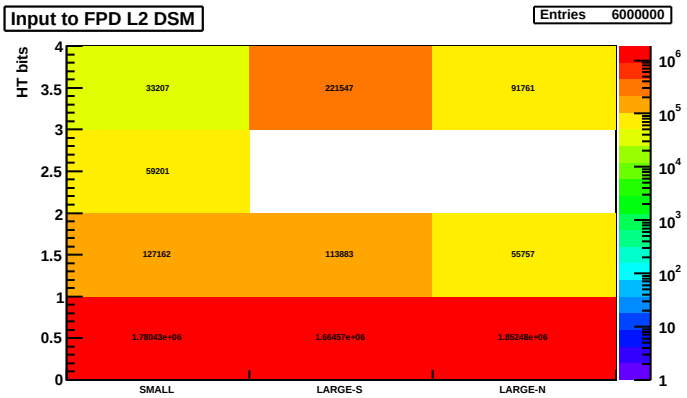

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

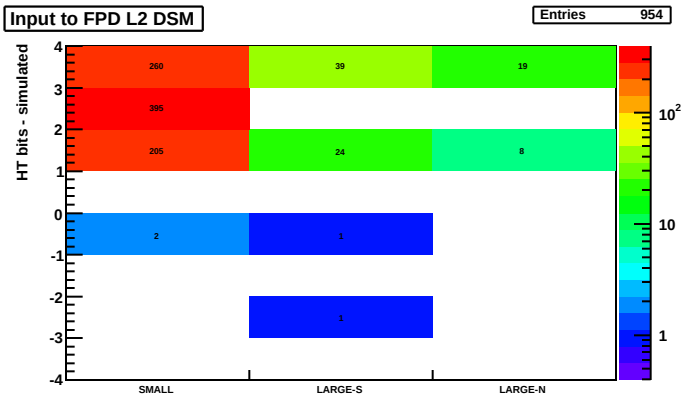

Input to FPD L2 DSM
Entries 2.4e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

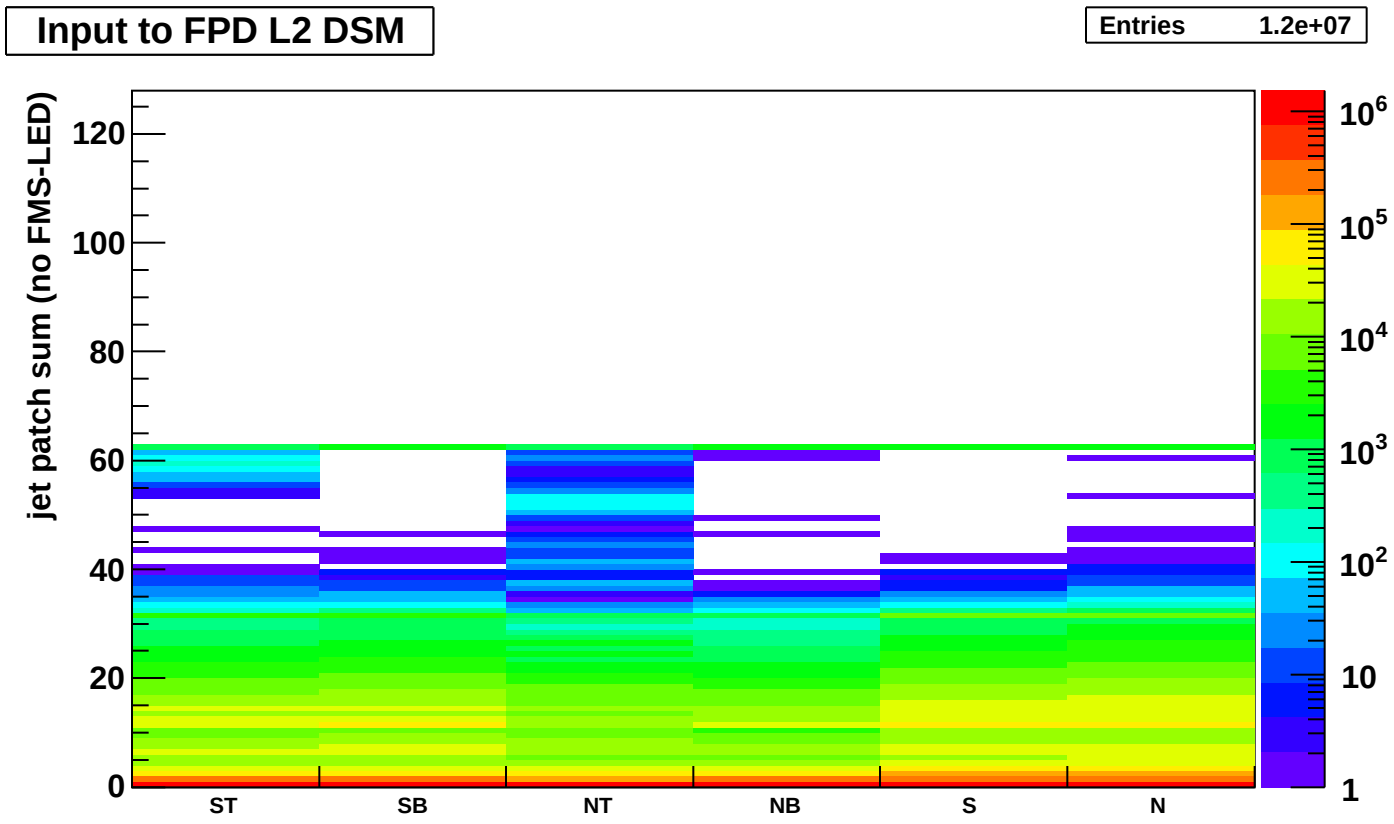

Input to FPD L2 DSM
Entries 3237
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

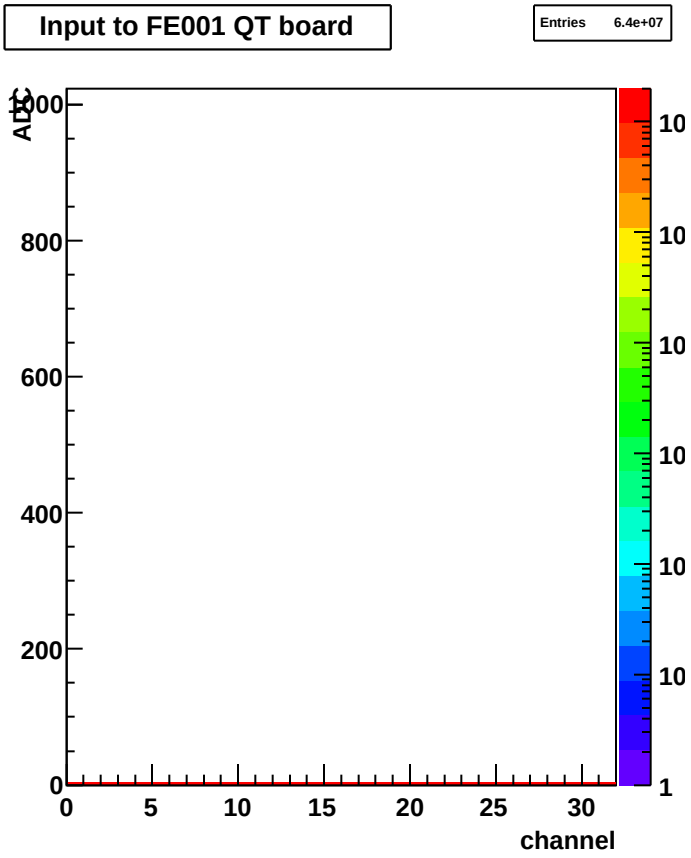

Input to FPD L2 DSM
Entries 6000000
CL bits
13914
7618
86989
132261
68539
170058
184580
92234
1.74295e+06
1.66924e+06
1.83161e+06
SMALL
LARGE-S
LARGE-N

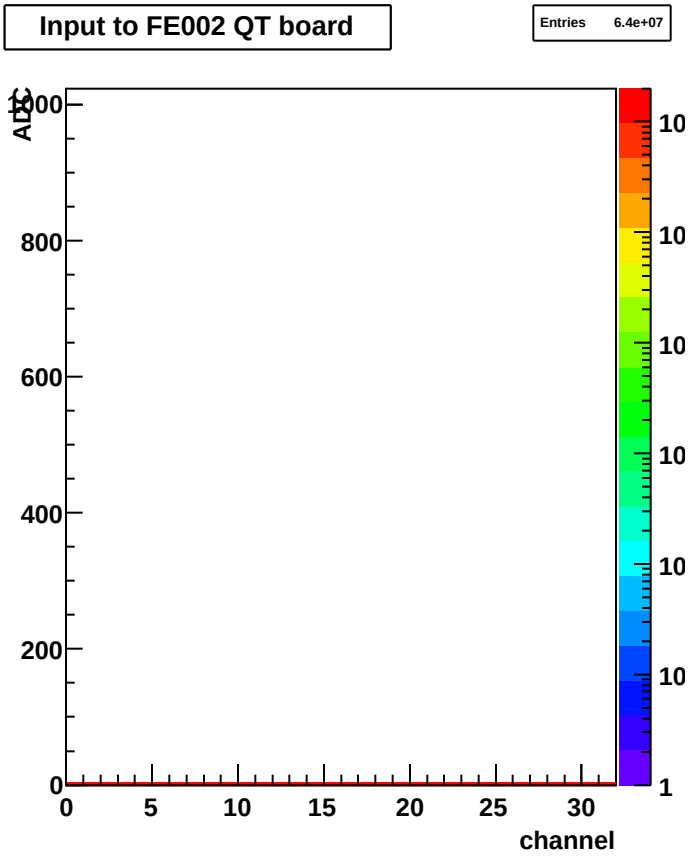

Input to FPD L2 DSM
Entries 869
CL bits - simulated
3
3
1
15
21
34
5
31
40
33
1
2
402
278
SMALL
LARGE-S
LARGE-N

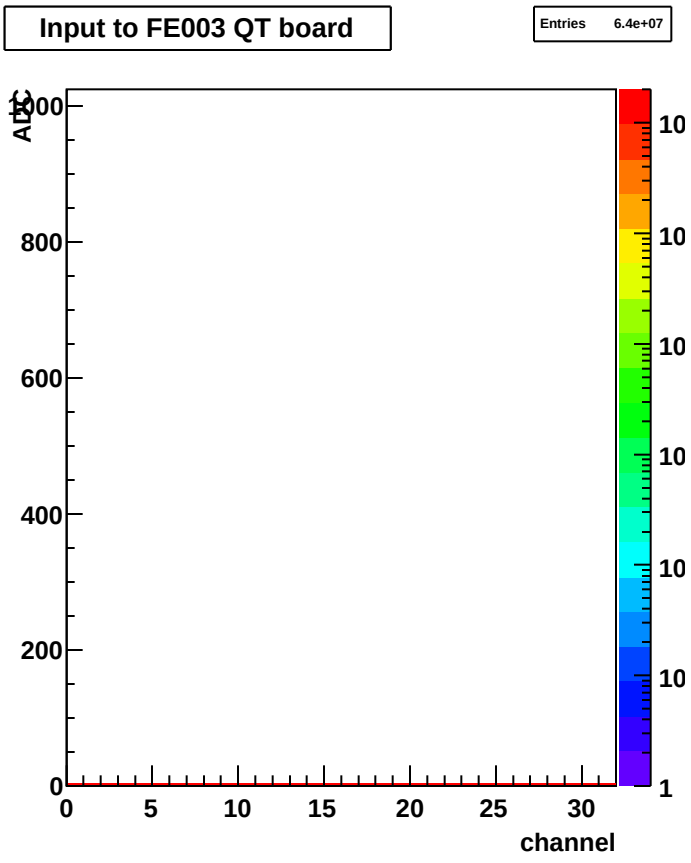

Input to FPD L2 DSM
Entries 6000000
HT bits
33207
221547
91761
59201
127162
113883
55757
1.78043e+06
1.66457e+06
1.85248e+06
SMALL
LARGE-S
LARGE-N

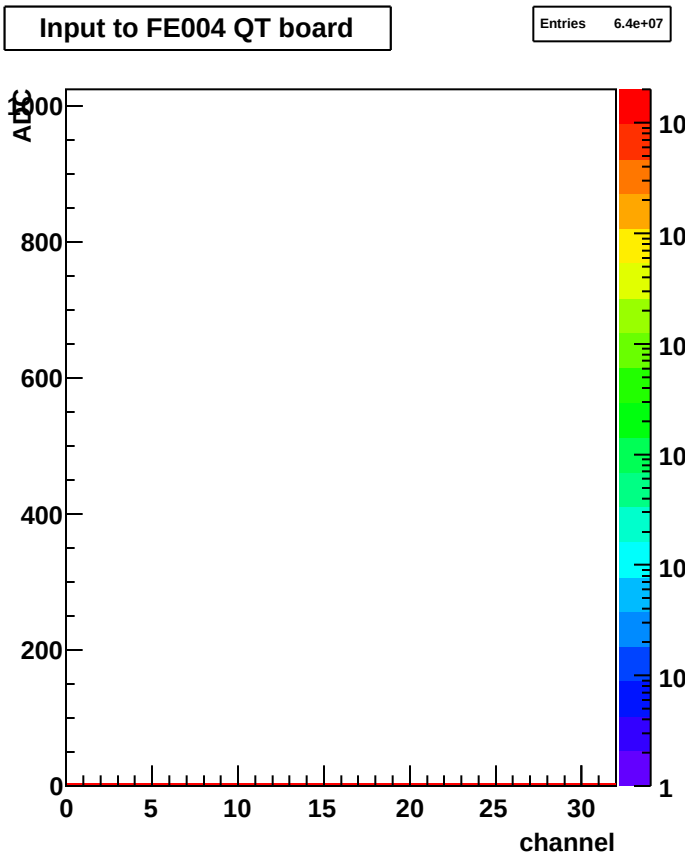

Input to FPD L2 DSM
Entries 954
HT bits - simulated
260
39
19
395
205
24
8
2
1
1
SMALL
LARGE-S
LARGE-N

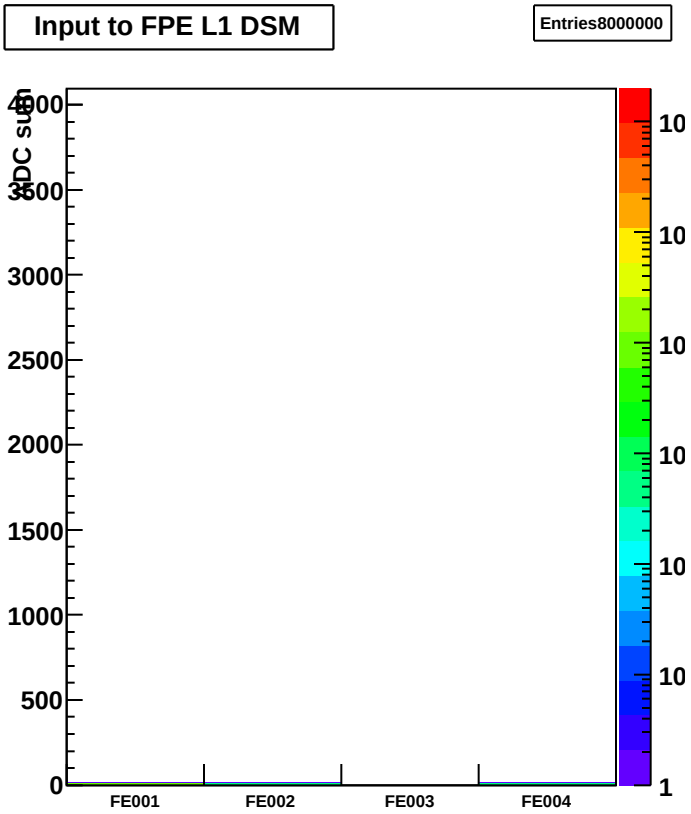

Input to FPD L2 DSM
Entries 1.2e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

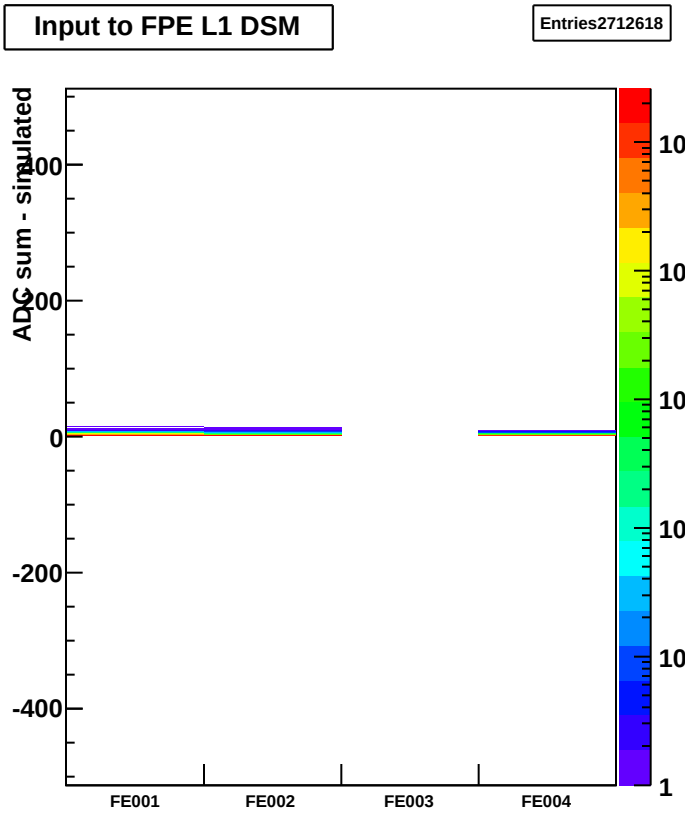

Input to FE001 QT board
Entries 6.4e+07
ADC
channel

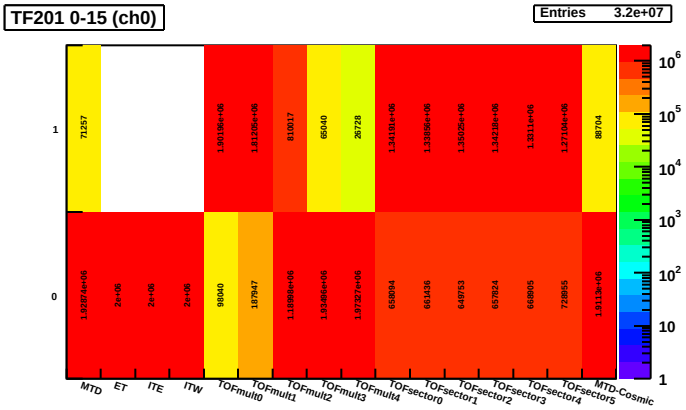

Input to FE002 QT board
Entries 6.4e+07
ADC
channel

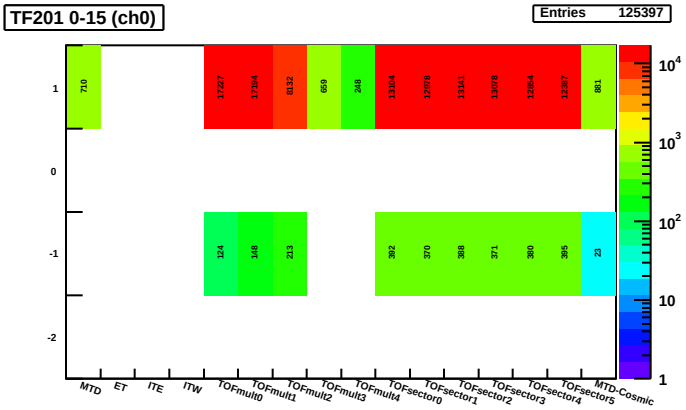

Input to FE003 QT board
Entries 6.4e+07
ADC
channel

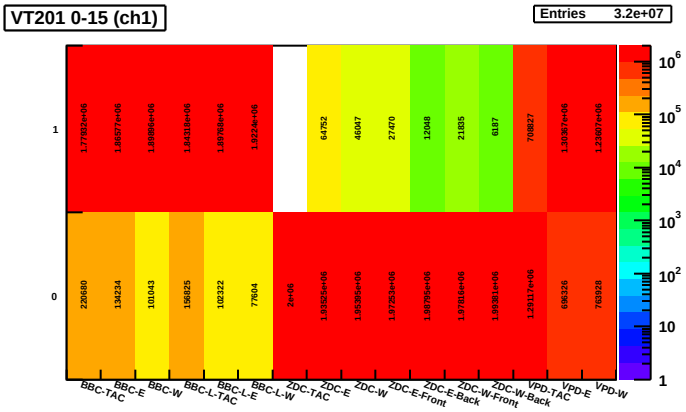

Input to FE004 QT board
Entries 6.4e+07
ADC
channel

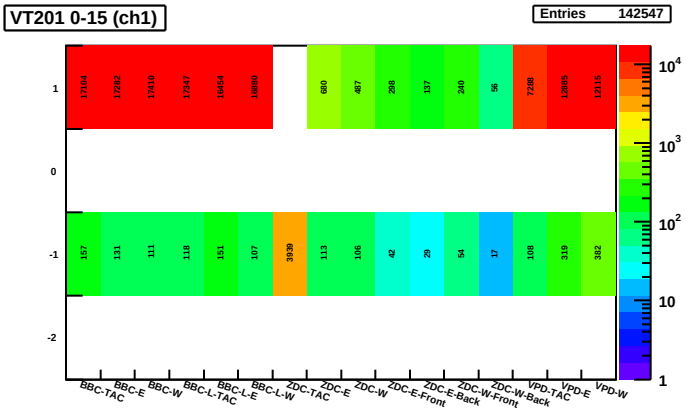

Input to FPE L1 DSM
Entries8000000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2712618
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.2e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 125397
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3.2e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 142547
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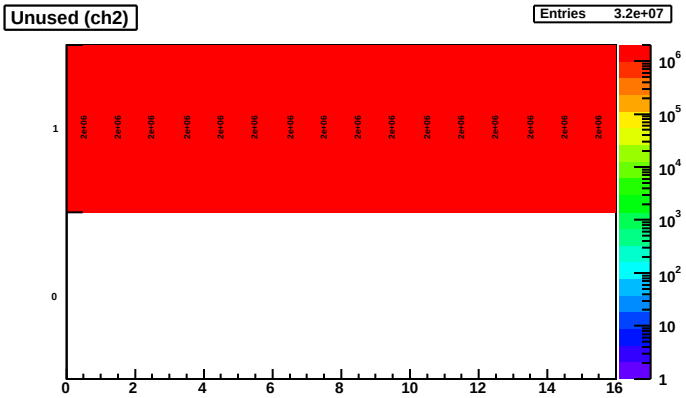
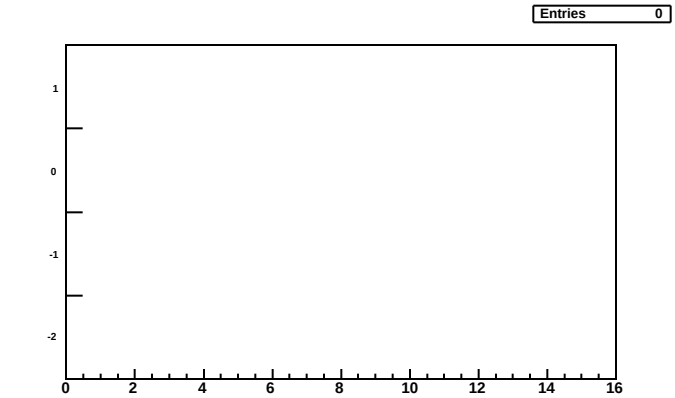
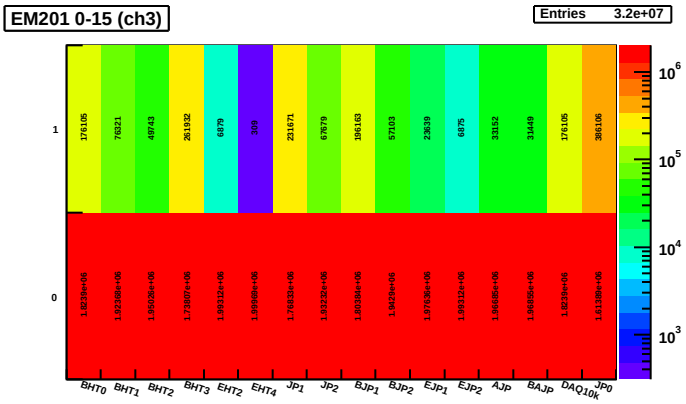
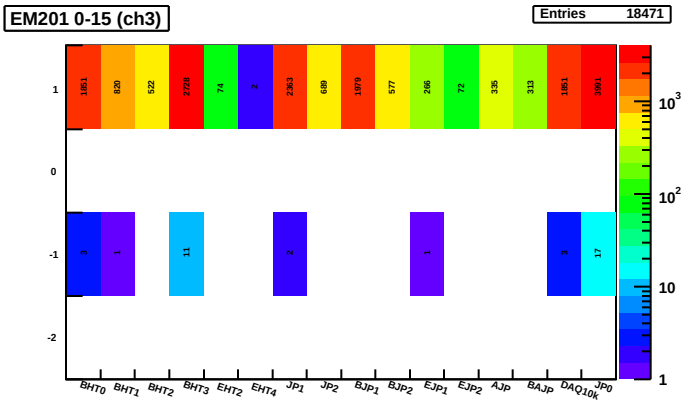

Unused (ch2)
Entries 3.2e+07


Entries 0


EM201 0-15 (ch3)
Entries 3.2e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 18471
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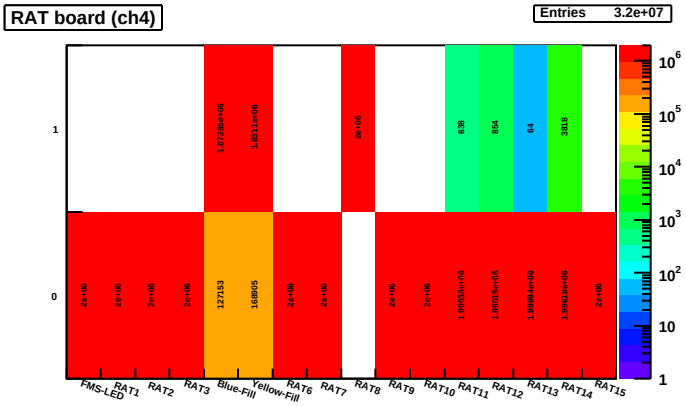
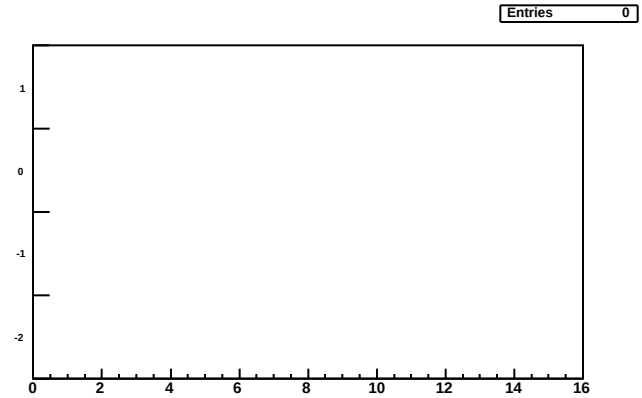
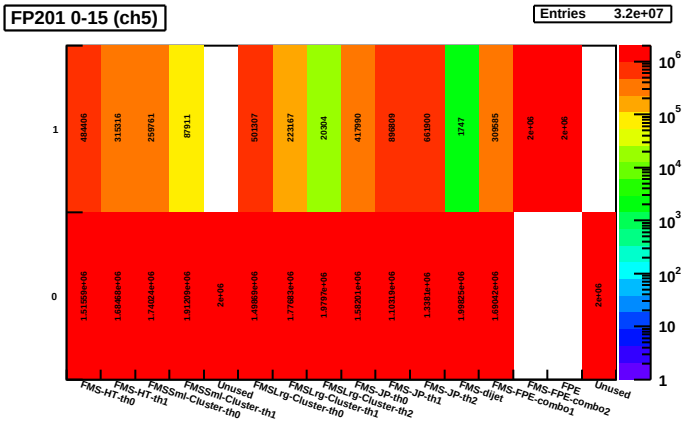
RAT board (ch4)





FP201 0-15 (ch5)



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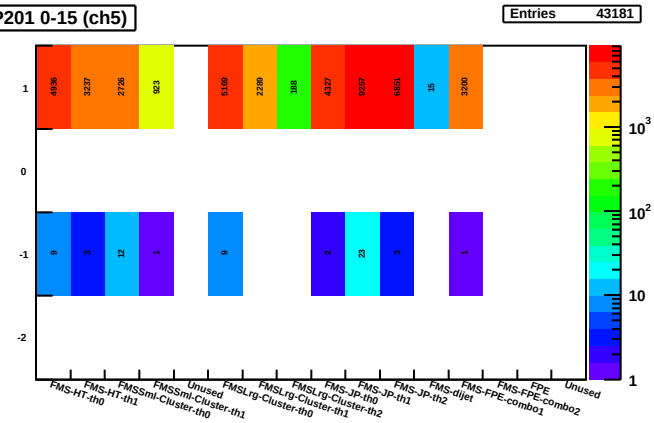
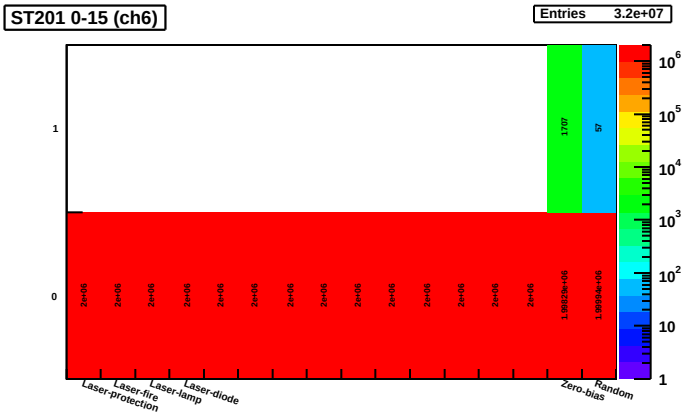
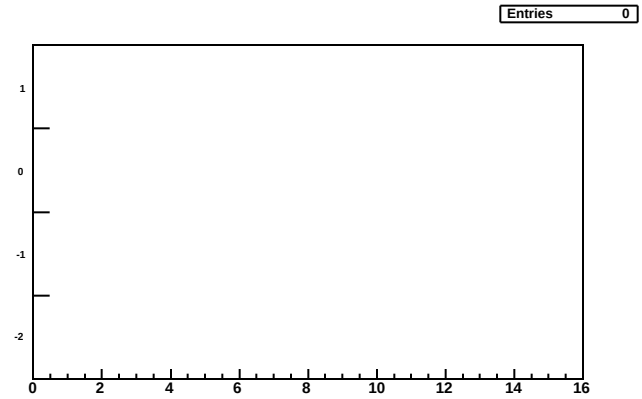
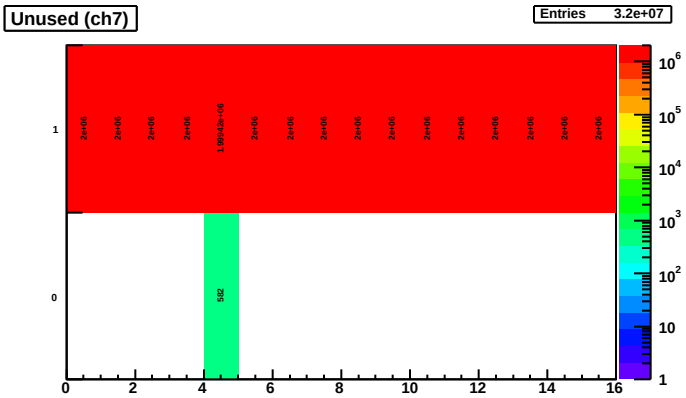
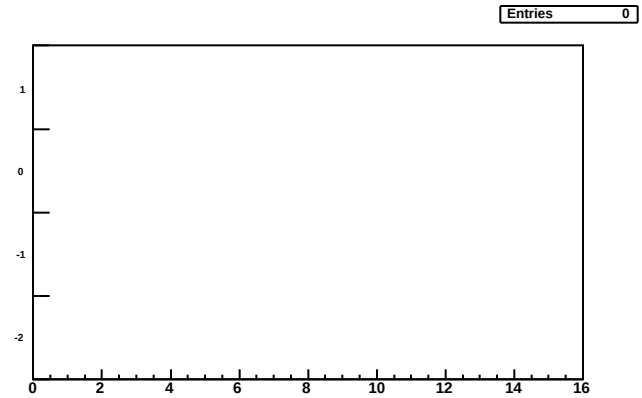


ST201 0-15 (ch6)





Unused (ch7)