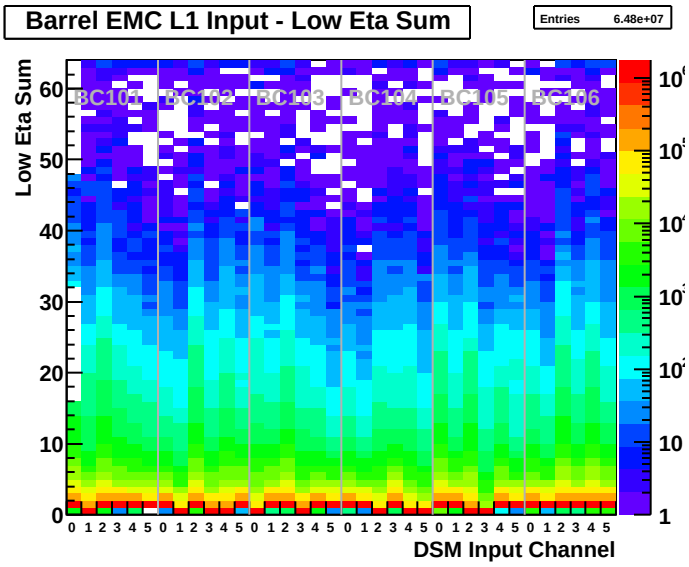
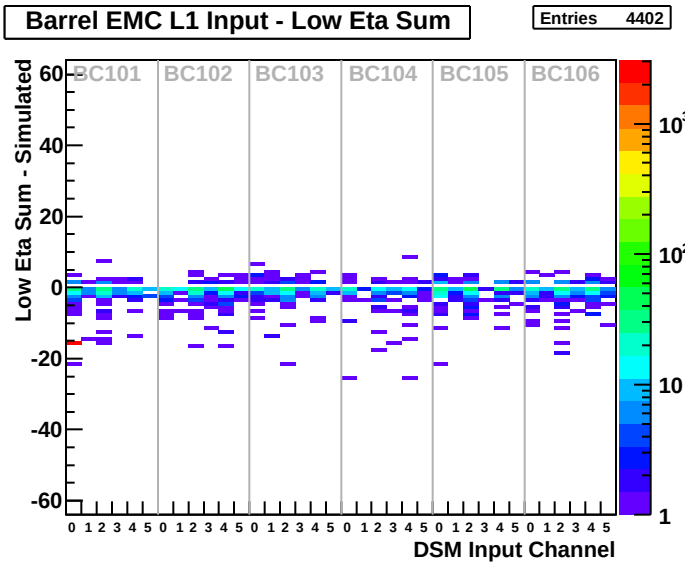
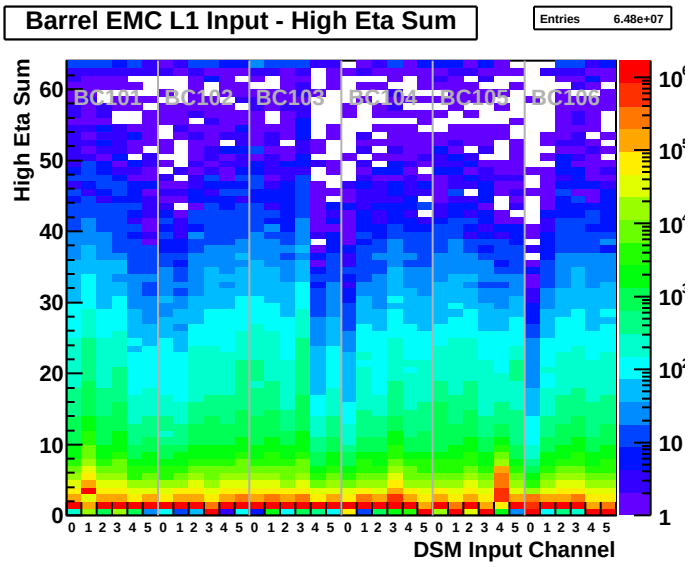
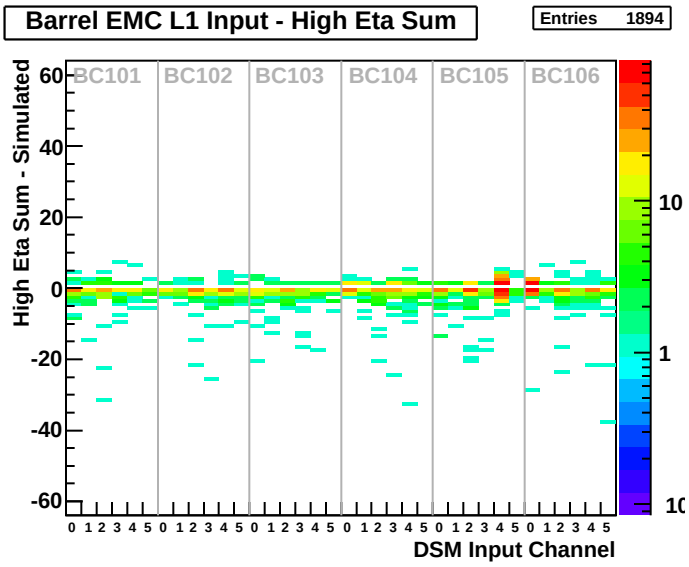
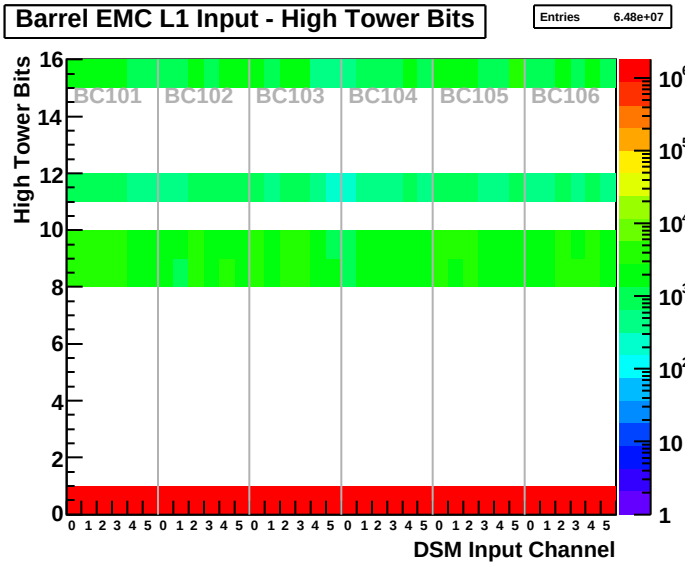
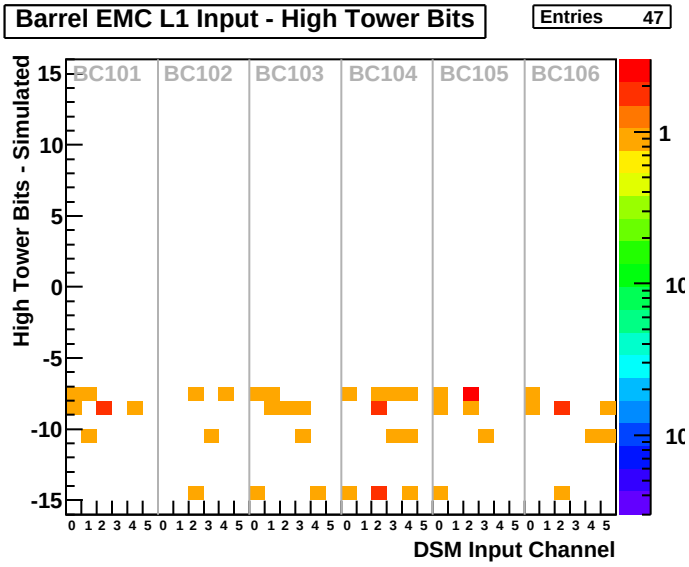

Barrel EMC L1 Input - Low Eta Sum
Entries 6.48e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

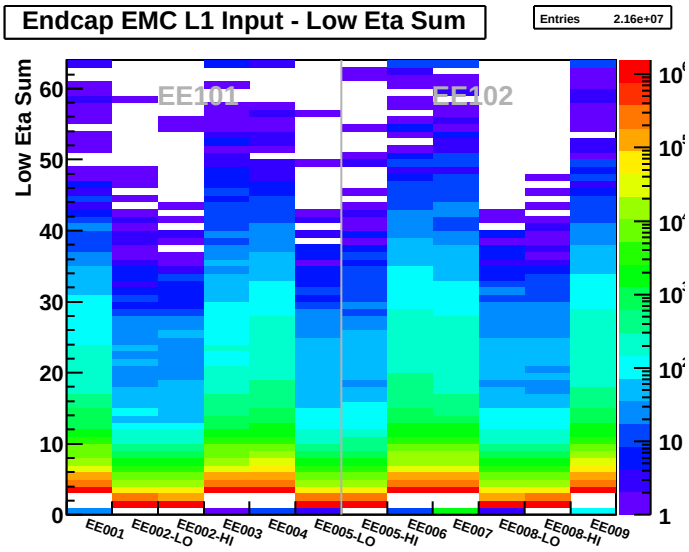

Barrel EMC L1 Input - Low Eta Sum
Entries 4402
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

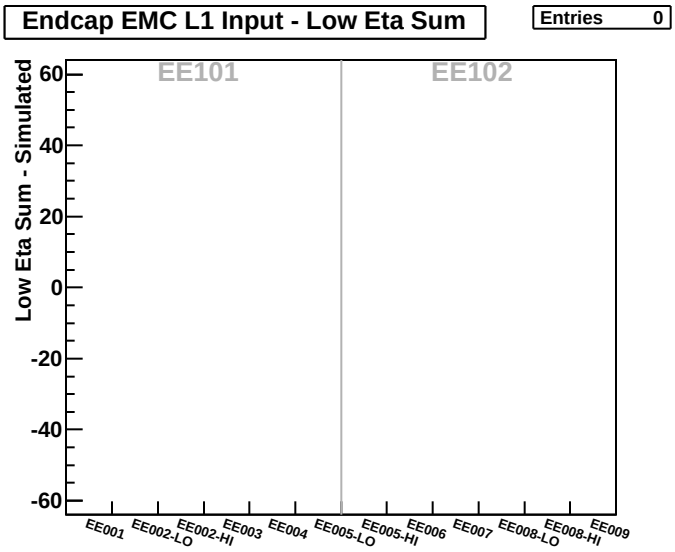

Barrel EMC L1 Input - High Eta Sum
Entries 6.48e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

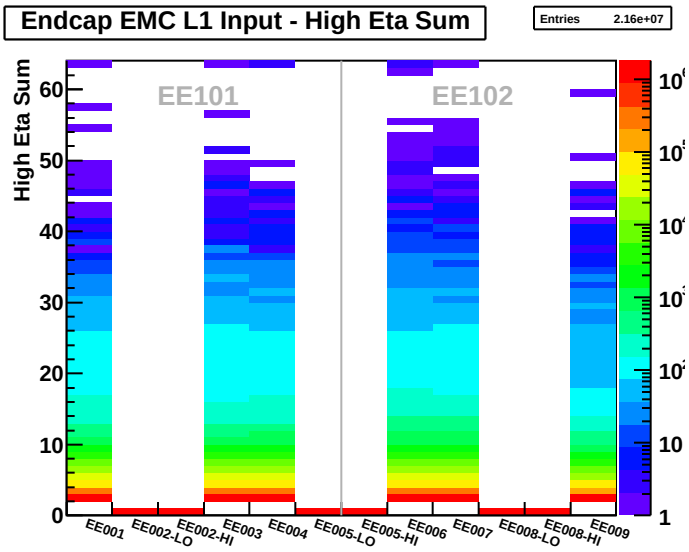

Barrel EMC L1 Input - High Eta Sum
Entries 1894
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

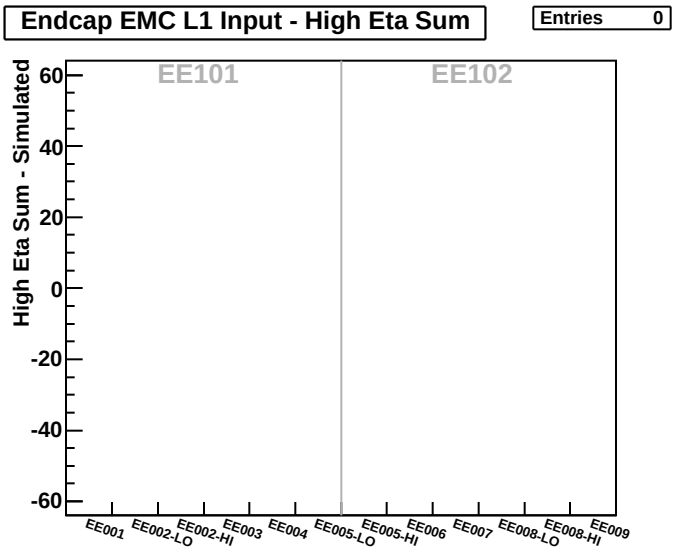

Barrel EMC L1 Input - High Tower Bits
Entries 6.48e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

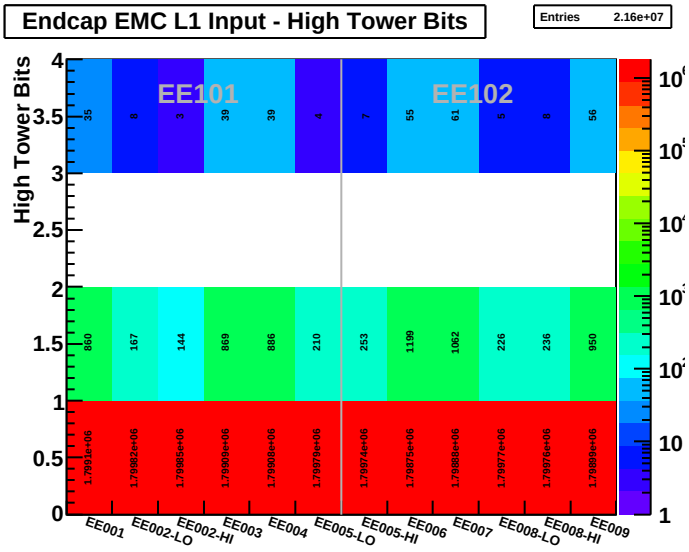

Barrel EMC L1 Input - High Tower Bits
Entries 47
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

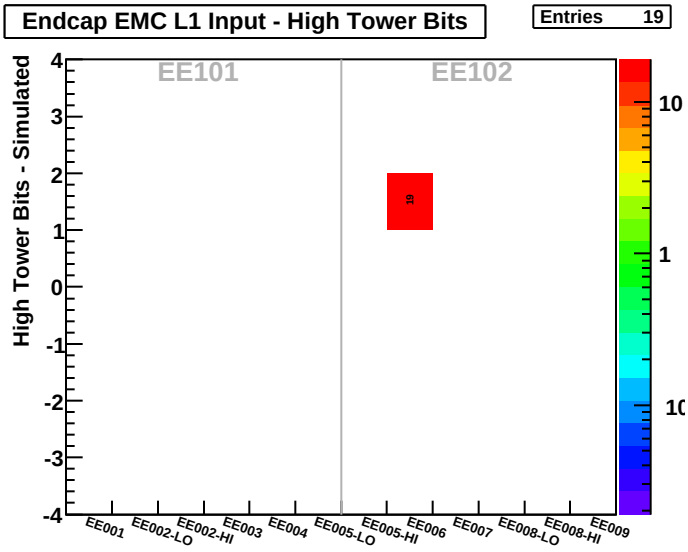

Endcap EMC L1 Input - Low Eta Sum
Entries 2.16e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

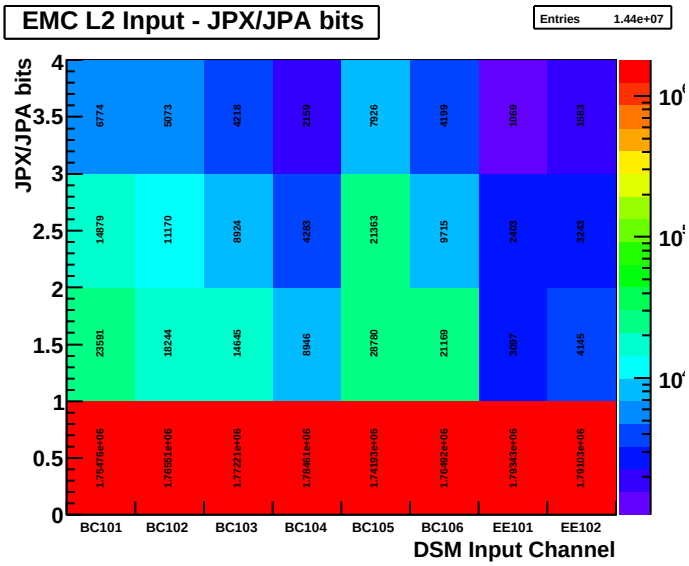

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

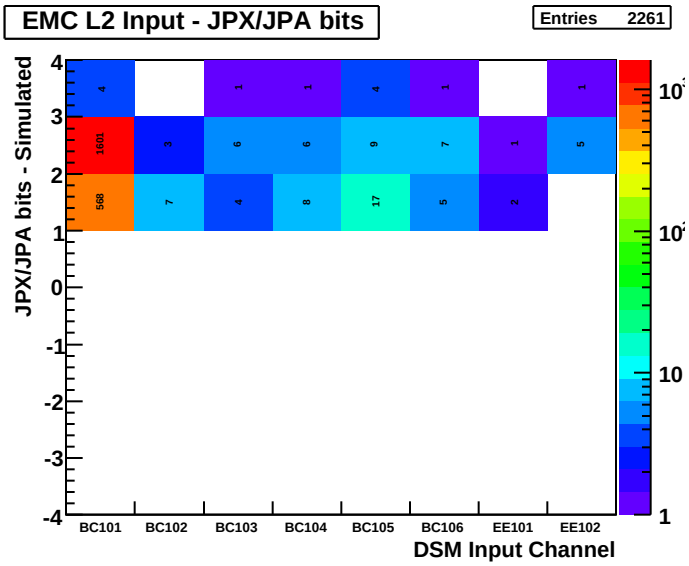

Endcap EMC L1 Input - High Eta Sum
Entries 2.16e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

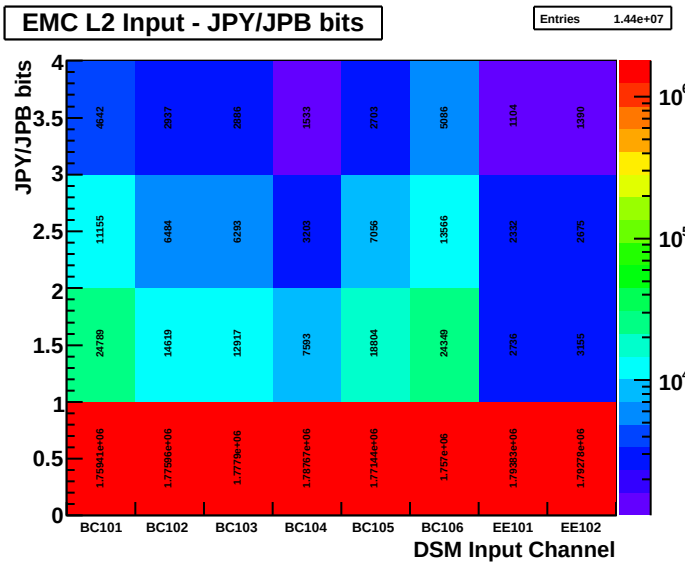

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

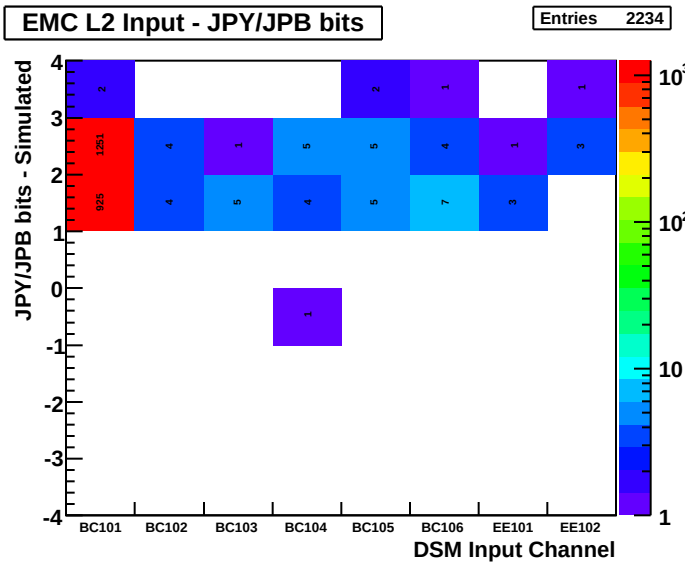

Endcap EMC L1 Input - High Tower Bits
Entries 2.16e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

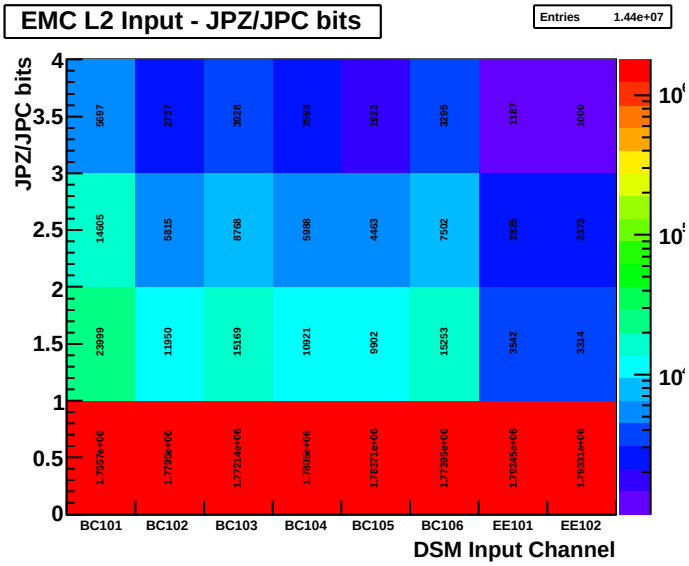

Endcap EMC L1 Input - High Tower Bits
Entries 19
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

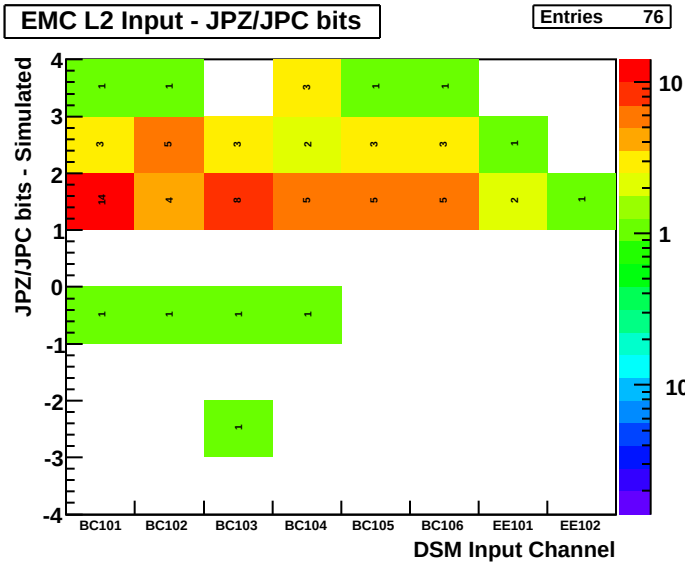

EMC L2 Input - JPX/JPA bits
Entries 1.44e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

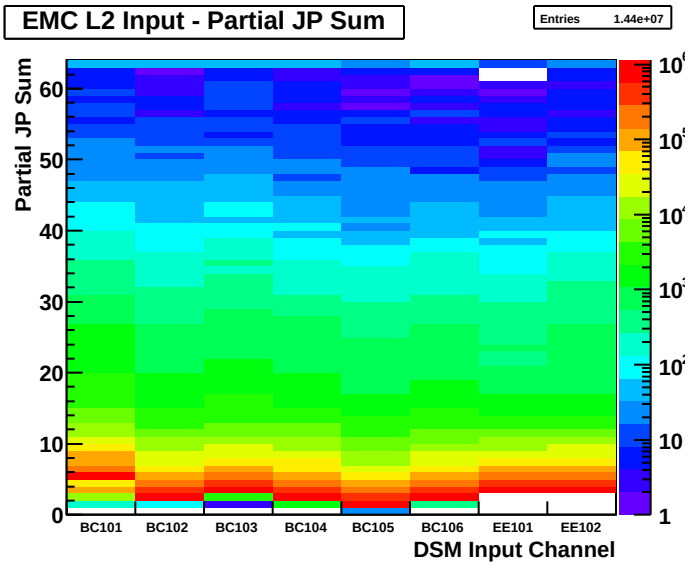

EMC L2 Input - JPX/JPA bits
Entries 2261
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

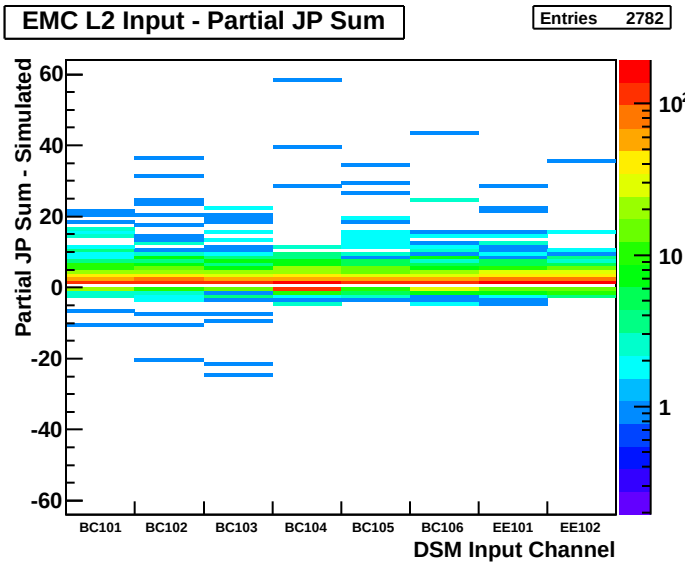

EMC L2 Input - JPY/JPB bits
Entries 1.44e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

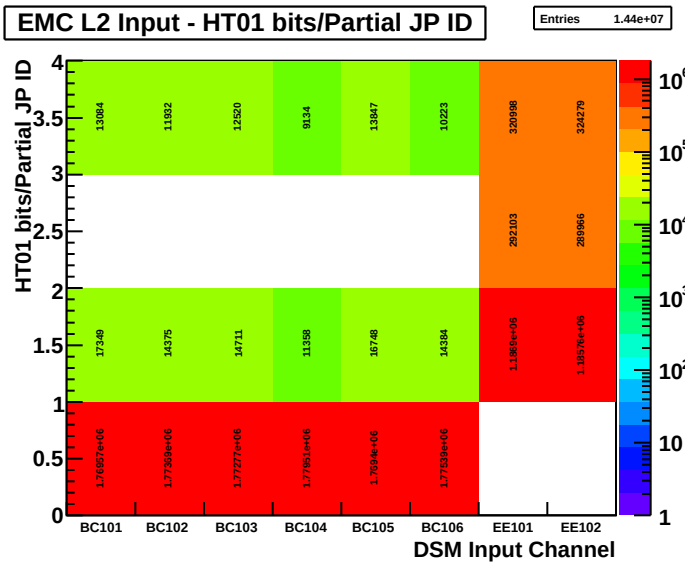

EMC L2 Input - JPY/JPB bits
Entries 2234
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

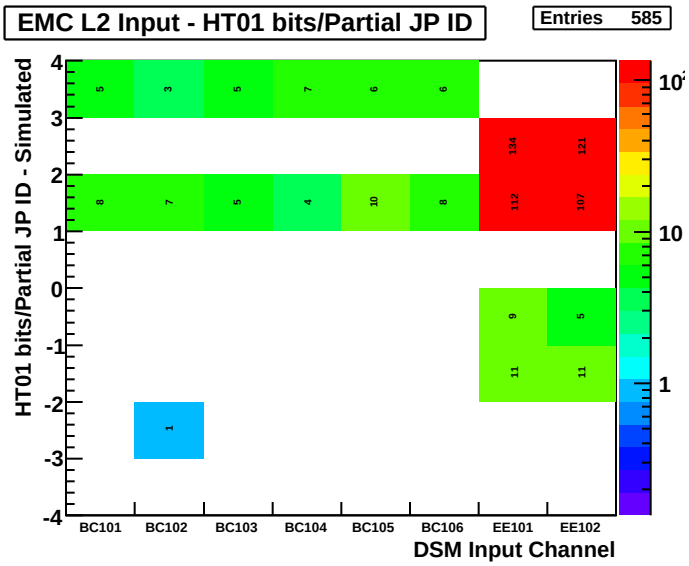

EMC L2 Input - JPZ/JPC bits
Entries 1.44e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

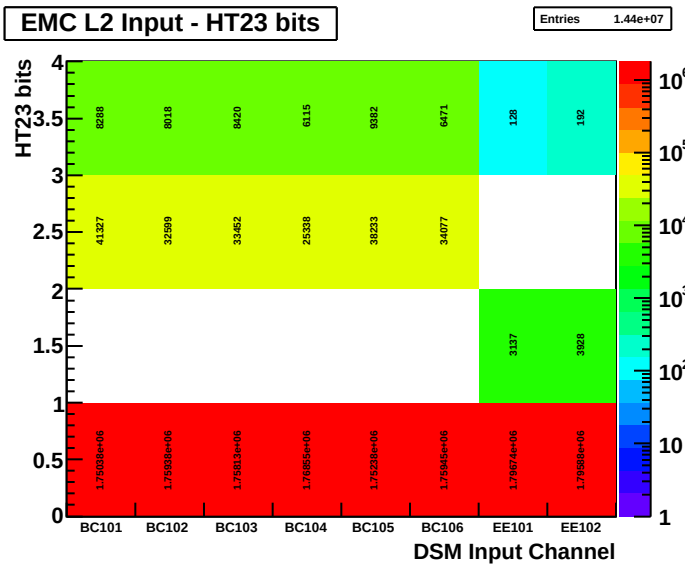

EMC L2 Input - JPZ/JPC bits
Entries 76
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

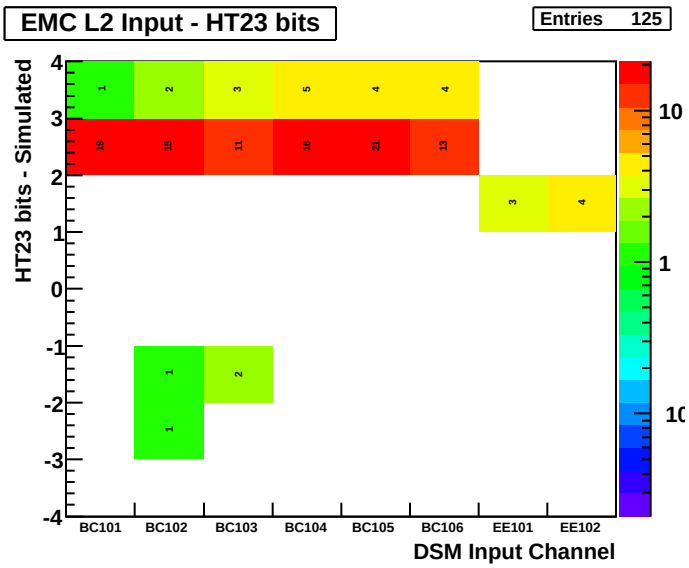

EMC L2 Input - Partial JP Sum
Entries 1.44e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

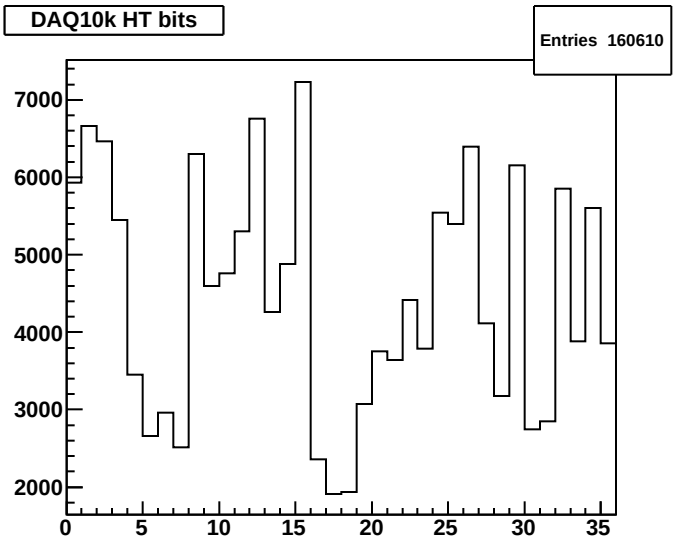

EMC L2 Input - Partial JP Sum
Entries 2782
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

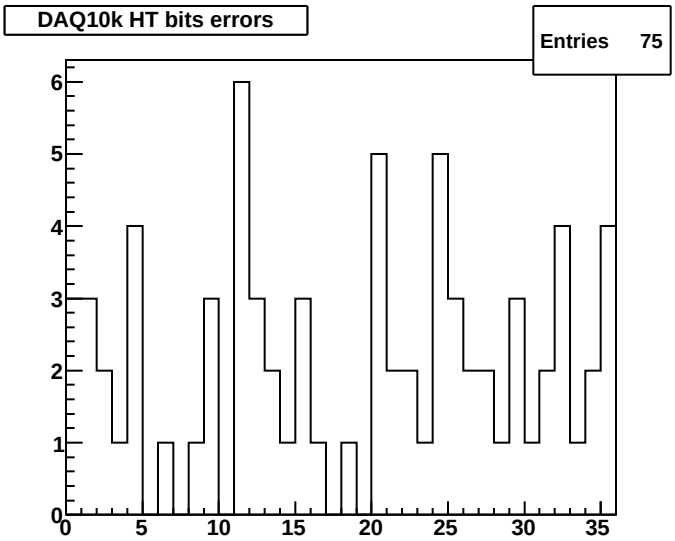

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.44e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

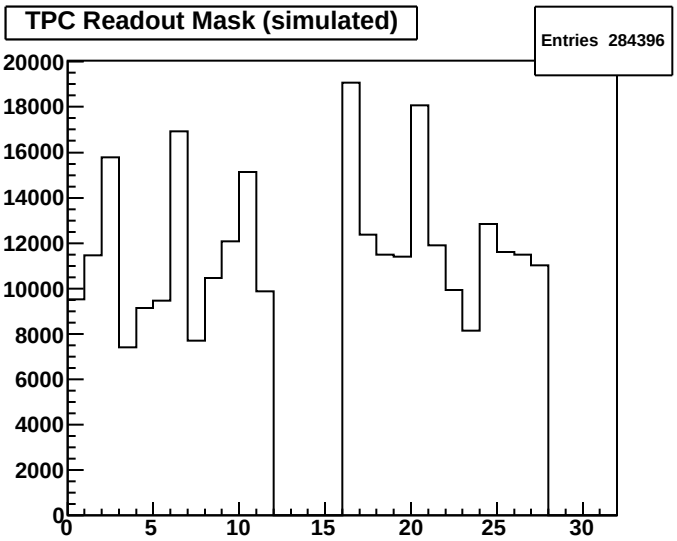

EMC L2 Input - HT01 bits/Partial JP ID
Entries 585
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

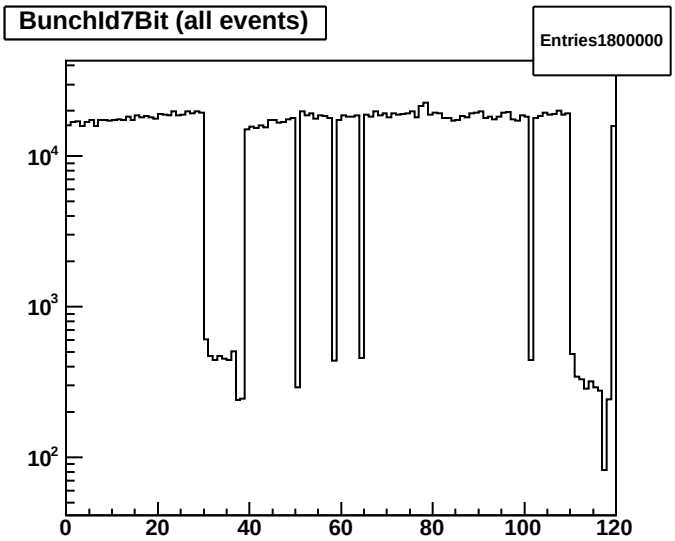

EMC L2 Input - HT23 bits
Entries 1.44e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

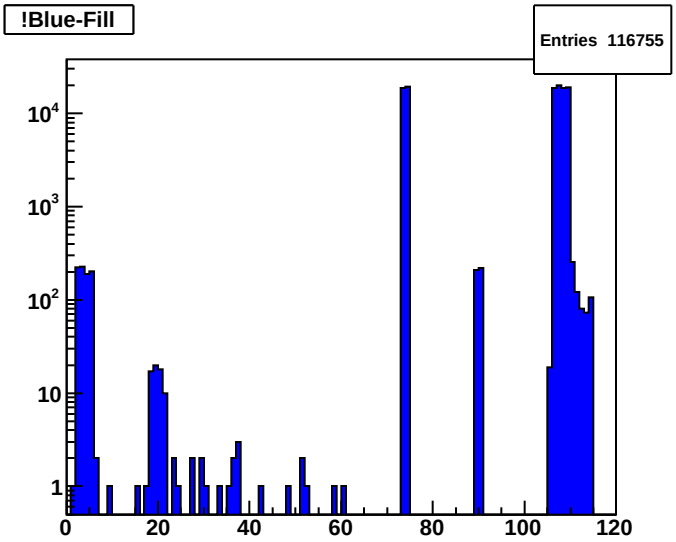

EMC L2 Input - HT23 bits
Entries 125
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

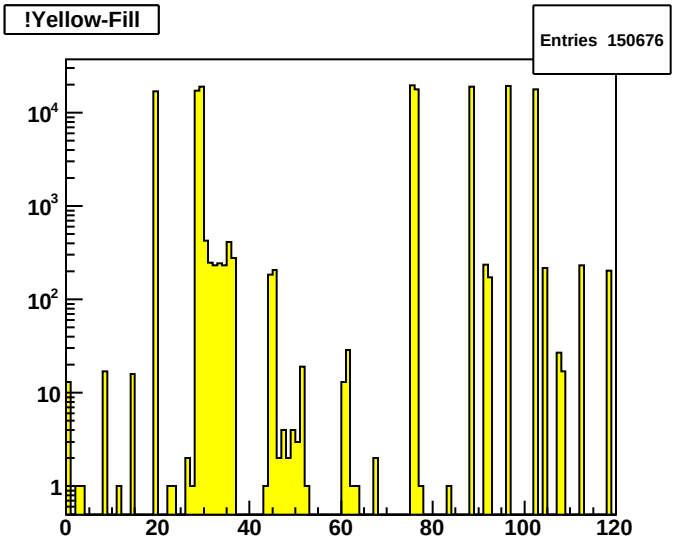

DAQ10k HT bits
Entries 160610

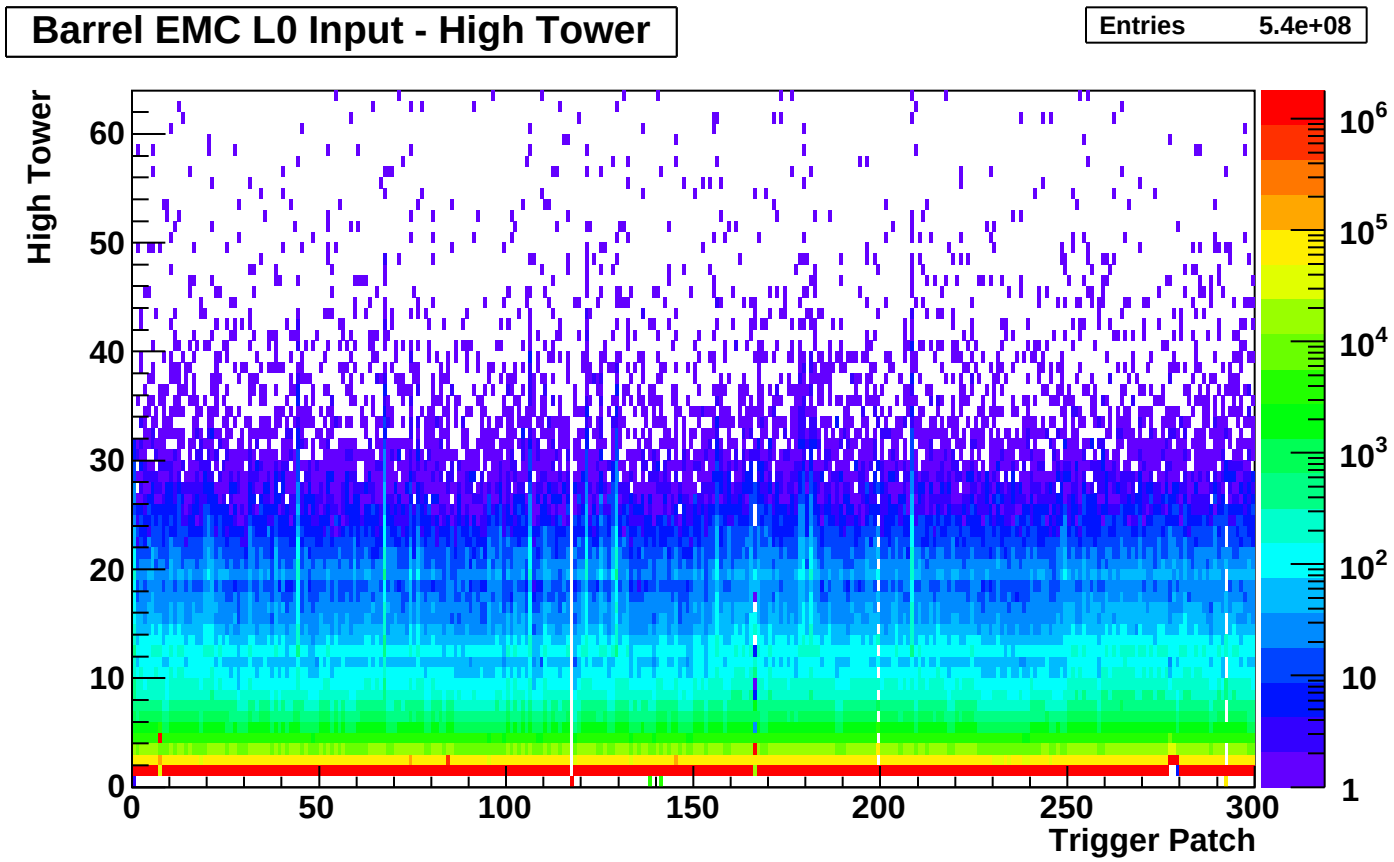

DAQ10k HT bits errors
Entries 75

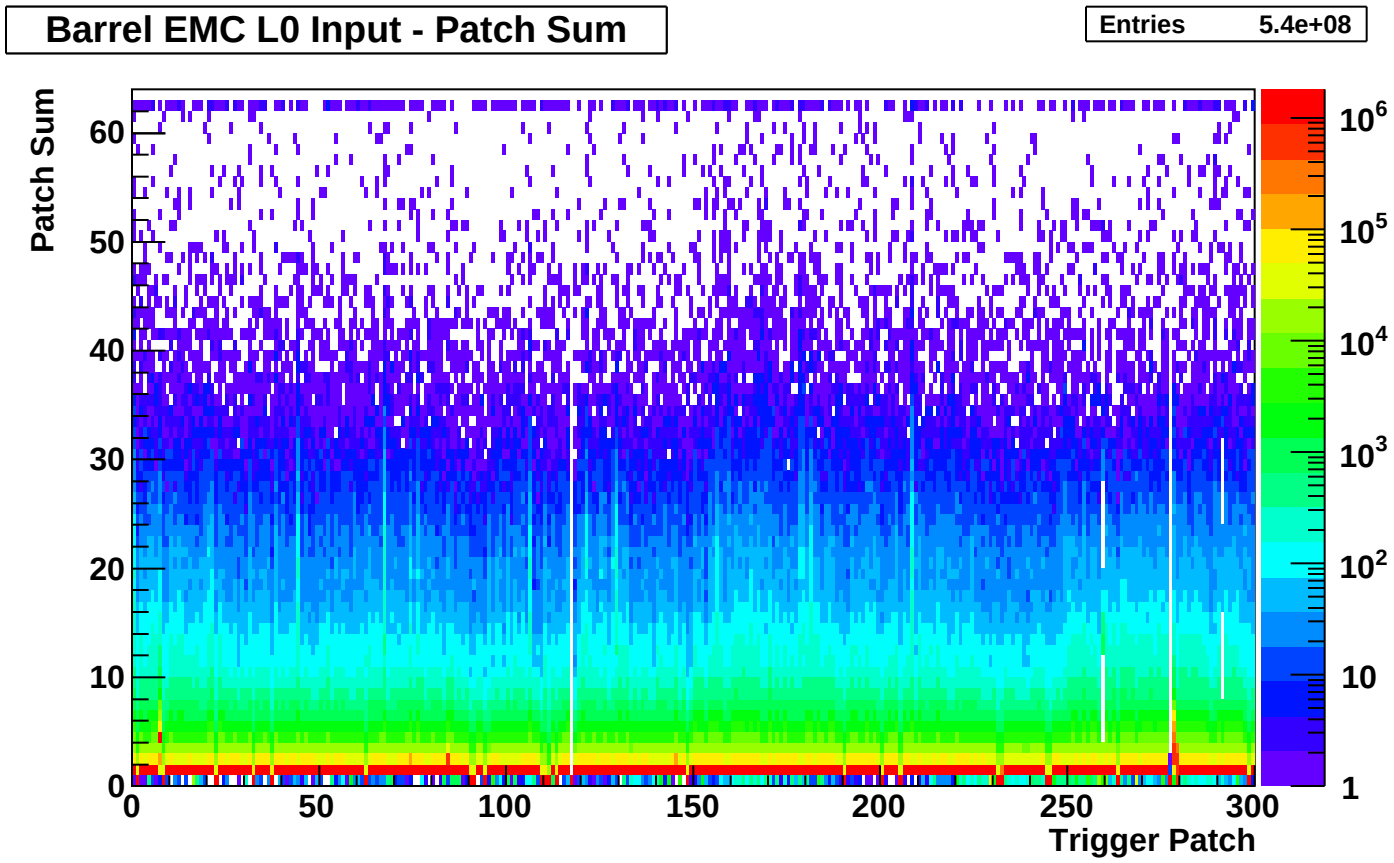

TPC Readout Mask (simulated)
Entries 284396

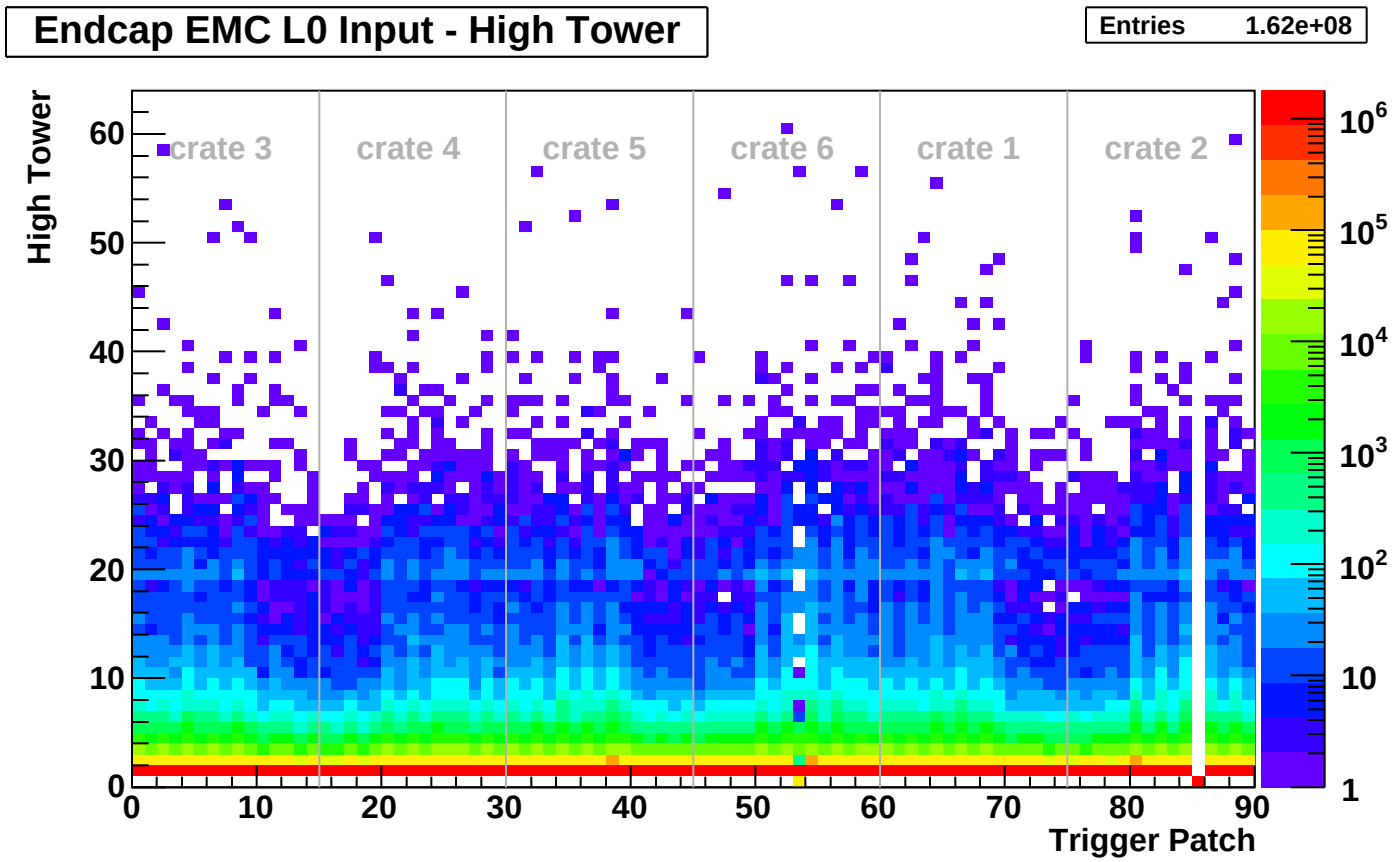

BunchId7Bit (all events)
Entries1800000

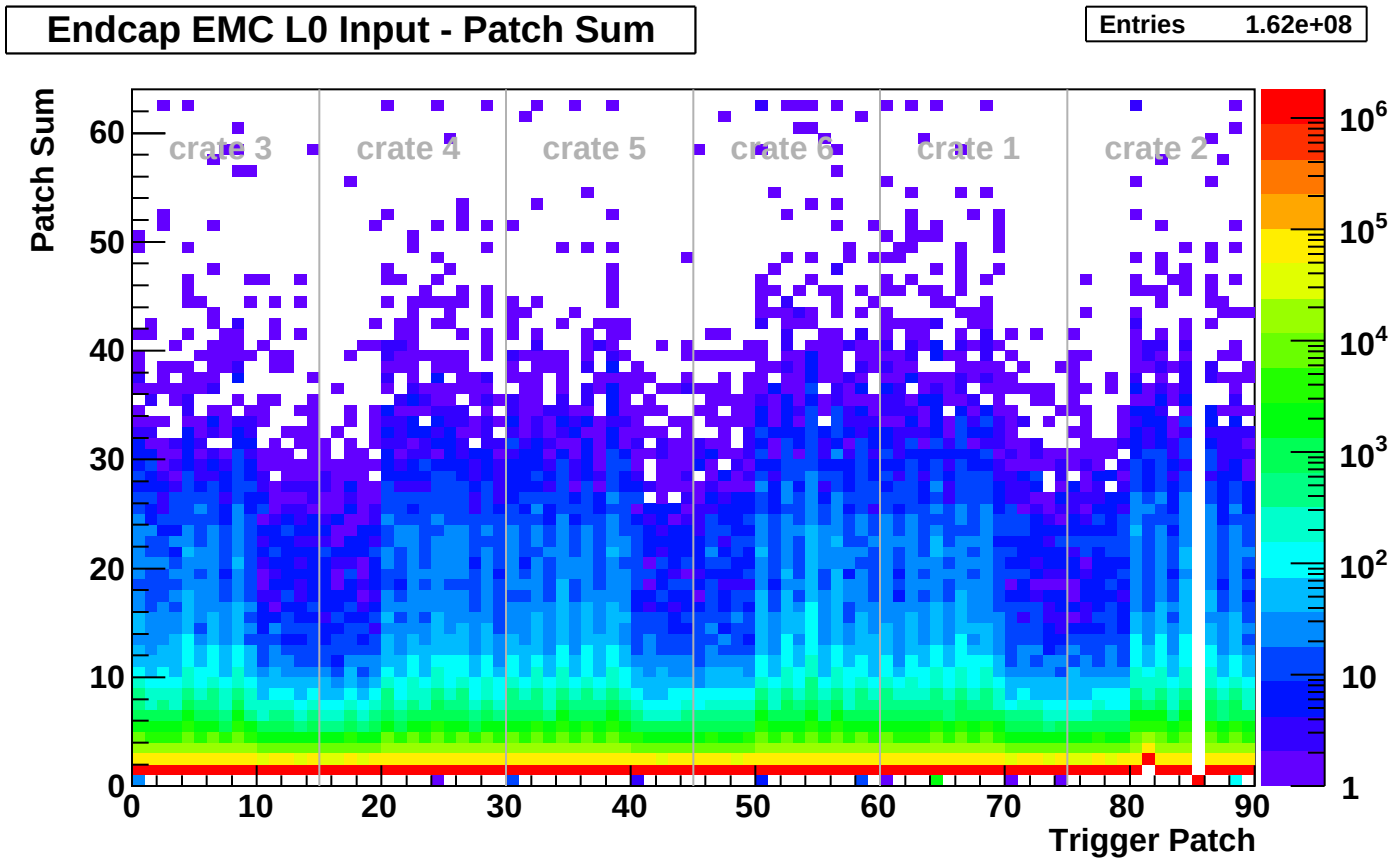

!Blue-Fill
Entries 116755

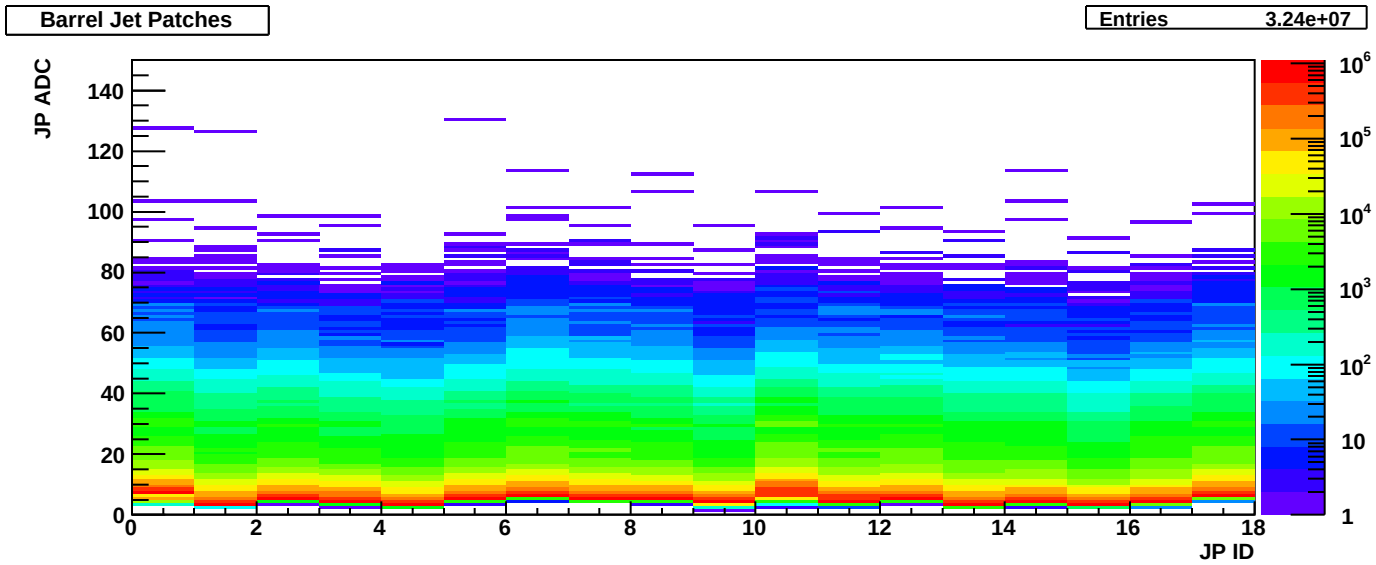

!Yellow-Fill
Entries 150676

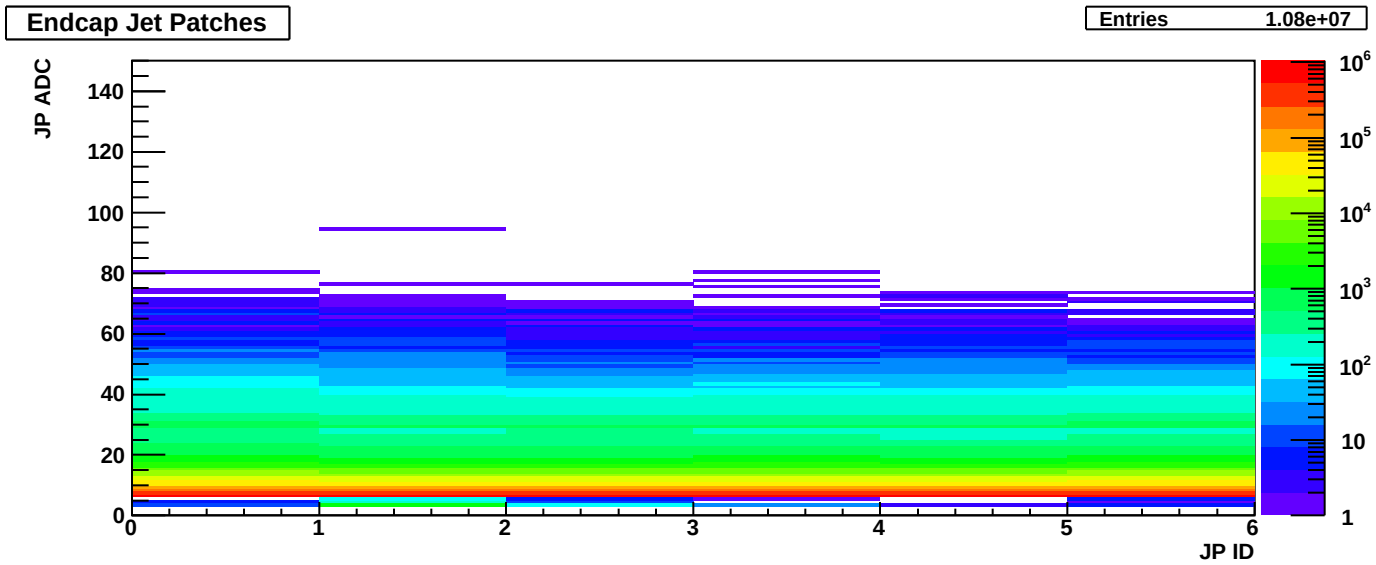

Barrel EMC L0 Input - High Tower
Entries 5.4e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

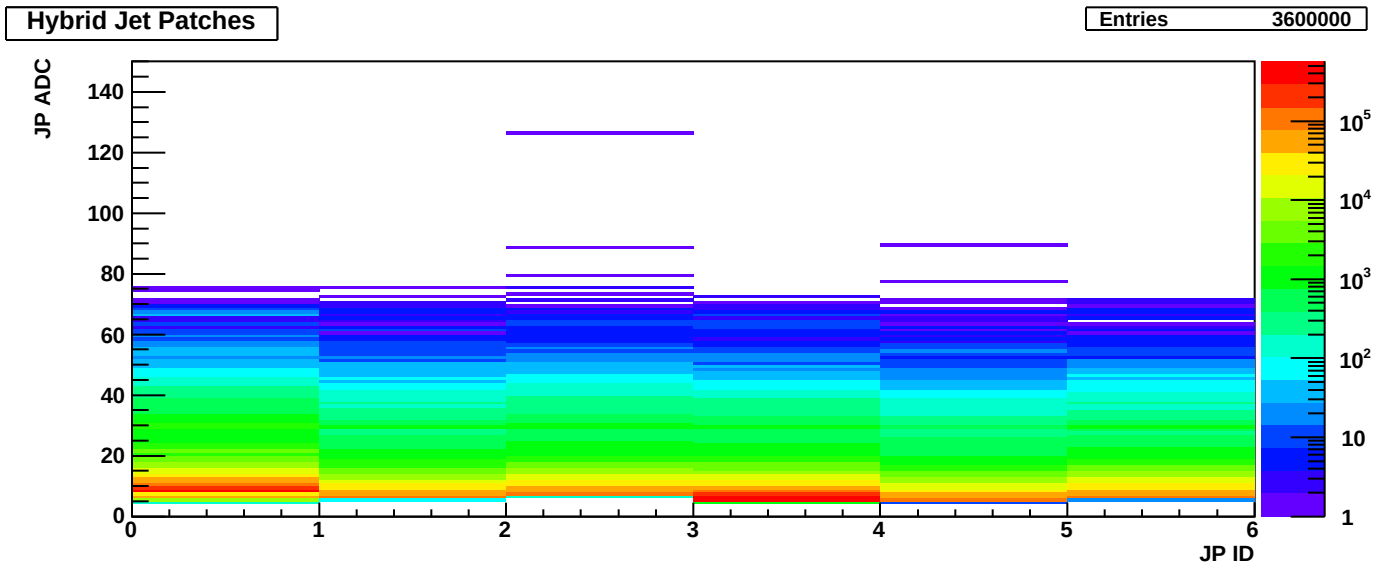

Barrel EMC L0 Input - Patch Sum
Entries 5.4e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

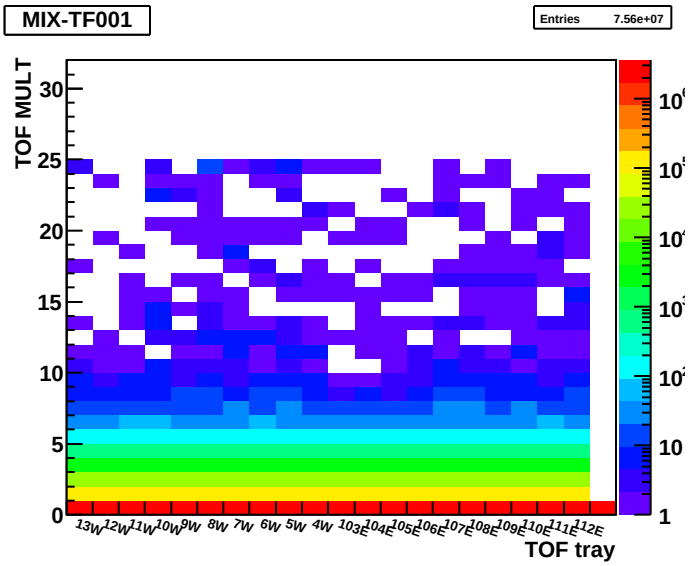

Endcap EMC L0 Input - High Tower
Entries 1.62e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

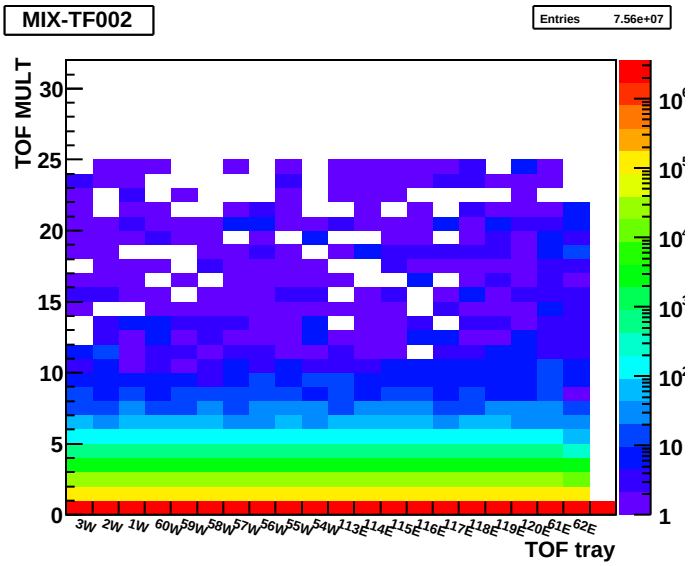

Endcap EMC L0 Input - Patch Sum
Entries 1.62e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

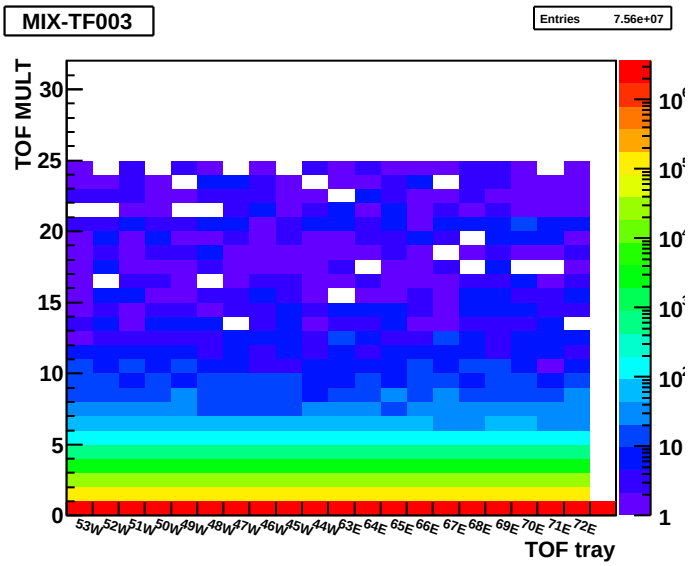

Barrel Jet Patches
Entries 3.24e+07
JP ADC
JP ID

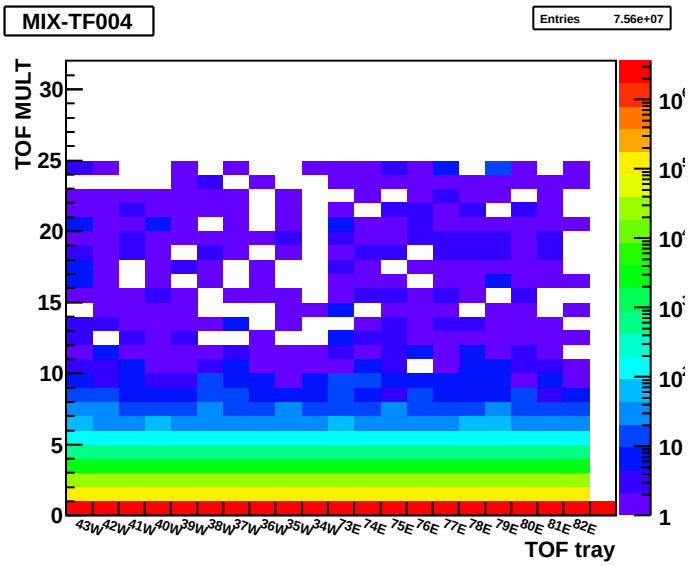

Endcap Jet Patches
Entries 1.08e+07
JP ADC
JP ID

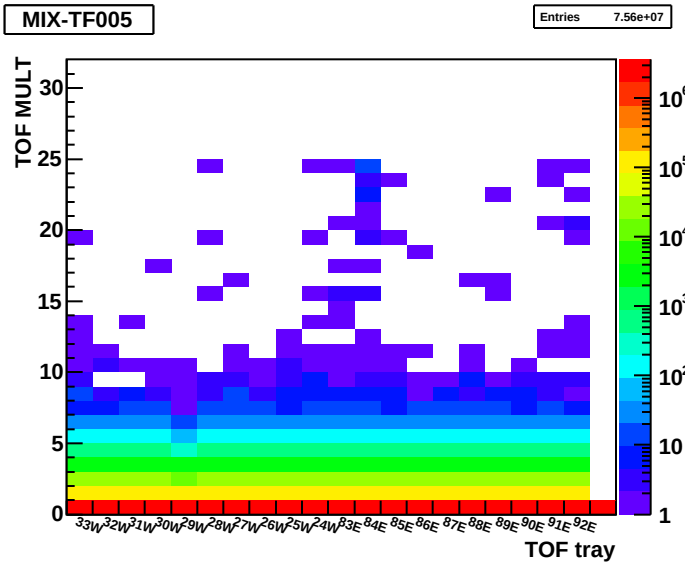

Hybrid Jet Patches
Entries 3600000
JP ADC
JP ID

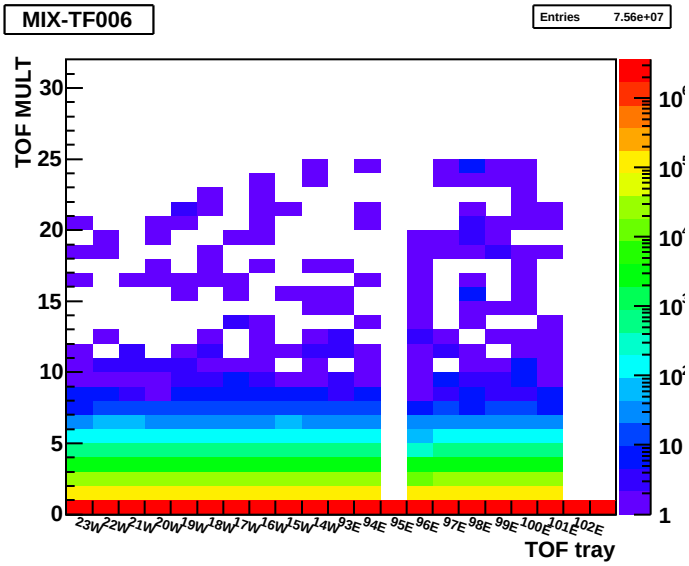

MIX-TF001
Entries 7.56e+07
TOF MULT
TOF tray

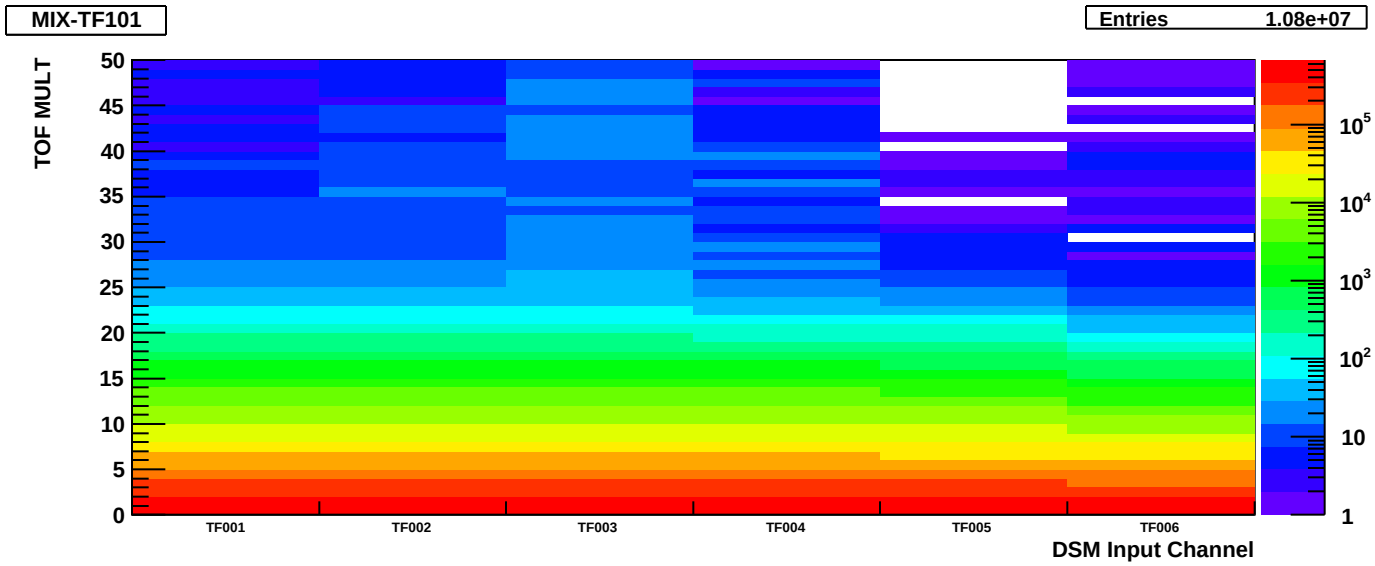

MIX-TF002
Entries 7.56e+07
TOF MULT
TOF tray

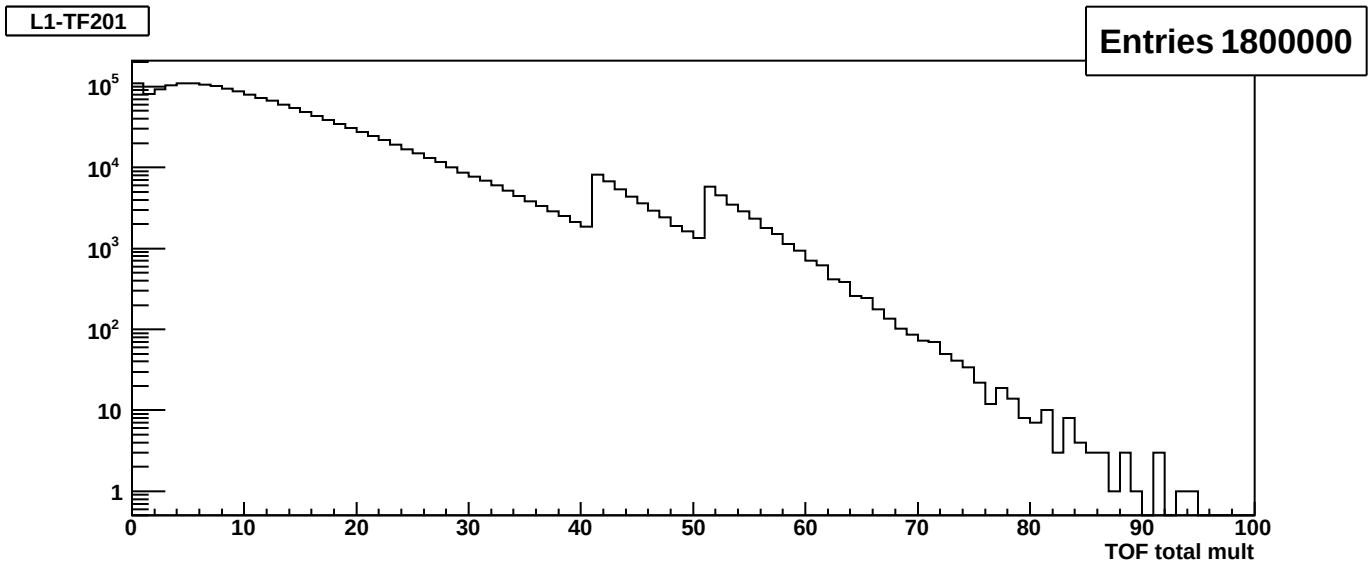

MIX-TF003
Entries 7.56e+07
TOF MULT
TOF tray

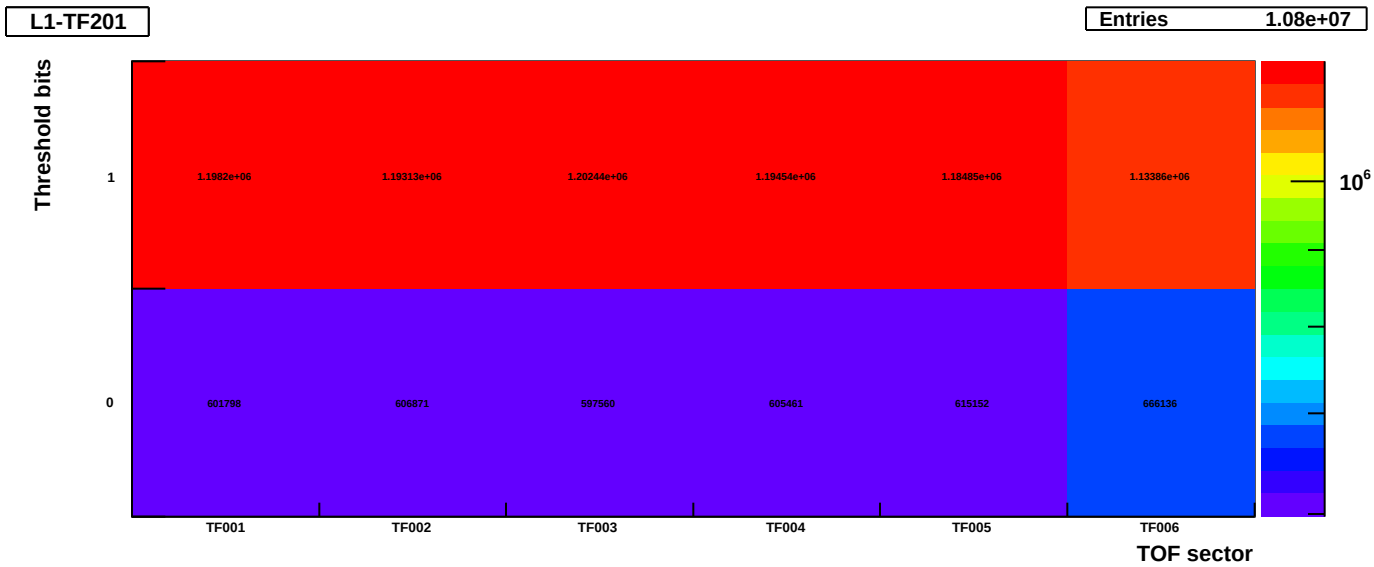

MIX-TF004
Entries 7.56e+07
TOF MULT
TOF tray

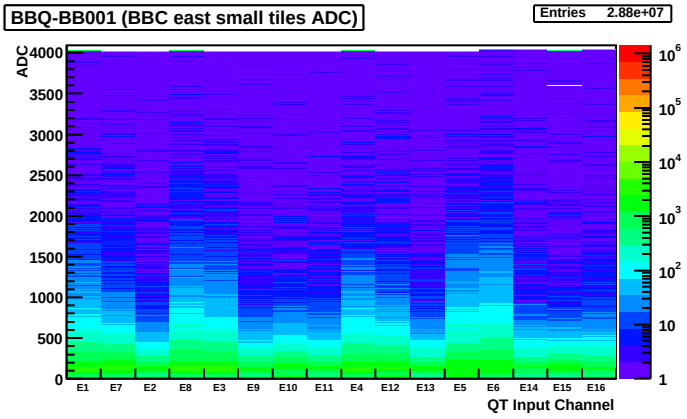

MIX-TF005
Entries 7.56e+07
TOF MULT
TOF tray

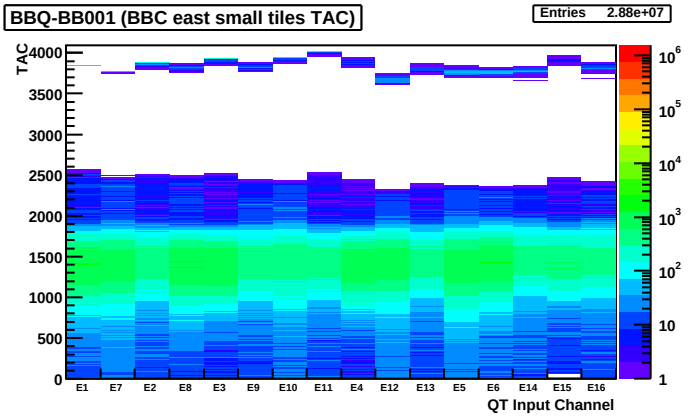

MIX-TF006
Entries 7.56e+07
TOF MULT
TOF tray

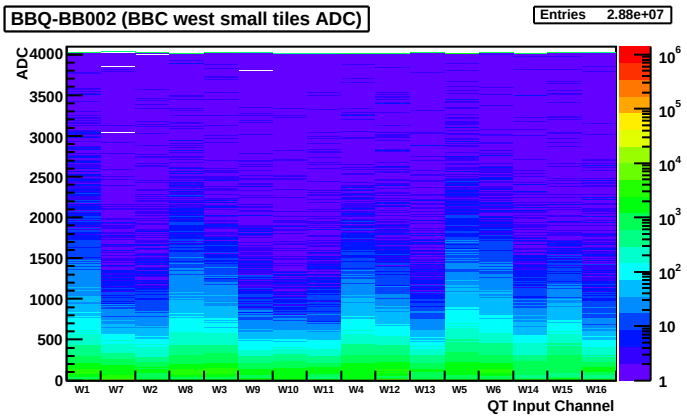

MIX-TF101
Entries 1.08e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

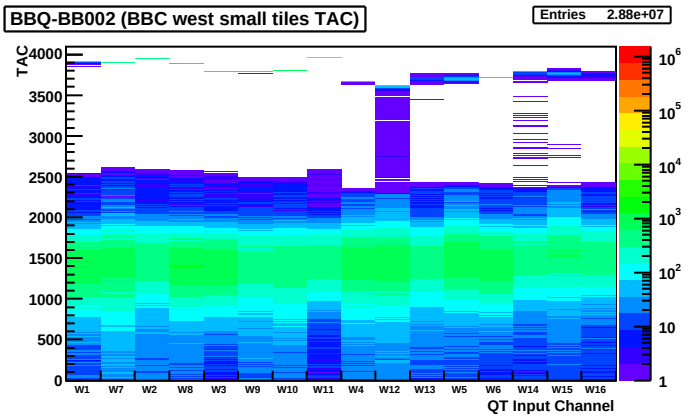

L1-TF201
Entries 1800000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

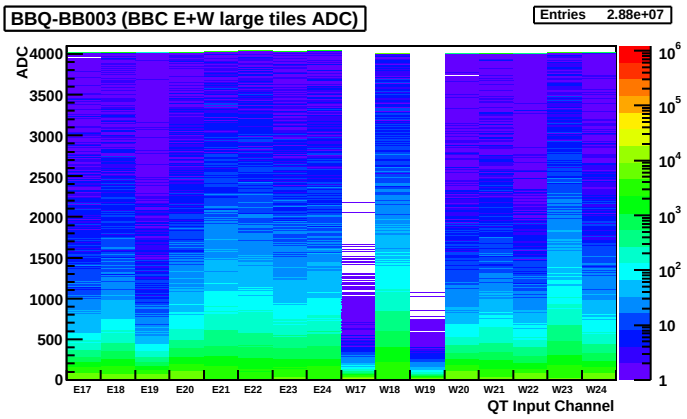

L1-TF201
Entries 1.08e+07
Threshold bits
1
0
1.1982e+06
1.19313e+06
1.20244e+06
1.19454e+06
1.18485e+06
1.13386e+06
601798
606871
597560
605461
615152
666136
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

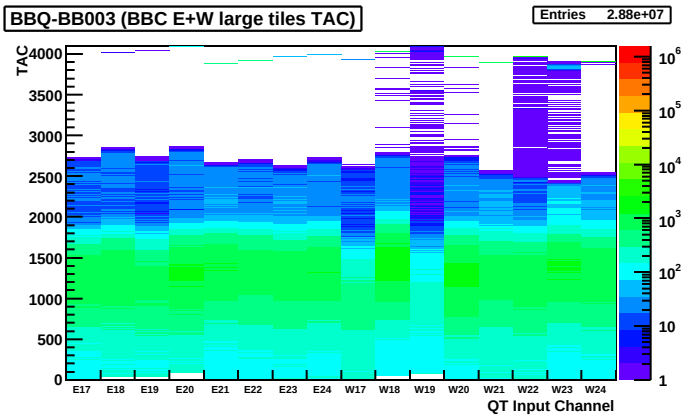

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

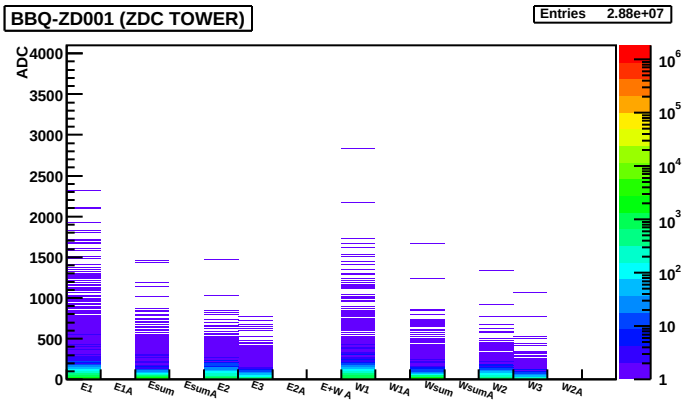

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

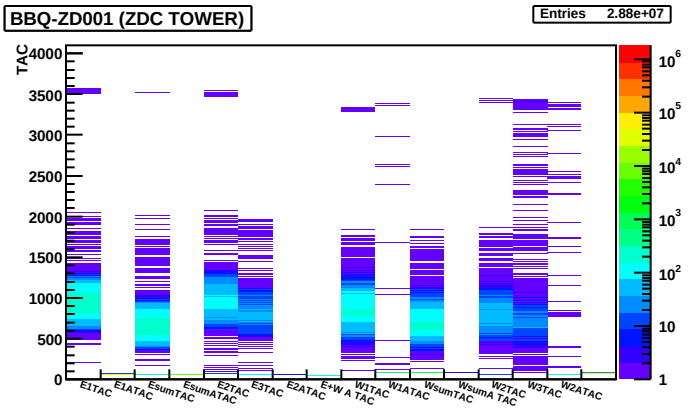

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

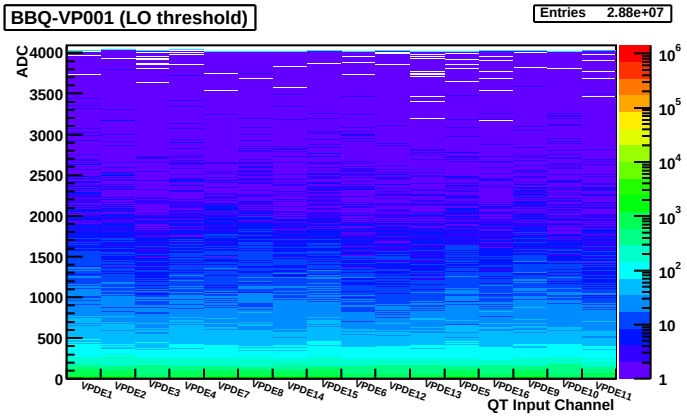

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

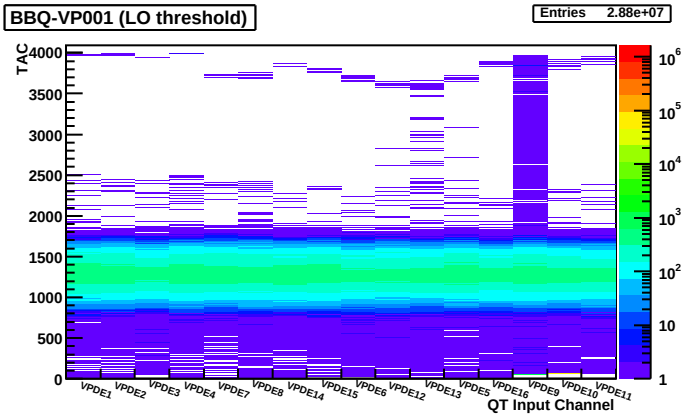

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

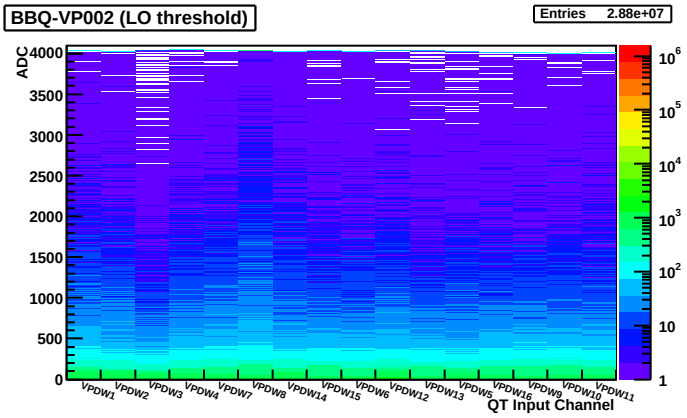

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

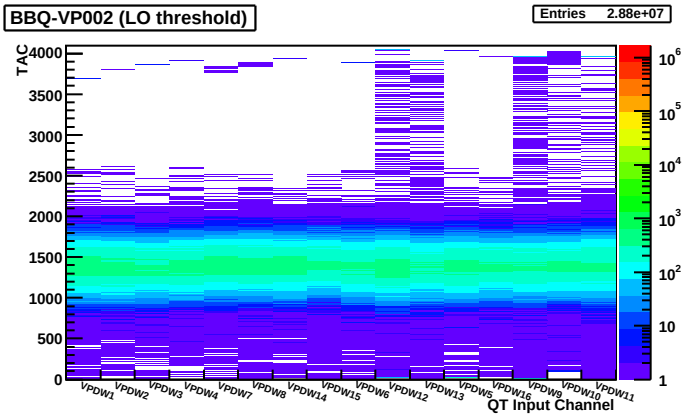

BBQ-ZD001 (ZDC TOWER)
Entries 2.88e+07
ADC

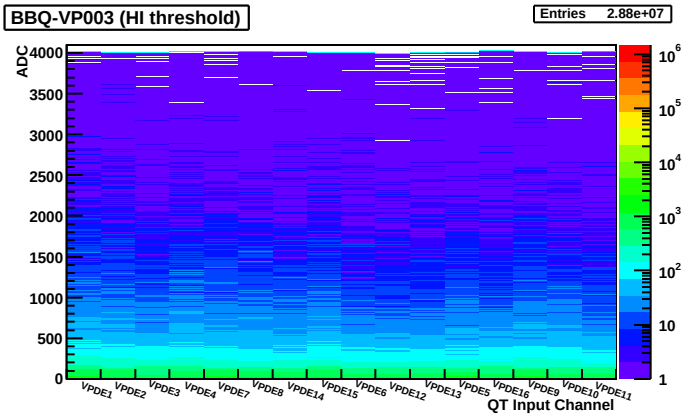

BBQ-ZD001 (ZDC TOWER)
Entries 2.88e+07
TAC

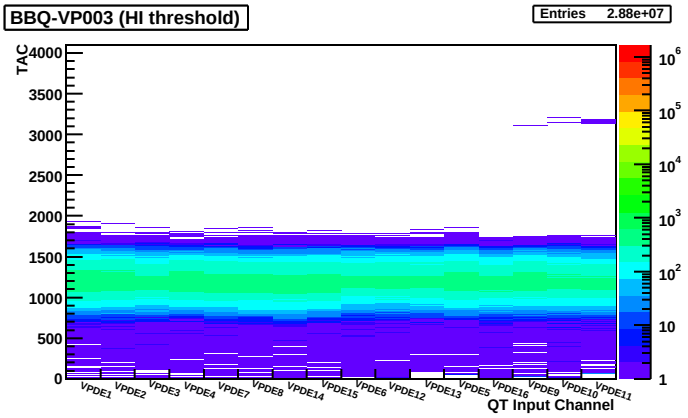

BBQ-VP001 (LO threshold)
Entries 2.88e+07
ADC
QT Input Channel

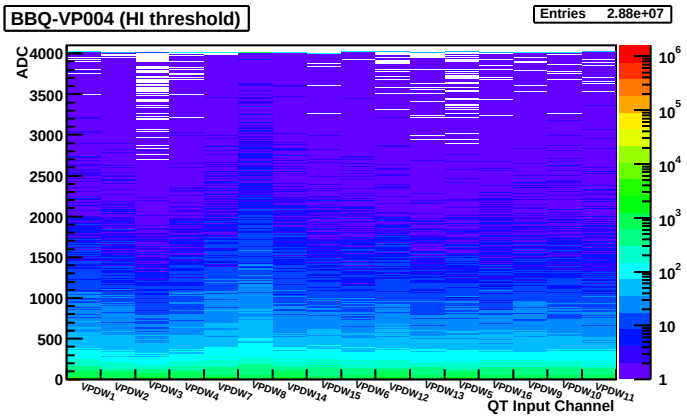

BBQ-VP001 (LO threshold)
Entries 2.88e+07
TAC
QT Input Channel

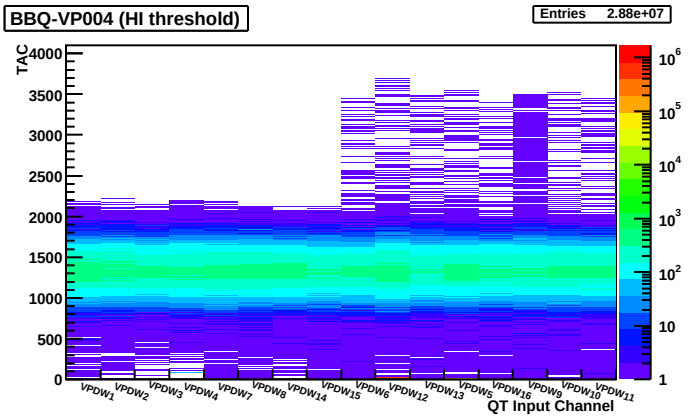

BBQ-VP002 (LO threshold)
Entries 2.88e+07
ADC
QT Input Channel

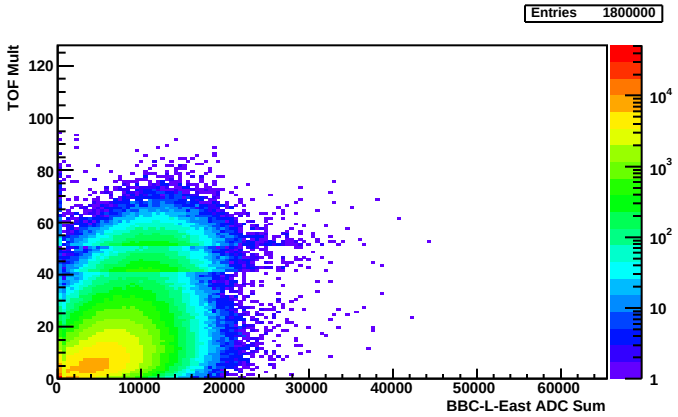

BBQ-VP002 (LO threshold)
Entries 2.88e+07
TAC
QT Input Channel

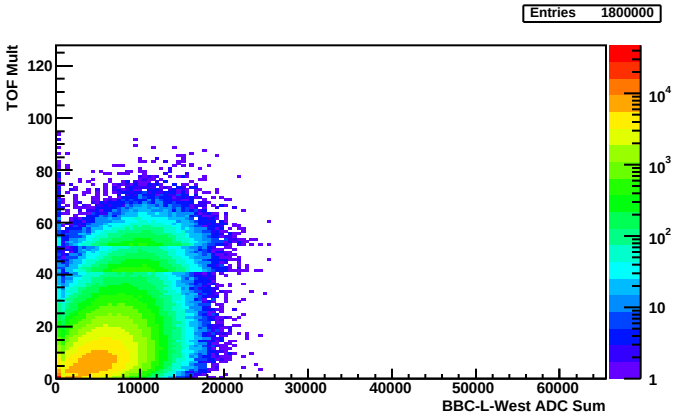

BBQ-VP003 (HI threshold)
Entries 2.88e+07
ADC
QT Input Channel

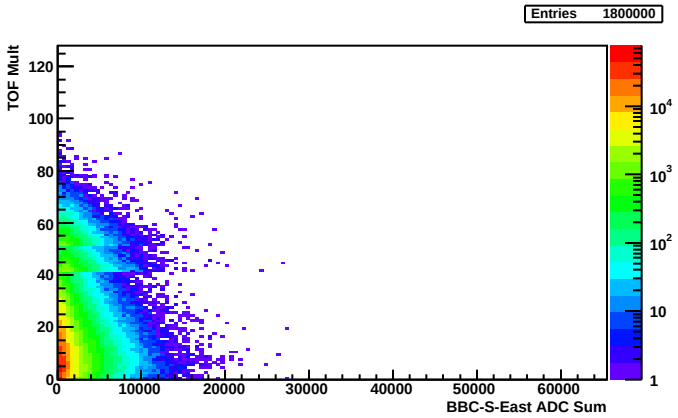

BBQ-VP003 (HI threshold)
Entries 2.88e+07
TAC
QT Input Channel

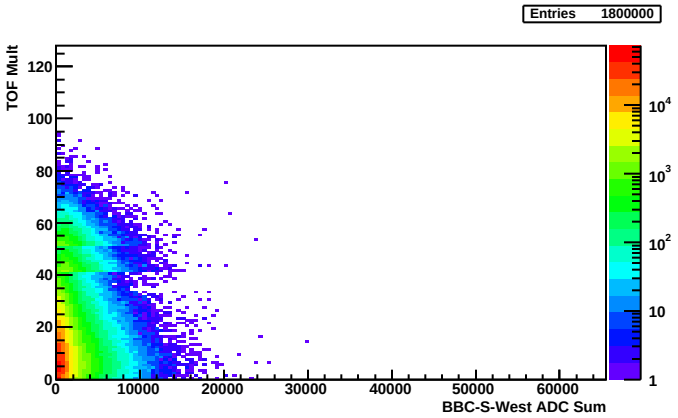

BBQ-VP004 (HI threshold)
Entries 2.88e+07
ADC
QT Input Channel

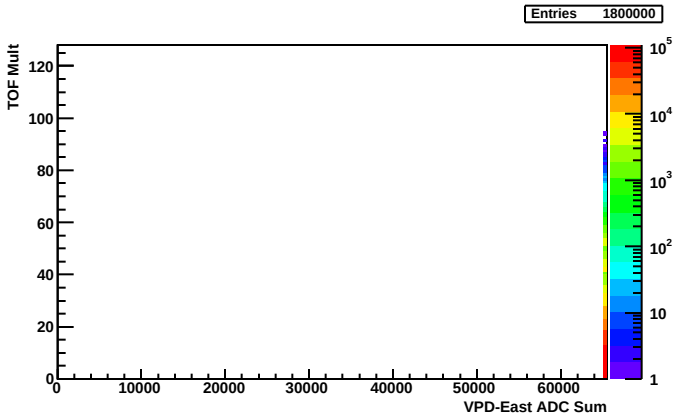

BBQ-VP004 (HI threshold)
Entries 2.88e+07
TAC
QT Input Channel

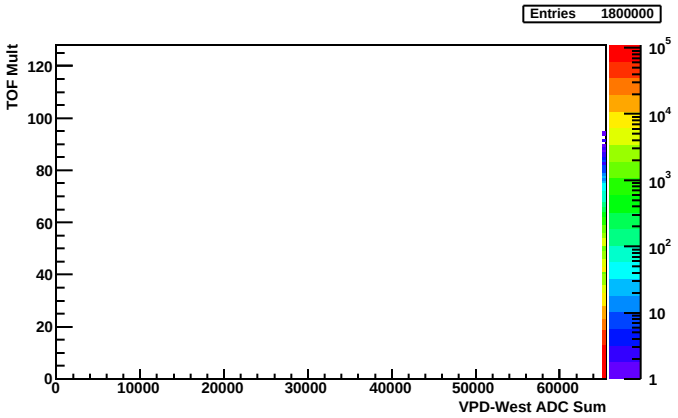

Entries 1800000
TOF Mult
BBC-L-East ADC Sum

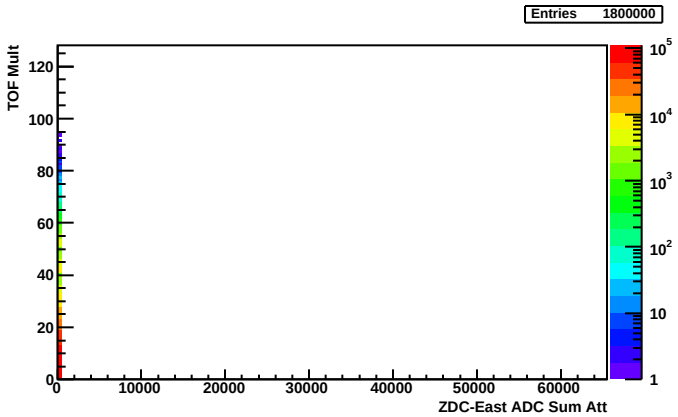

Entries 1800000
TOF Mult
BBC-L-West ADC Sum

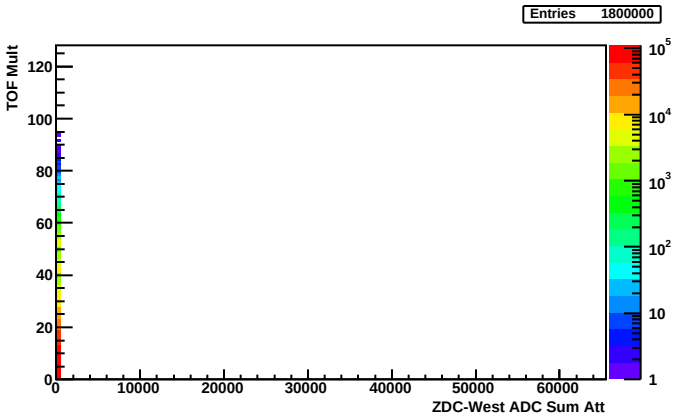

Entries 1800000
TOF Mult
BBC-S-East ADC Sum

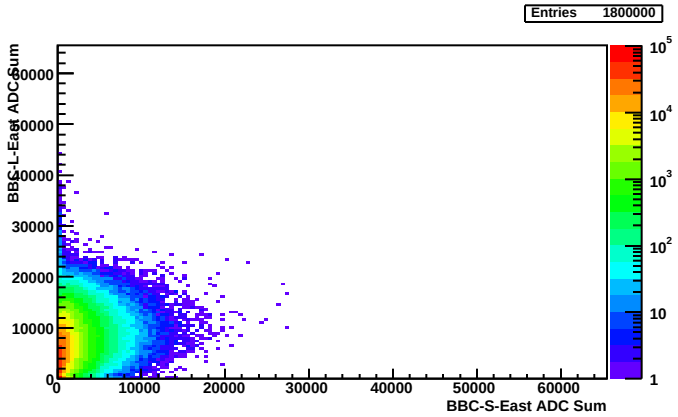

Entries 1800000
TOF Mult
BBC-S-West ADC Sum

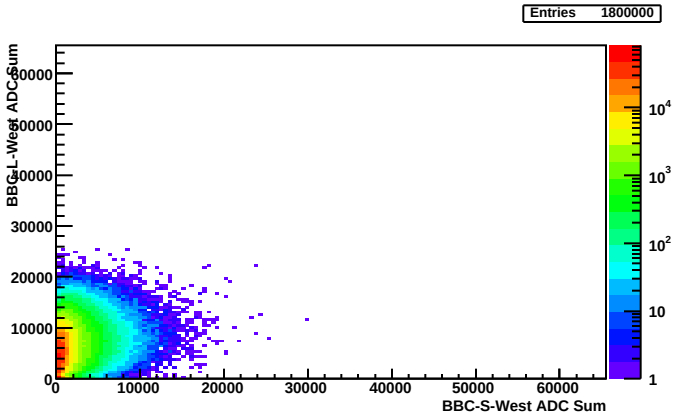

Entries 1800000
TOF Mult
VPD-East ADC Sum

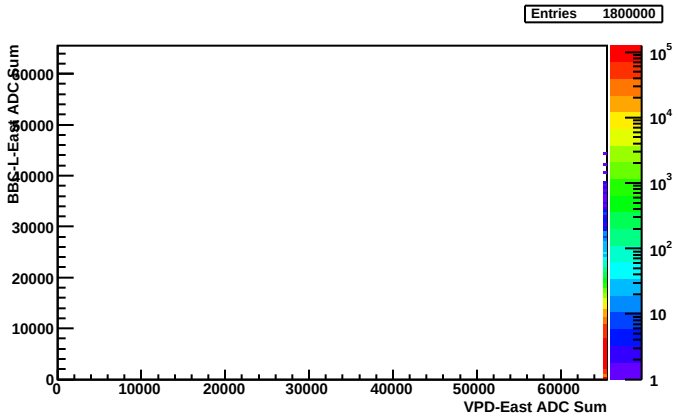

Entries 1800000
TOF Mult
VPD-West ADC Sum

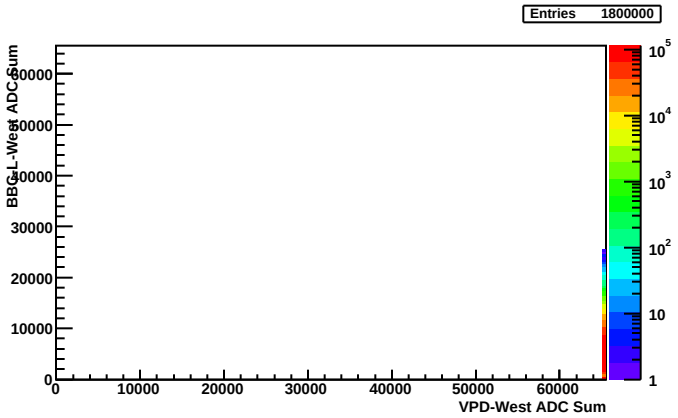

Entries 1800000
TOF Mult
ZDC-East ADC Sum Att

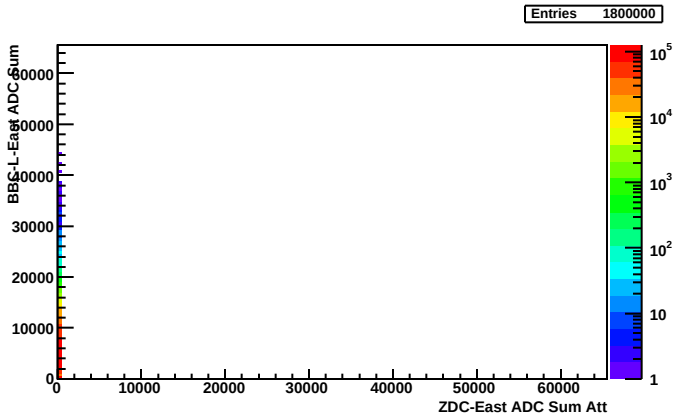

Entries 1800000
TOF Mult
ZDC-West ADC Sum Att

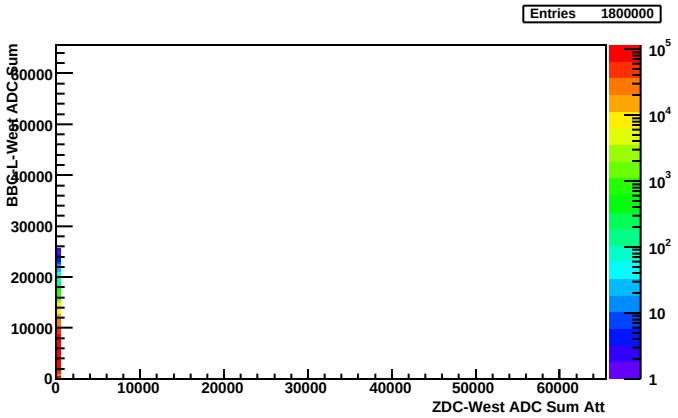

Entries 1800000
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1800000
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1800000
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1800000
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 1800000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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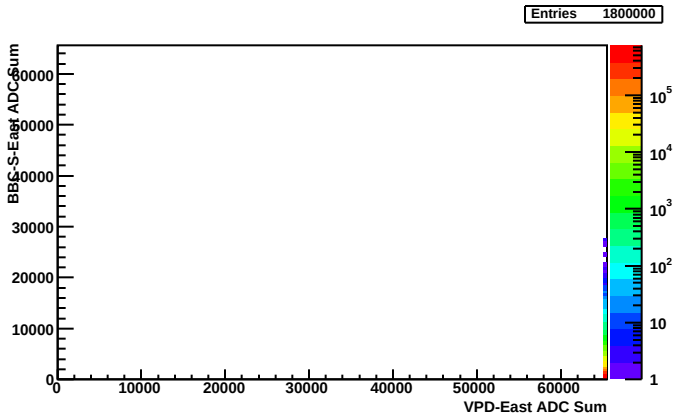
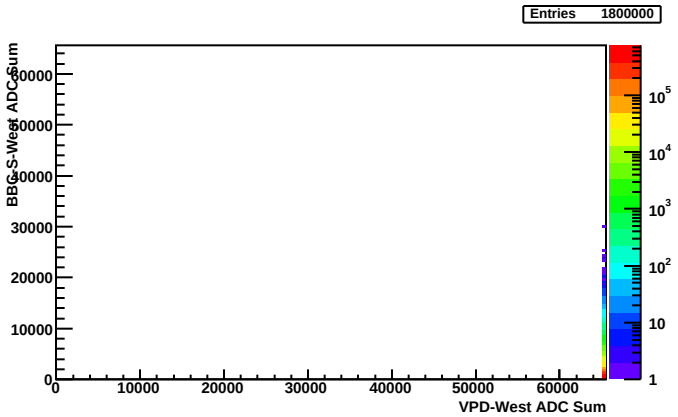
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 1800000
BBC-S-East ADC Sum
VPD-East ADC Sum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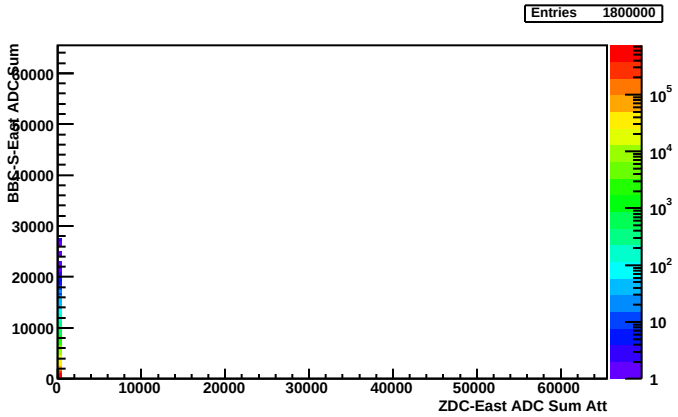
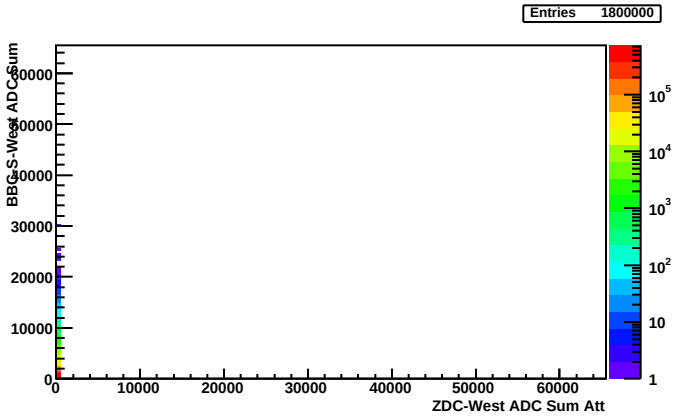
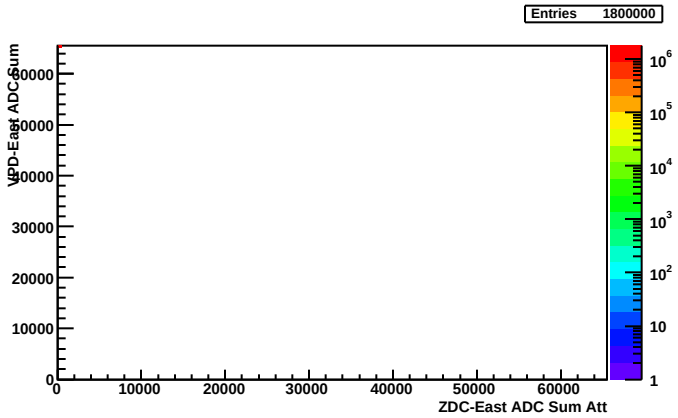
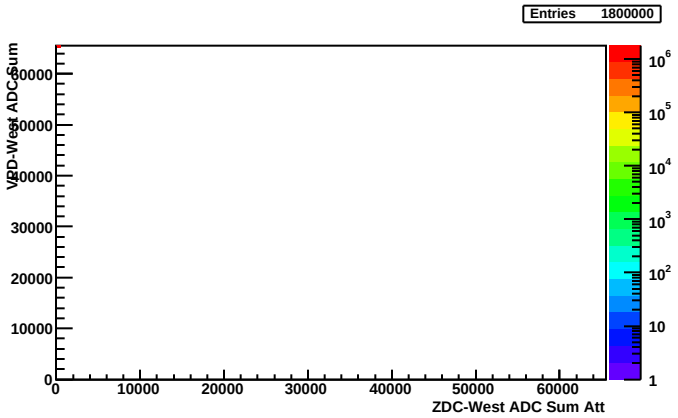
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1800000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1800000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1800000
VPD-East ADC Sum
ZDC-East ADC Sum Att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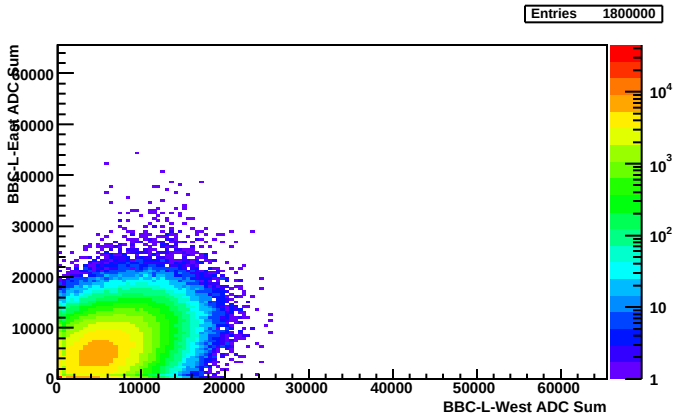
VPD-West ADC Sum
ZDC-West ADC Sum Att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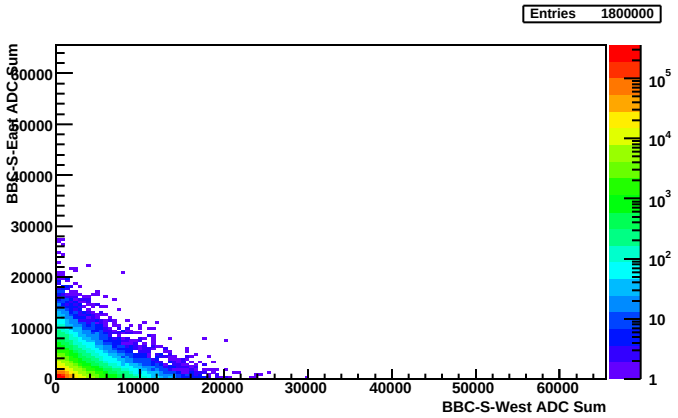
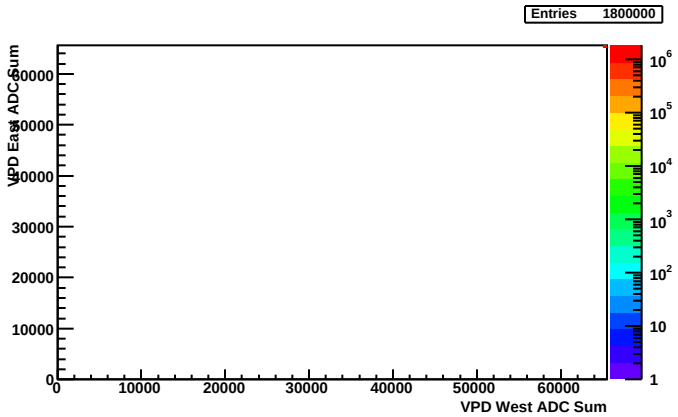
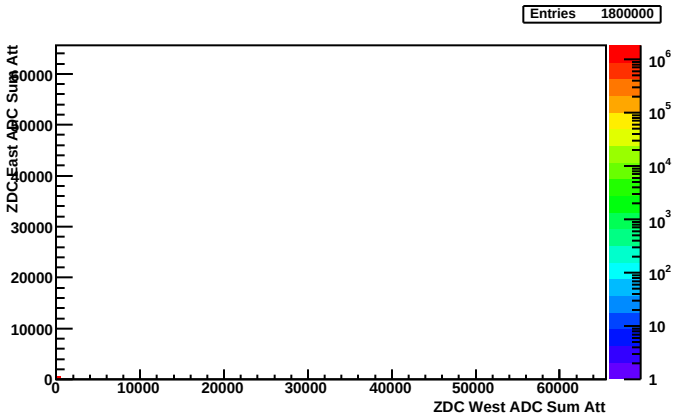
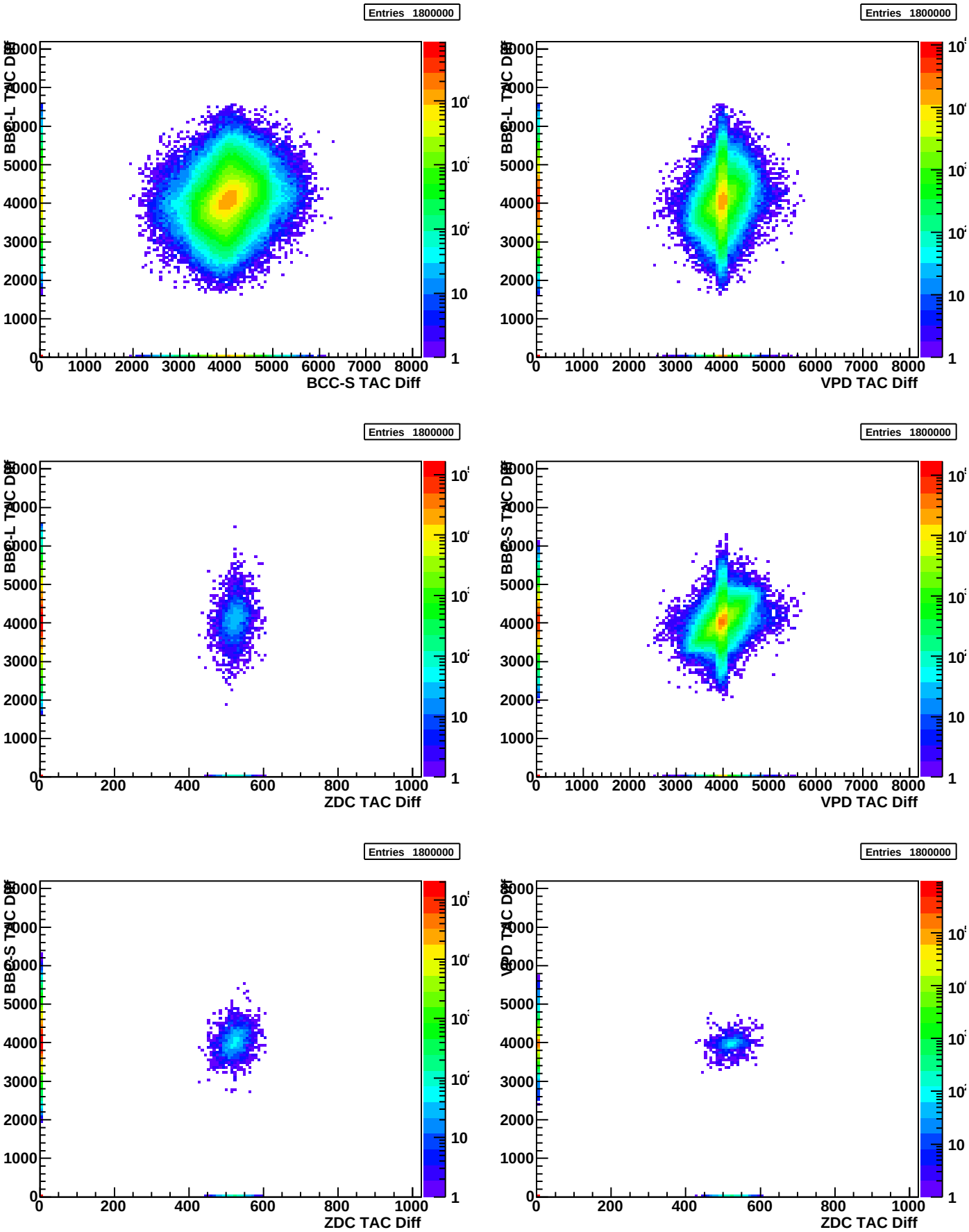
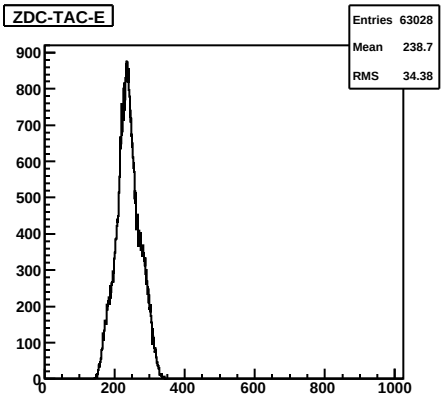
BBC-L-East ADC Sum
BBC-L-West ADC Sum

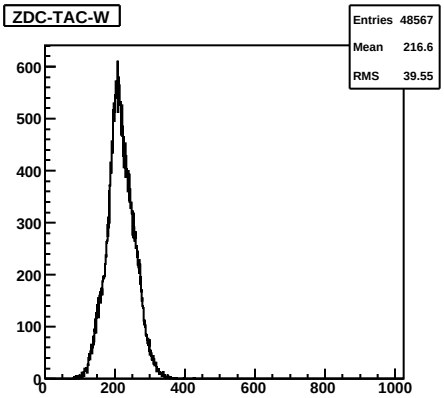

Entries 1800000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

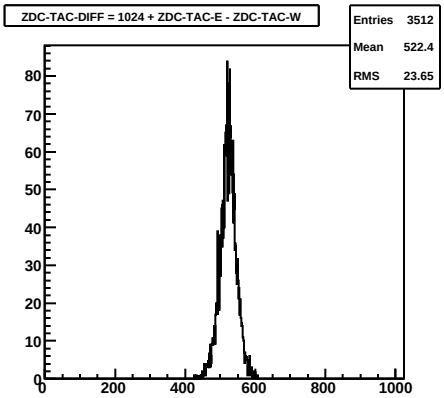

Entries 1800000
VPD East ADC Sum
VPD West ADC Sum

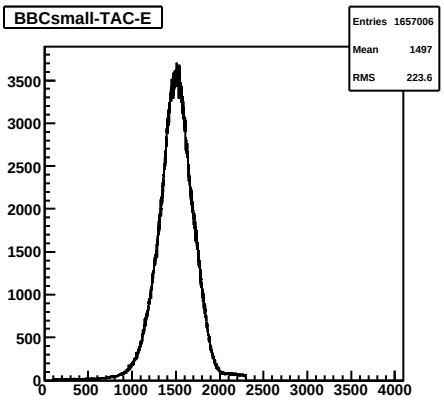

Entries 1800000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

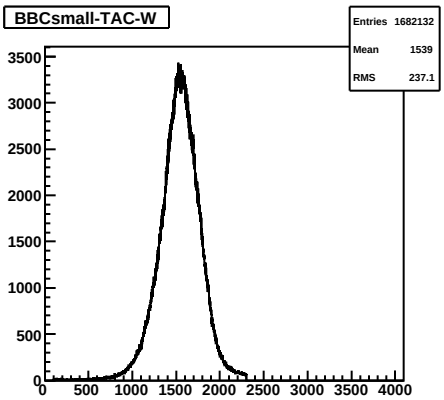

Entries 1800000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1800000
BBC-L TAC Diff
VPD TAC Diff
Entries 1800000
BBC-L TAC Diff
ZDC TAC Diff
Entries 1800000
BBC-S TAC Diff
VPD TAC Diff
Entries 1800000
BBC-S TAC Diff
ZDC TAC Diff
Entries 1800000
VPD TAC Diff
ZDC TAC Diff

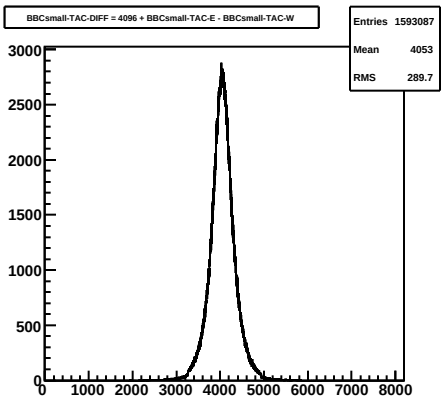

ZDC-TAC-E
Entries 63028
Mean 238.7
RMS 34.38

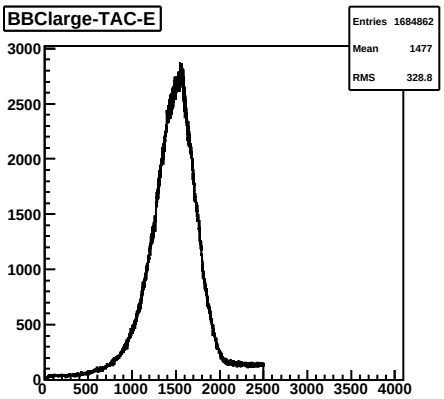

ZDC-TAC-W
Entries 48567
Mean 216.6
RMS 39.55

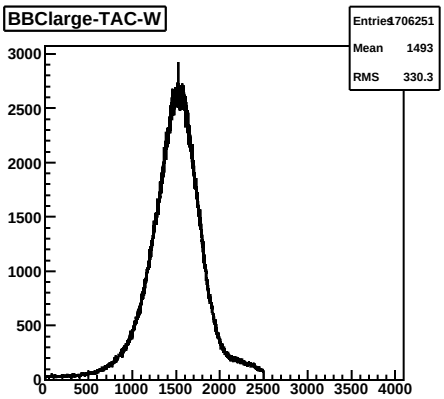

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3512
Mean 522.4
RMS 23.65

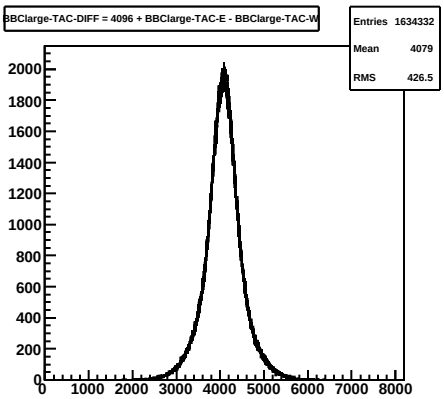

BBCsmall-TAC-E
Entries 1657006
Mean 1497
RMS 223.6

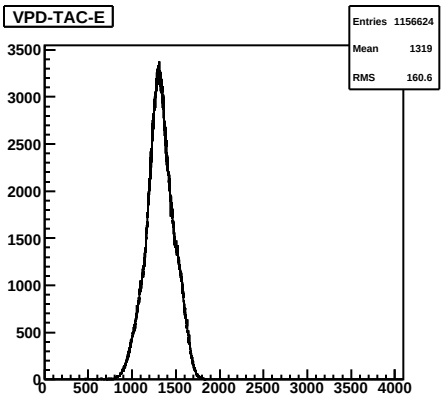

BBCsmall-TAC-W
Entries 1682132
Mean 1539
RMS 237.1

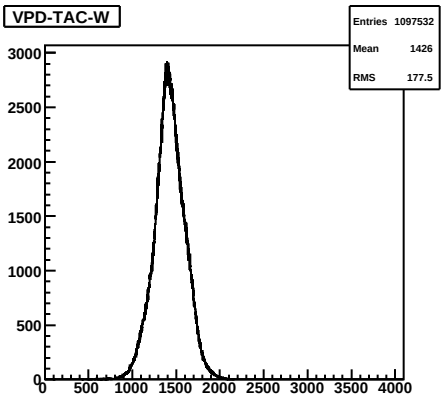

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1593087
Mean 4053
RMS 289.7

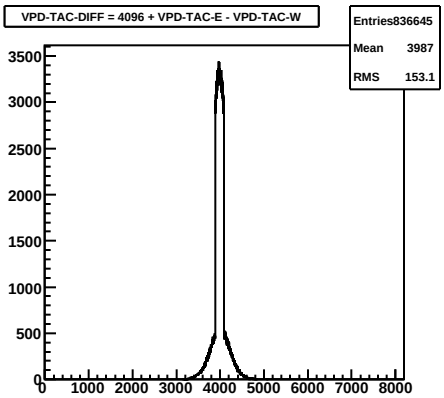

BBClarge-TAC-E
Entries 1684862
Mean 1477
RMS 328.8

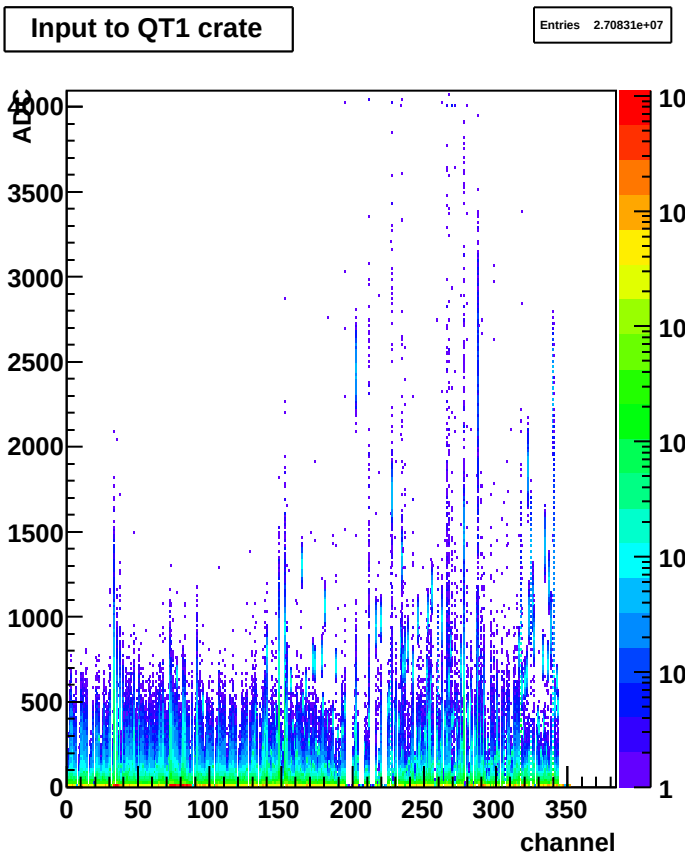

BBClarge-TAC-W
Entries 1706251
Mean 1493
RMS 330.3

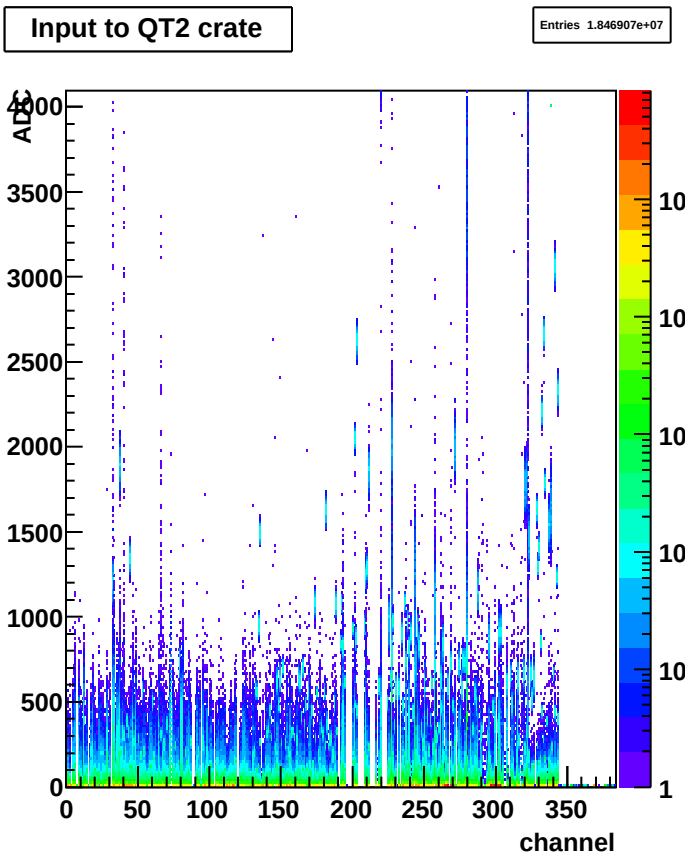

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1634332
Mean 4079
RMS 426.5

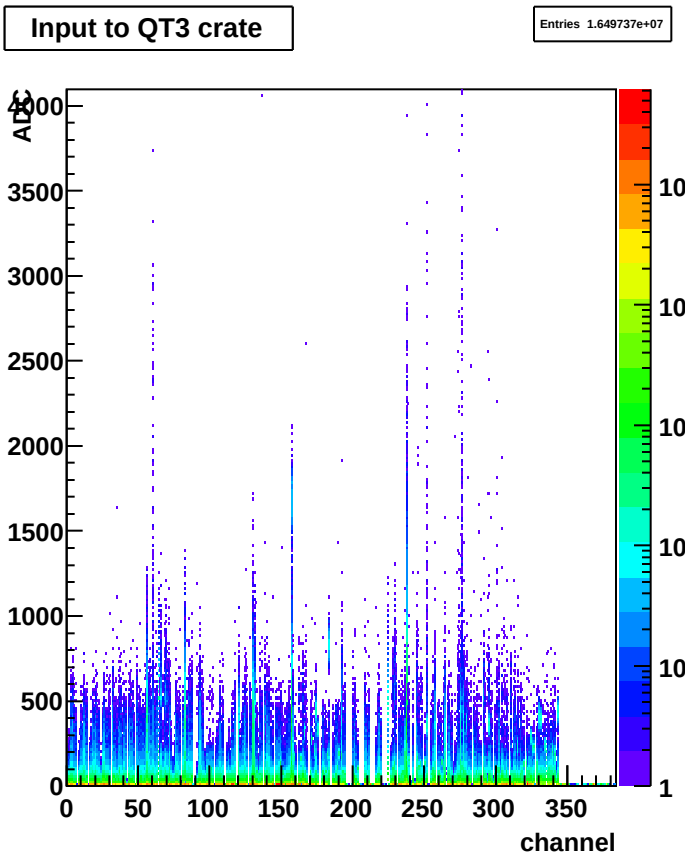

VPD-TAC-E
Entries 1156624
Mean 1319
RMS 160.6

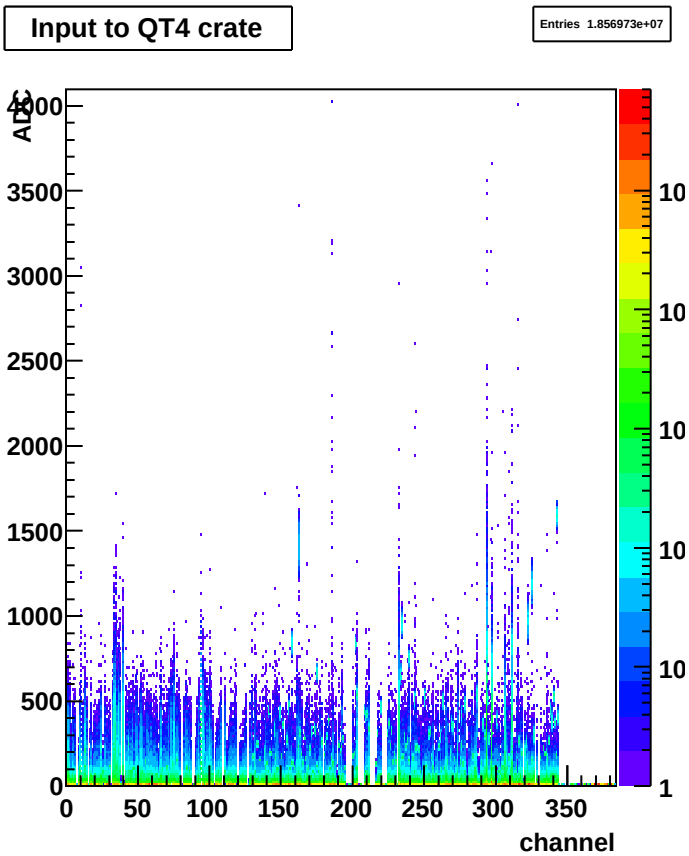

VPD-TAC-W
Entries 1097532
Mean 1426
RMS 177.5

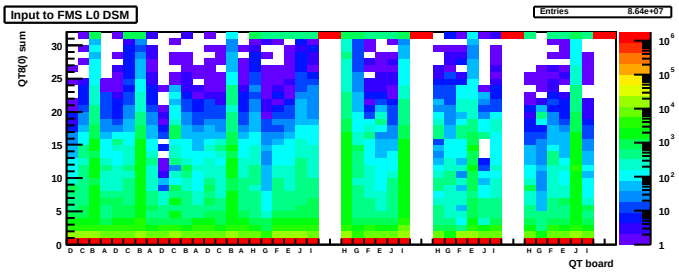

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 836645
Mean 3987
RMS 153.1

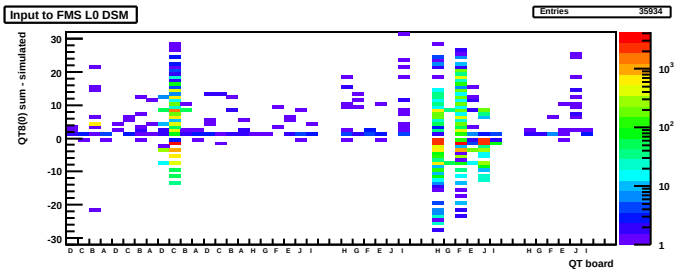

Input to QT1 crate
Entries 2.70831e+07
ADC
channel

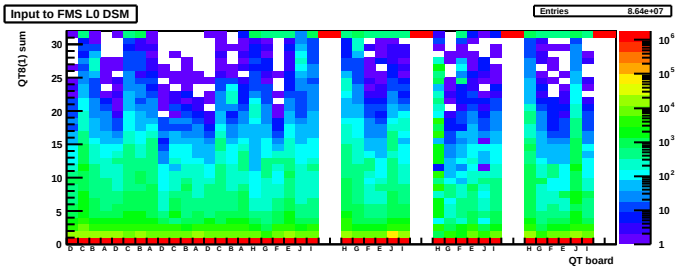

Input to QT2 crate
Entries 1.846907e+07
ADC
channel

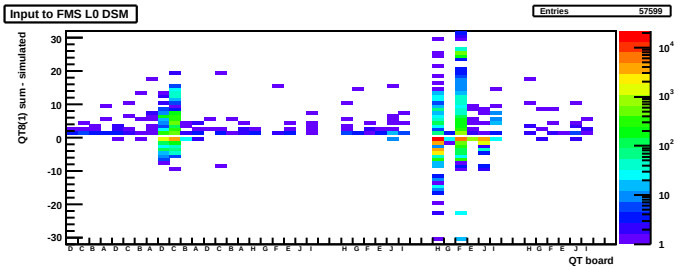

Input to QT3 crate
Entries 1.649737e+07
ADC
channel

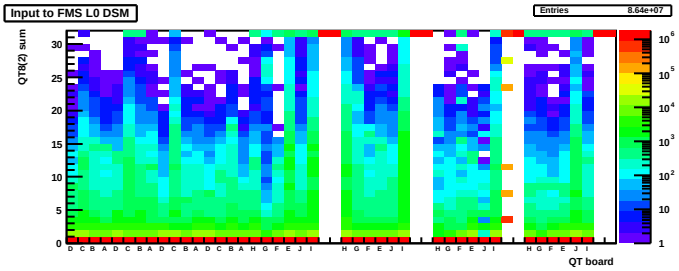

Input to QT4 crate
Entries 1.856973e+07
ADC
channel

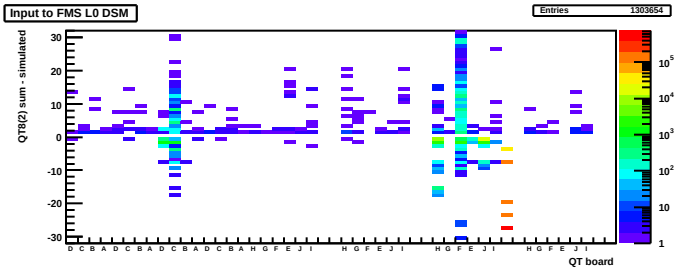

Input to FMS L0 DSM
Entries 8.64e+07
QT8(0) sum
QT board

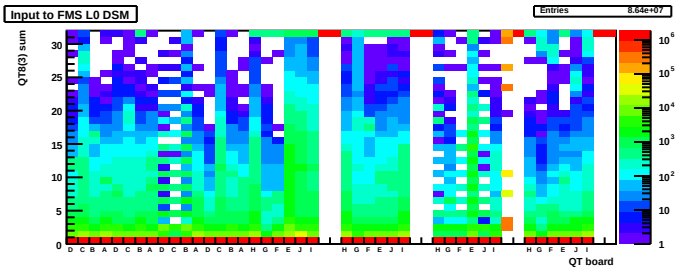

Input to FMS L0 DSM
Entries 35934
QT8(0) sum - simulated
QT board

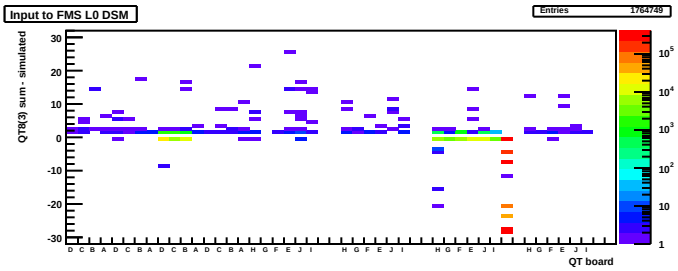

Input to FMS L0 DSM
Entries 8.64e+07
QT8(1) sum
QT board

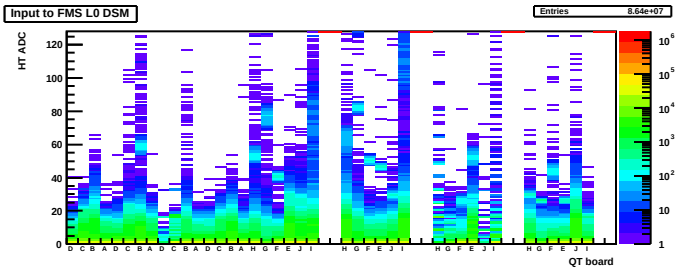

Input to FMS L0 DSM
Entries 57599
QT8(1) sum - simulated
QT board

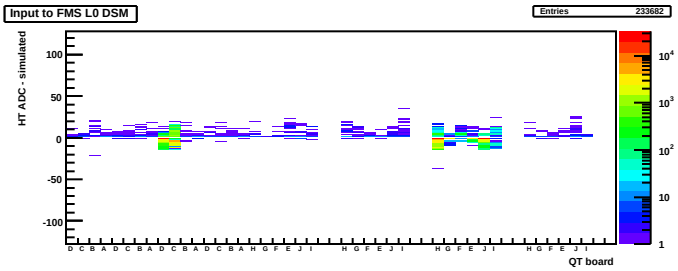

Input to FMS L0 DSM
Entries 8.64e+07
QT8(2) sum
QT board

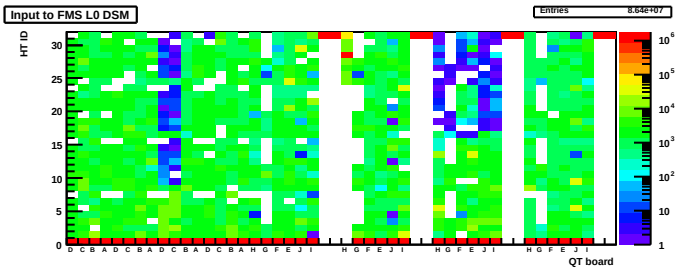

Input to FMS L0 DSM
Entries 1303654
QT8(2) sum - simulated
QT board

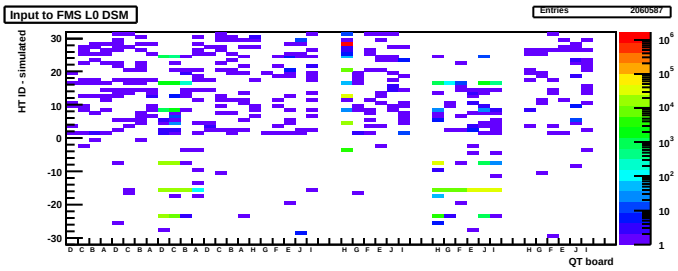

Input to FMS L0 DSM
Entries 8.64e+07
QT8(3) sum
QT board

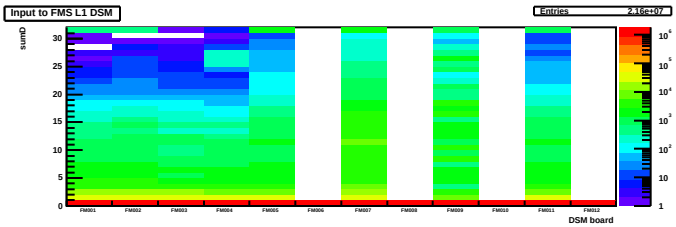

Input to FMS L0 DSM
Entries 1764749
QT8(3) sum - simulated
QT board

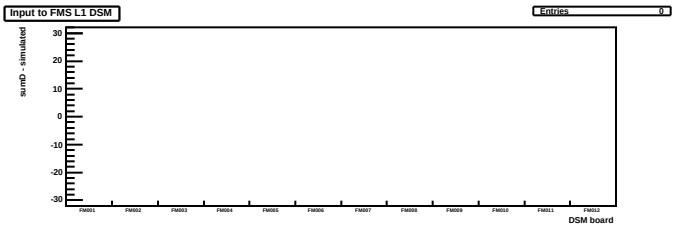

Input to FMS L0 DSM
Entries 8.64e+07
HT ADC
QT board

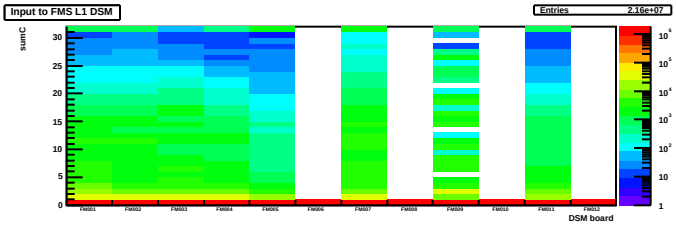

Input to FMS L0 DSM
Entries 233682
HT ADC - simulated
QT board

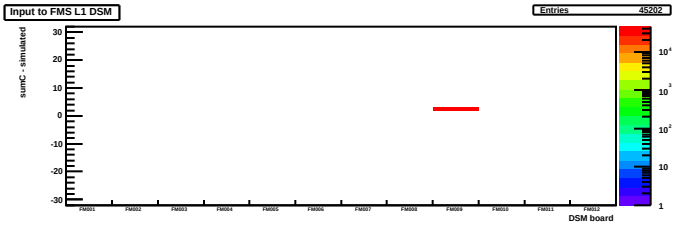

Input to FMS L0 DSM
Entries 8.64e+07
HT ID
QT board

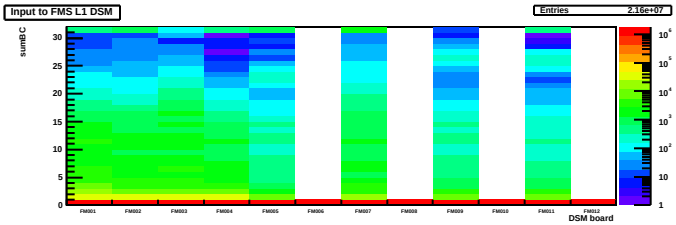

Input to FMS L0 DSM
Entries 2060587
HT ID - simulated
QT board

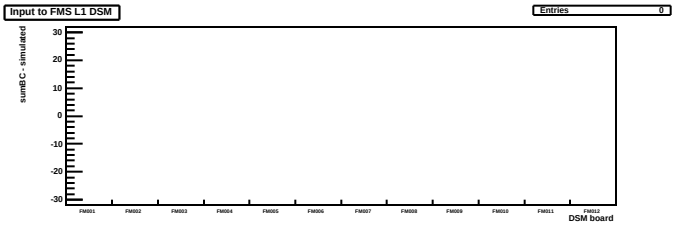

Input to FMS L1 DSM
Entries 2.16e+07
sumD
DSM board

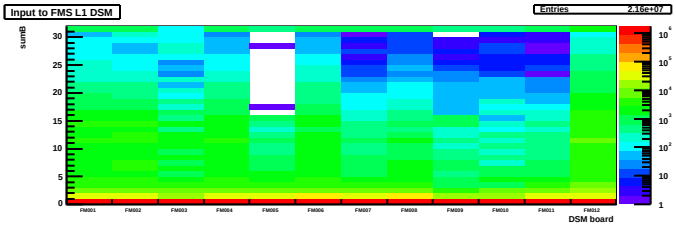

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

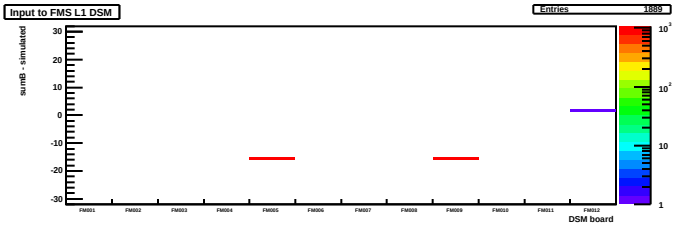

Input to FMS L1 DSM
Entries 2.16e+07
sumC
DSM board

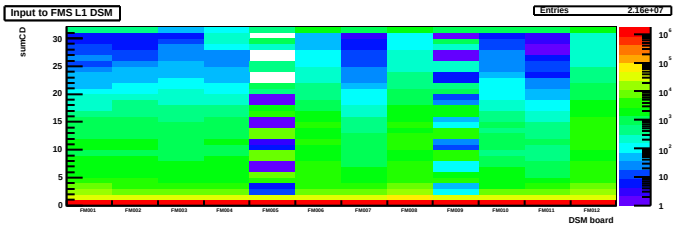

Input to FMS L1 DSM
Entries 45202
sumC - simulated
DSM board

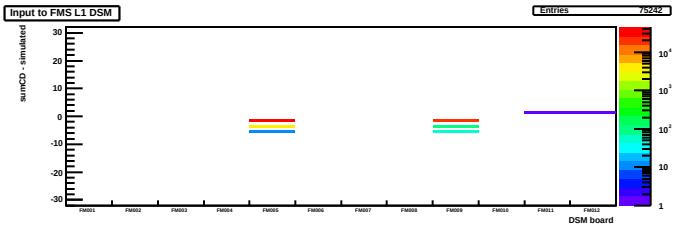

Input to FMS L1 DSM
Entries 2.16e+07
sumBC
DSM board

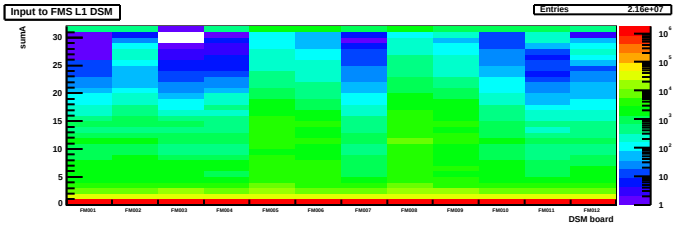

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

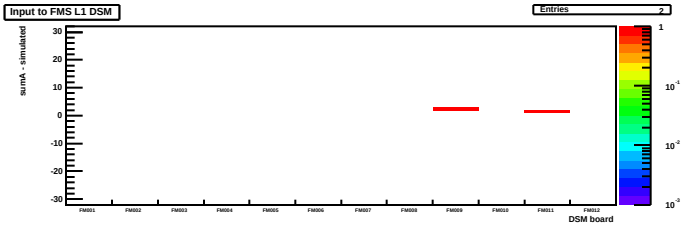

Input to FMS L1 DSM
Entries 2.16e+07
sumB
DSM board

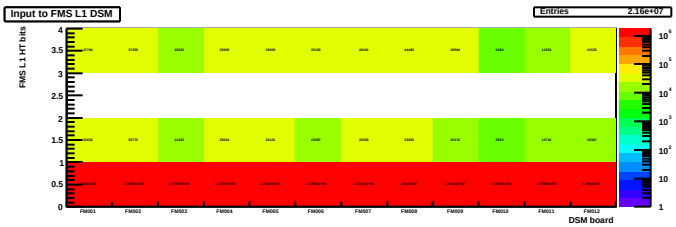

Input to FMS L1 DSM
Entries 1889
sumB - simulated
DSM board

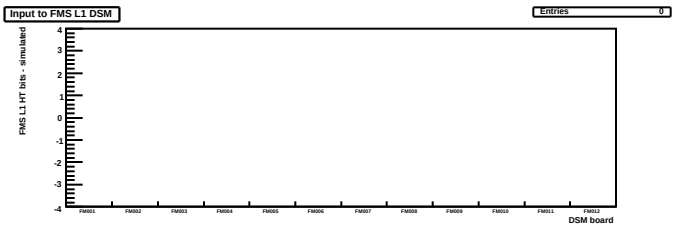

Input to FMS L1 DSM
Entries 2.16e+07
sumCD
DSM board

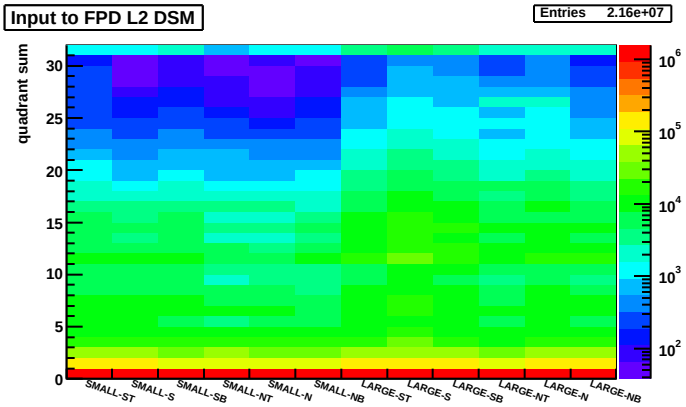

Input to FMS L1 DSM
Entries 75242
sumCD - simulated
DSM board

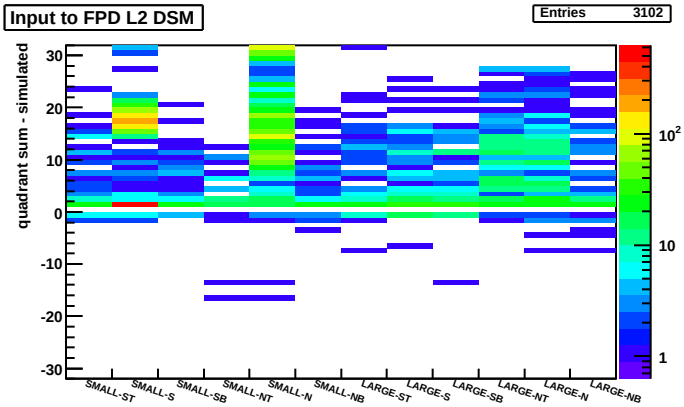

Input to FMS L1 DSM
Entries 2.16e+07
sumA
DSM board

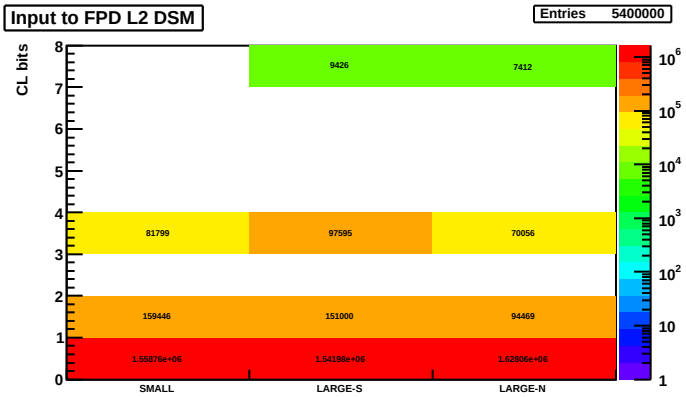

Input to FMS L1 DSM
Entries 2
sumA - simulated
DSM board

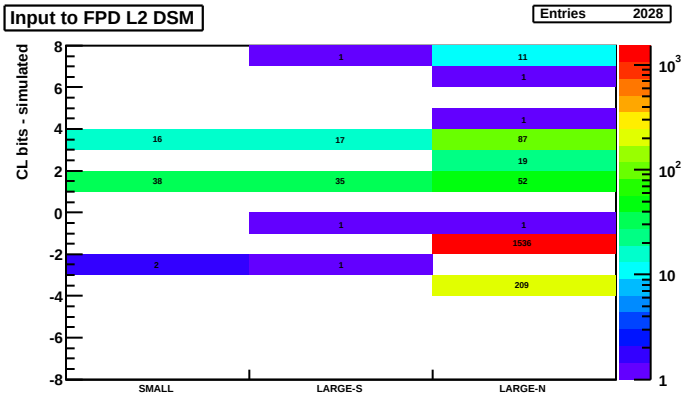

Input to FMS L1 DSM
Entries 2.16e+07
FMS L1 HT bits
DSM board

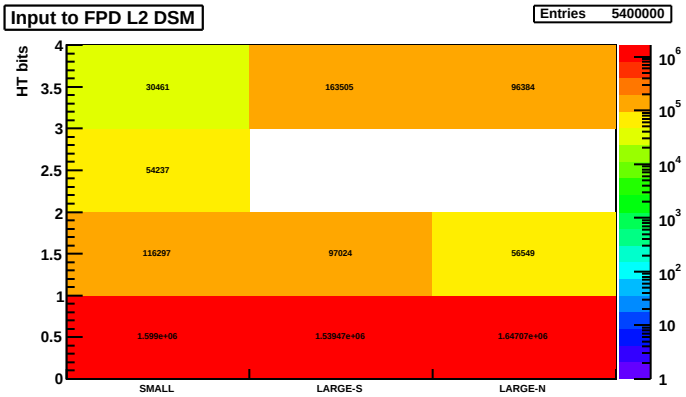

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

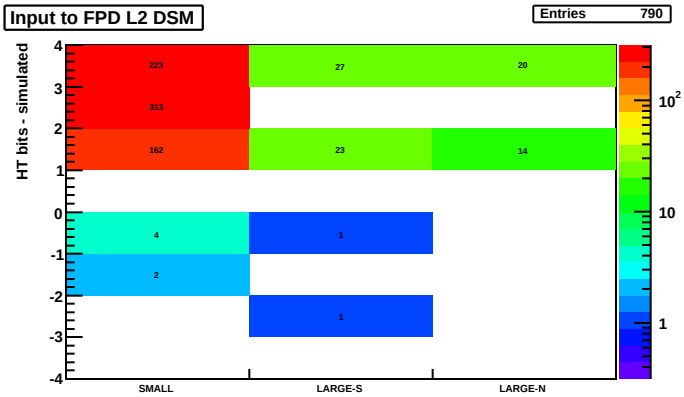

Input to FPD L2 DSM
Entries 2.16e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

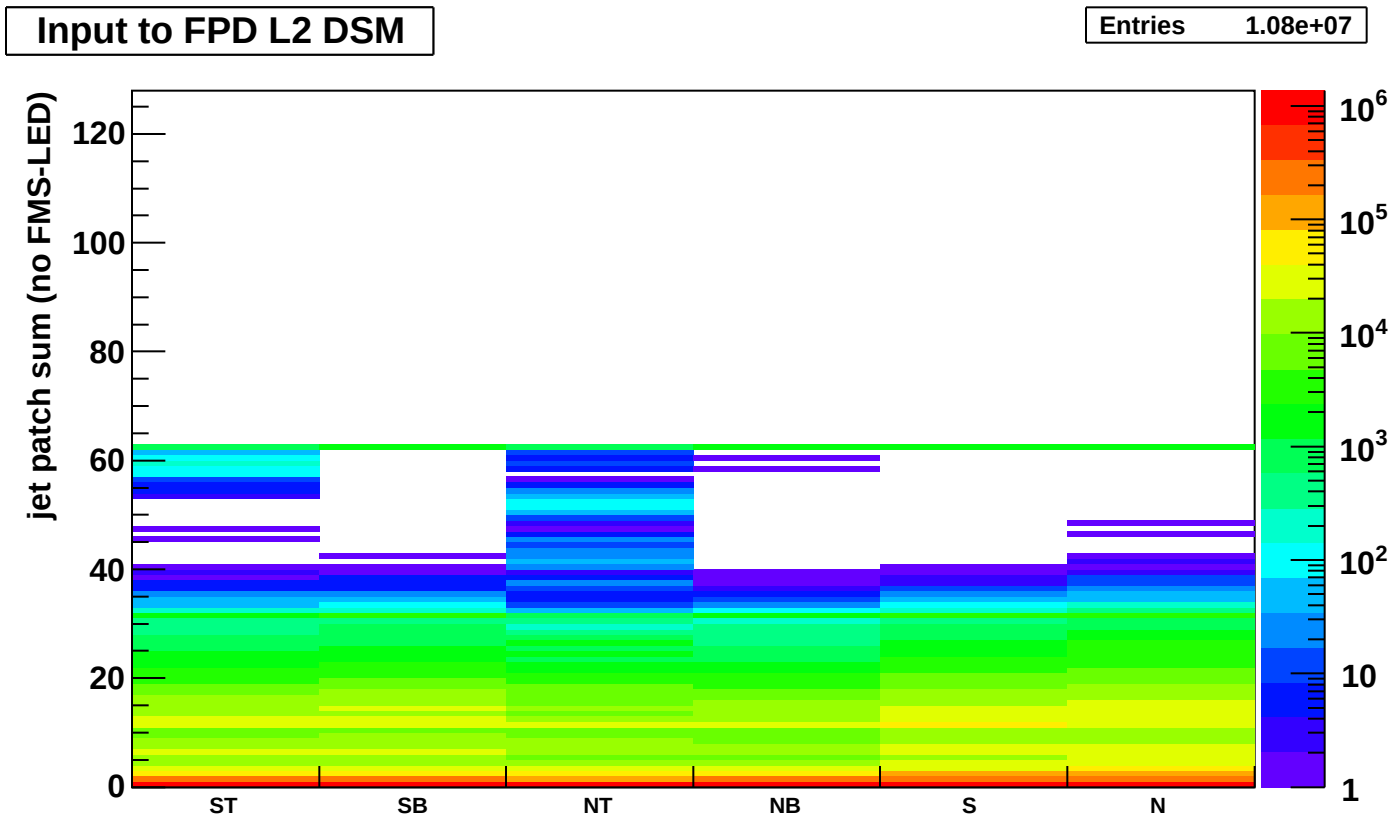

Input to FPD L2 DSM
Entries 3102
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

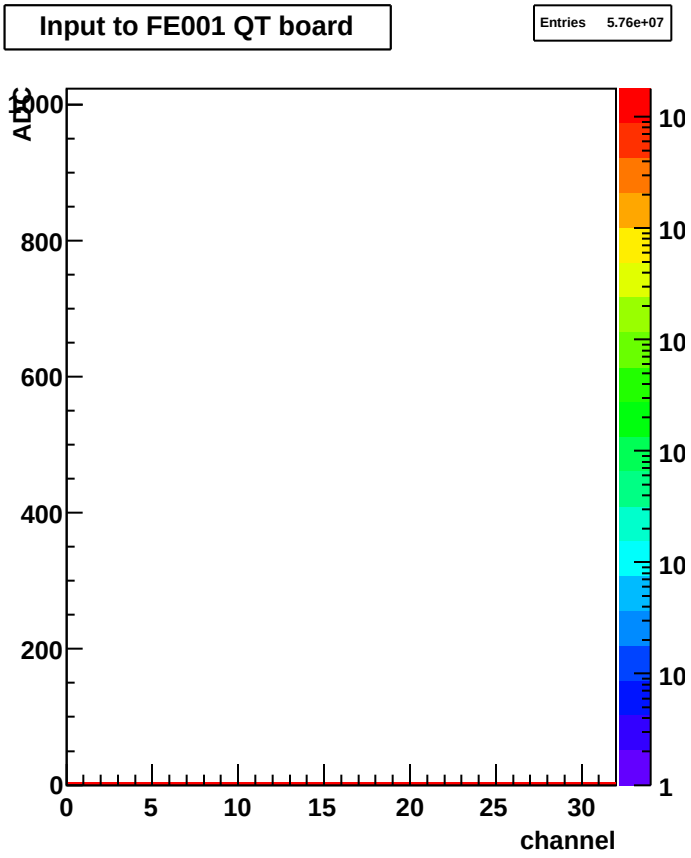

Input to FPD L2 DSM
Entries 5400000
CL bits
9426
7412
81799
97595
70056
159446
151000
94469
1.55876e+06
1.54198e+06
1.62806e+06
SMALL
LARGE-S
LARGE-N

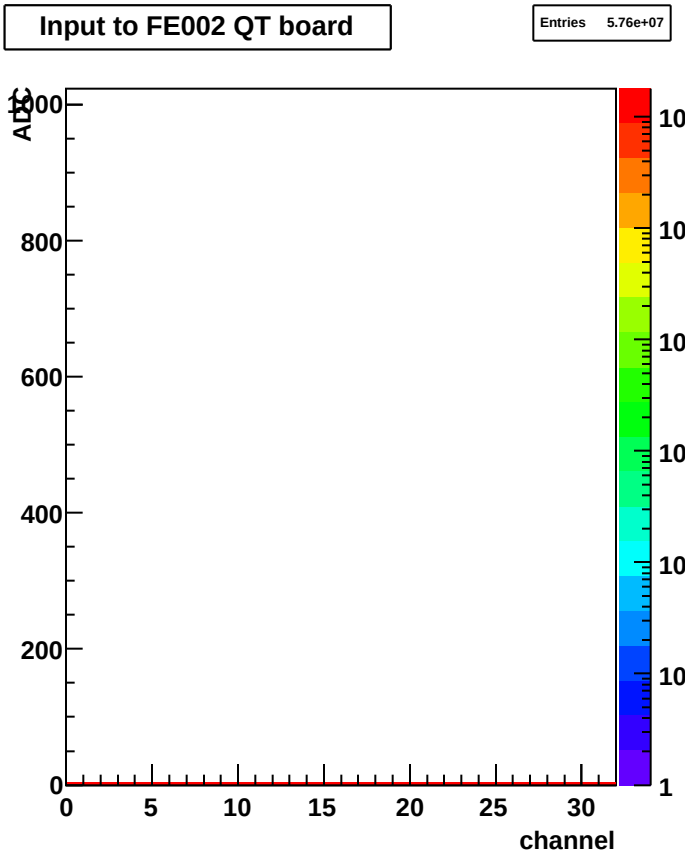

Input to FPD L2 DSM
Entries 2028
CL bits - simulated
1
11
1
1
16
17
87
19
38
35
52
1
1
1536
2
1
209
SMALL
LARGE-S
LARGE-N

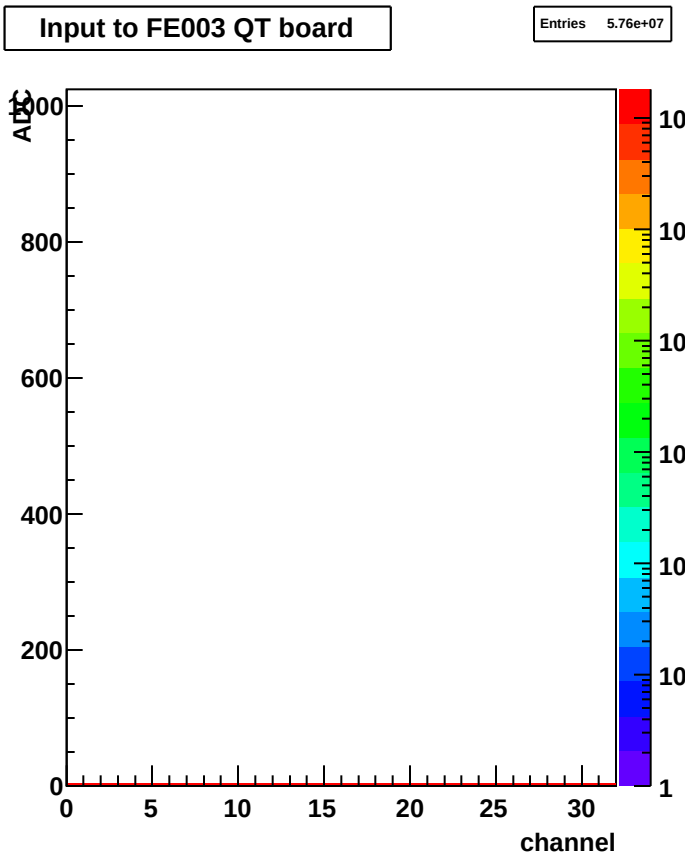

Input to FPD L2 DSM
Entries 5400000
HT bits
30461
163505
96384
54237
116297
97024
56549
1.599e+06
1.53947e+06
1.64707e+06
SMALL
LARGE-S
LARGE-N

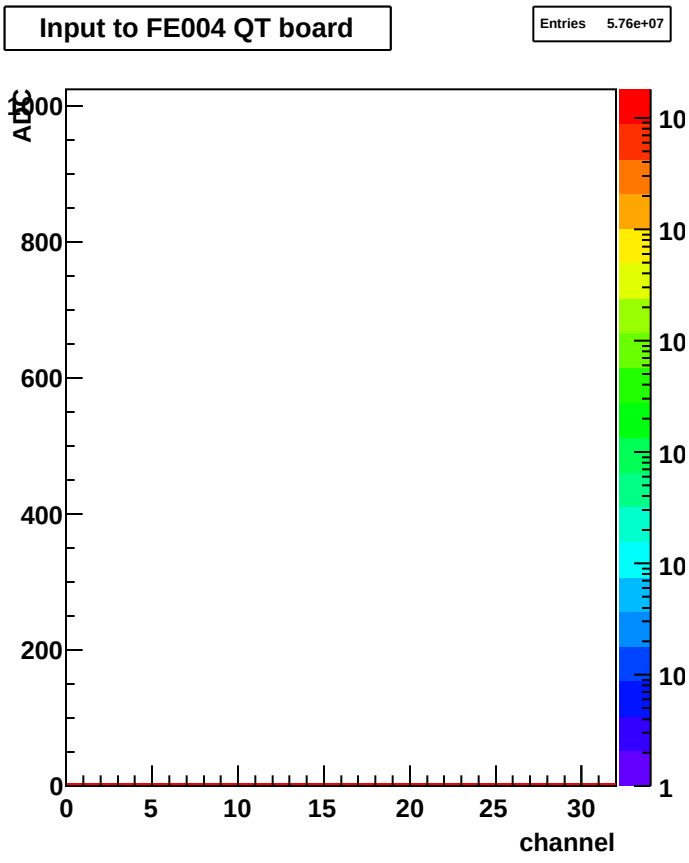

Input to FPD L2 DSM
Entries 790
HT bits - simulated
223
27
20
313
162
23
14
4
1
2
1
SMALL
LARGE-S
LARGE-N

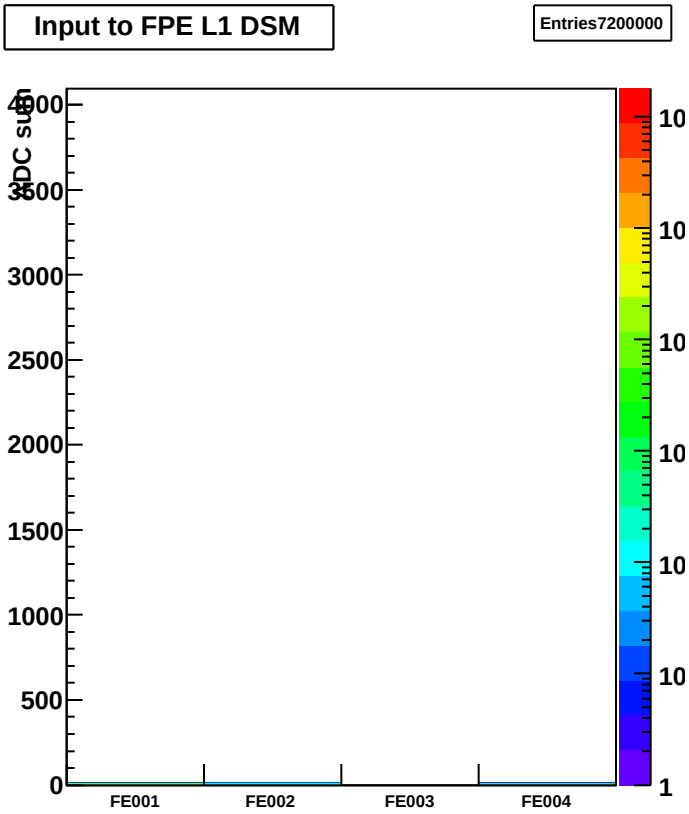

Input to FPD L2 DSM
Entries 1.08e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

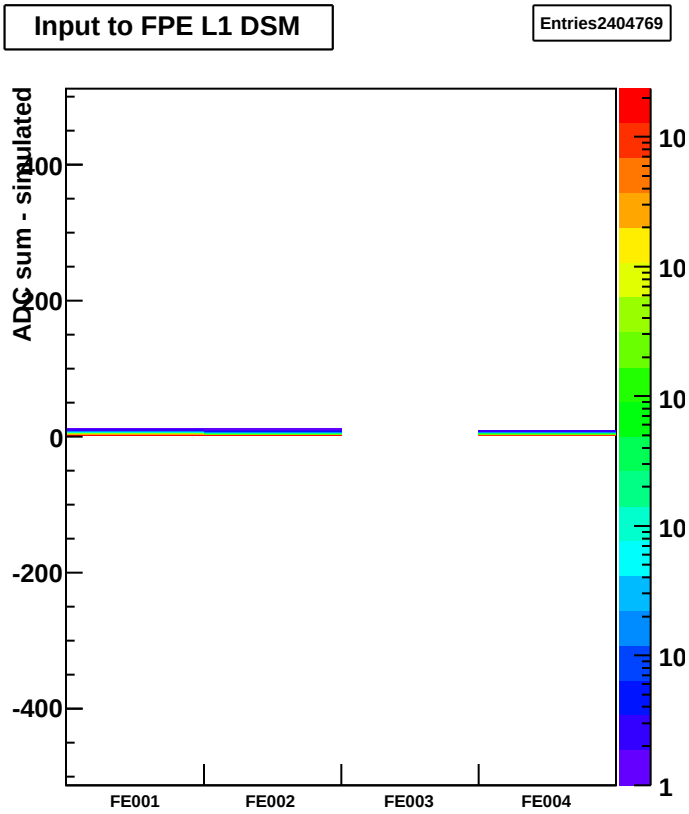

Input to FE001 QT board
Entries 5.76e+07
ADC
channel


Input to FE002 QT board
Entries 5.76e+07
ADC
channel

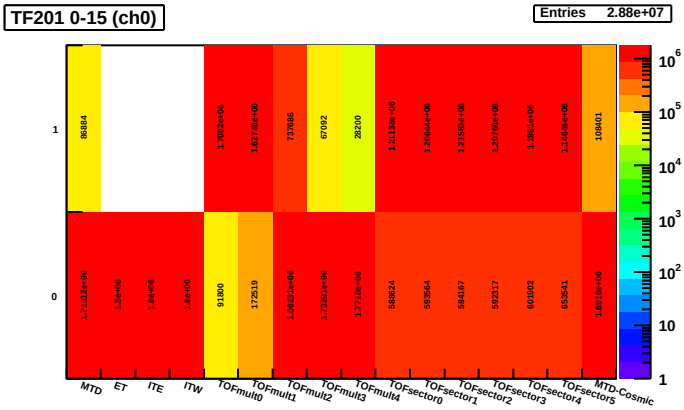

Input to FE003 QT board
Entries 5.76e+07
ADC
channel


Input to FE004 QT board
Entries 5.76e+07
ADC
channel

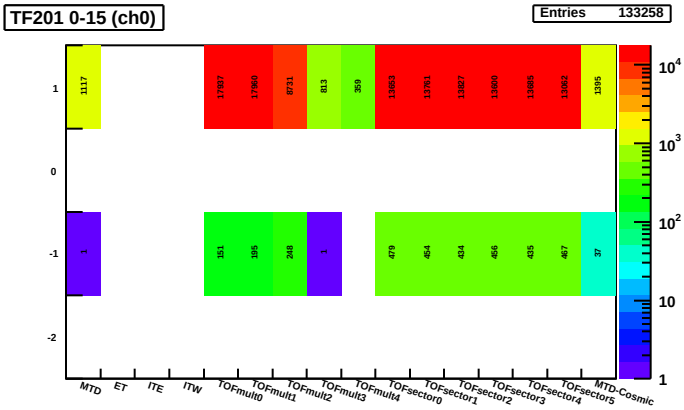

Input to FPE L1 DSM
Entries7200000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2404769
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

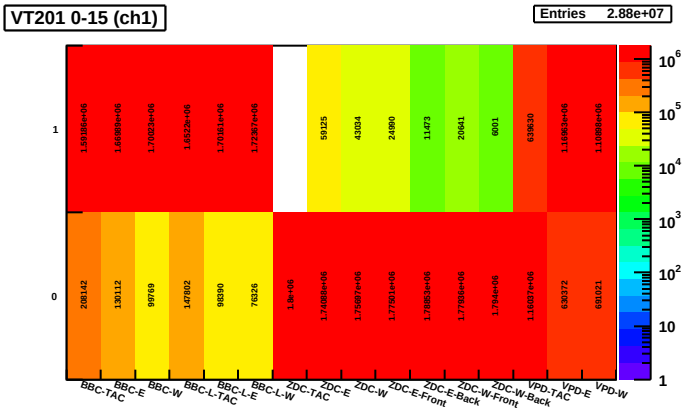
TF201 0-15 (ch0)



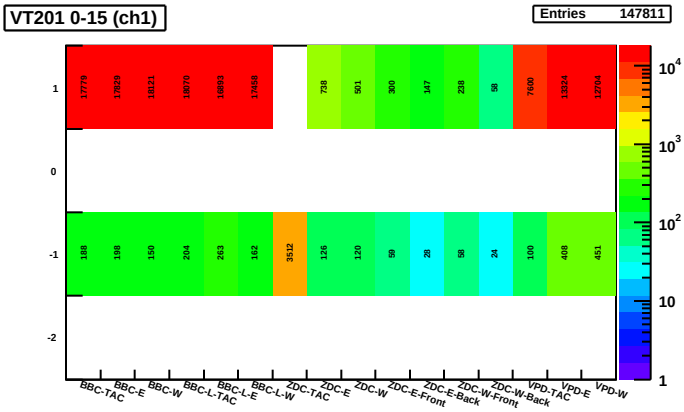
TF201 0-15 (ch0)



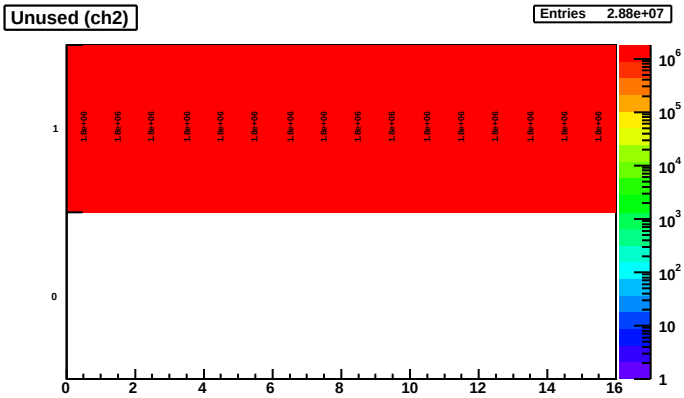
VT201 0-15 (ch1)

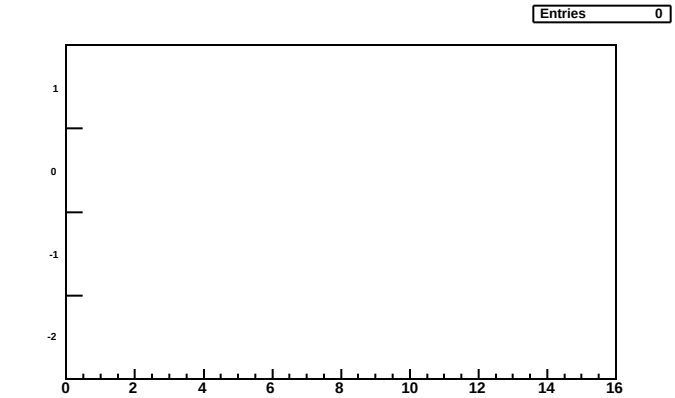


VT201 0-15 (ch1)



Unused (ch2)





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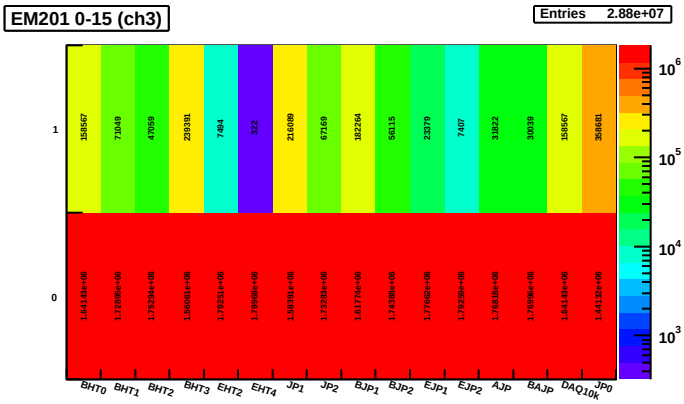

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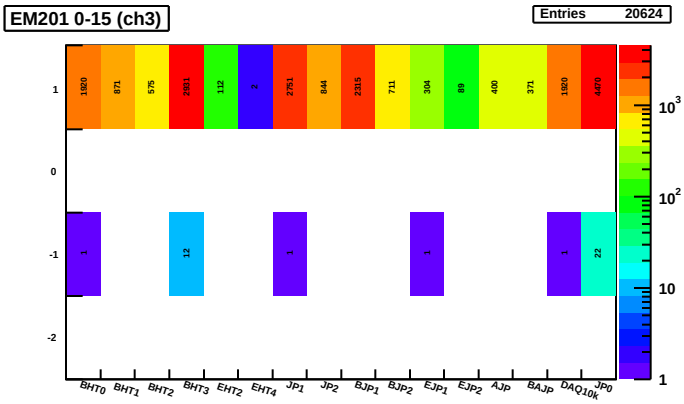
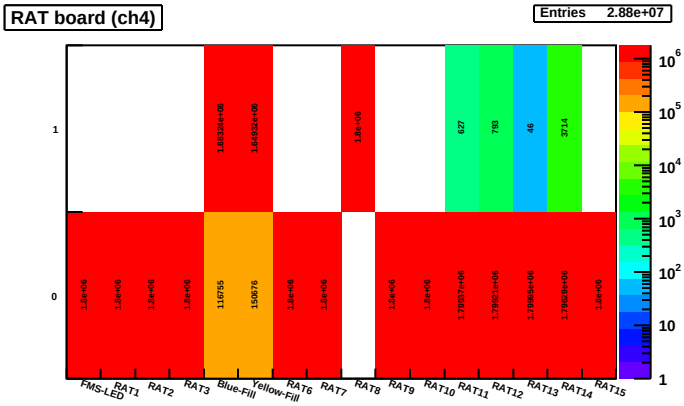
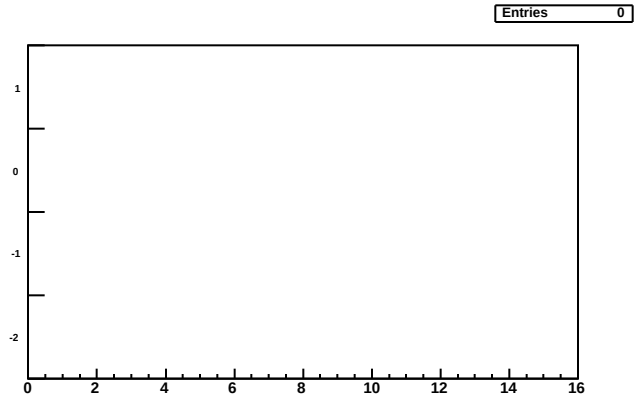


RAT board (ch4)





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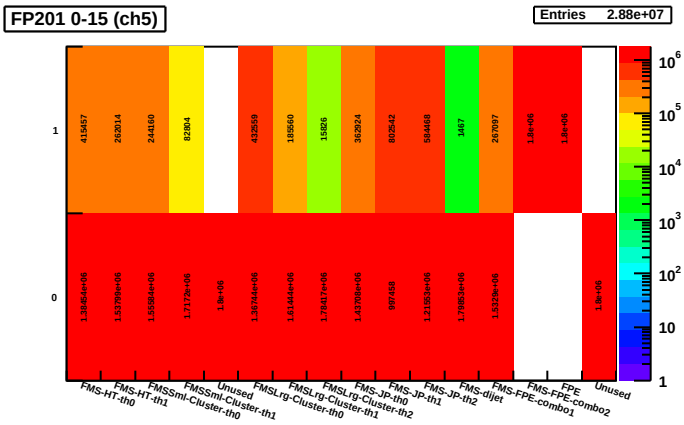

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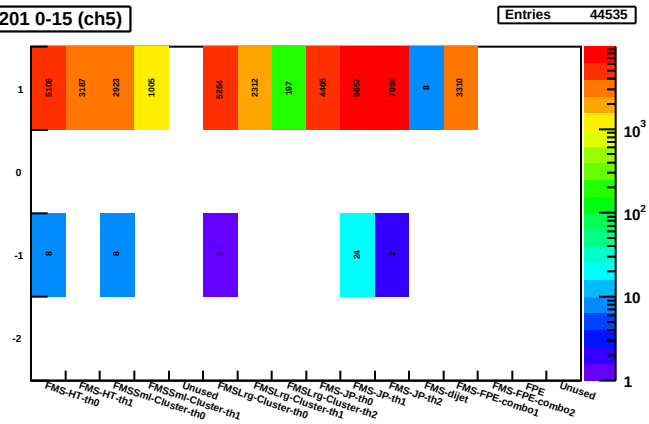

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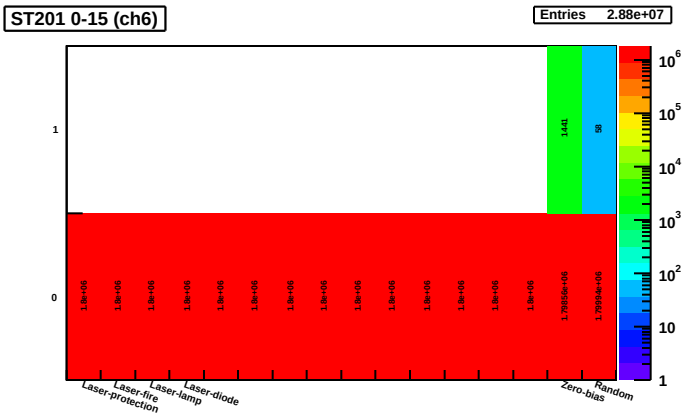

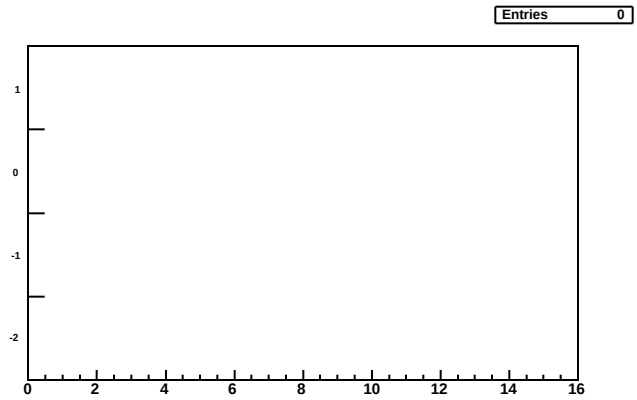
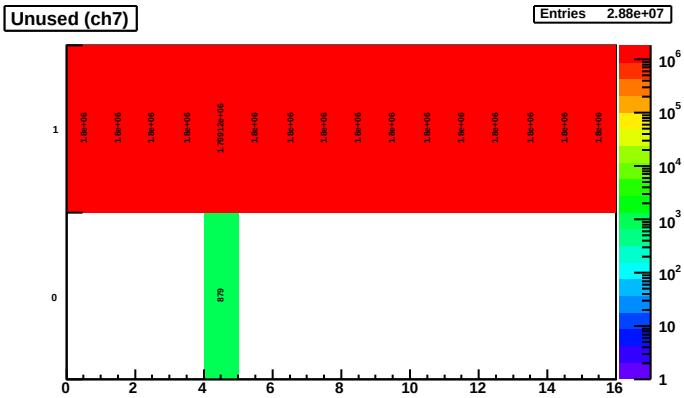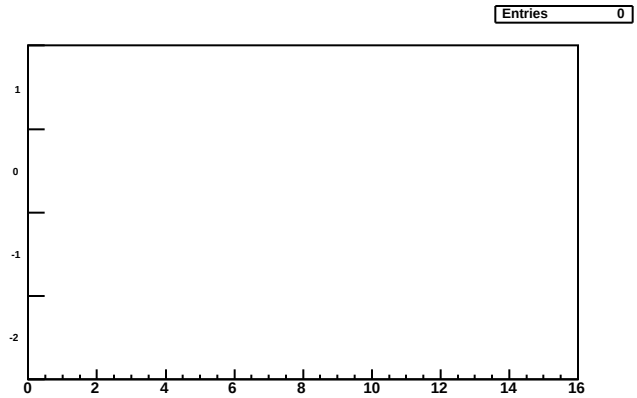
Unused (ch7)