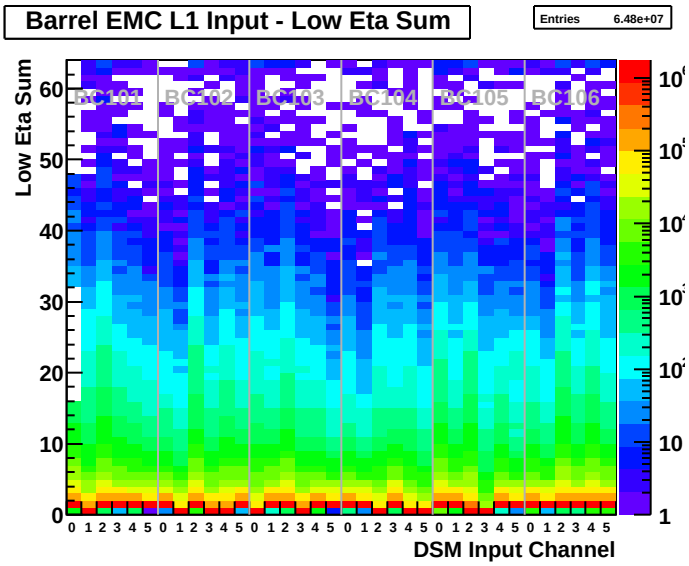
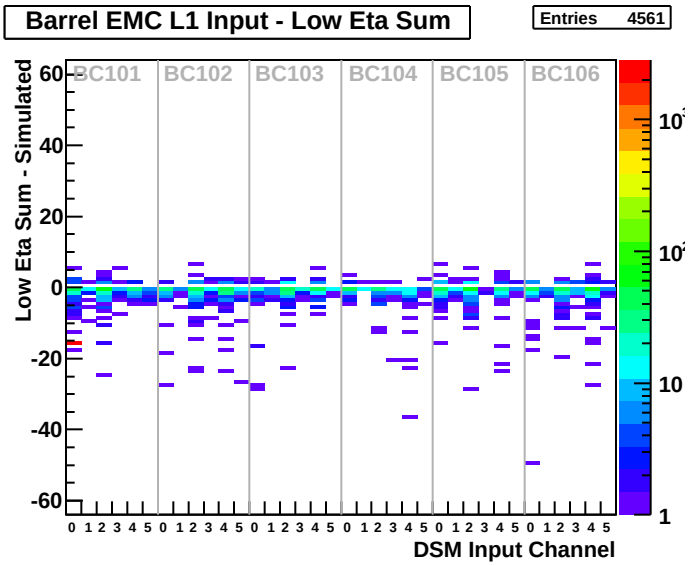
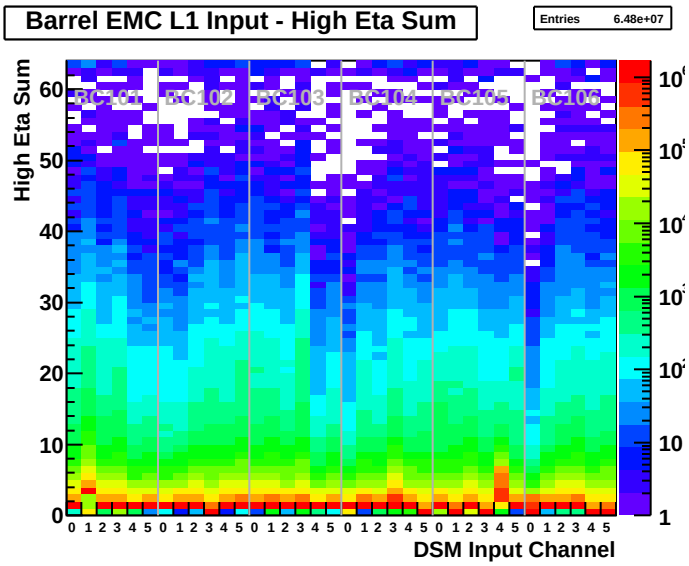
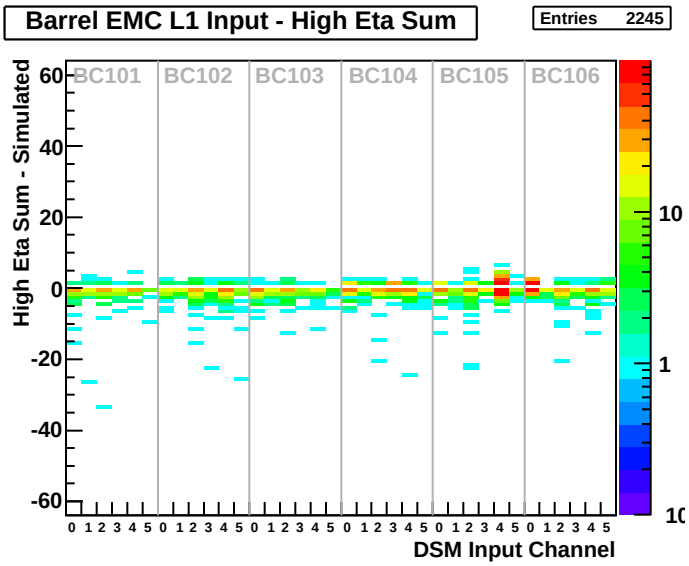
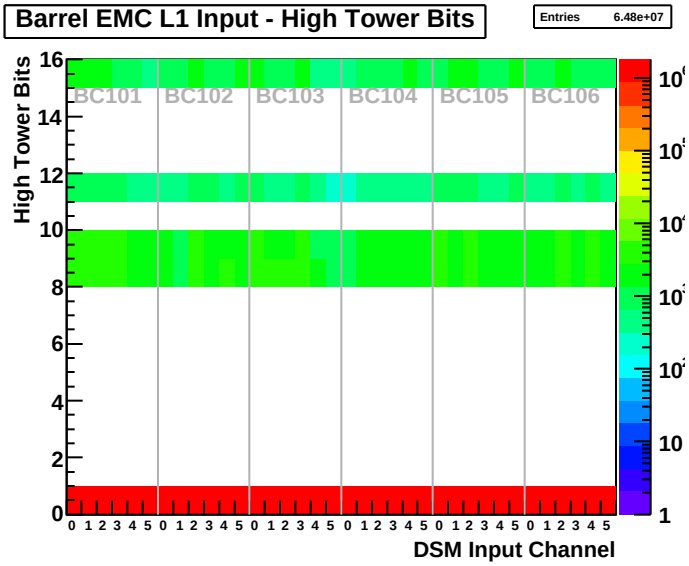
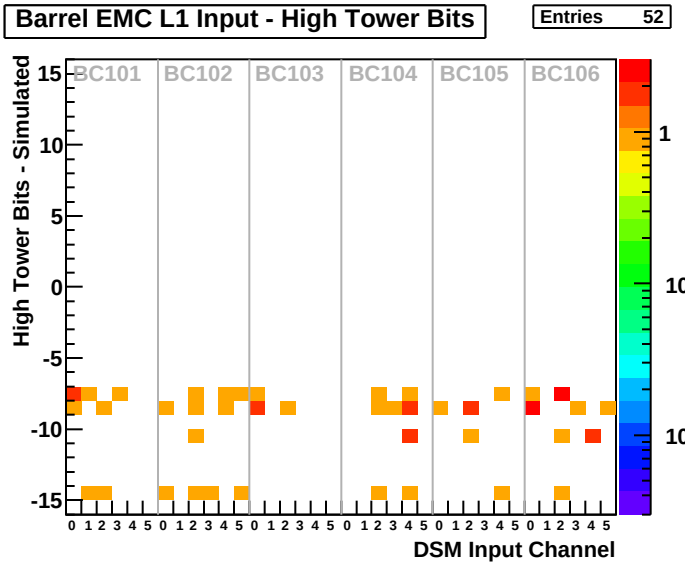

Barrel EMC L1 Input - Low Eta Sum
Entries 6.48e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

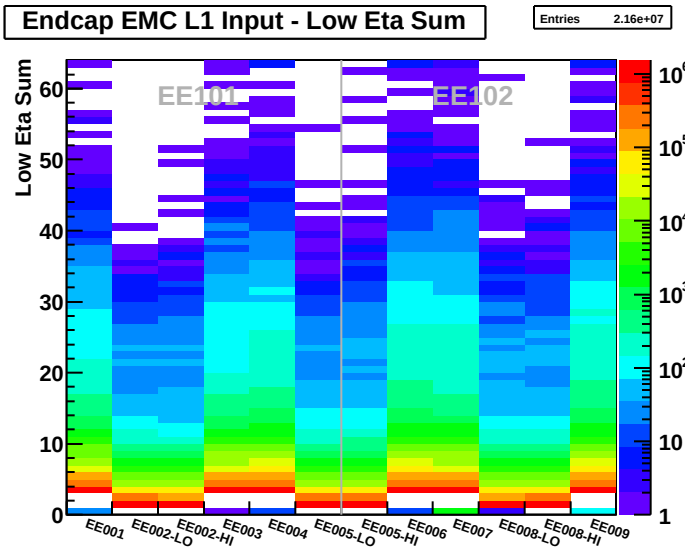

Barrel EMC L1 Input - Low Eta Sum
Entries 4561
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

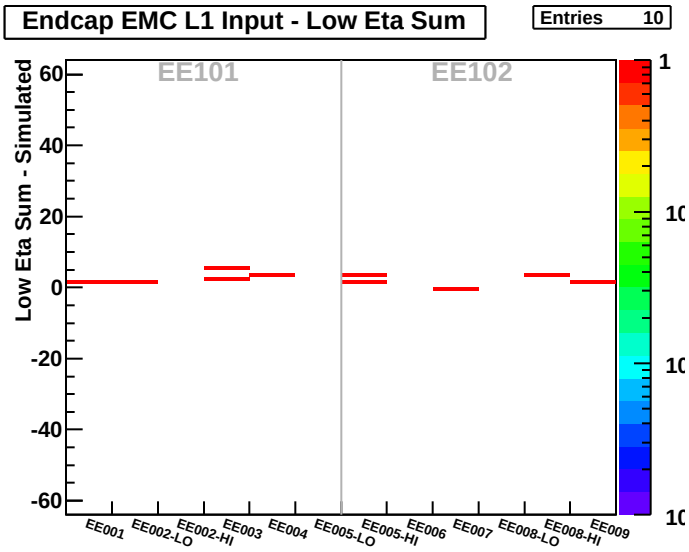

Barrel EMC L1 Input - High Eta Sum
Entries 6.48e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

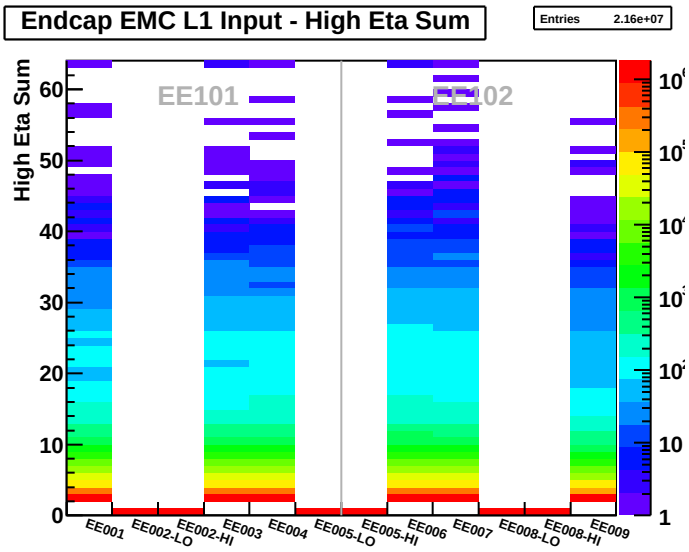

Barrel EMC L1 Input - High Eta Sum
Entries 2245
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

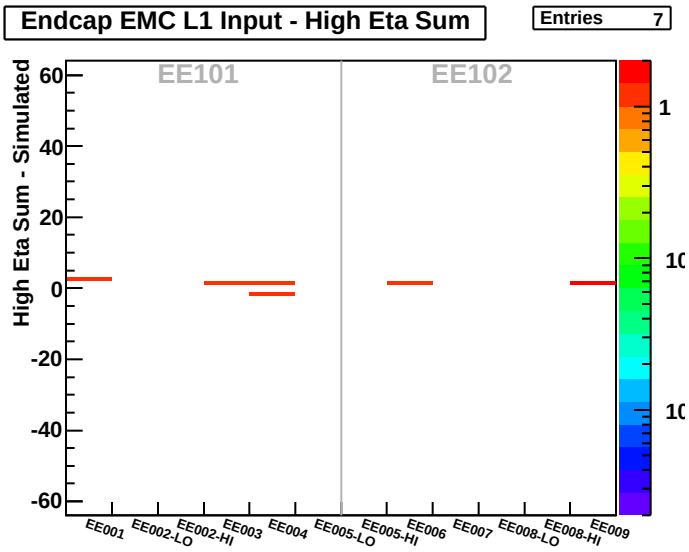

Barrel EMC L1 Input - High Tower Bits
Entries 6.48e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

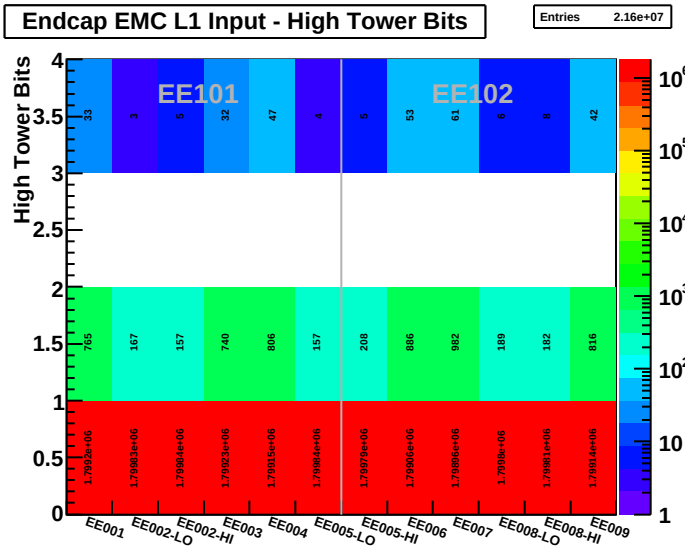

Barrel EMC L1 Input - High Tower Bits
Entries 52
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

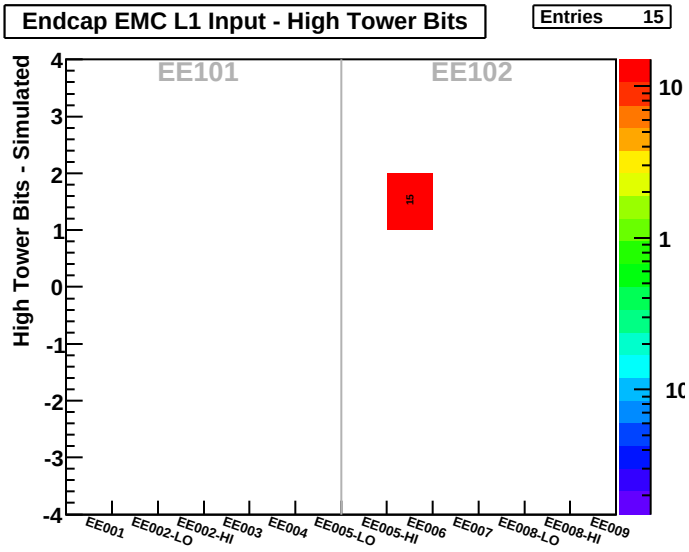

Endcap EMC L1 Input - Low Eta Sum
Entries 2.16e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

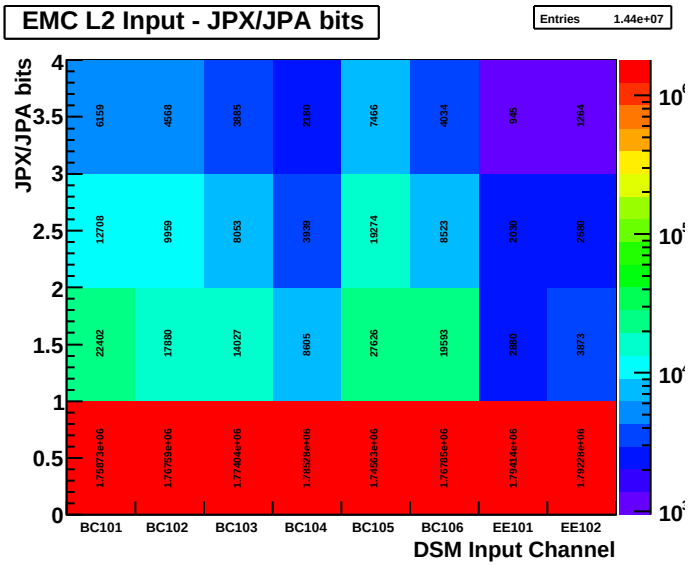

Endcap EMC L1 Input - Low Eta Sum
Entries 10
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

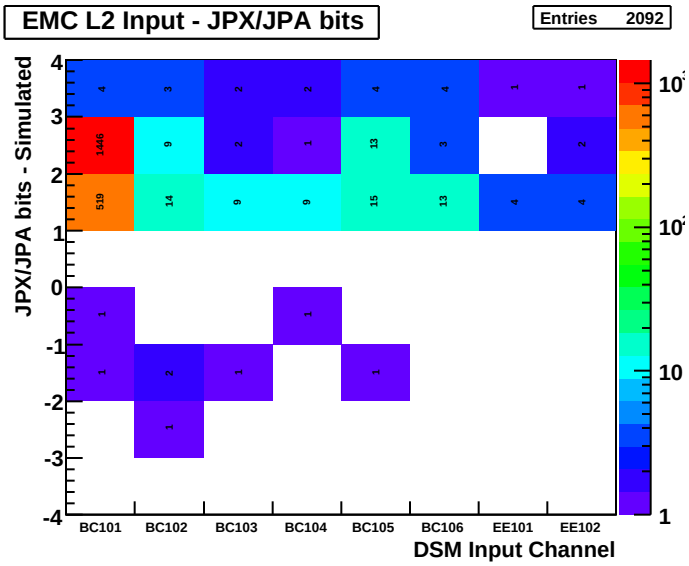

Endcap EMC L1 Input - High Eta Sum
Entries 2.16e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

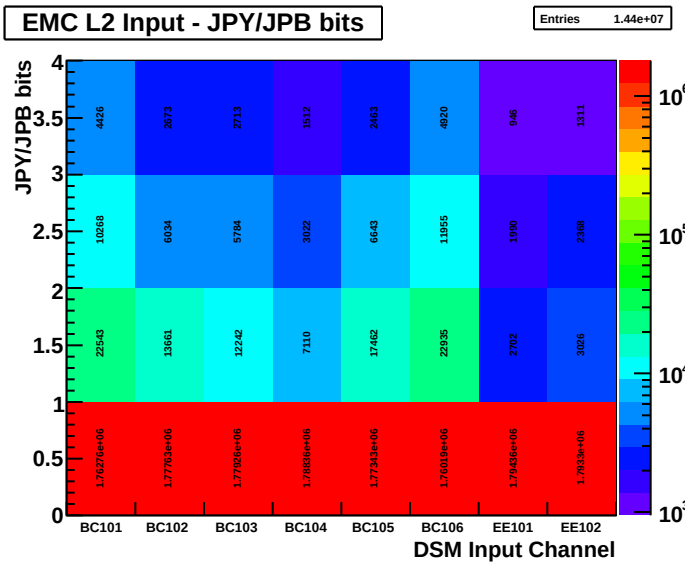

Endcap EMC L1 Input - High Eta Sum
Entries 7
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

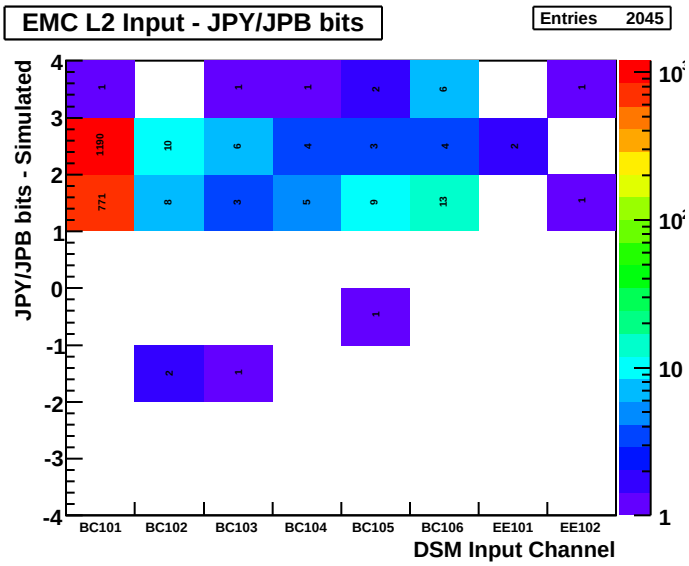

Endcap EMC L1 Input - High Tower Bits
Entries 2.16e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

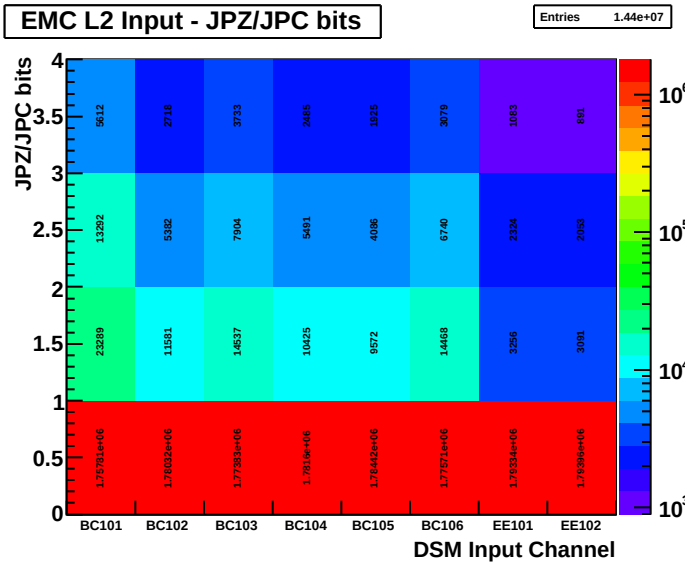

Endcap EMC L1 Input - High Tower Bits
Entries 15
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

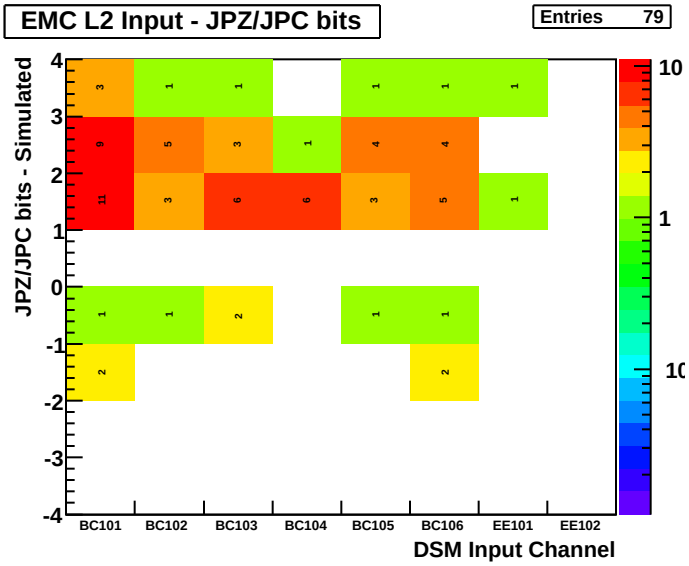

EMC L2 Input - JPX/JPA bits
Entries 1.44e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

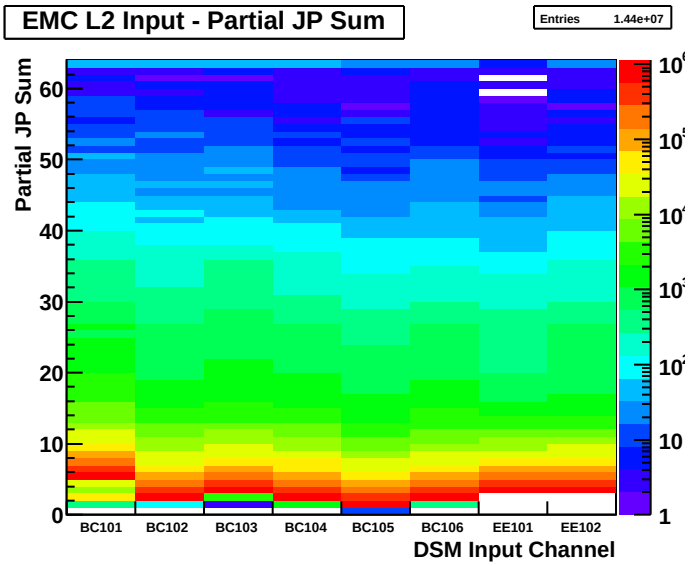

EMC L2 Input - JPX/JPA bits
Entries 2092
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

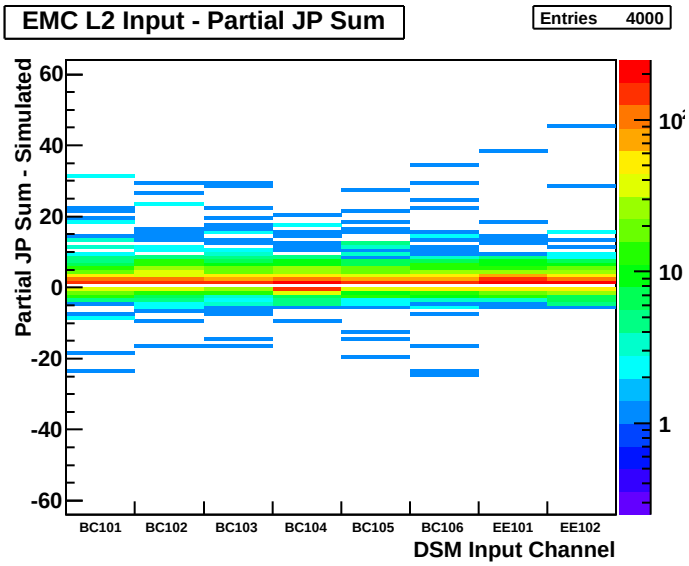

EMC L2 Input - JPY/JPB bits
Entries 1.44e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

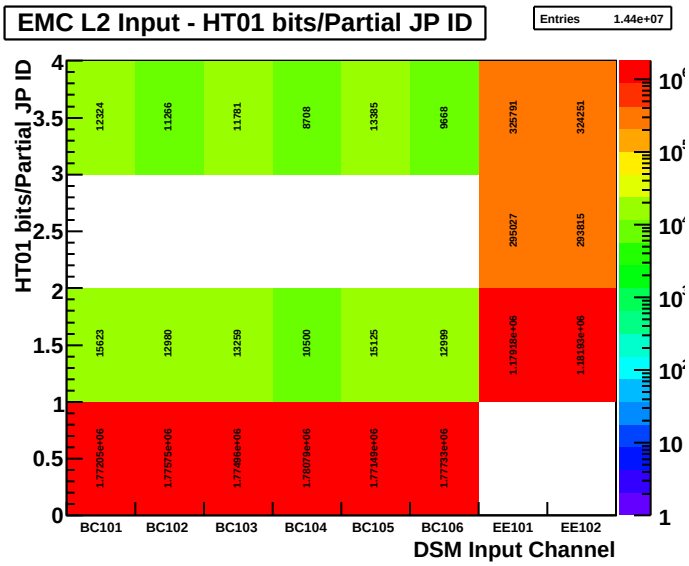

EMC L2 Input - JPY/JPB bits
Entries 2045
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

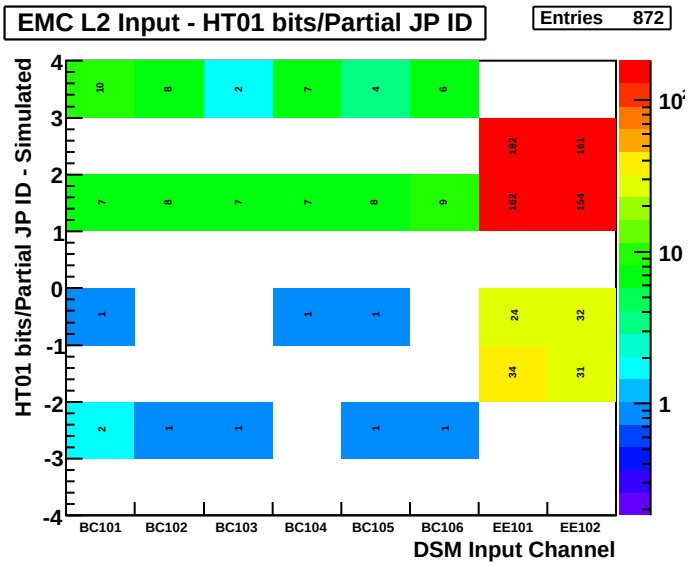

EMC L2 Input - JPZ/JPC bits
Entries 1.44e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

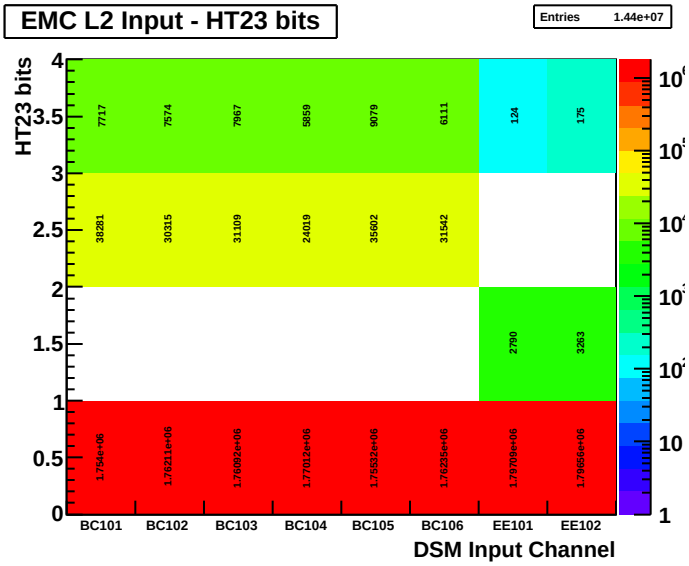

EMC L2 Input - JPZ/JPC bits
Entries 79
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

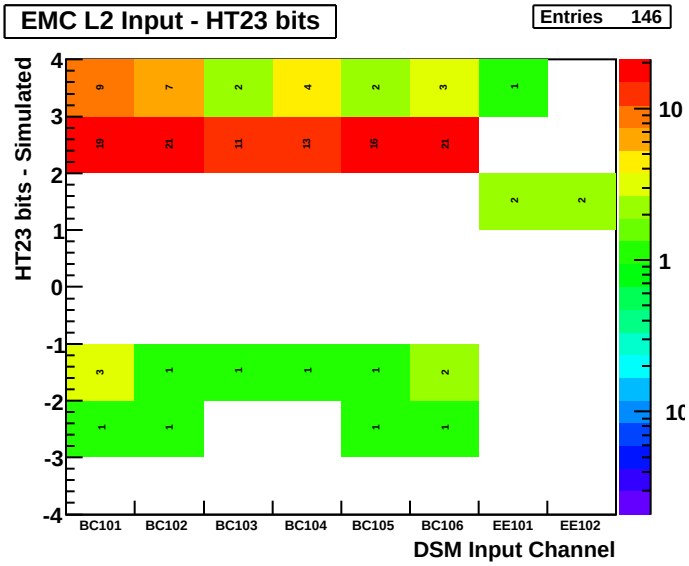

EMC L2 Input - Partial JP Sum
Entries 1.44e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

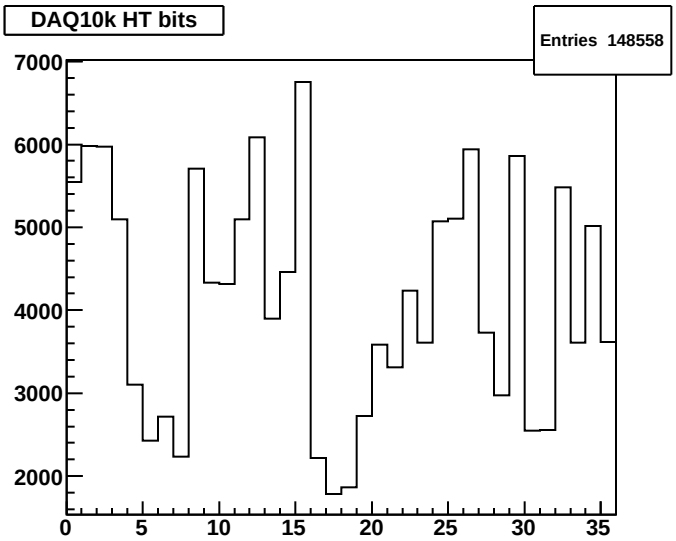

EMC L2 Input - Partial JP Sum
Entries 4000
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

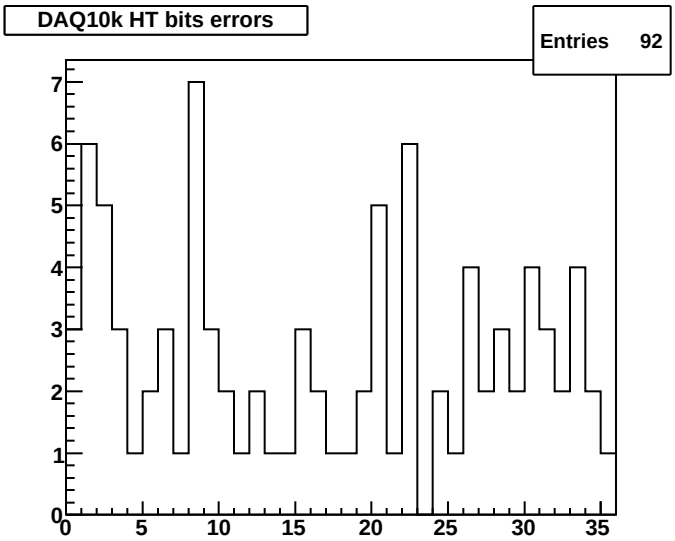

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.44e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

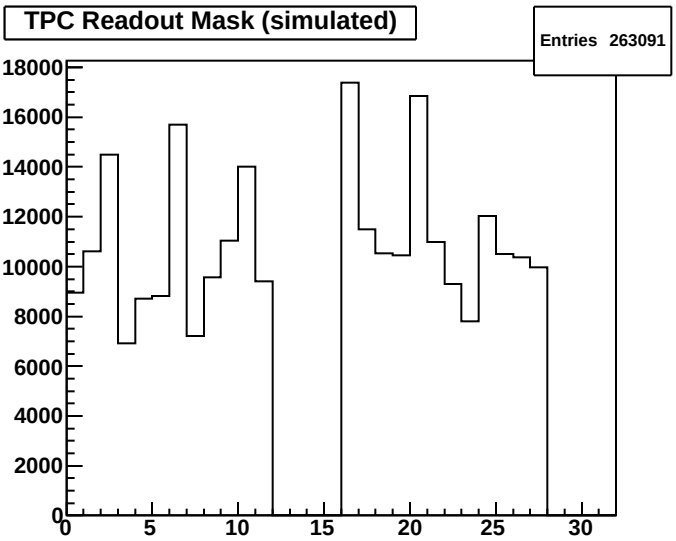

EMC L2 Input - HT01 bits/Partial JP ID
Entries 872
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

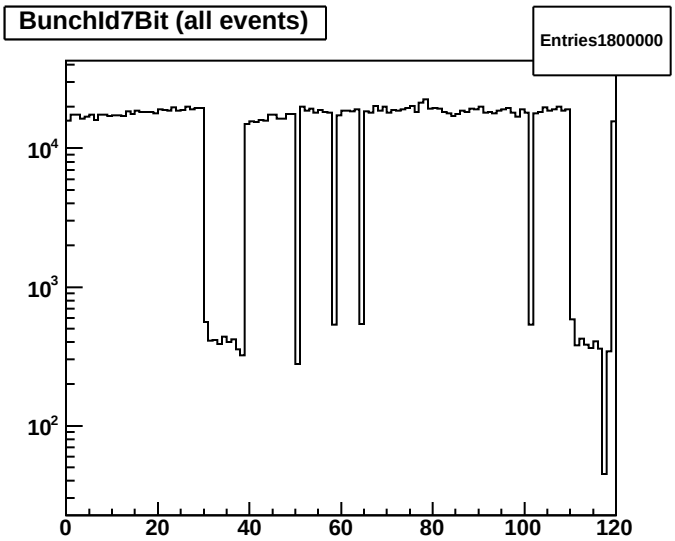

EMC L2 Input - HT23 bits
Entries 1.44e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

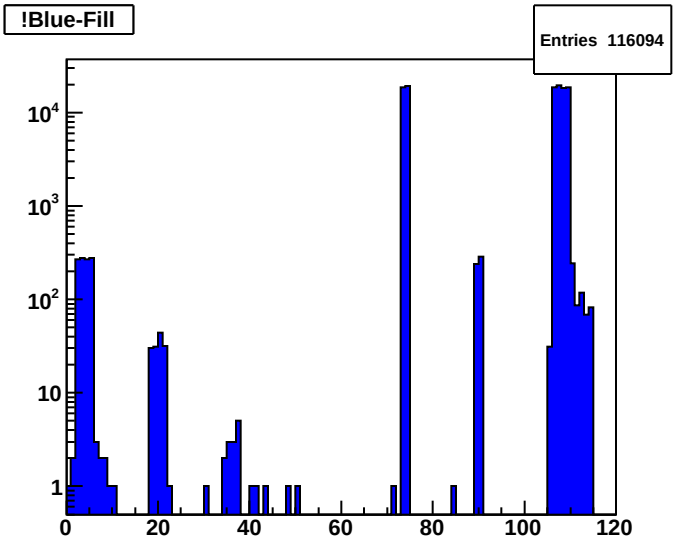

EMC L2 Input - HT23 bits
Entries 146
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

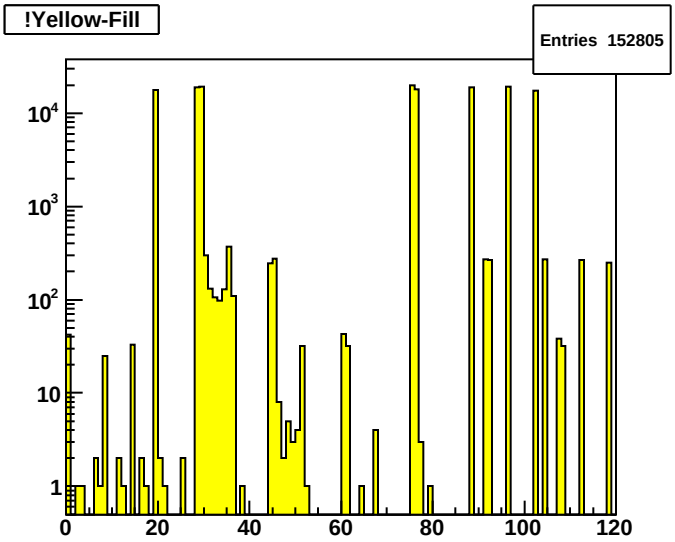

DAQ10k HT bits
Entries 148558

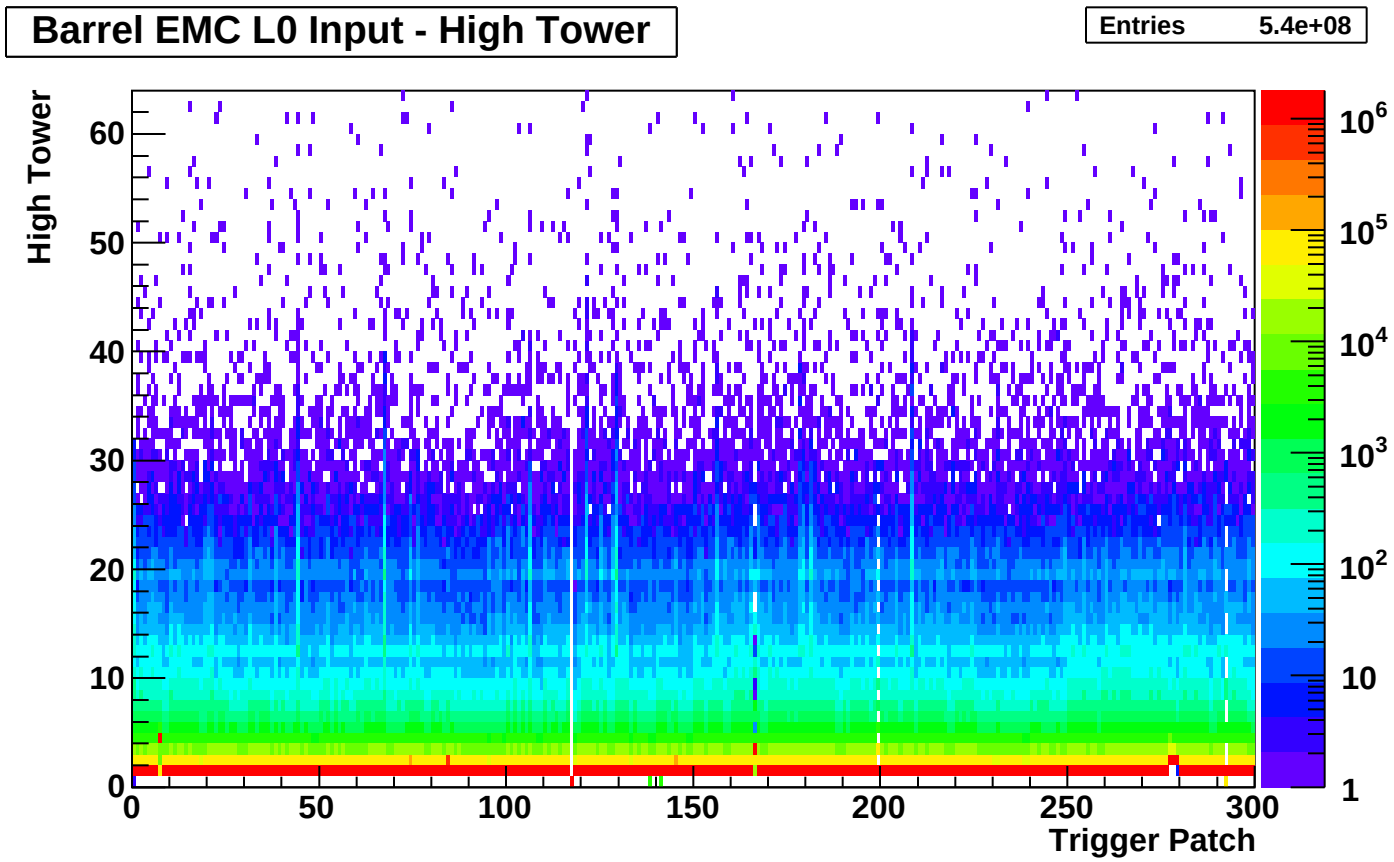

DAQ10k HT bits errors
Entries 92

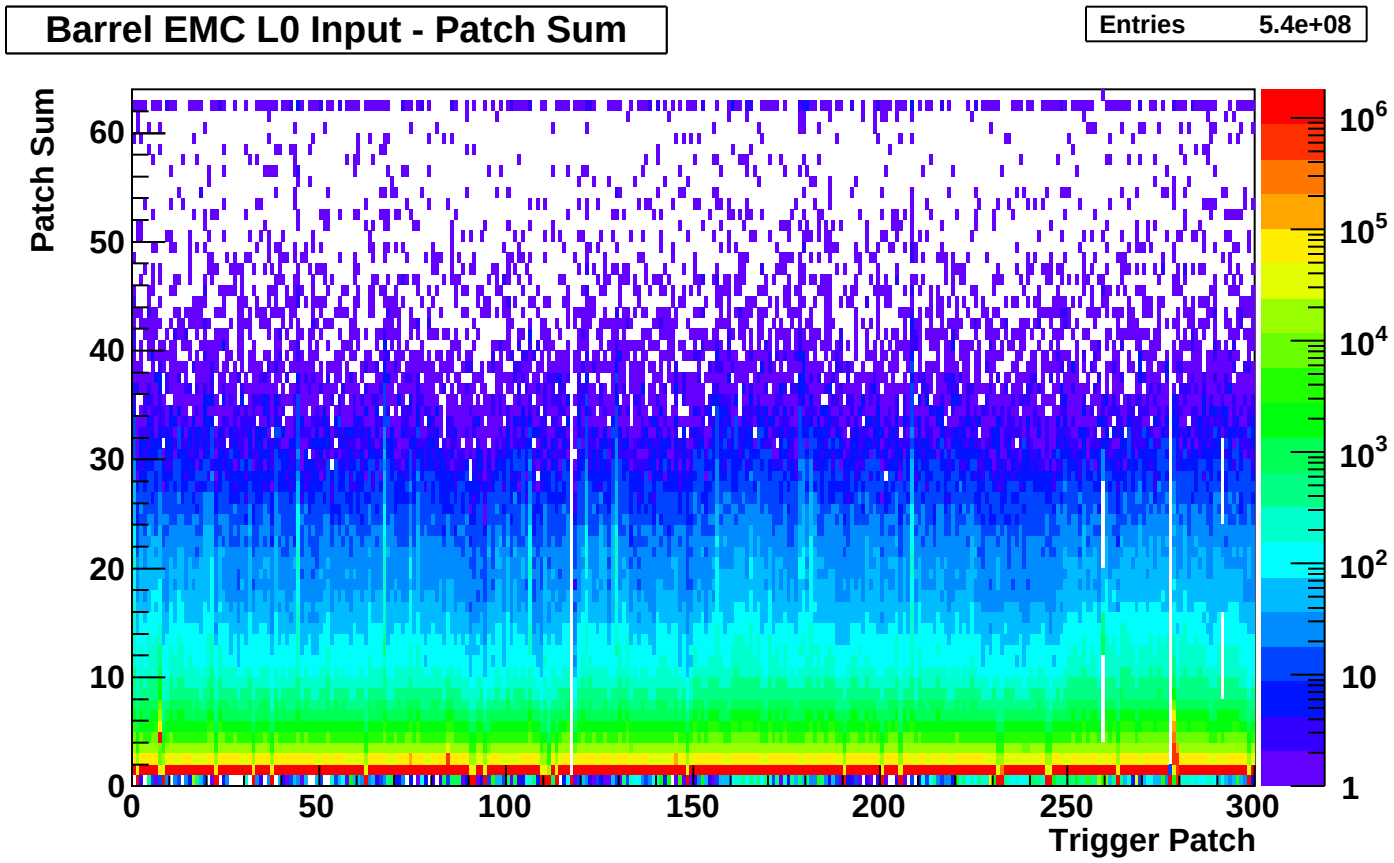

TPC Readout Mask (simulated)
Entries 263091

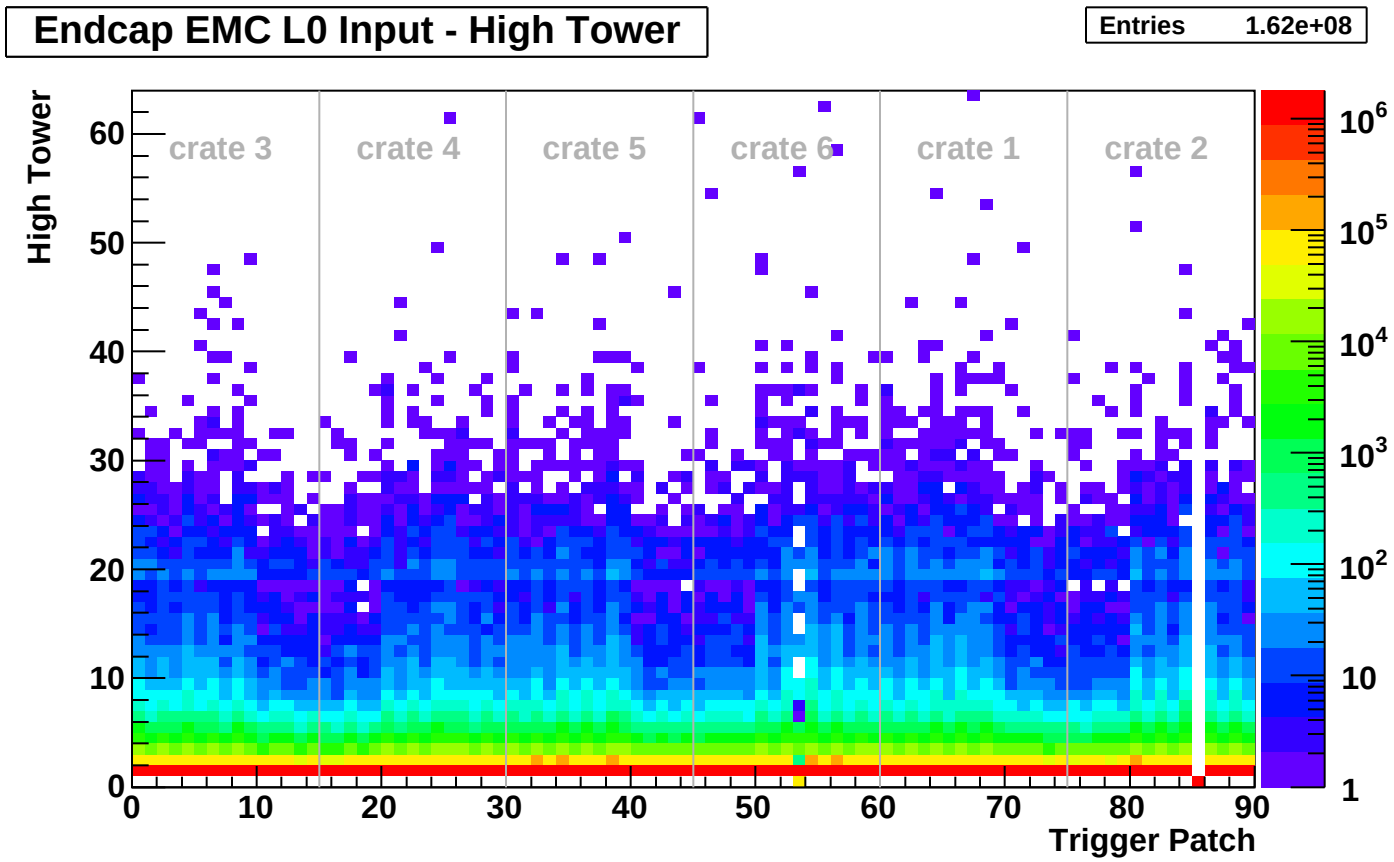

BunchId7Bit (all events)
Entries1800000

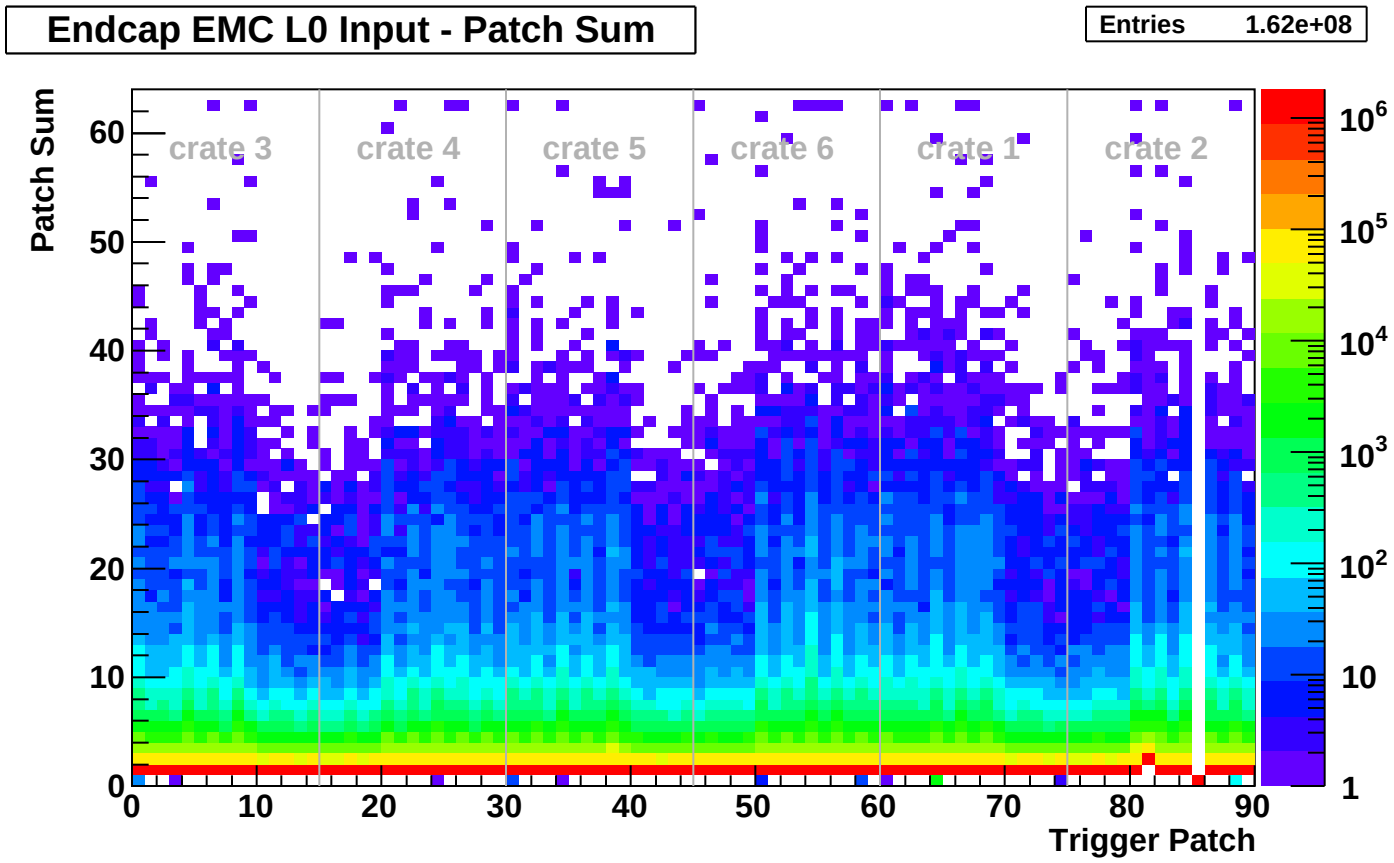

!Blue-Fill
Entries 116094

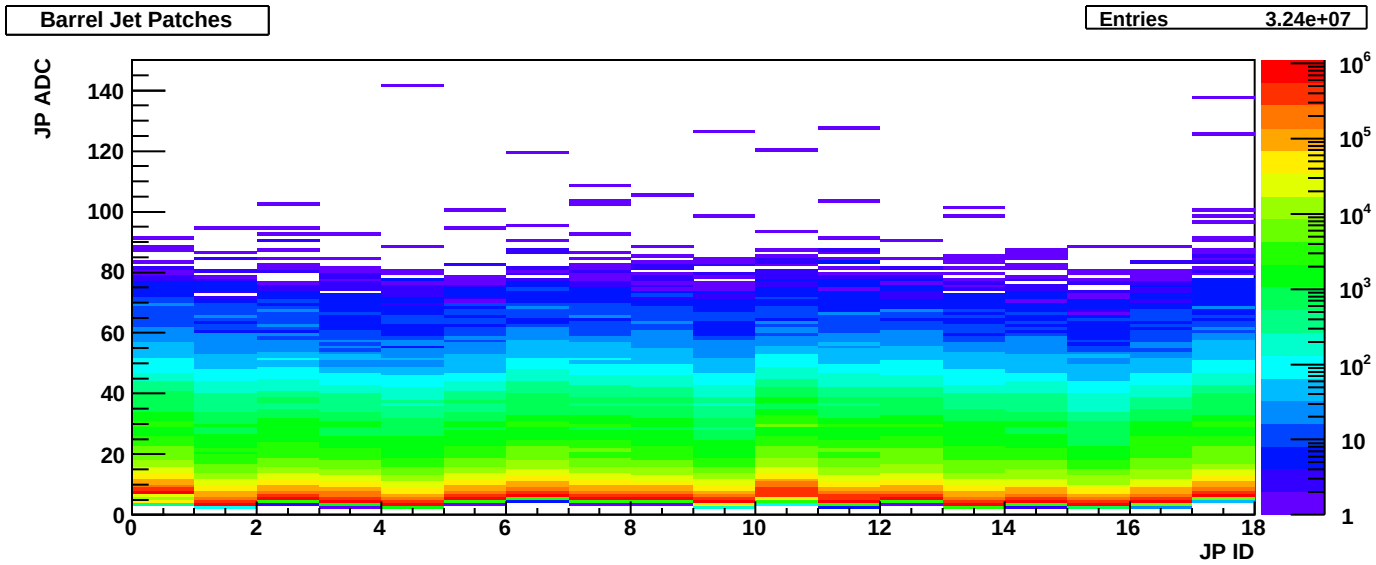

!Yellow-Fill
Entries 152805

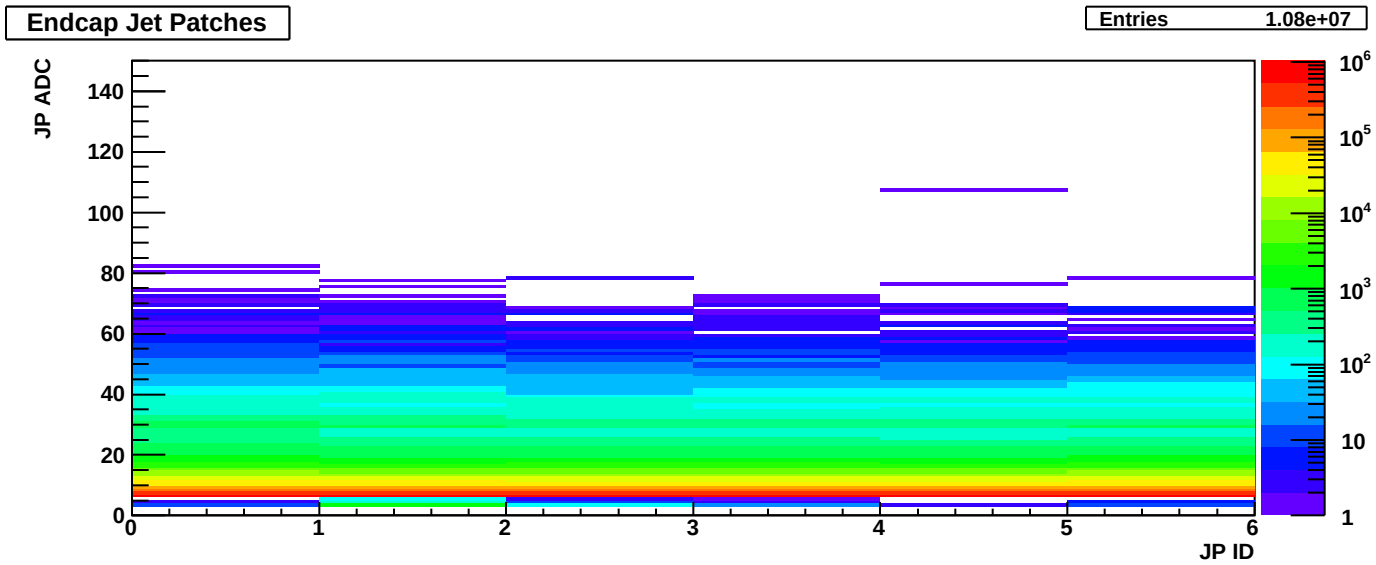

Barrel EMC L0 Input - High Tower
Entries 5.4e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

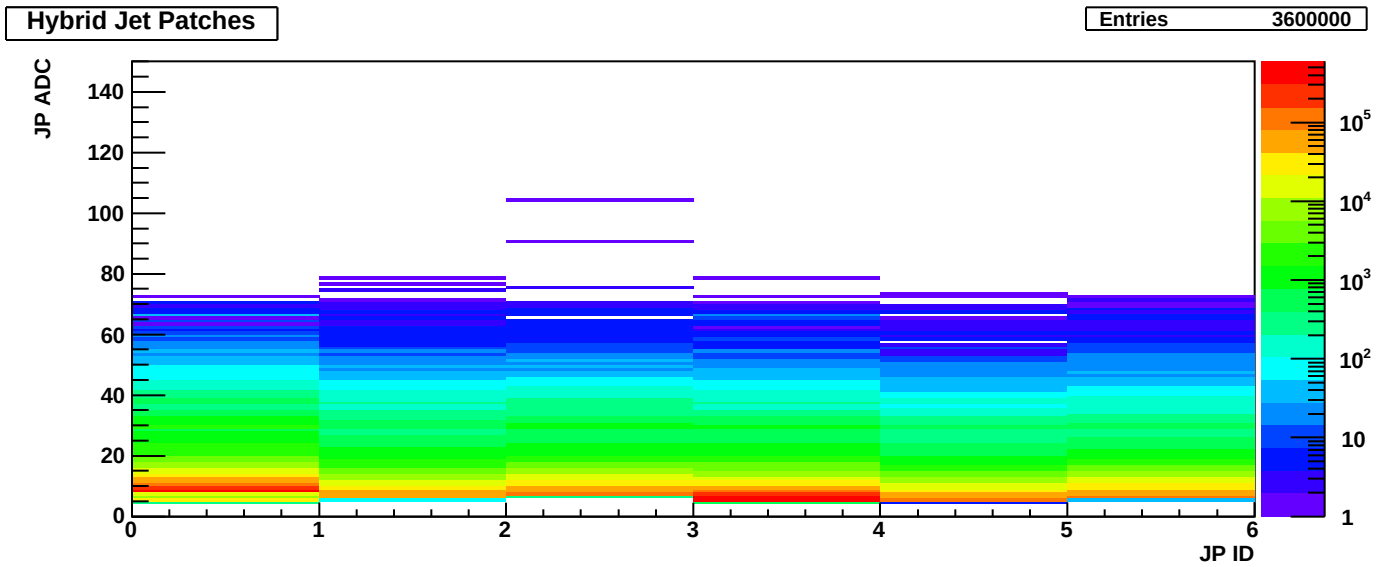

Barrel EMC L0 Input - Patch Sum
Entries 5.4e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

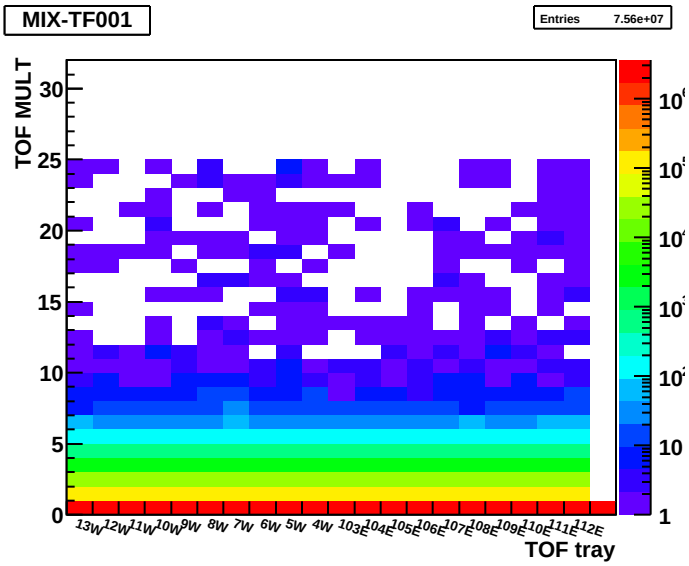

Endcap EMC L0 Input - High Tower
Entries 1.62e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

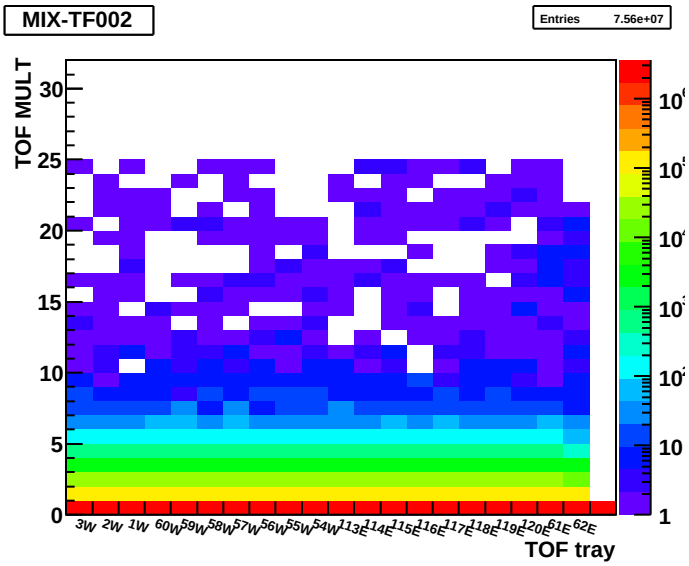

Endcap EMC L0 Input - Patch Sum
Entries 1.62e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

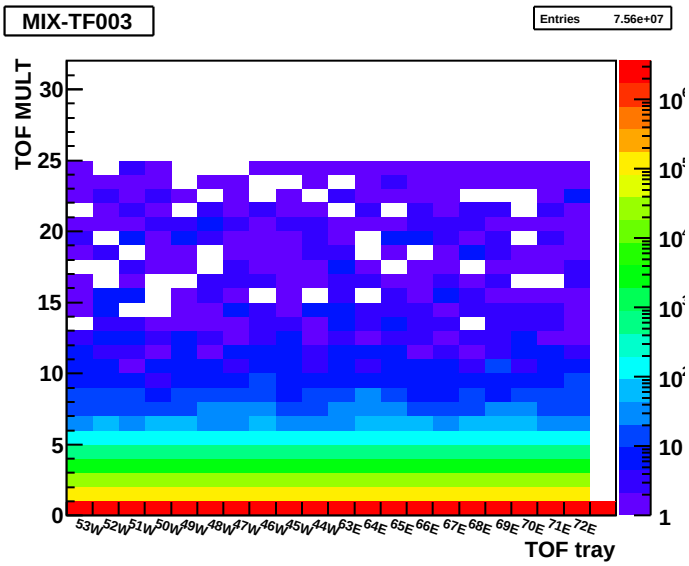

Barrel Jet Patches
Entries 3.24e+07
JP ADC
JP ID

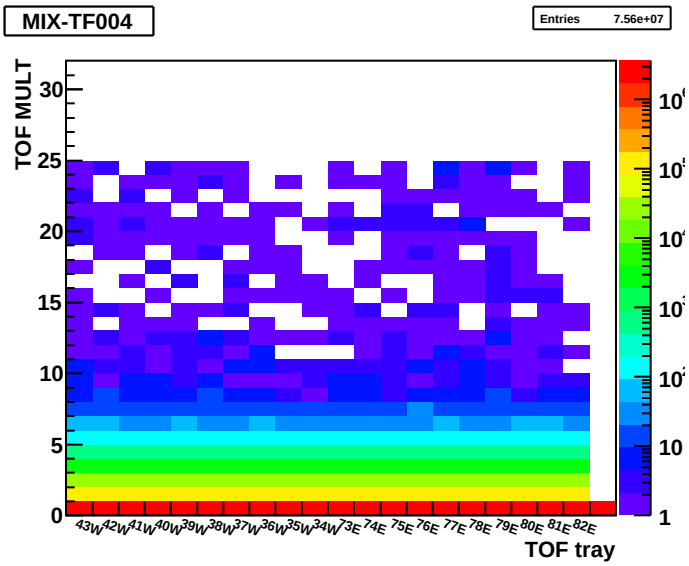

Endcap Jet Patches
Entries 1.08e+07
JP ADC
JP ID

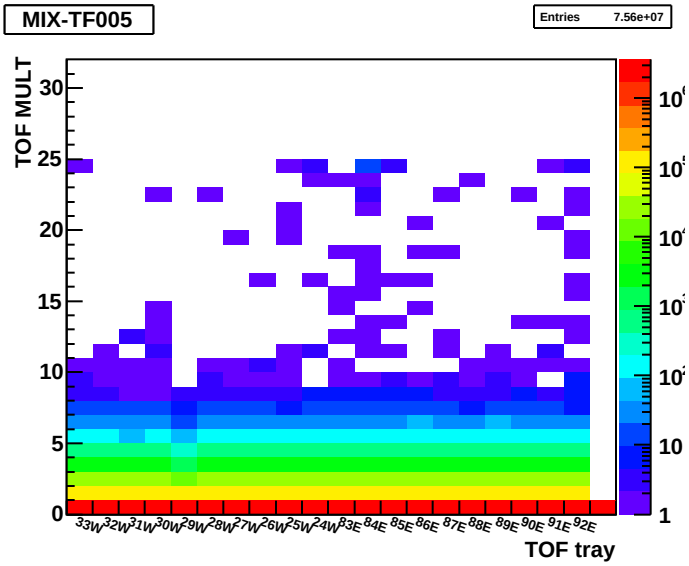

Hybrid Jet Patches
Entries 3600000
JP ADC
JP ID

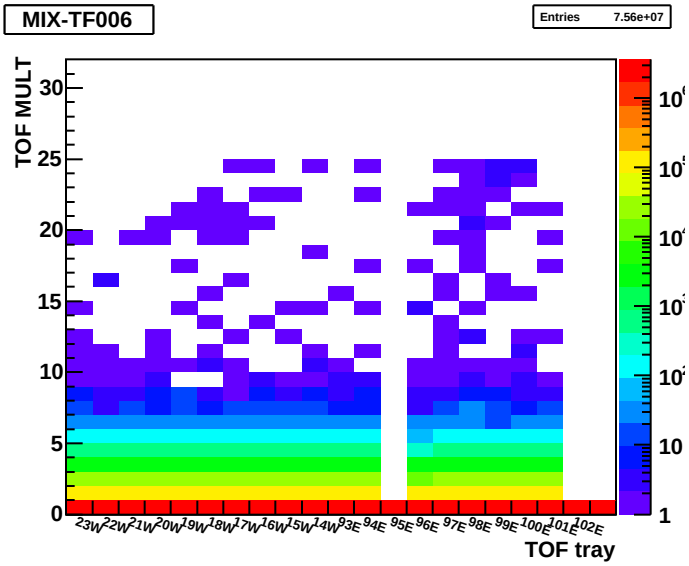

MIX-TF001
Entries 7.56e+07
TOF MULT
TOF tray

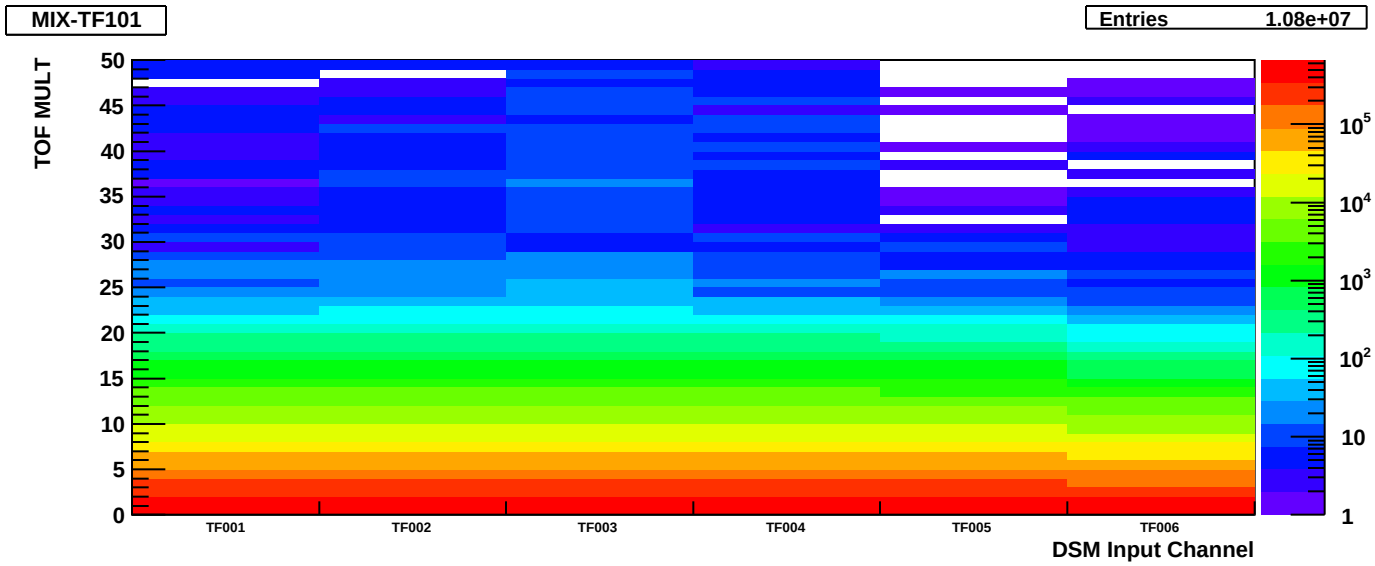

MIX-TF002
Entries 7.56e+07
TOF MULT
TOF tray

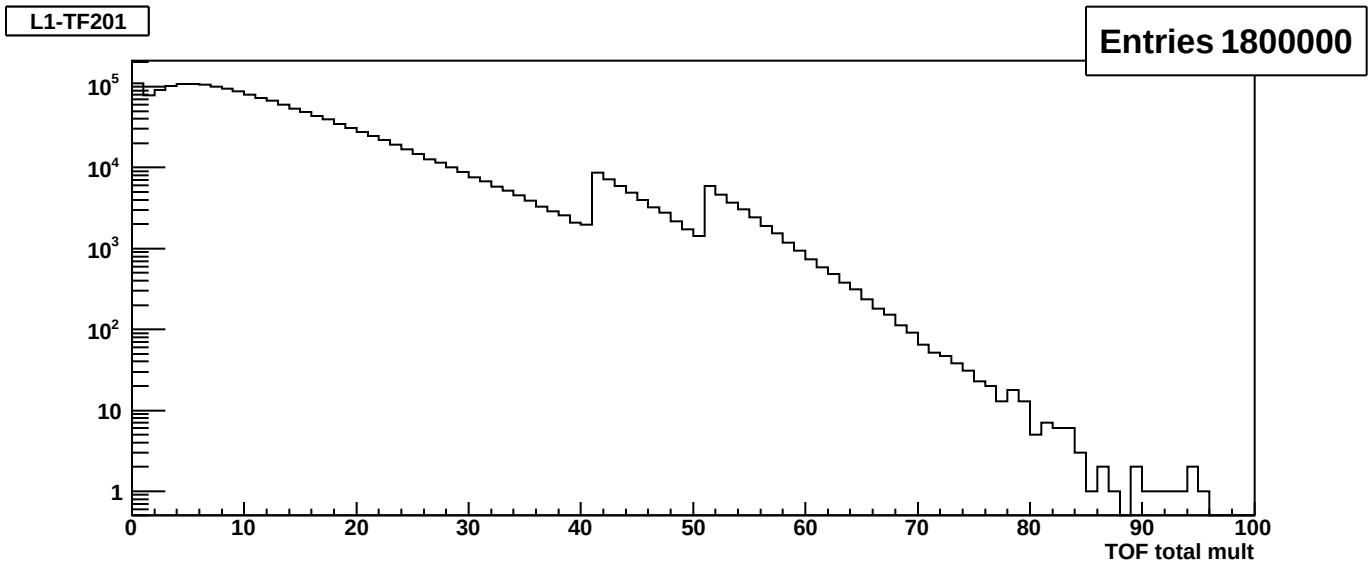

MIX-TF003
Entries 7.56e+07
TOF MULT
TOF tray

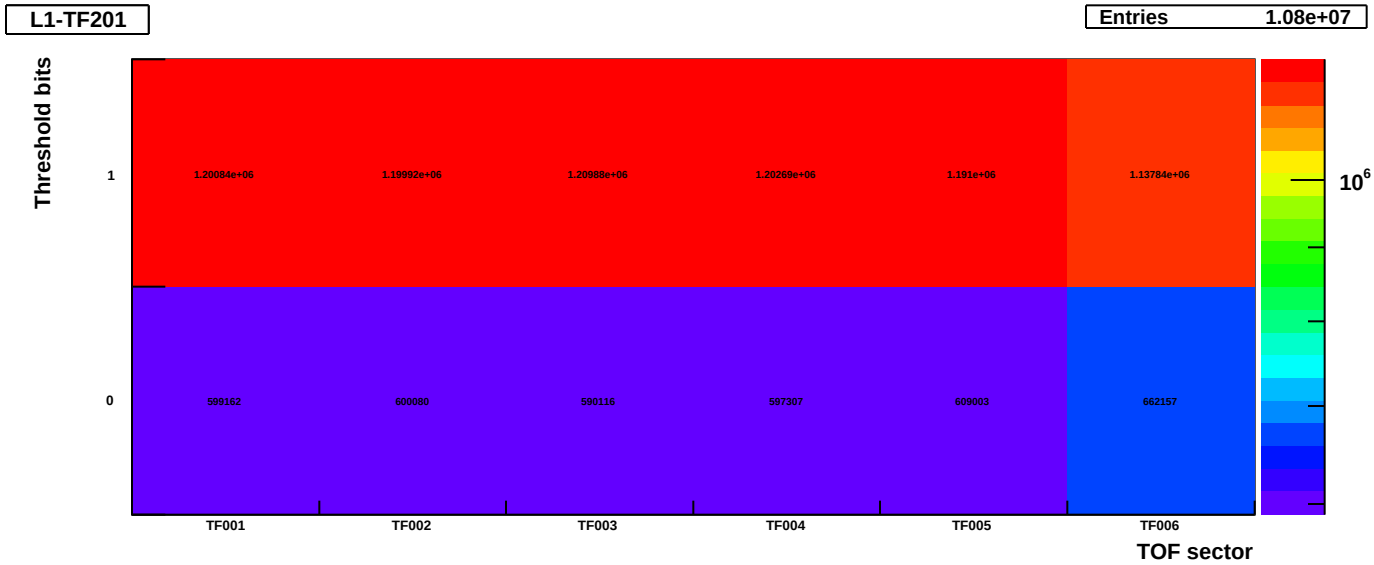

MIX-TF004
Entries 7.56e+07
TOF MULT
TOF tray

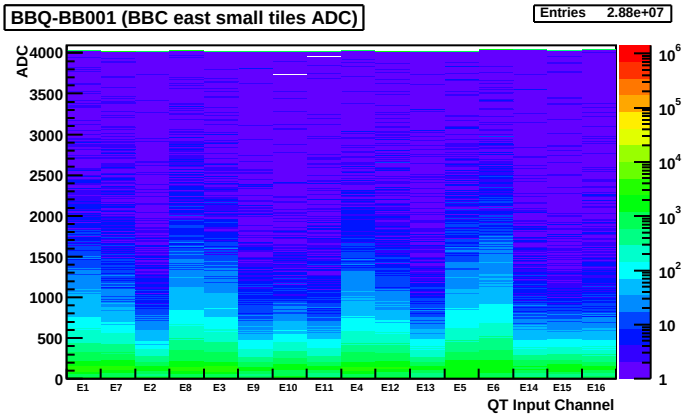

MIX-TF005
Entries 7.56e+07
TOF MULT
TOF tray

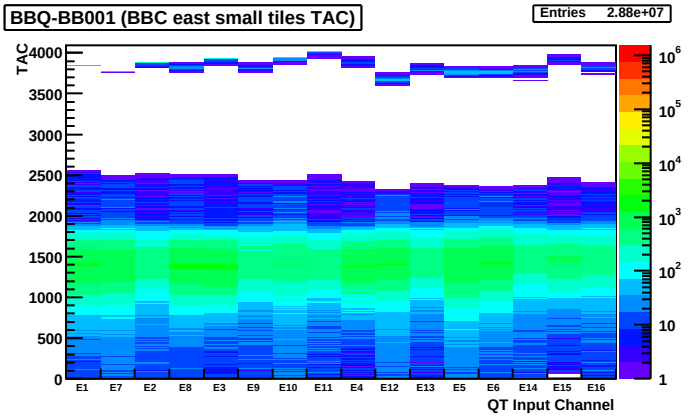

MIX-TF006
Entries 7.56e+07
TOF MULT
TOF tray

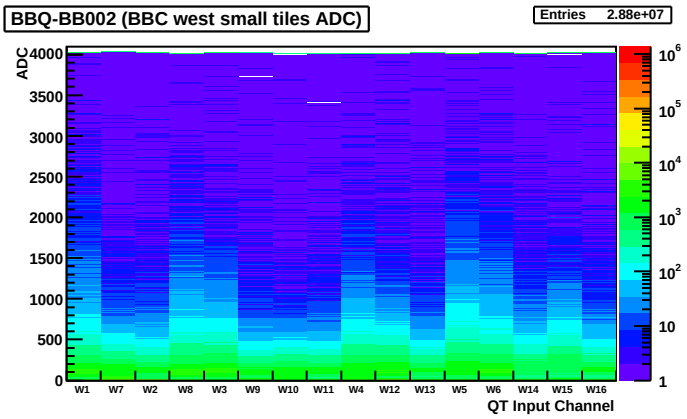

MIX-TF101
Entries 1.08e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

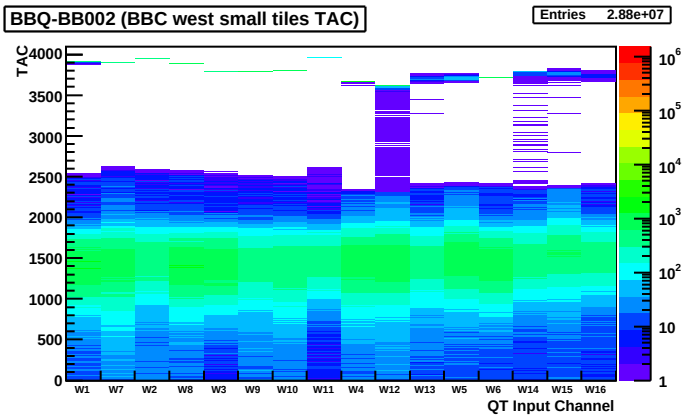

L1-TF201
Entries 1800000
TOF total mult

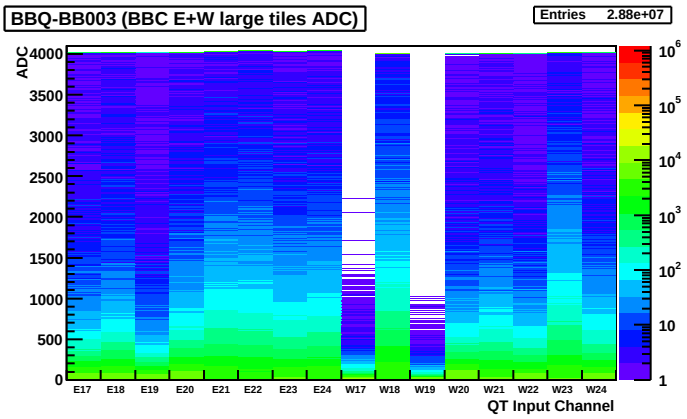

L1-TF201
Entries 1.08e+07
Threshold bits
1.20084e+06
1.19992e+06
1.20988e+06
1.20269e+06
1.191e+06
1.13784e+06
599162
600080
590116
597307
609003
662157
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

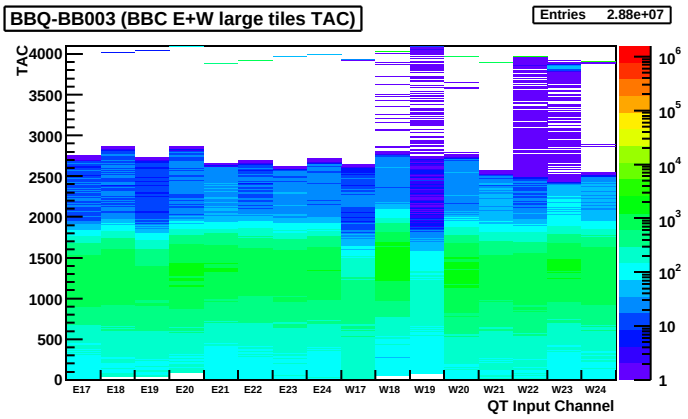

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

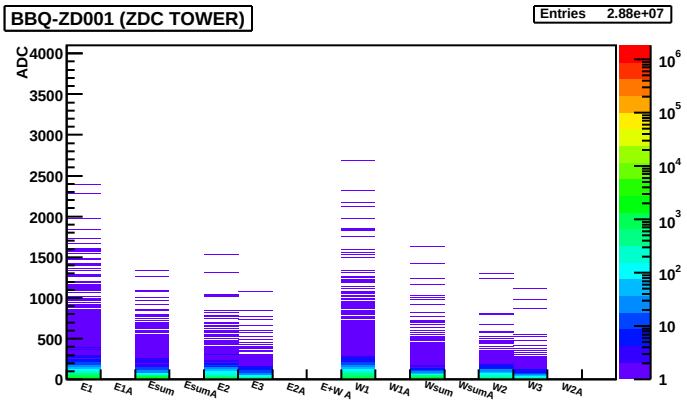

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

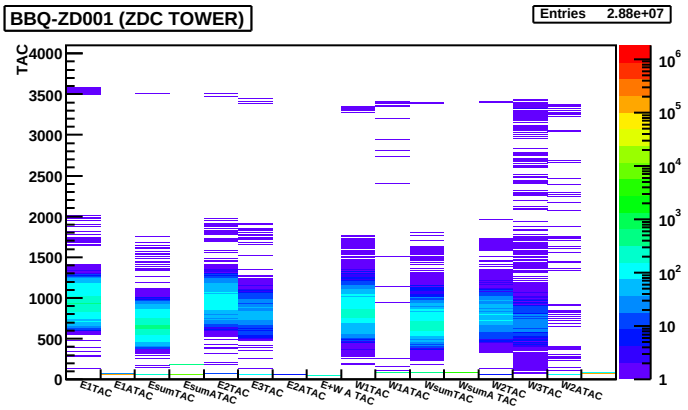

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

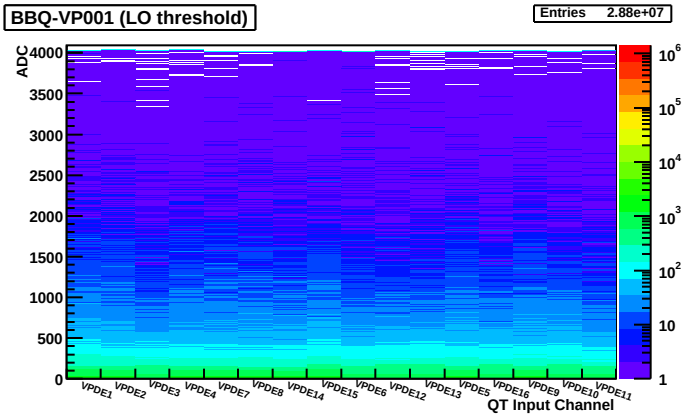

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

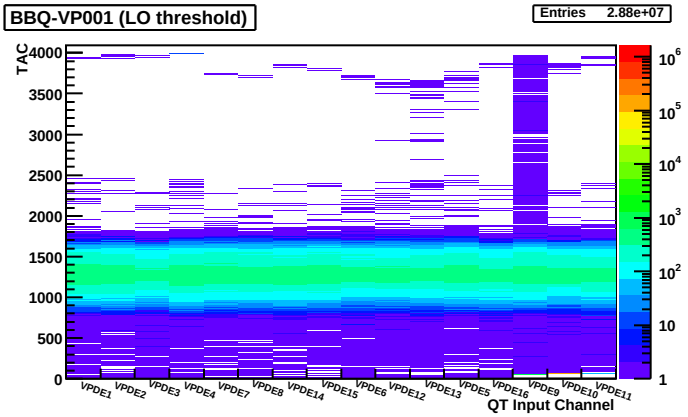

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

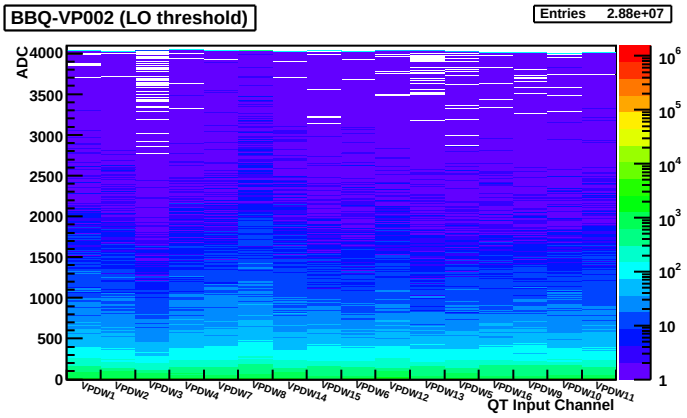

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

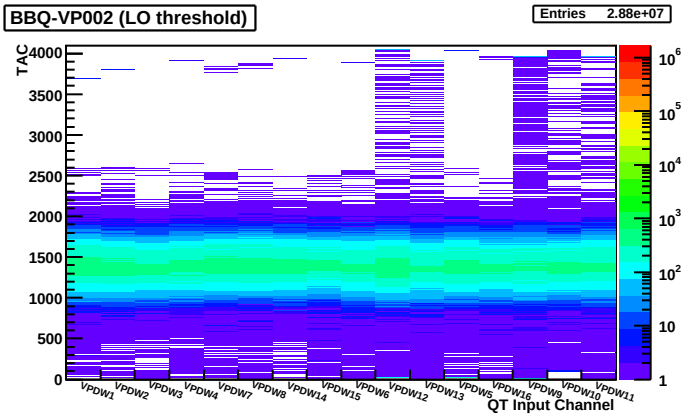

BBQ-ZD001 (ZDC TOWER)
Entries 2.88e+07
ADC

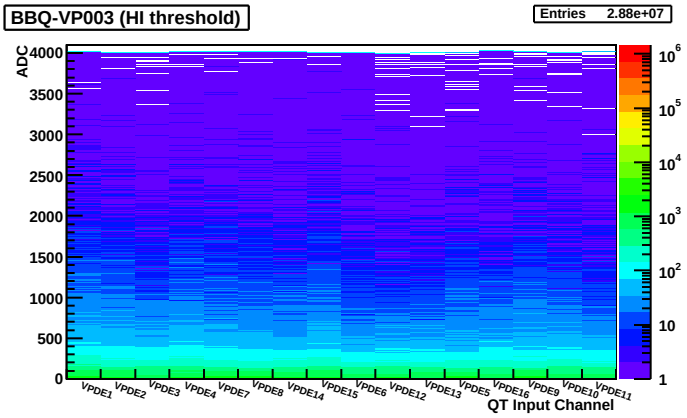

BBQ-ZD001 (ZDC TOWER)
Entries 2.88e+07
TAC

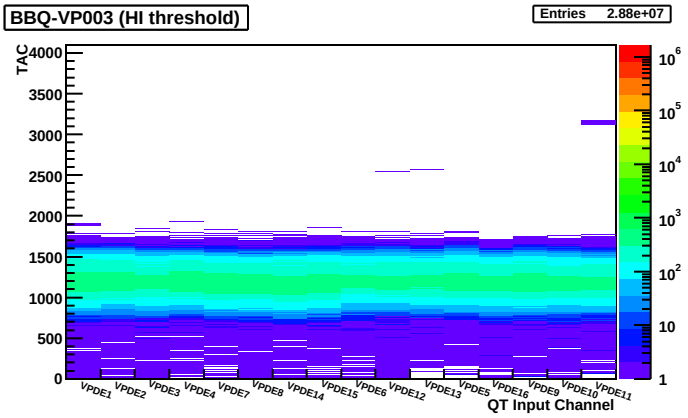

BBQ-VP001 (LO threshold)
Entries 2.88e+07
ADC
QT Input Channel

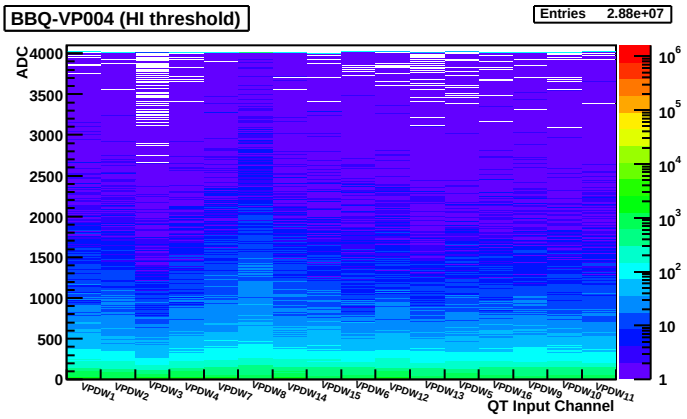

BBQ-VP001 (LO threshold)
Entries 2.88e+07
TAC
QT Input Channel

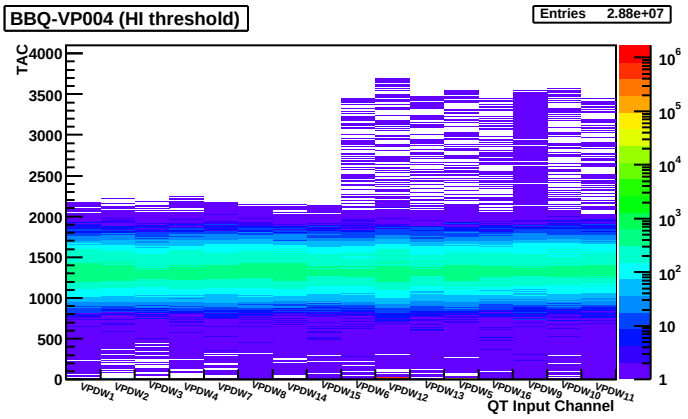

BBQ-VP002 (LO threshold)
Entries 2.88e+07
ADC
QT Input Channel

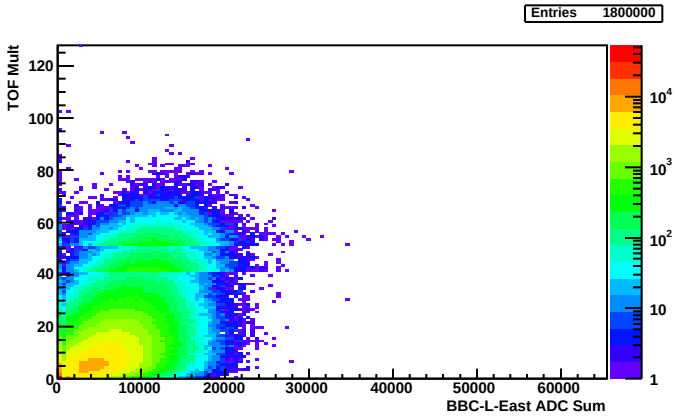

BBQ-VP002 (LO threshold)
Entries 2.88e+07
TAC
QT Input Channel

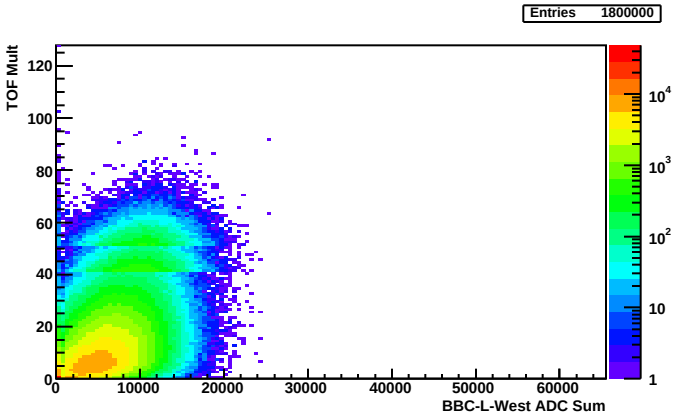

BBQ-VP003 (HI threshold)
Entries 2.88e+07
ADC
QT Input Channel

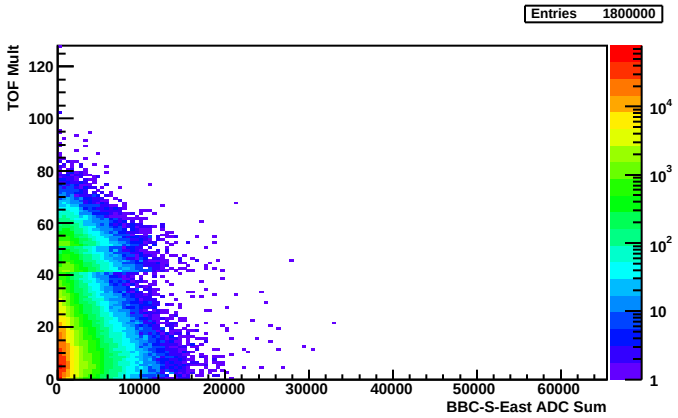

BBQ-VP003 (HI threshold)
Entries 2.88e+07
TAC
QT Input Channel

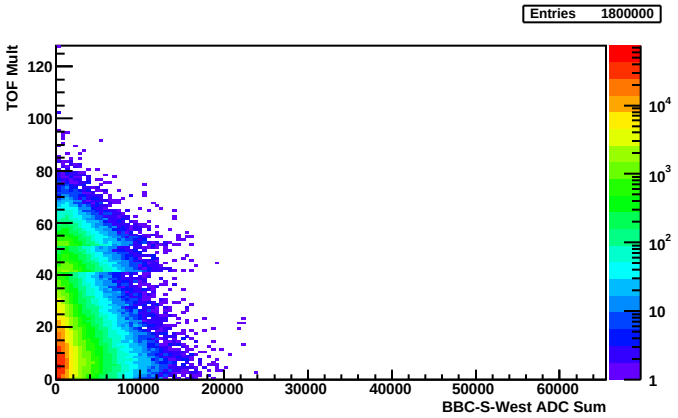

BBQ-VP004 (HI threshold)
Entries 2.88e+07
ADC
QT Input Channel

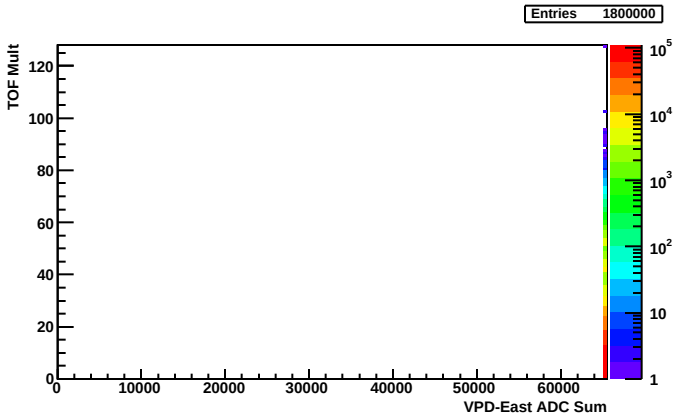

BBQ-VP004 (HI threshold)
Entries 2.88e+07
TAC
QT Input Channel

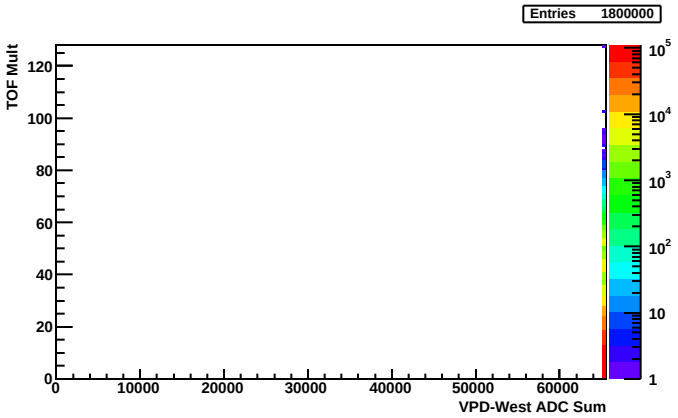

Entries 1800000
TOF Mult
BBC-L-East ADC Sum

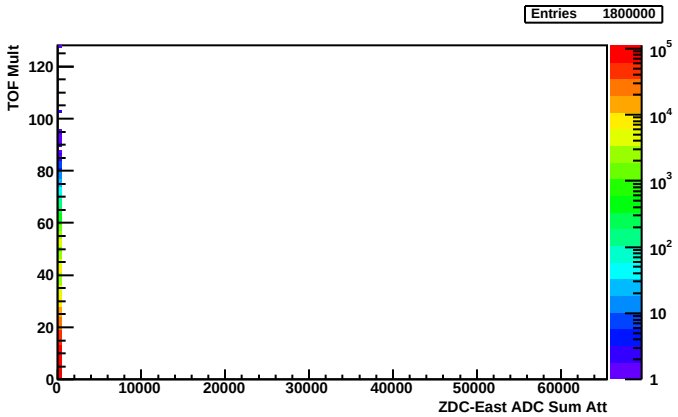

Entries 1800000
TOF Mult
BBC-L-West ADC Sum

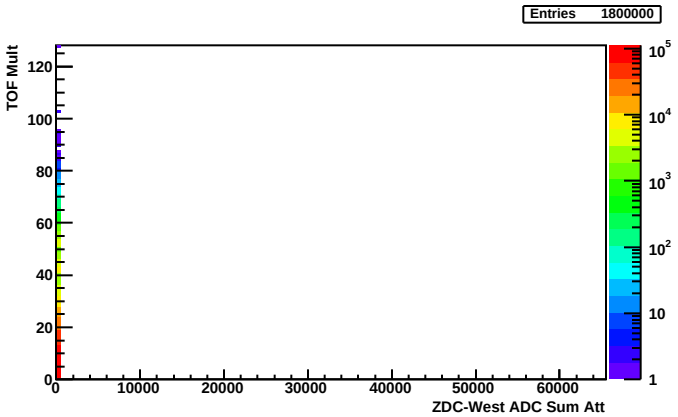

Entries 1800000
TOF Mult
BBC-S-East ADC Sum

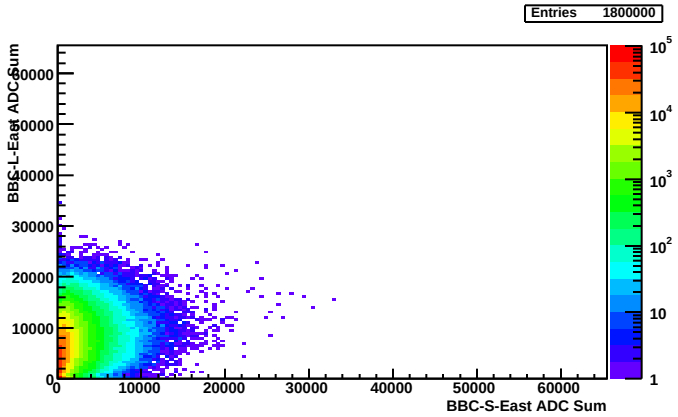

Entries 1800000
TOF Mult
BBC-S-West ADC Sum

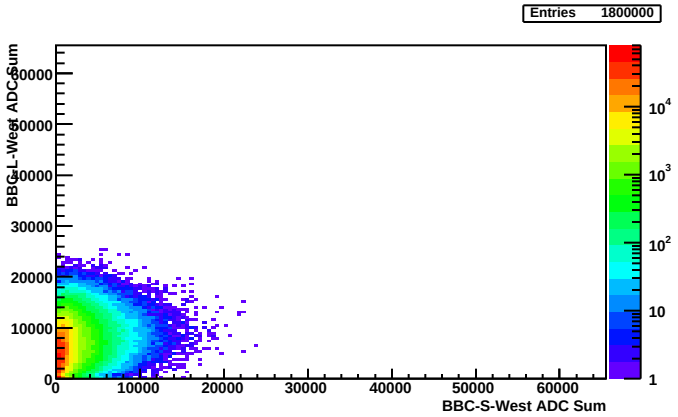

Entries 1800000
TOF Mult
VPD-East ADC Sum

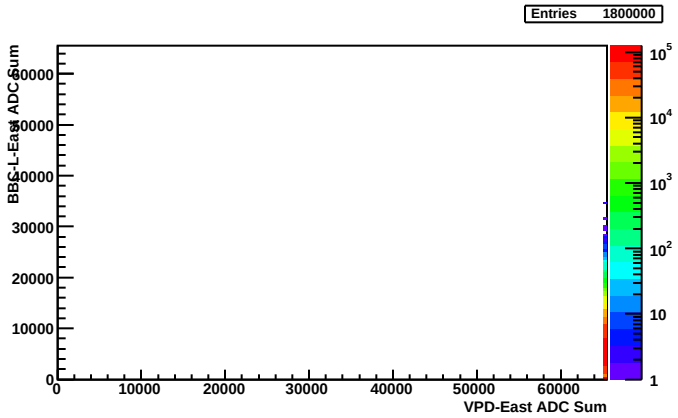

Entries 1800000
TOF Mult
VPD-West ADC Sum

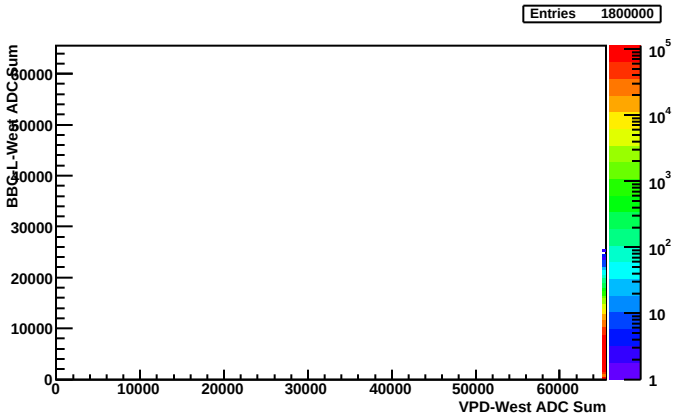

Entries 1800000
TOF Mult
ZDC-East ADC Sum Att

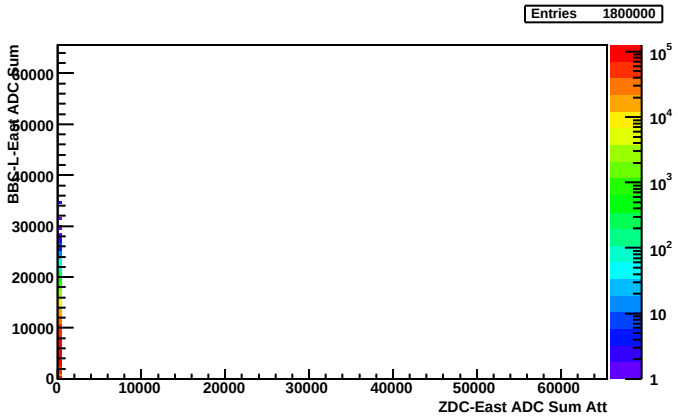

Entries 1800000
TOF Mult
ZDC-West ADC Sum Att

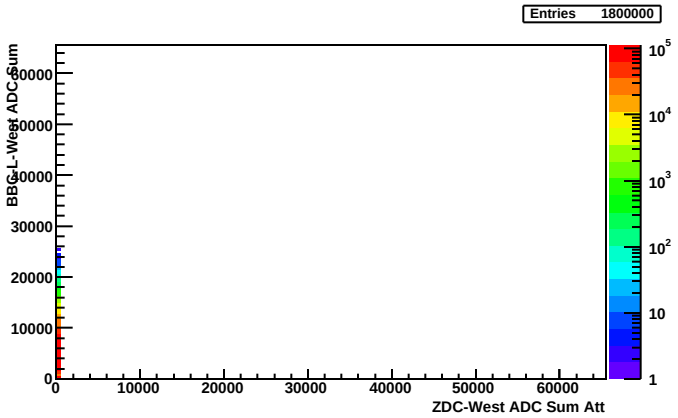

Entries 1800000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

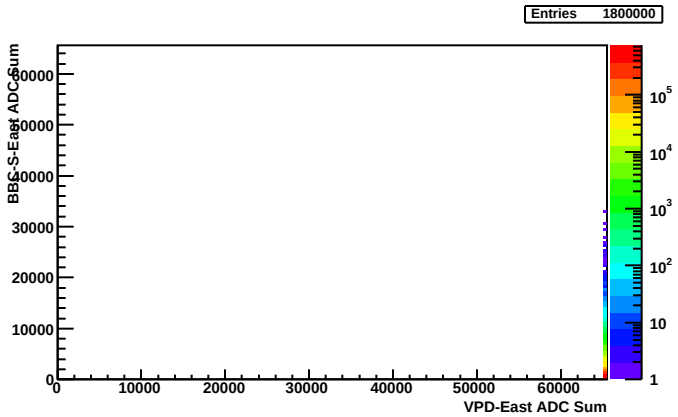

Entries 1800000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

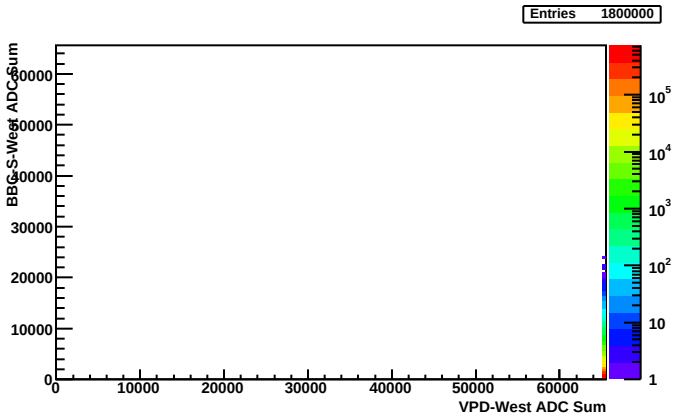

Entries 1800000
BBC-L-East ADC Sum
VPD-East ADC Sum

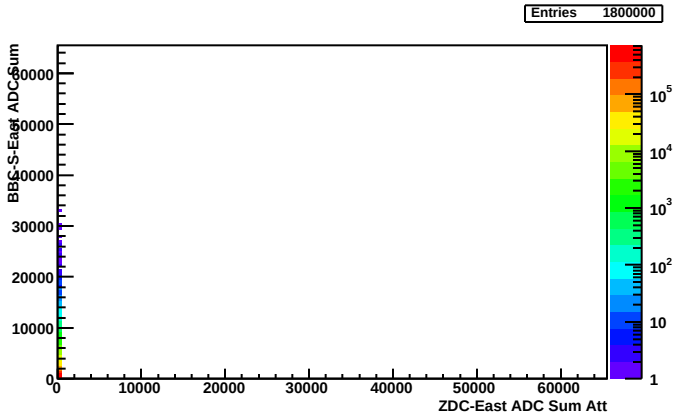

Entries 1800000
BBC-L-West ADC Sum
VPD-West ADC Sum

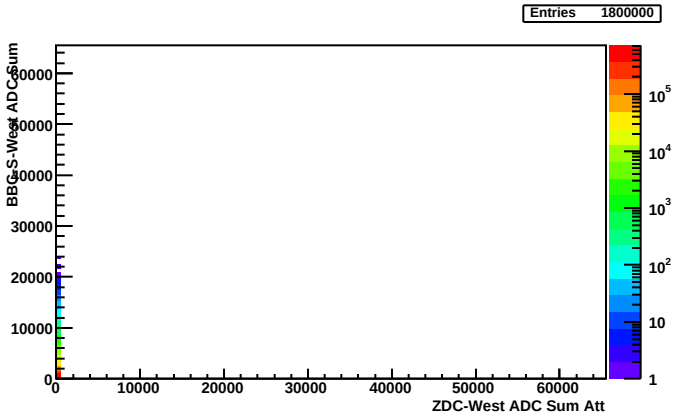

Entries 1800000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

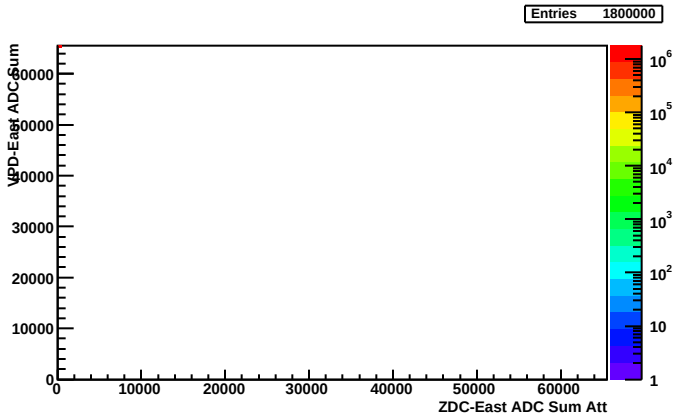

Entries 1800000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

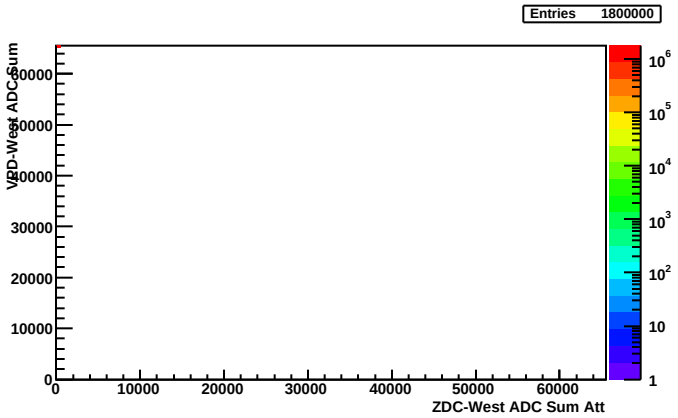

Entries 1800000
BBC-S-East ADC Sum
VPD-East ADC Sum

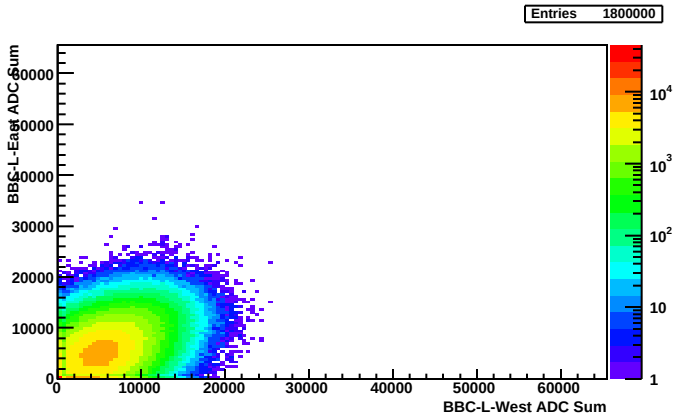

Entries 1800000
BBC-S-West ADC Sum
VPD-West ADC Sum

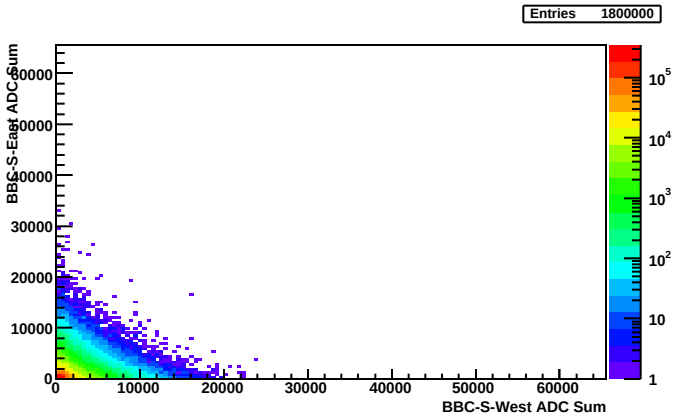

Entries 1800000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

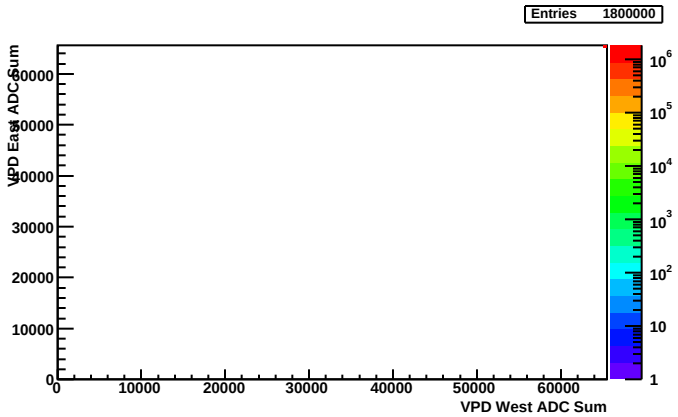

Entries 1800000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

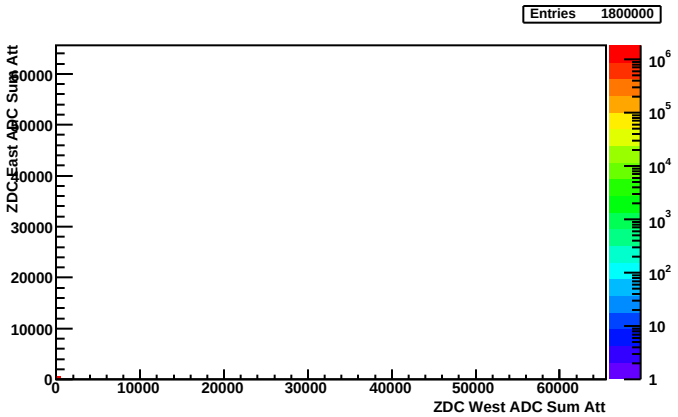

Entries 1800000
VPD-East ADC Sum
ZDC-East ADC Sum Att

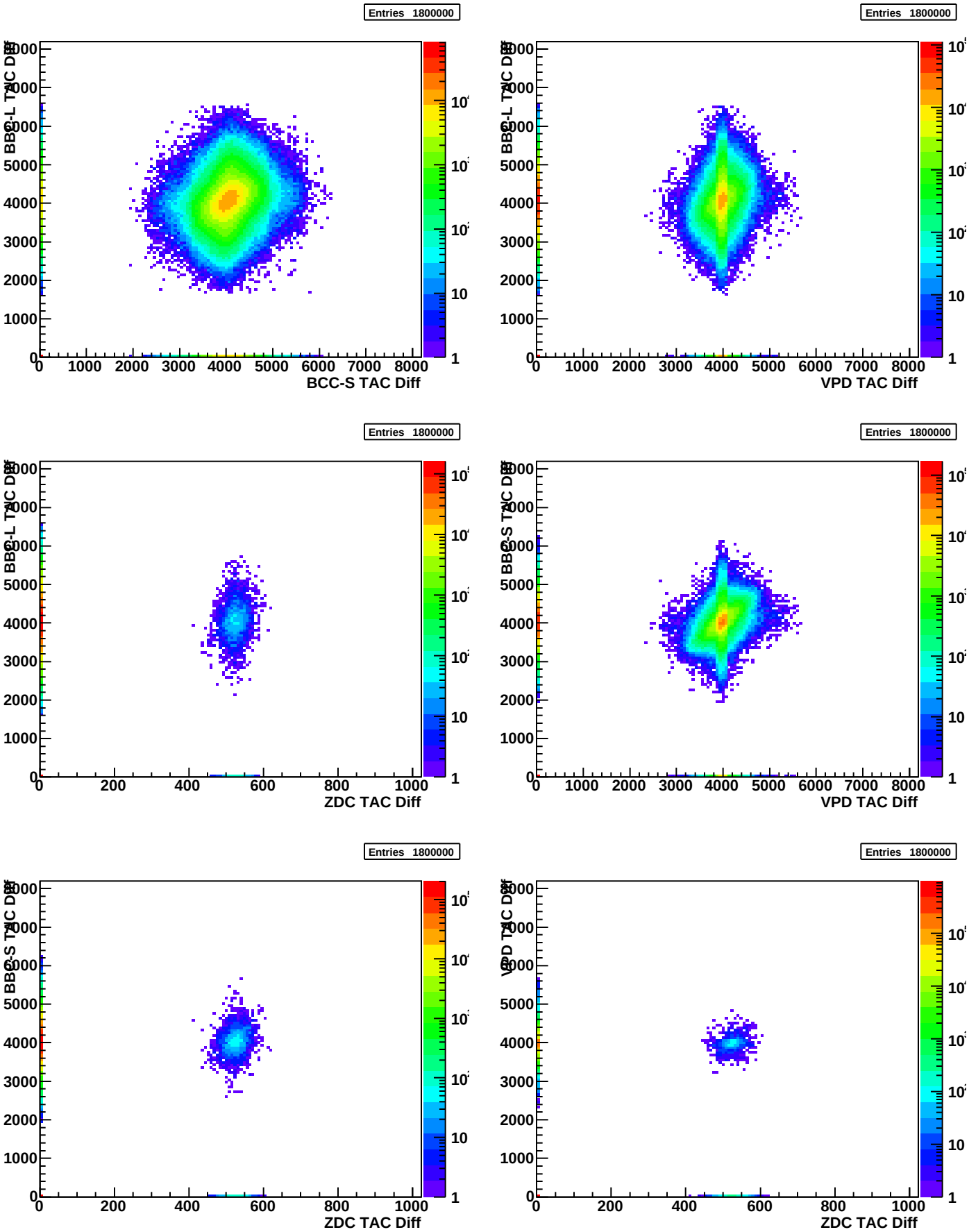

Entries 1800000
VPD-West ADC Sum
ZDC-West ADC Sum Att

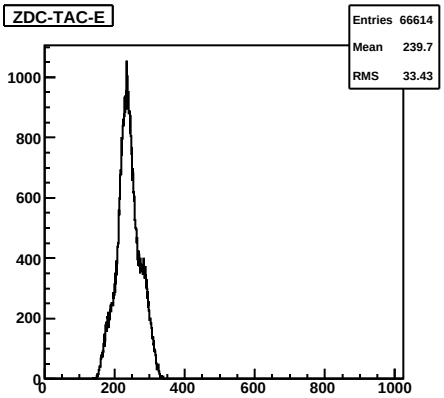

Entries 1800000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

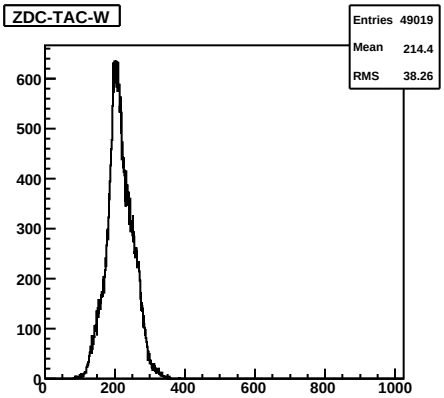

Entries 1800000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

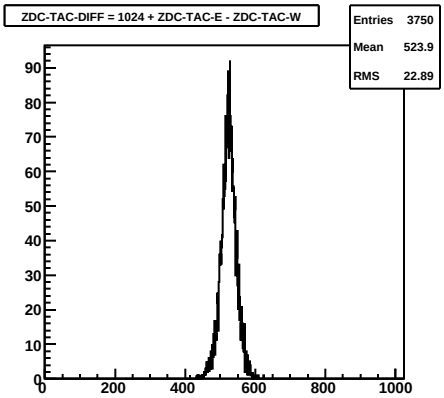

Entries 1800000
VPD East ADC Sum
VPD West ADC Sum

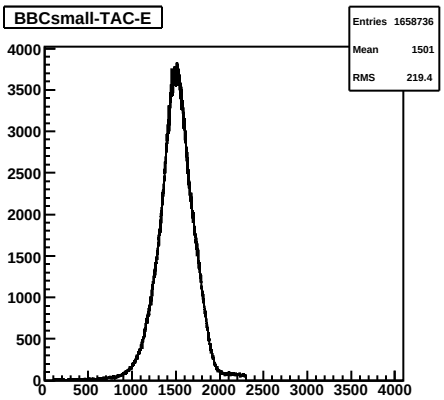

Entries 1800000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

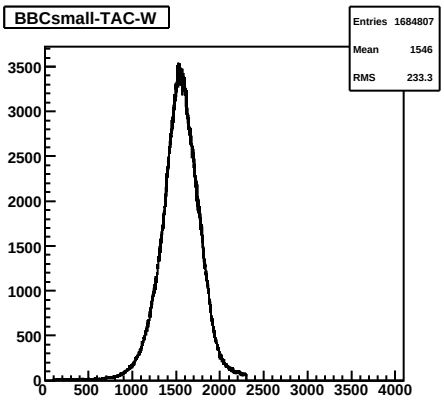

Entries 1800000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1800000
BBC-L TAC Diff
VPD TAC Diff
Entries 1800000
BBC-L TAC Diff
ZDC TAC Diff
Entries 1800000
BBC-S TAC Diff
VPD TAC Diff
Entries 1800000
BBC-S TAC Diff
ZDC TAC Diff
Entries 1800000
VPD TAC Diff
ZDC TAC Diff

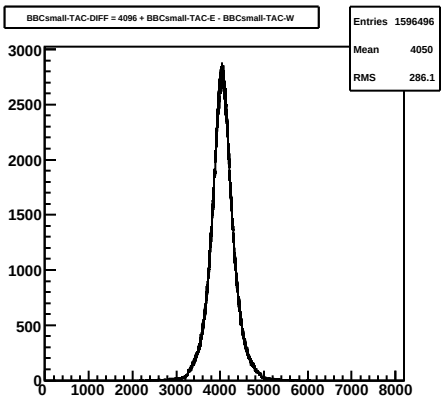

ZDC-TAC-E
Entries 66614
Mean 239.7
RMS 33.43

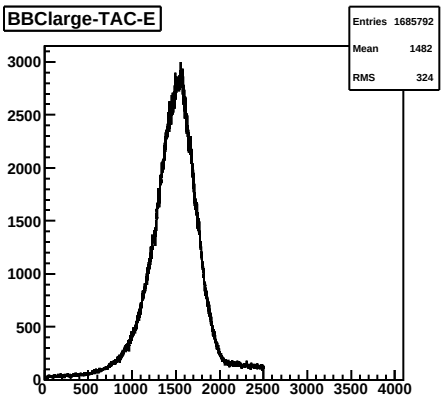

ZDC-TAC-W
Entries 49019
Mean 214.4
RMS 38.26

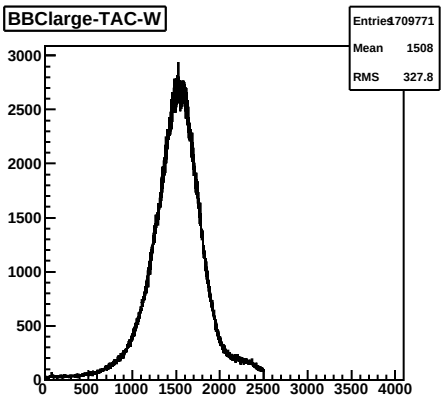

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3750
Mean 523.9
RMS 22.89

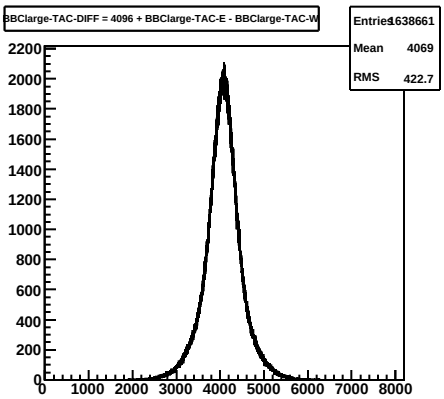

BBCsmall-TAC-E
Entries 1658736
Mean 1501
RMS 219.4

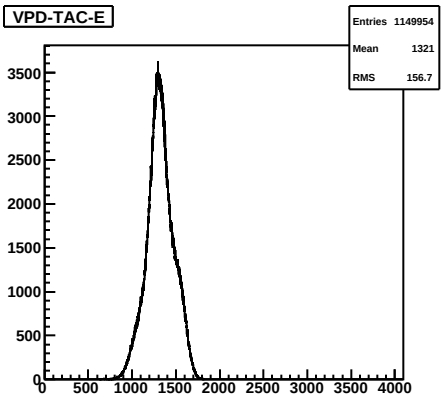

BBCsmall-TAC-W
Entries 1684807
Mean 1546
RMS 233.3

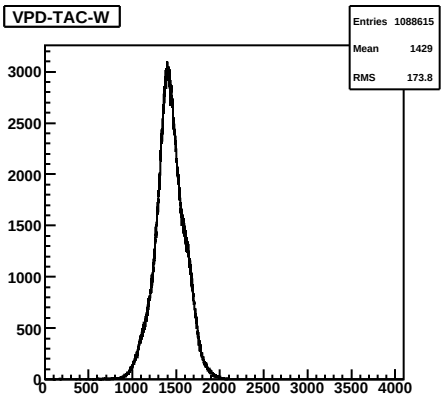

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1596496
Mean 4050
RMS 286.1

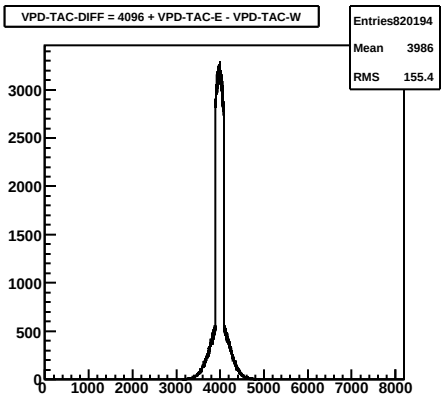

BBClarge-TAC-E
Entries 1685792
Mean 1482
RMS 324

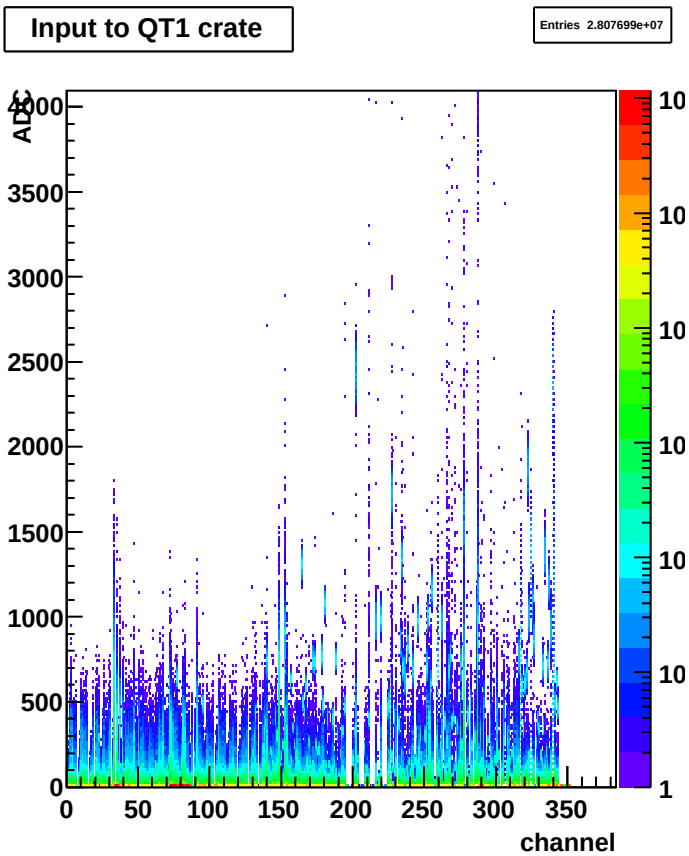

BBClarge-TAC-W
Entries 1709771
Mean 1508
RMS 327.8

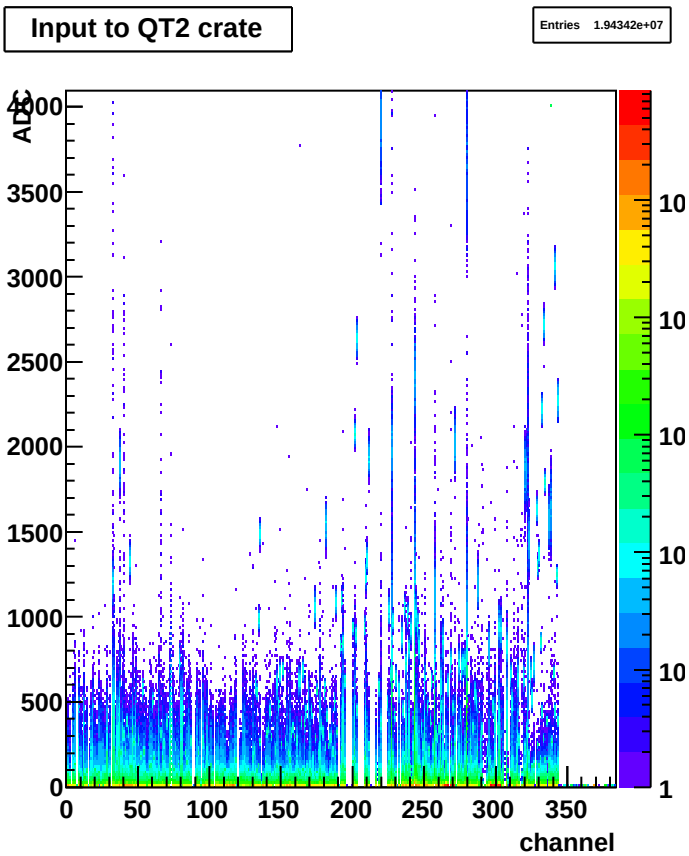

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1638661
Mean 4069
RMS 422.7

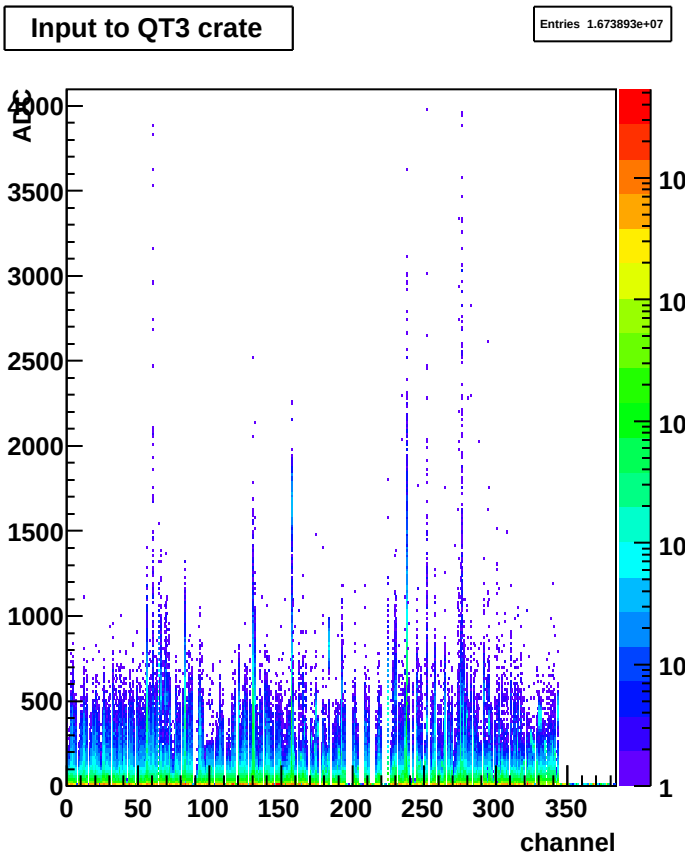

VPD-TAC-E
Entries 1149954
Mean 1321
RMS 156.7

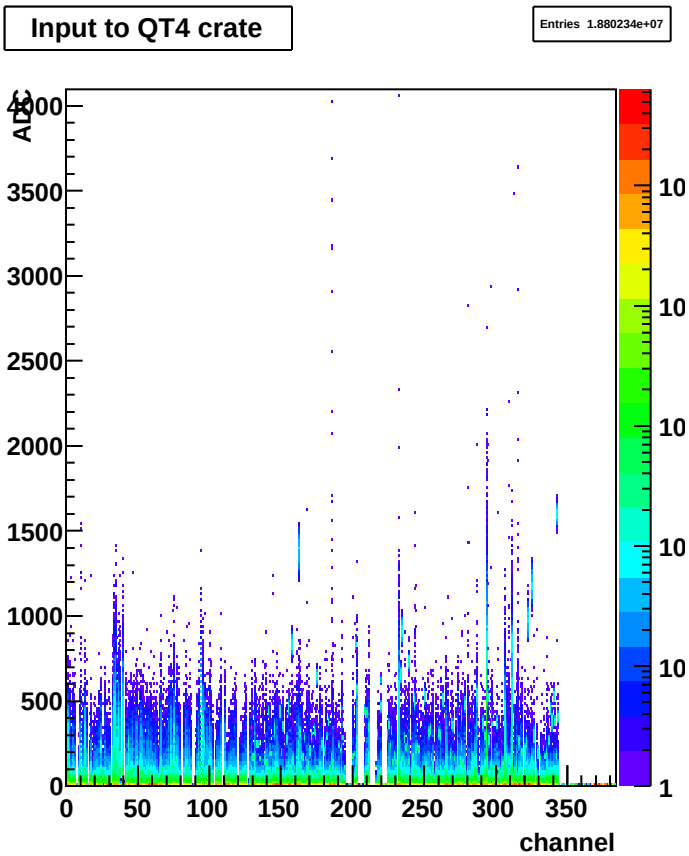

VPD-TAC-W
Entries 1088615
Mean 1429
RMS 173.8

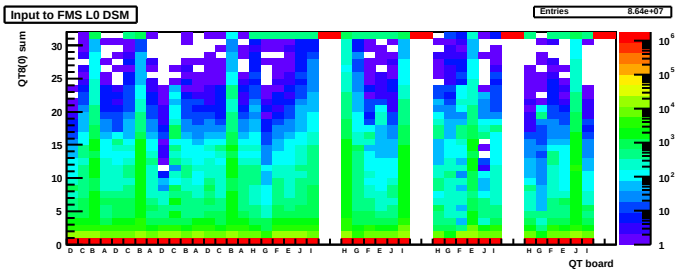

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 820194
Mean 3986
RMS 155.4

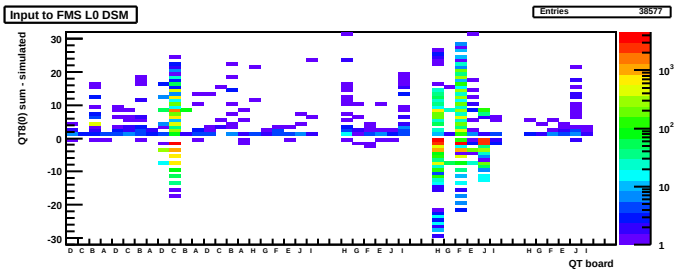

Input to QT1 crate
Entries 2.807699e+07
ADC
channel

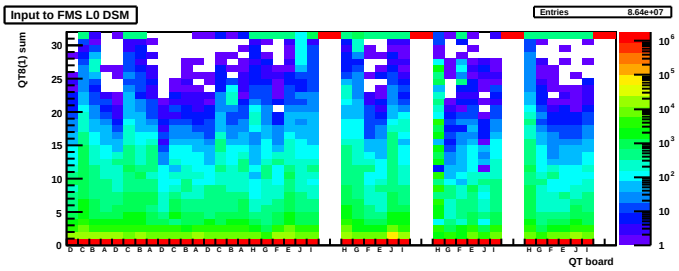

Input to QT2 crate
Entries 1.94342e+07
ADC
channel

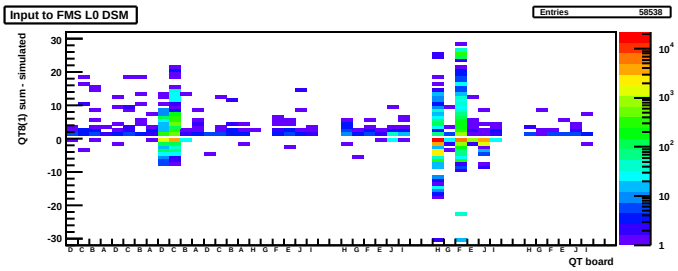

Input to QT3 crate
Entries 1.673893e+07
ADC
channel

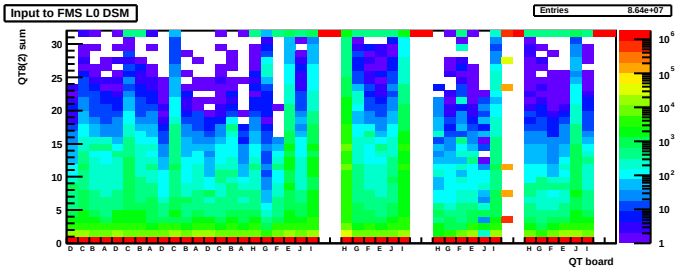

Input to QT4 crate
Entries 1.880234e+07
ADC
channel

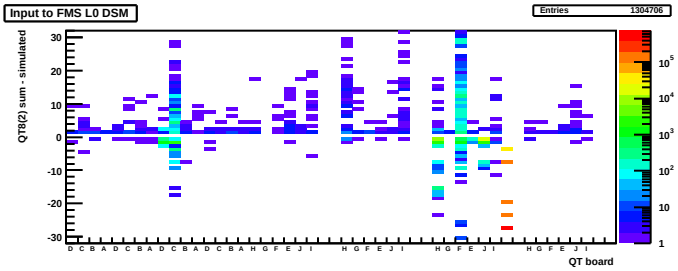

Input to FMS L0 DSM
Entries 8.64e+07
QT8(0) sum
QT board

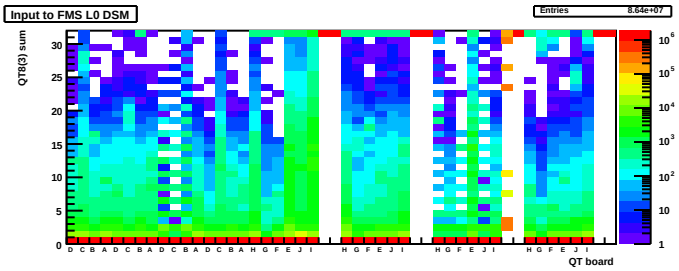

Input to FMS L0 DSM
Entries 38577
QT8(0) sum - simulated
QT board

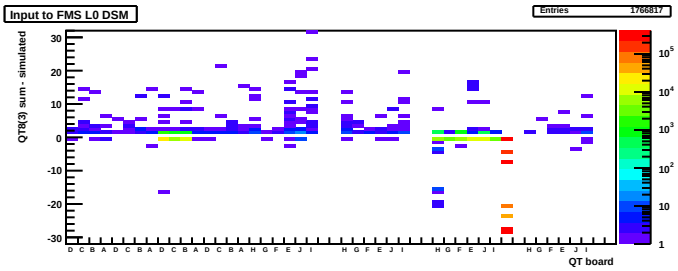

Input to FMS L0 DSM
Entries 8.64e+07
QT8(1) sum
QT board

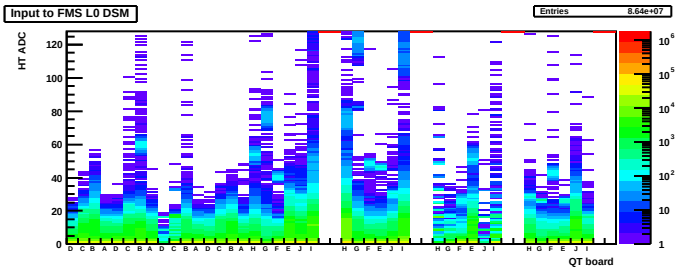

Input to FMS L0 DSM
Entries 58538
QT8(1) sum - simulated
QT board

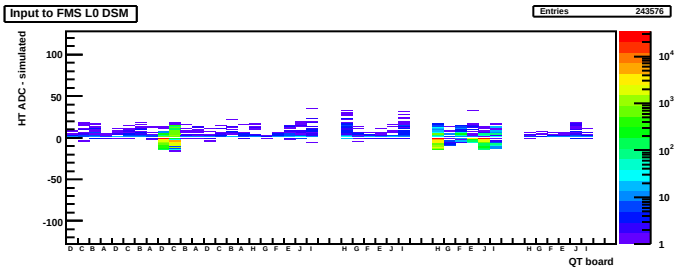

Input to FMS L0 DSM
Entries 8.64e+07
QT8(2) sum
QT board

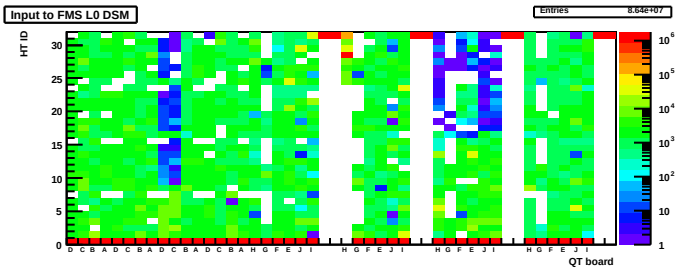

Input to FMS L0 DSM
Entries 1304706
QT8(2) sum - simulated
QT board

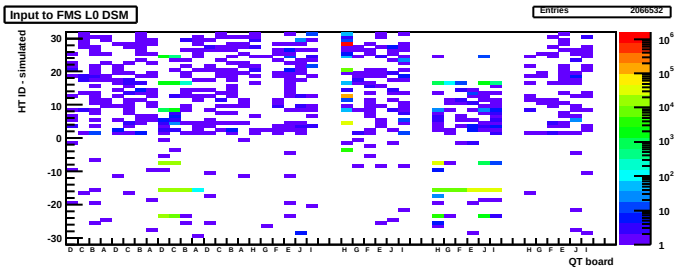

Input to FMS L0 DSM
Entries 8.64e+07
QT8(3) sum
QT board

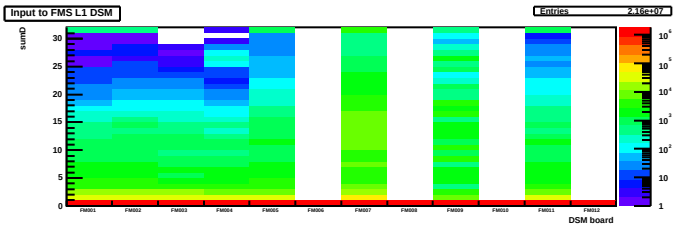

Input to FMS L0 DSM
Entries 1766817
QT8(3) sum - simulated
QT board

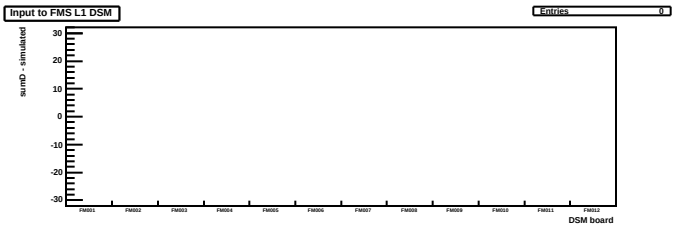

Input to FMS L0 DSM
Entries 8.64e+07
HT ADC
QT board

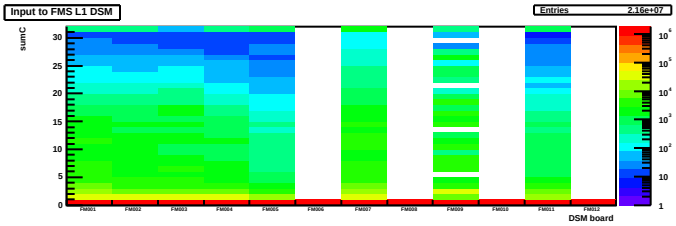

Input to FMS L0 DSM
Entries 243576
HT ADC - simulated
QT board

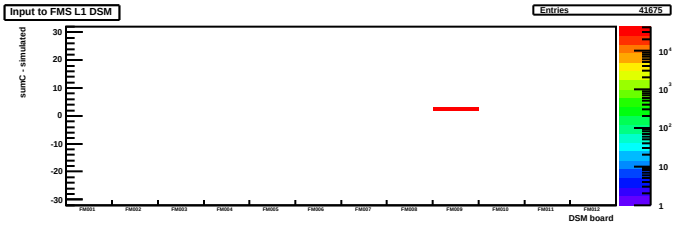

Input to FMS L0 DSM
Entries 8.64e+07
HT ID
QT board

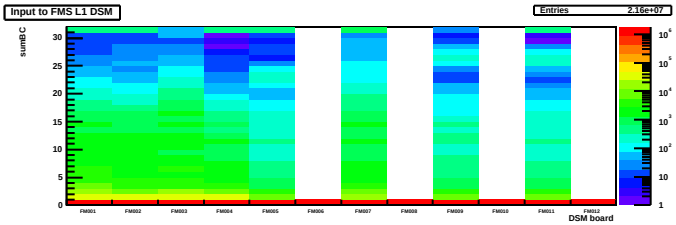

Input to FMS L0 DSM
Entries 2066532
HT ID - simulated
QT board

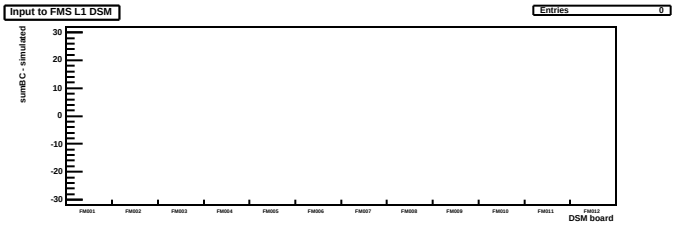

Input to FMS L1 DSM
Entries 2.16e+07
sumD
DSM board

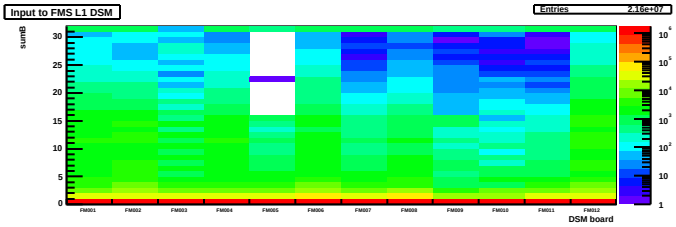

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

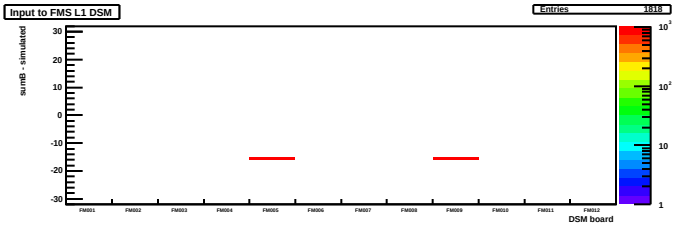

Input to FMS L1 DSM
Entries 2.16e+07
sumC
DSM board

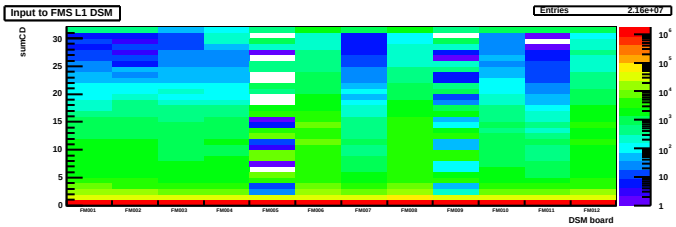

Input to FMS L1 DSM
Entries 41675
sumC - simulated
DSM board

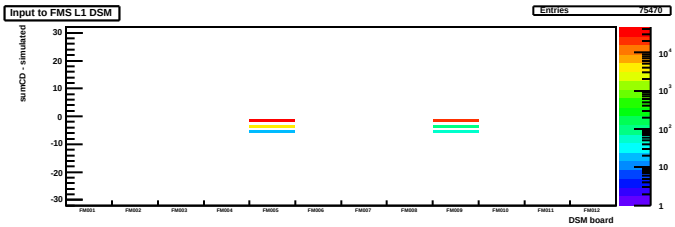

Input to FMS L1 DSM
Entries 2.16e+07
sumBC
DSM board

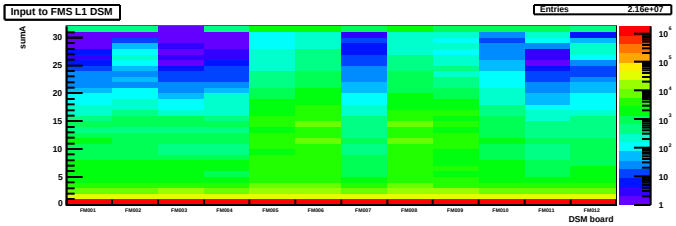

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

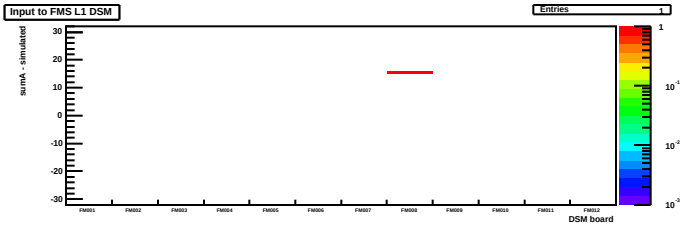

Input to FMS L1 DSM
Entries 2.16e+07
sumB
DSM board

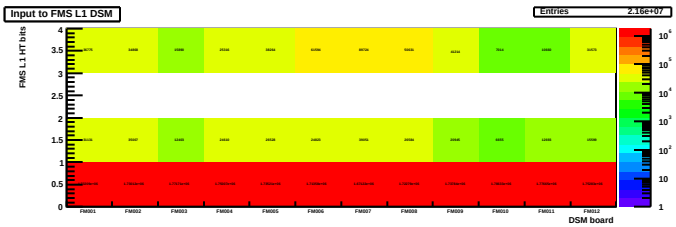

Input to FMS L1 DSM
Entries 1818
sumB - simulated
DSM board

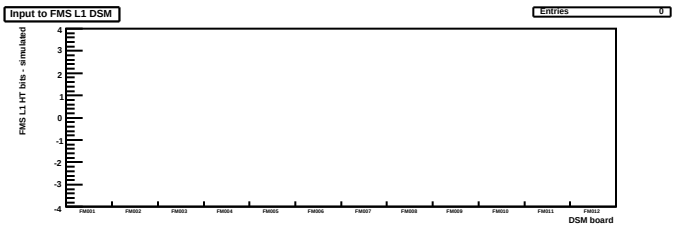

Input to FMS L1 DSM
Entries 2.16e+07
sumCD
DSM board

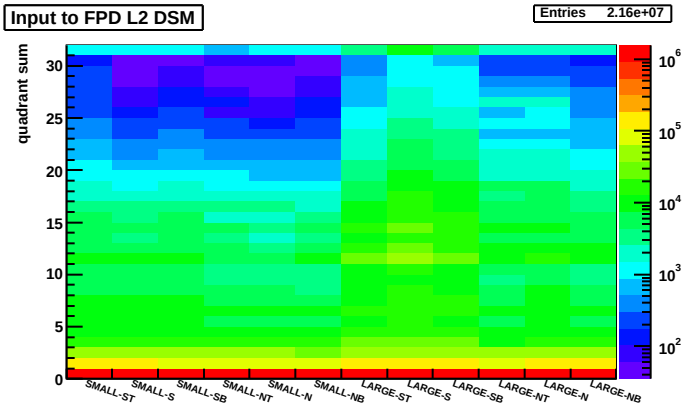

Input to FMS L1 DSM
Entries 75470
sumCD - simulated
DSM board

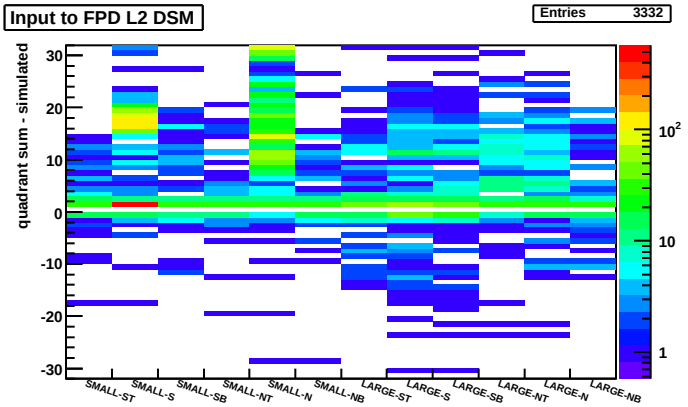

Input to FMS L1 DSM
Entries 2.16e+07
sumA
DSM board

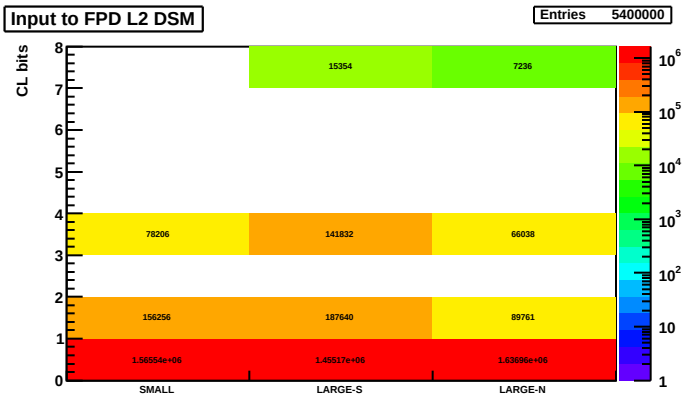

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

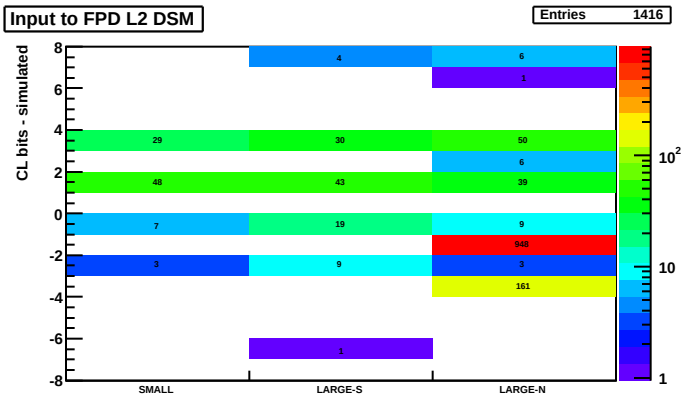

Input to FMS L1 DSM
Entries 2.16e+07
FMS L1 HT bits
DSM board

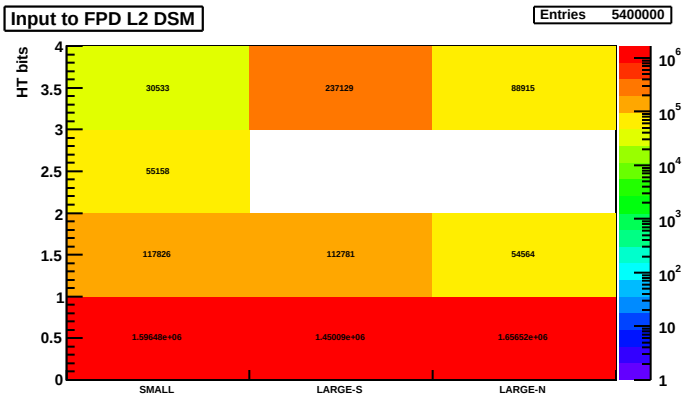

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

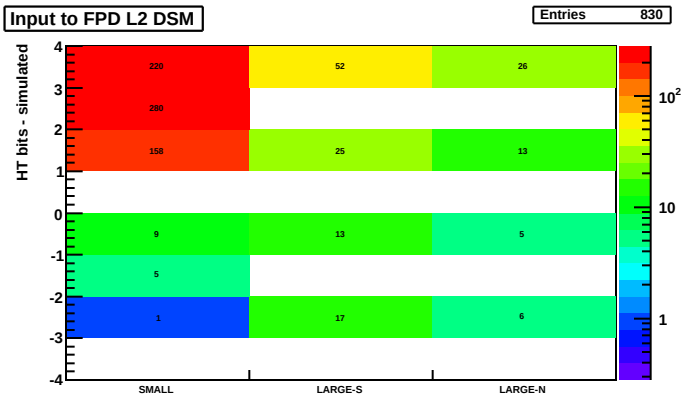

Input to FPD L2 DSM
Entries 2.16e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

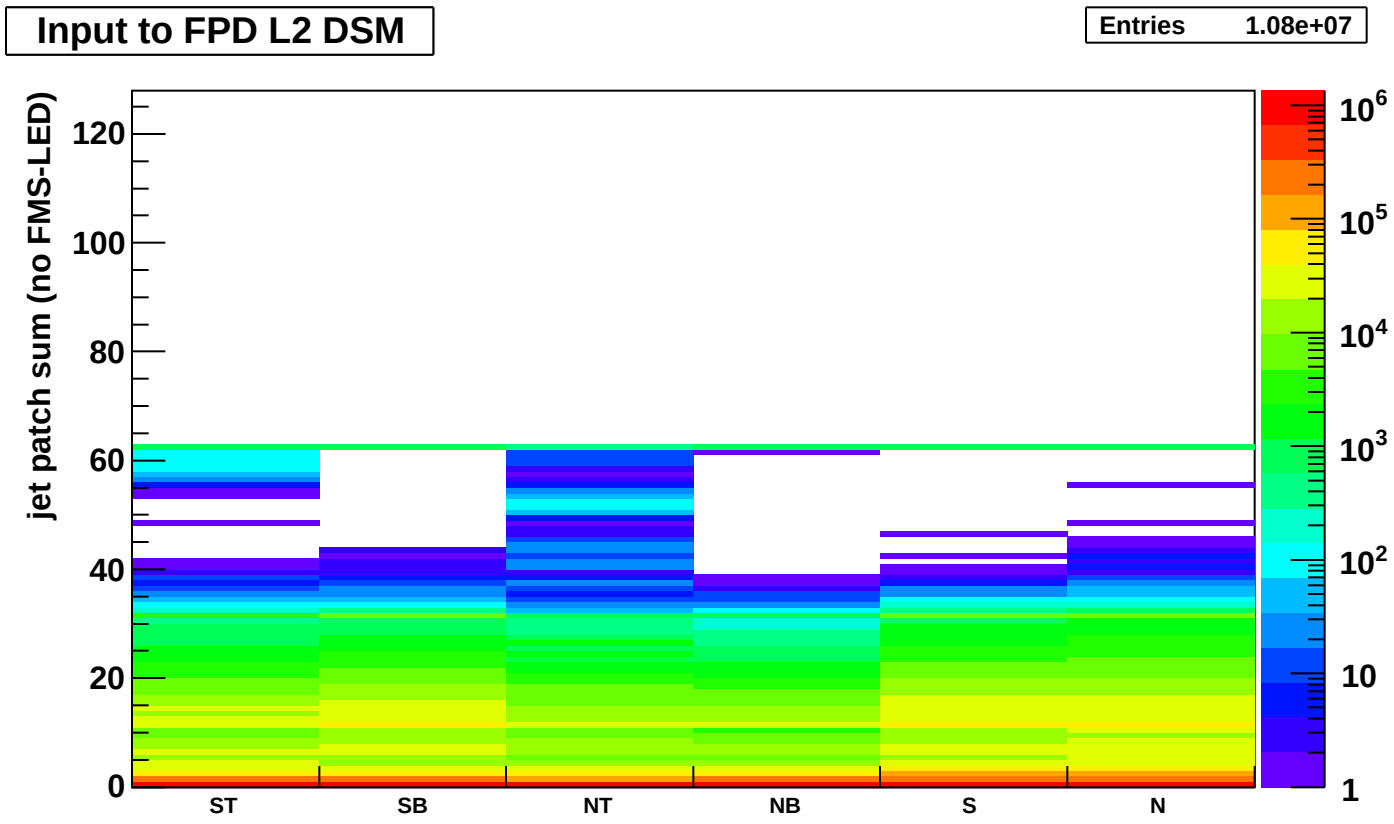

Input to FPD L2 DSM
Entries 3332
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

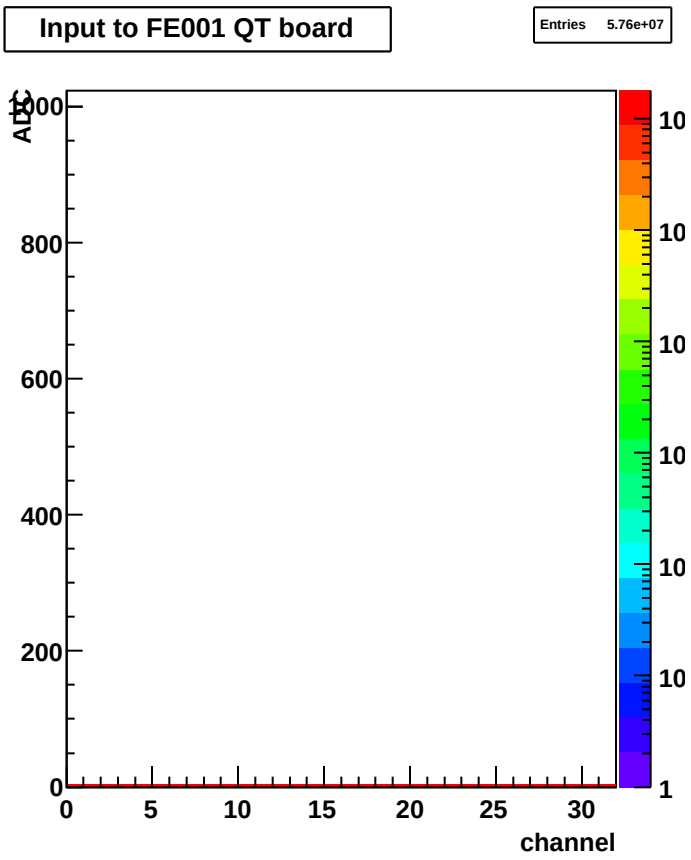

Input to FPD L2 DSM
Entries 5400000
CL bits
15354
7236
78206
141832
66038
156256
187640
89761
1.56554e+06
1.45517e+06
1.63696e+06
SMALL
LARGE-S
LARGE-N

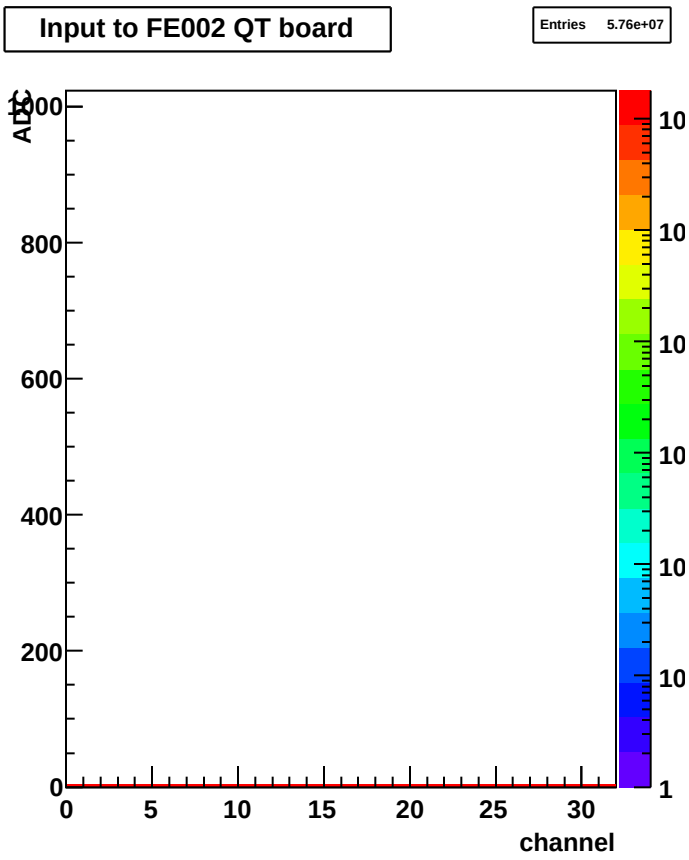

Input to FPD L2 DSM
Entries 1416
CL bits - simulated
4
6
1
29
30
50
6
48
43
39
7
19
9
948
3
9
3
161
1
SMALL
LARGE-S
LARGE-N

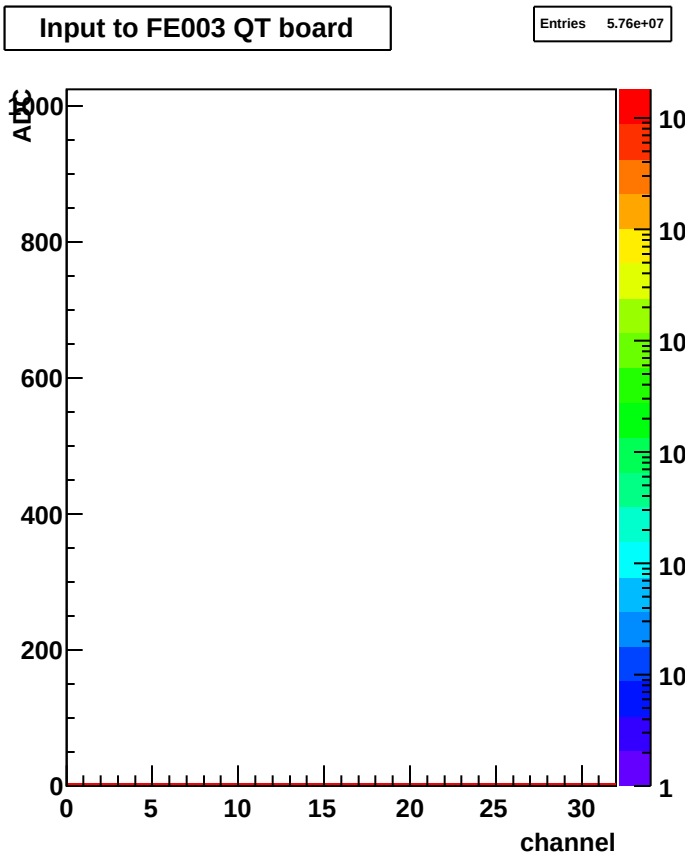

Input to FPD L2 DSM
Entries 5400000
HT bits
30533
237129
88915
55158
117826
112781
54564
1.59648e+06
1.45009e+06
1.65652e+06
SMALL
LARGE-S
LARGE-N

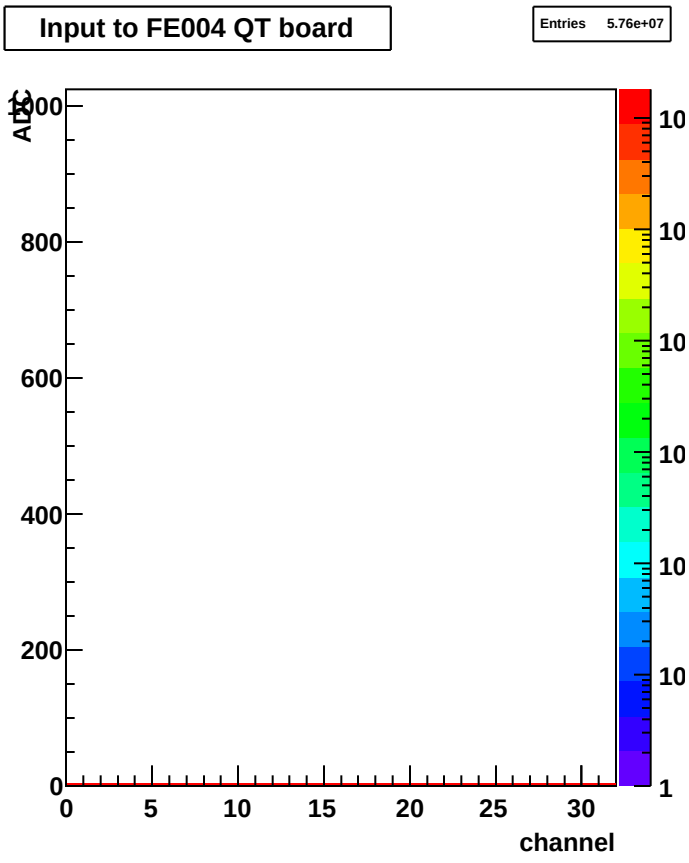

Input to FPD L2 DSM
Entries 830
HT bits - simulated
220
52
26
280
158
25
13
9
13
5
5
1
17
6
SMALL
LARGE-S
LARGE-N

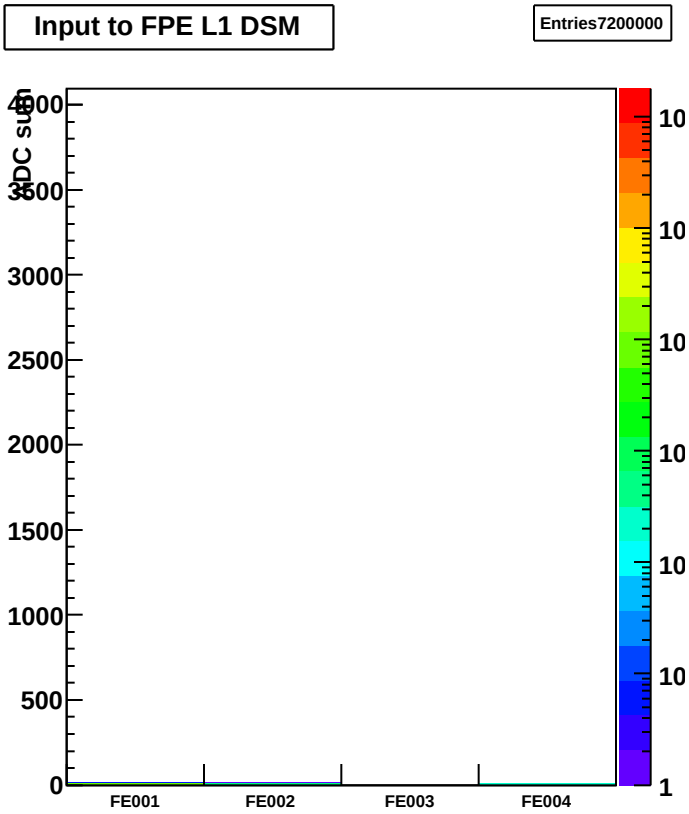

Input to FPD L2 DSM
Entries 1.08e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

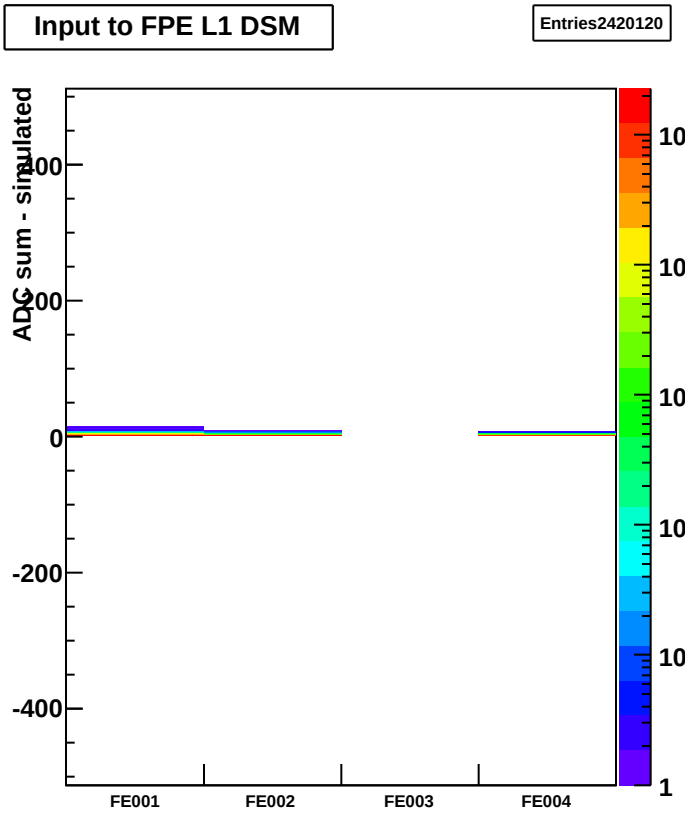

Input to FE001 QT board
Entries 5.76e+07
ADC
channel


Input to FE002 QT board
Entries 5.76e+07
ADC
channel

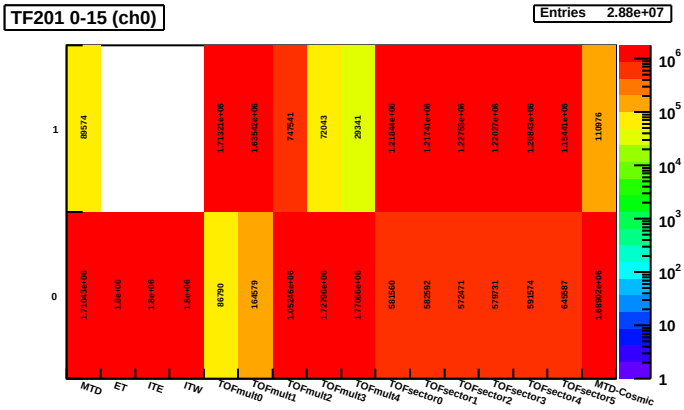

Input to FE003 QT board
Entries 5.76e+07
ADC
channel


Input to FE004 QT board
Entries 5.76e+07
ADC
channel

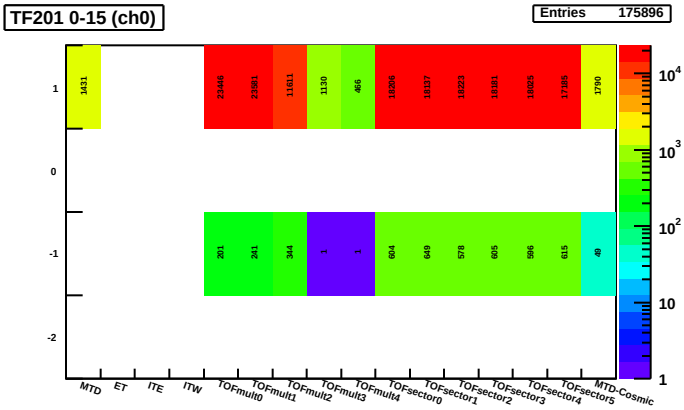

Input to FPE L1 DSM
Entries7200000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2420120
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

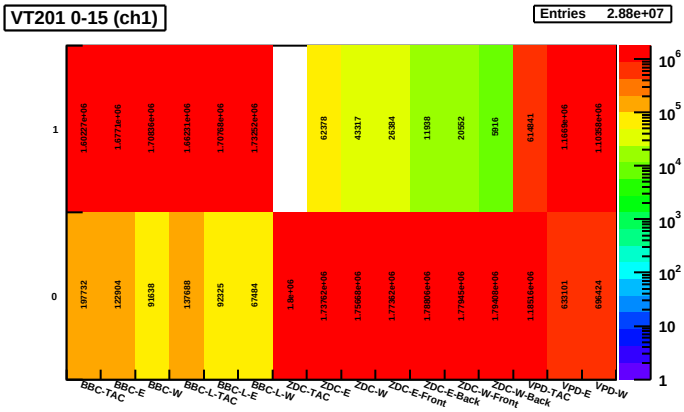
TF201 0-15 (ch0)



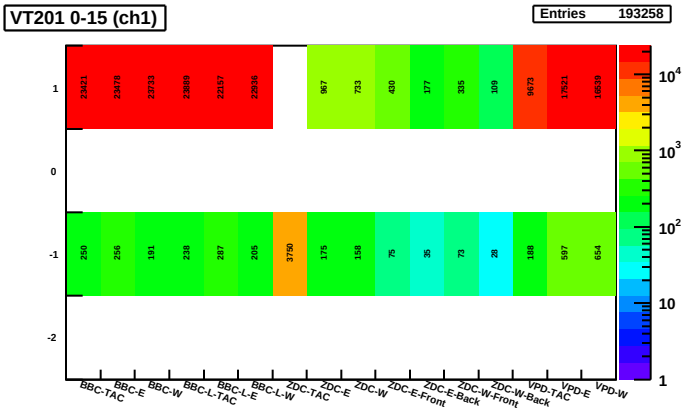
TF201 0-15 (ch0)



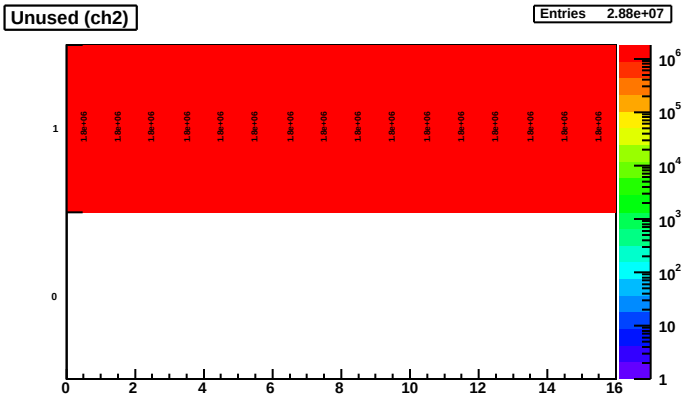
VT201 0-15 (ch1)

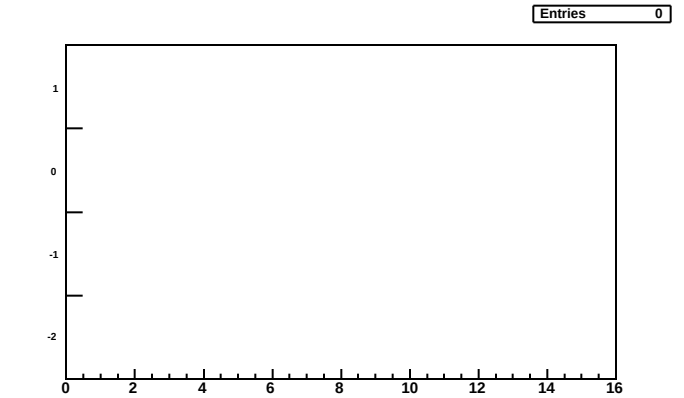


VT201 0-15 (ch1)

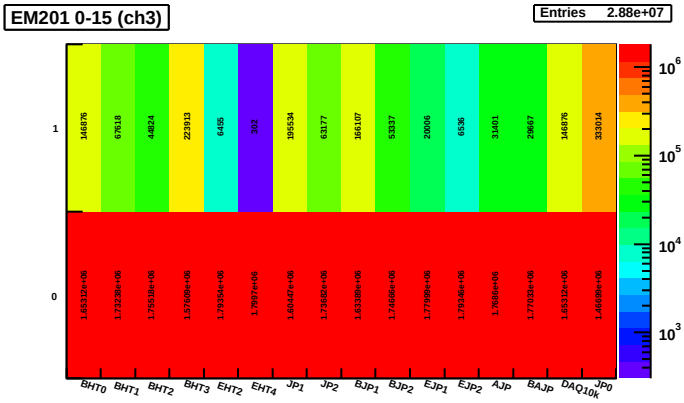


Unused (ch2)

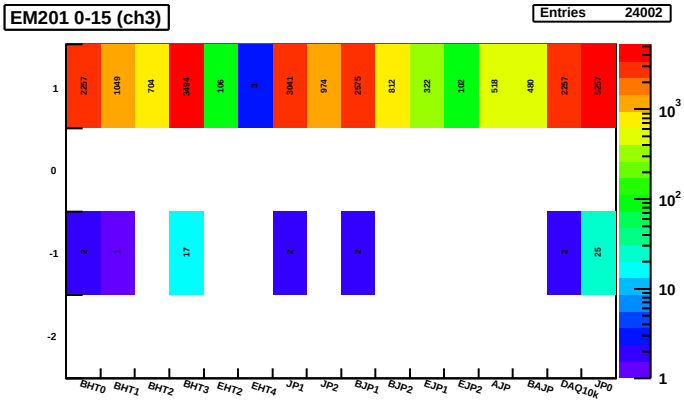




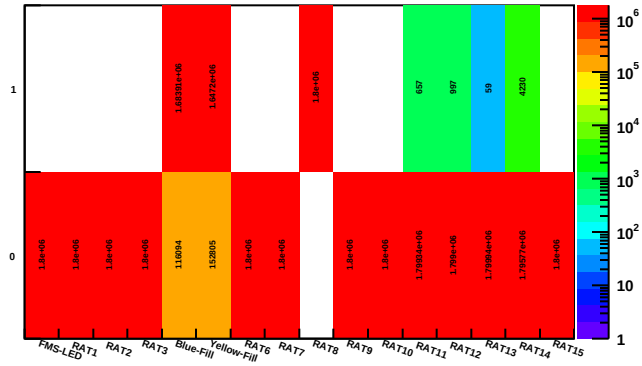
EM201 0-15 (ch3)

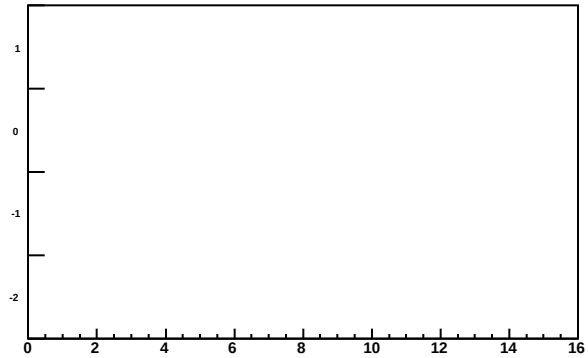


EM201 0-15 (ch3)

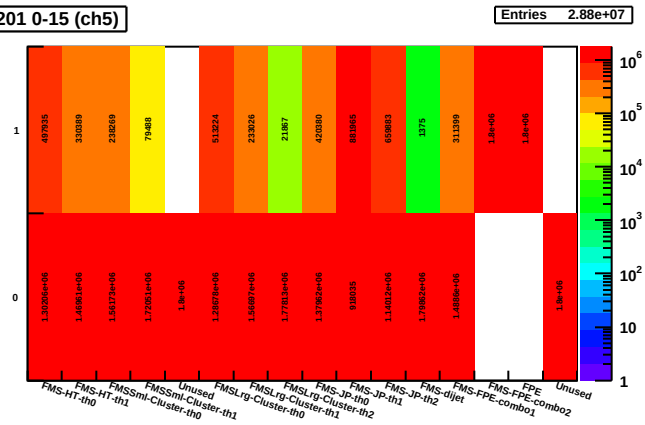


RAT board (ch4)

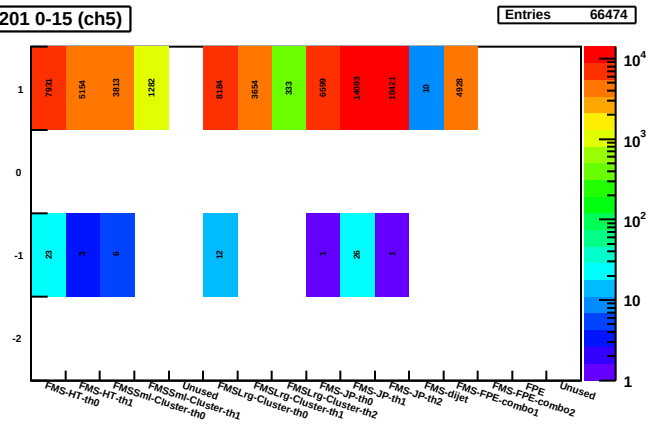




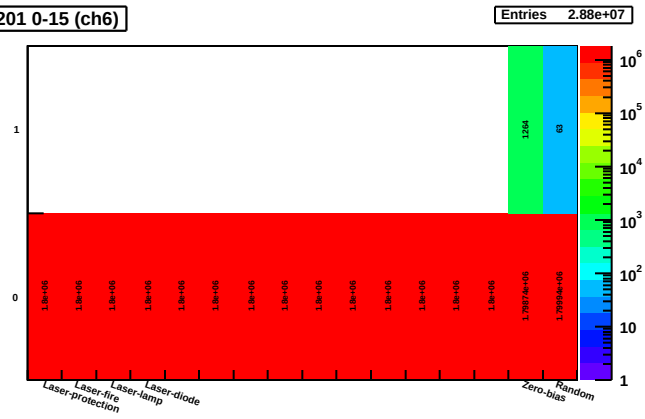
FP201 0-15 (ch5)

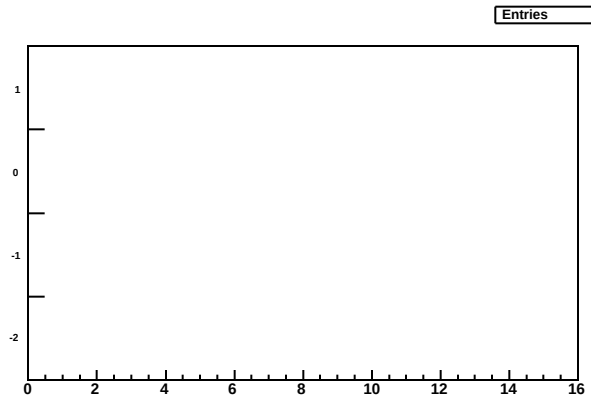
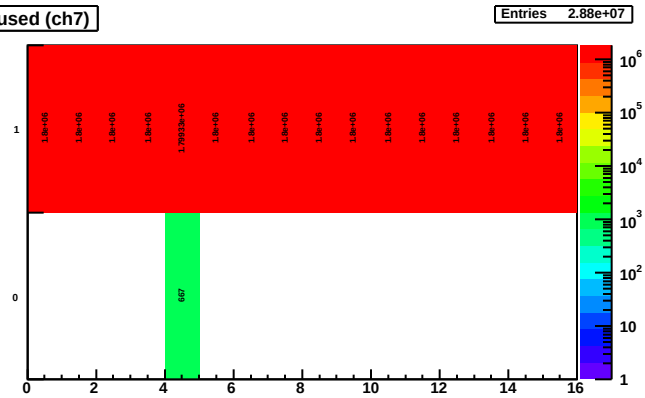
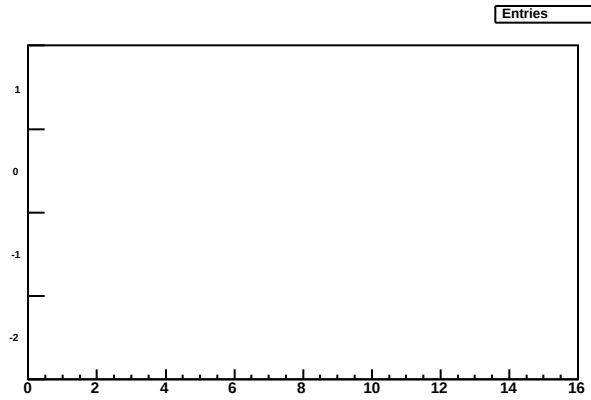


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)