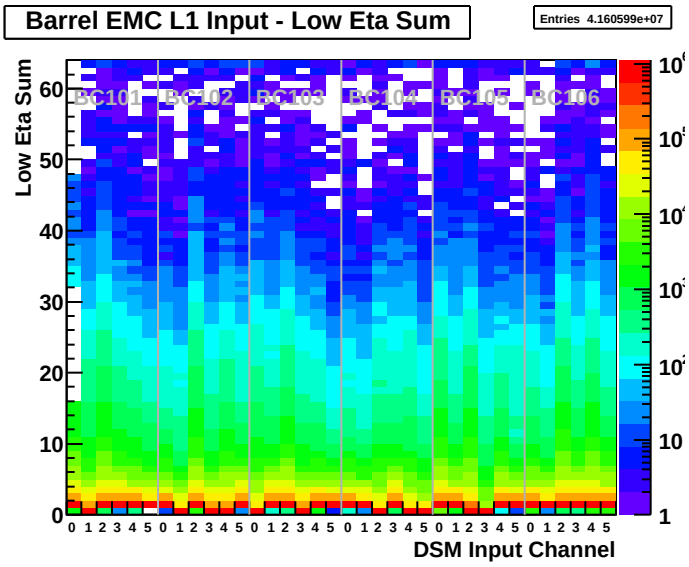
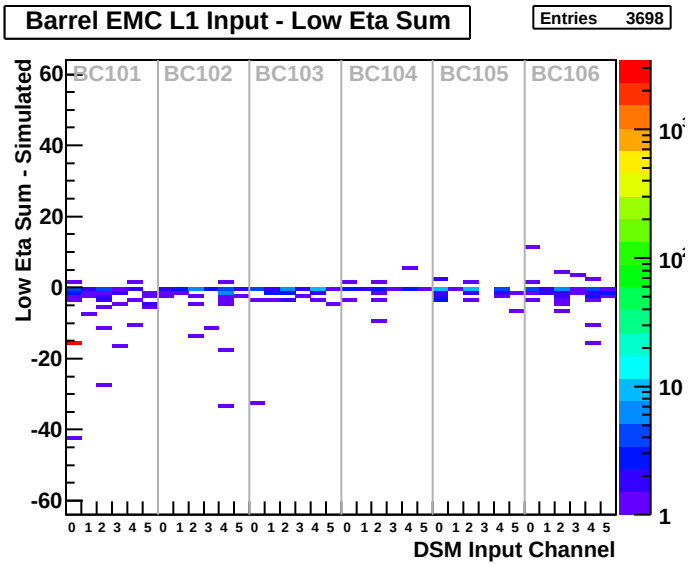
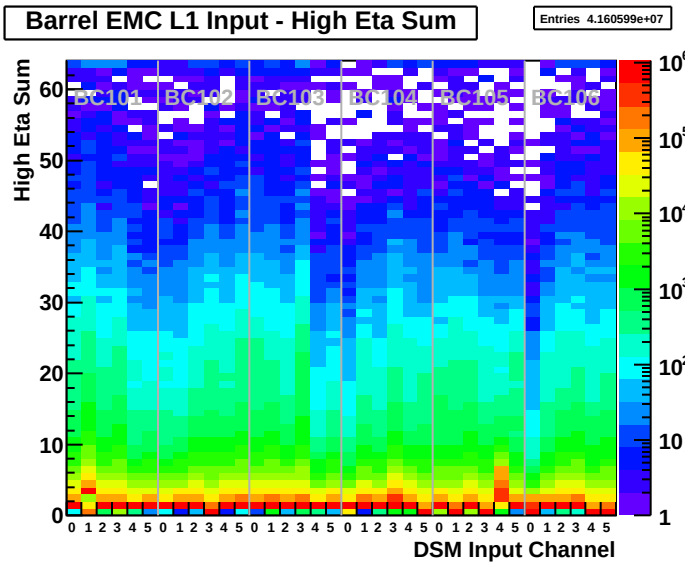
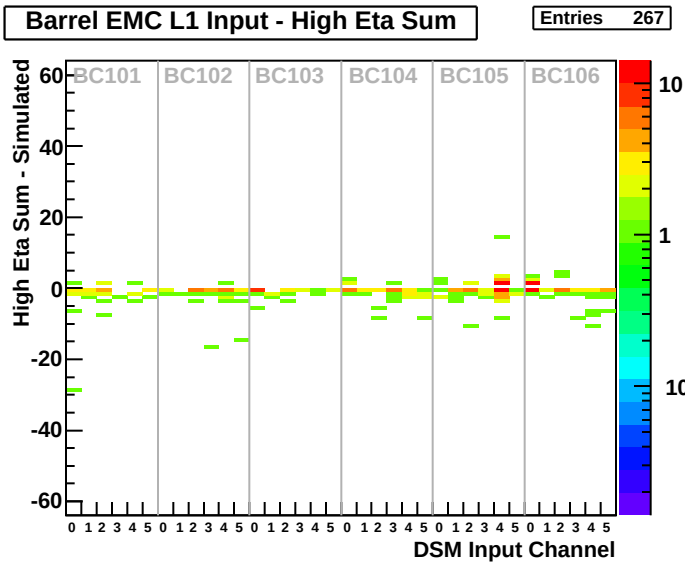
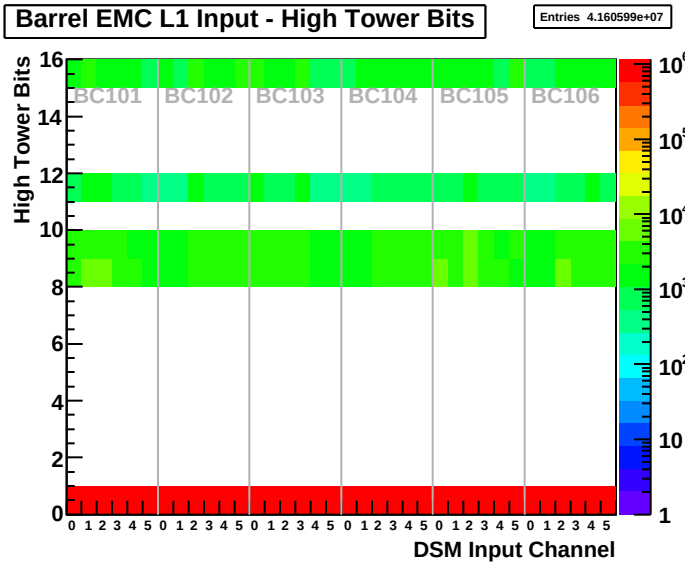
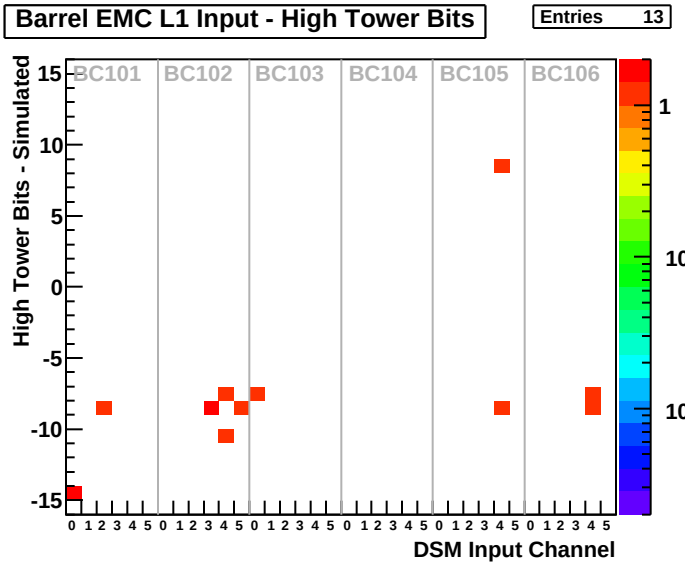

Barrel EMC L1 Input - Low Eta Sum
Entries 4.160599e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

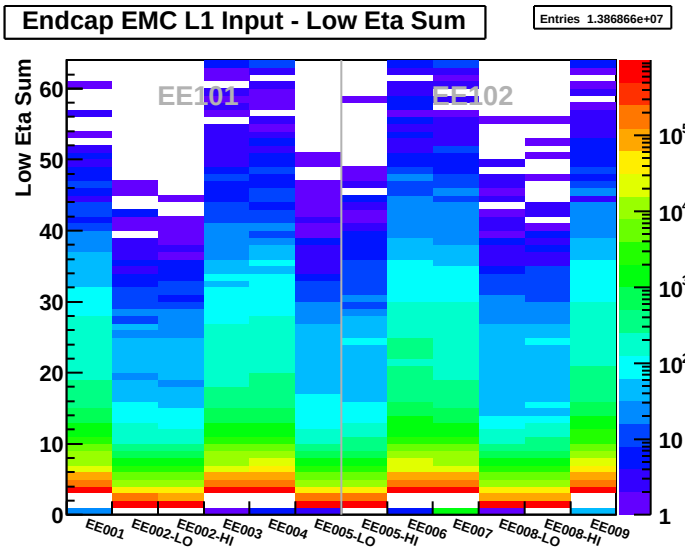

Barrel EMC L1 Input - Low Eta Sum
Entries 3698
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

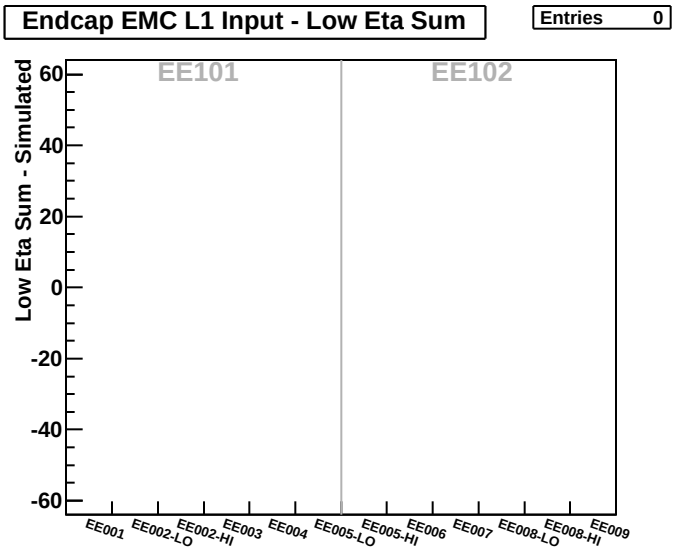

Barrel EMC L1 Input - High Eta Sum
Entries 4.160599e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

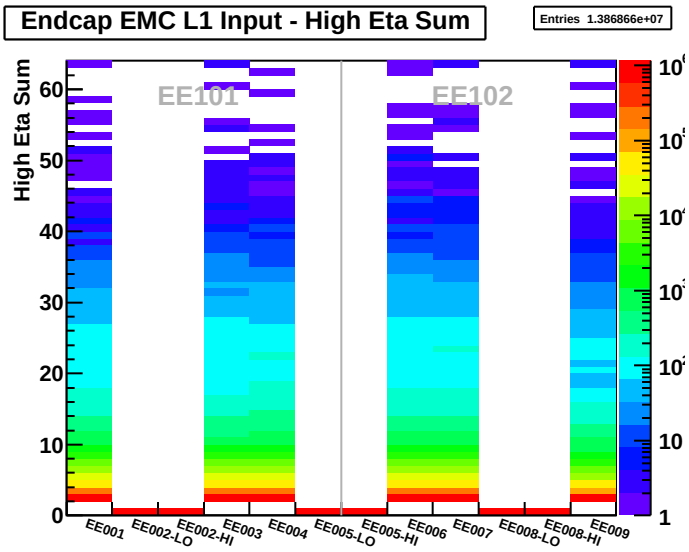

Barrel EMC L1 Input - High Eta Sum
Entries 267
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

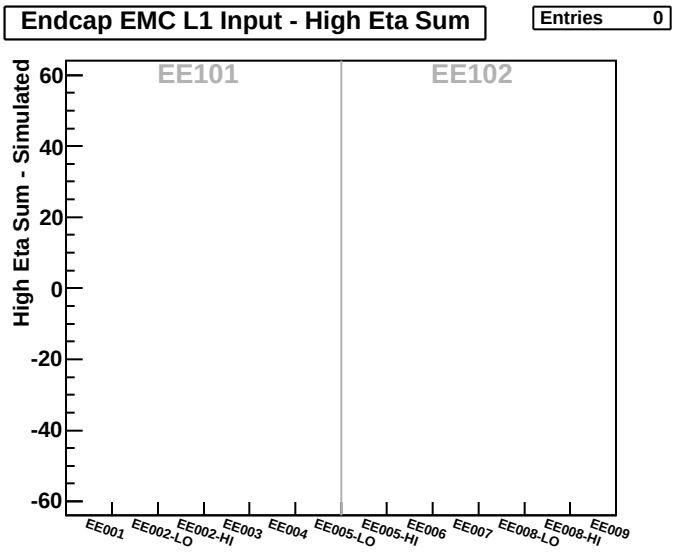

Barrel EMC L1 Input - High Tower Bits
Entries 4.160599e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

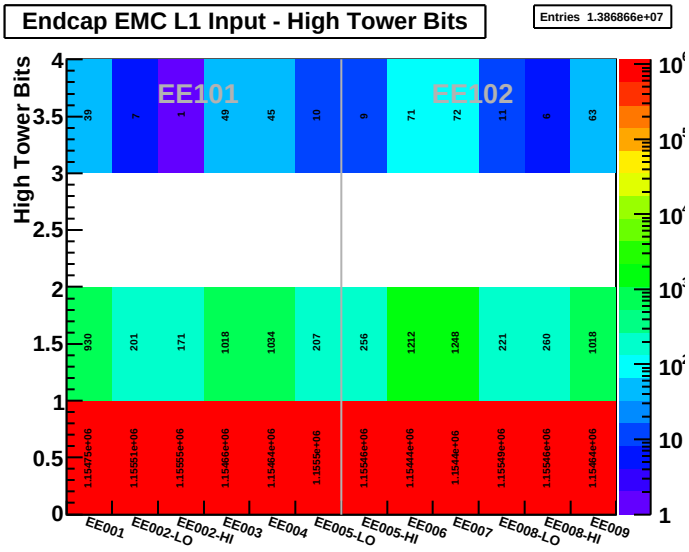

Barrel EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

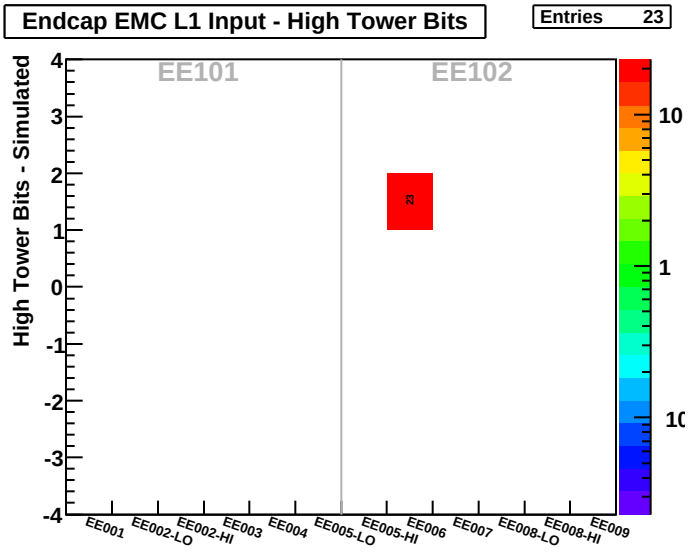

Endcap EMC L1 Input - Low Eta Sum
Entries 1.386866e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

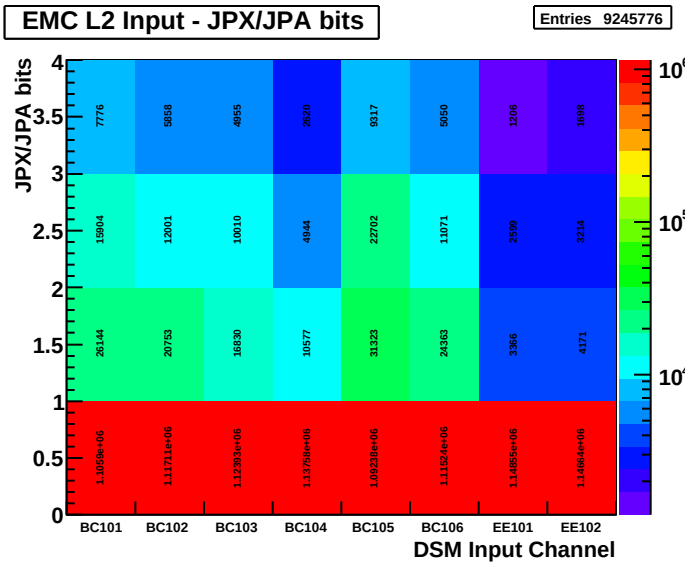

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

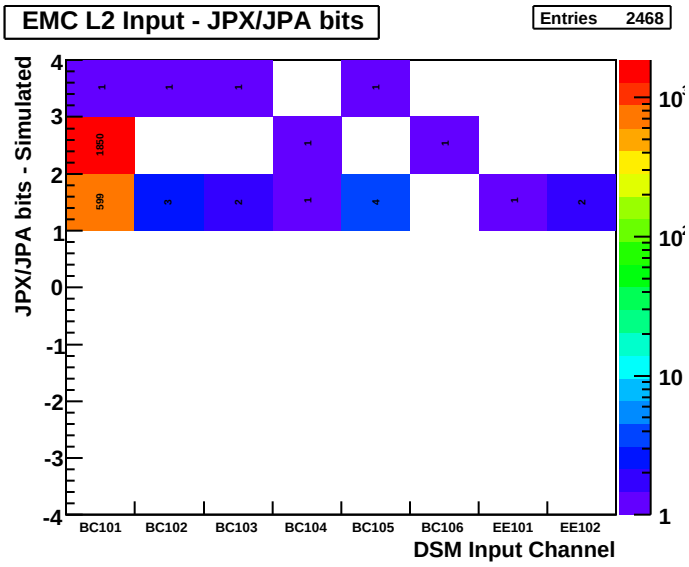

Endcap EMC L1 Input - High Eta Sum
Entries 1.386866e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

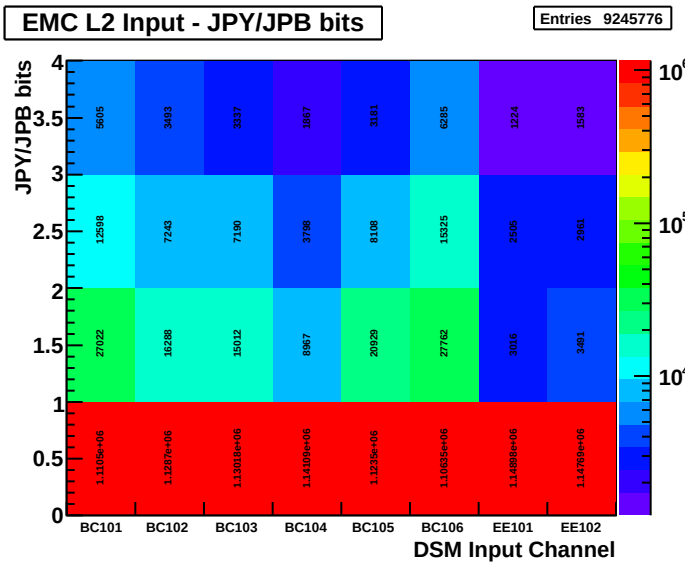

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

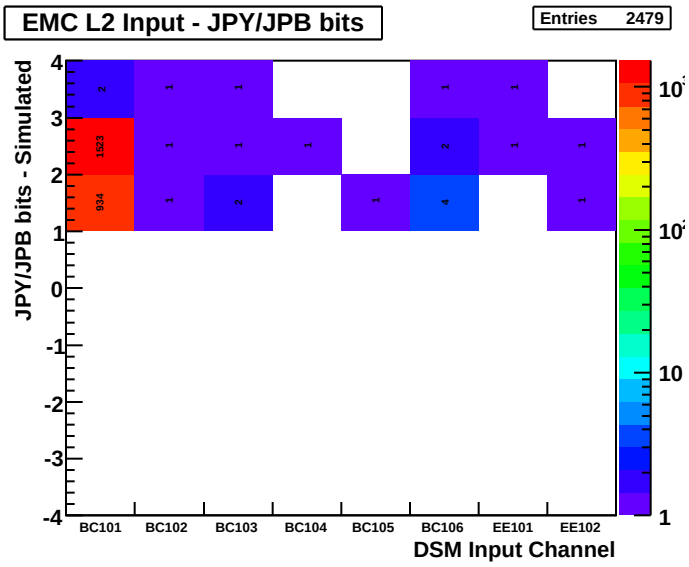

Endcap EMC L1 Input - High Tower Bits
Entries 1.386866e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

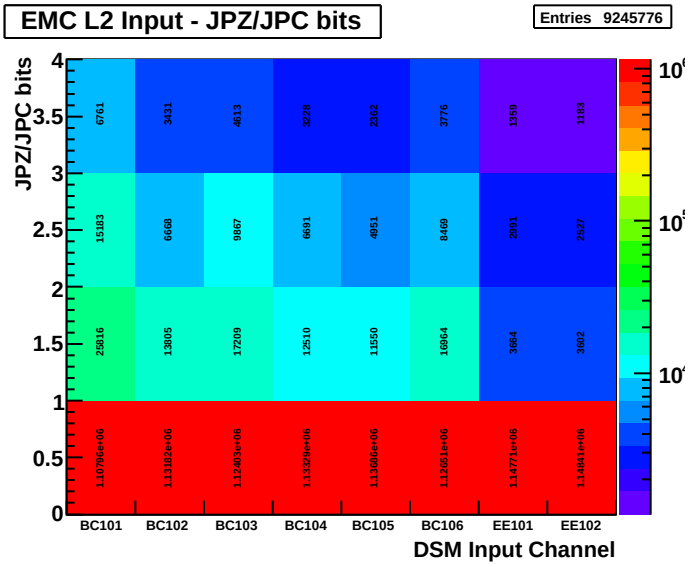

Endcap EMC L1 Input - High Tower Bits
Entries 23
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

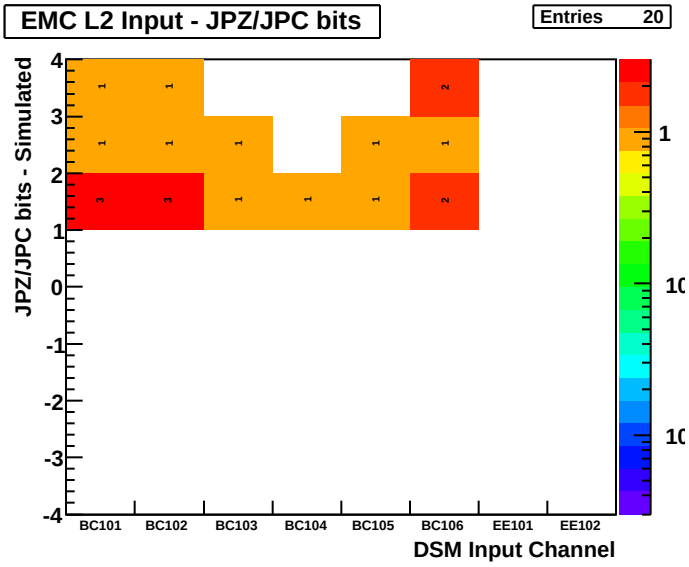

EMC L2 Input - JPX/JPA bits
Entries 9245776
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7776 5858 4955 2820 9317 5050 1206 1698
15904 12001 10010 4944 22702 11071 2599 3214
26144 20753 16830 10577 31323 24363 3366 4171
1.1059e+06 1.11711e+06 1.12933e+06 1.13758e+06 1.09238e+06 1.11524e+06 1.14895e+06 1.14664e+06

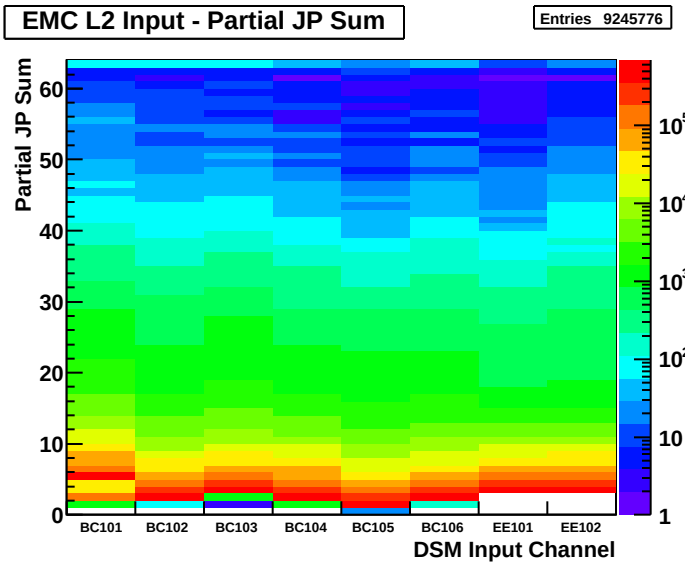

EMC L2 Input - JPX/JPA bits
Entries 2468
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1 1
1850 1 1
599 3 2 1 4 1 2

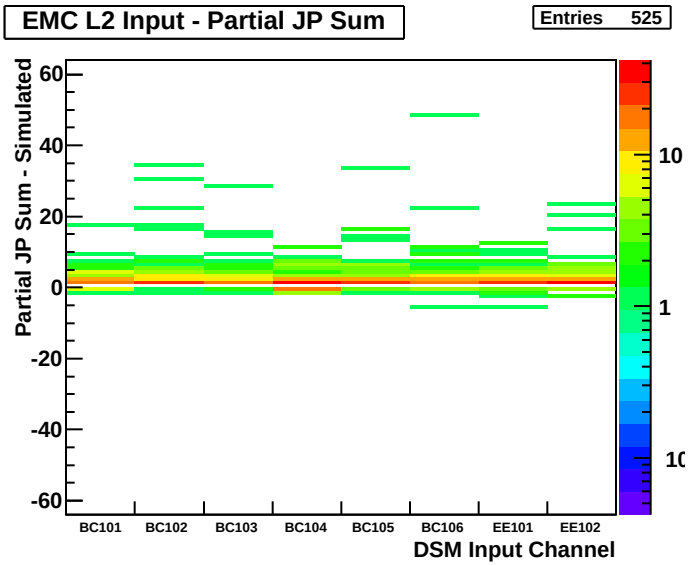

EMC L2 Input - JPY/JPB bits
Entries 9245776
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5605 3493 3337 1867 3181 6285 1224 1583
12598 7243 7190 3798 8108 15325 2505 2961
27022 16288 15012 8967 20929 27762 3016 3491
1.1105e+06 1.1287e+06 1.13018e+06 1.14109e+06 1.1235e+06 1.10635e+06 1.14898e+06 1.14769e+06

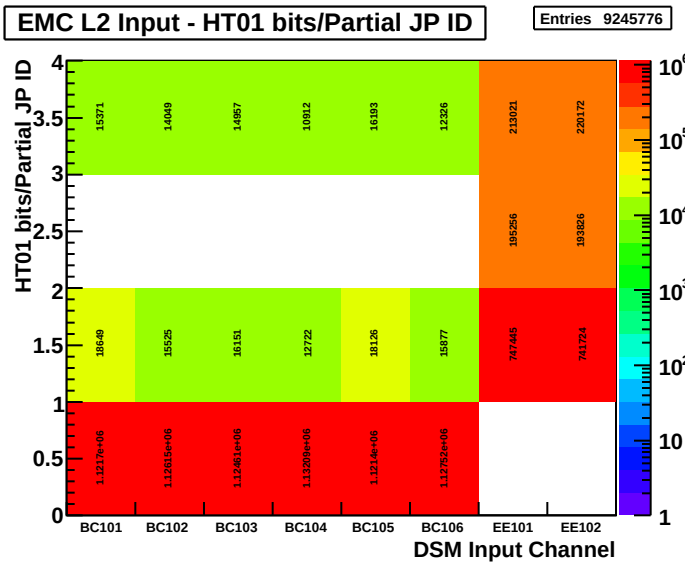

EMC L2 Input - JPY/JPB bits
Entries 2479
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 1 1 1
1523 1 1 1 2 1 1
934 1 2 1 4 1

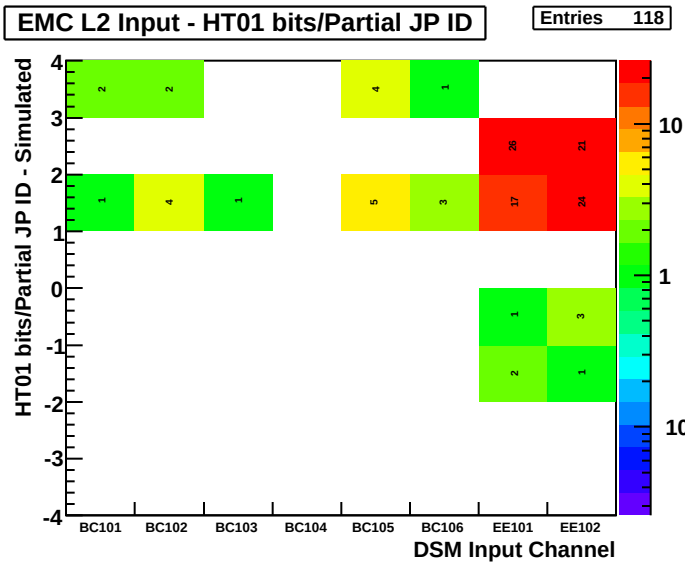

EMC L2 Input - JPZ/JPC bits
Entries 9245776
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6761 3431 4613 3228 2262 3776 1359 1183
15183 6668 9867 6691 4951 8469 2991 2527
25816 13805 17209 12510 11550 16964 3664 3602
1.10796e+06 1.13182e+06 1.12403e+06 1.13329e+06 1.13886e+06 1.12651e+06 1.14771e+06 1.14841e+06

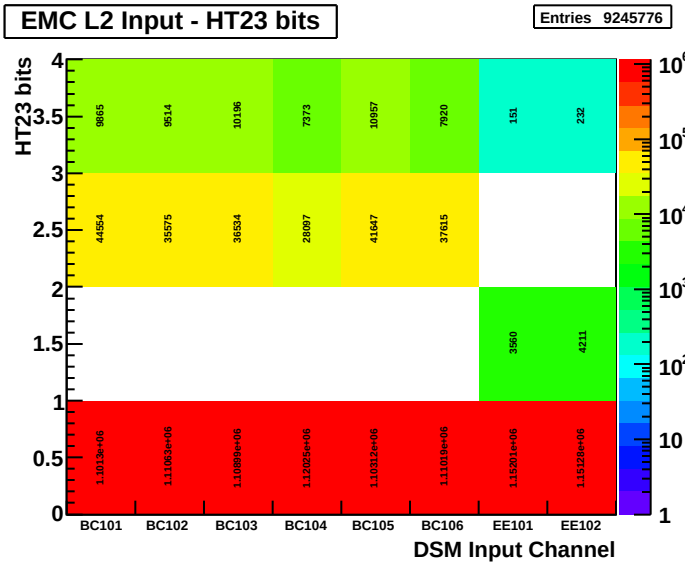

EMC L2 Input - JPZ/JPC bits
Entries 20
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 2
1 1 1 1 1
3 3 1 1 1 2

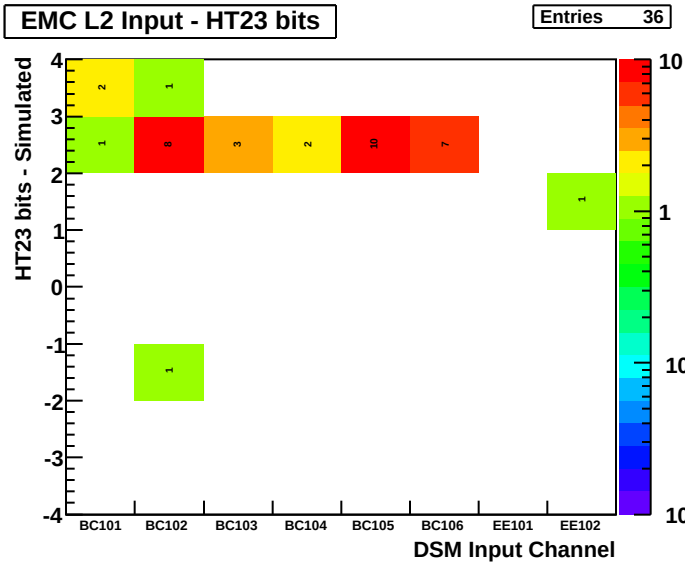

EMC L2 Input - Partial JP Sum
Entries 9245776
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

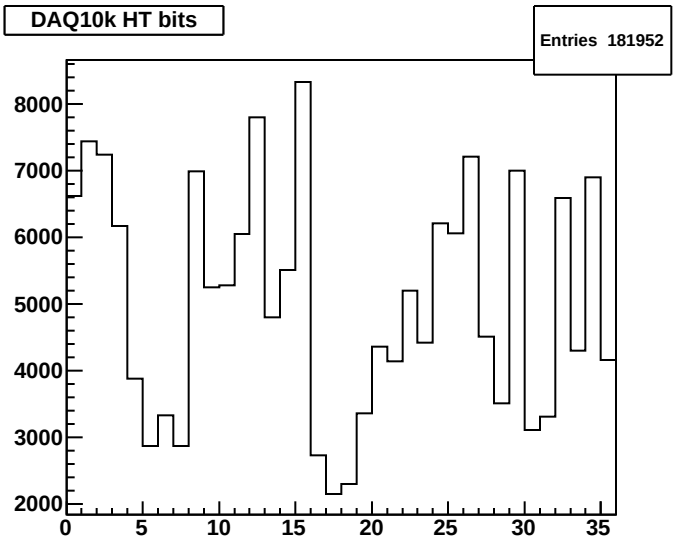

EMC L2 Input - Partial JP Sum
Entries 525
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

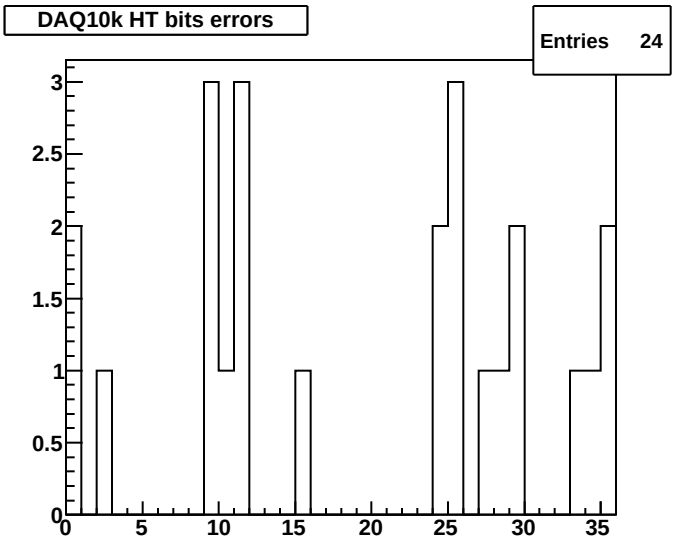

EMC L2 Input - HT01 bits/Partial JP ID
Entries 9245776
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

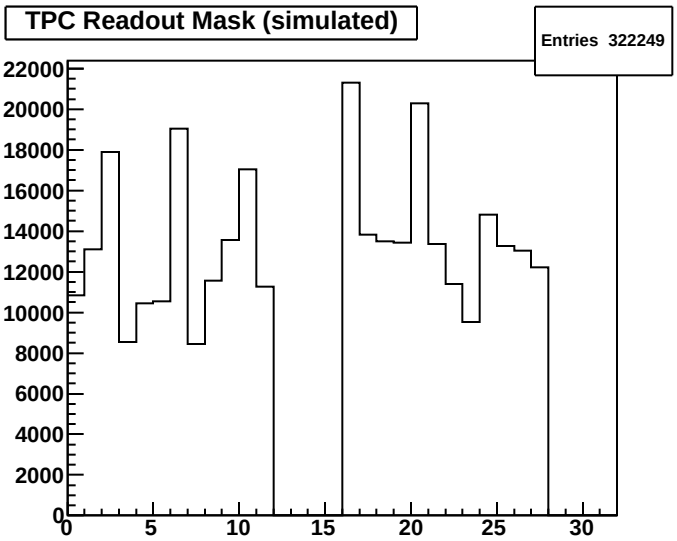

EMC L2 Input - HT01 bits/Partial JP ID
Entries 118
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

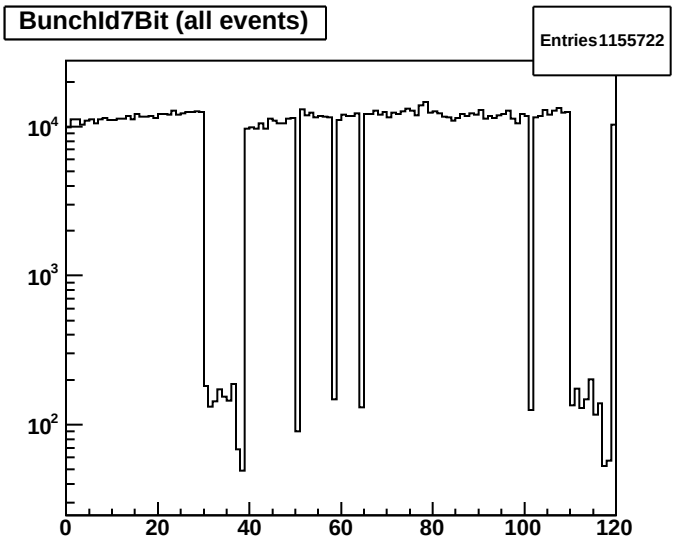

EMC L2 Input - HT23 bits
Entries 9245776
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

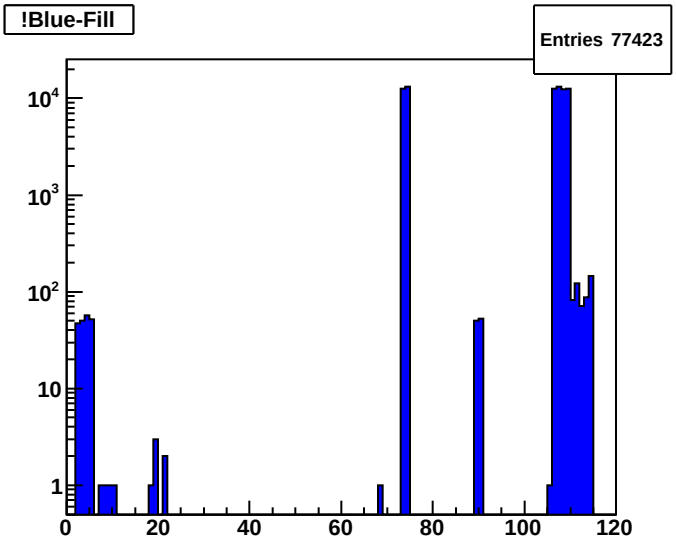

EMC L2 Input - HT23 bits
Entries 36
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

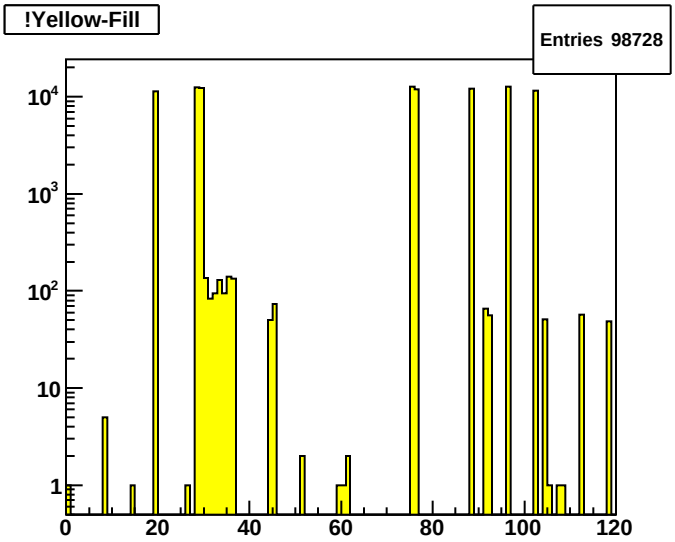

DAQ10k HT bits
Entries 181952

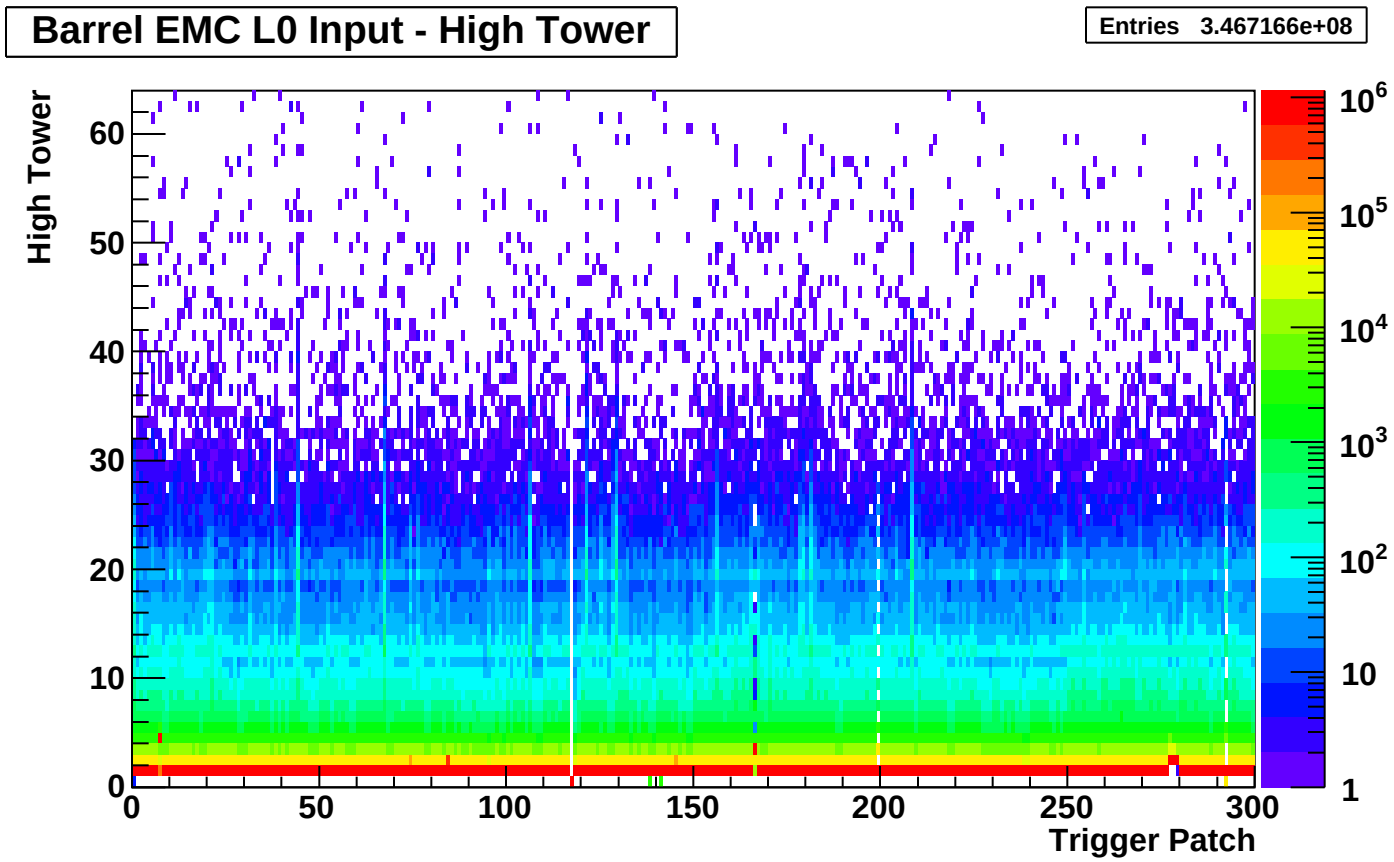

DAQ10k HT bits errors
Entries 24

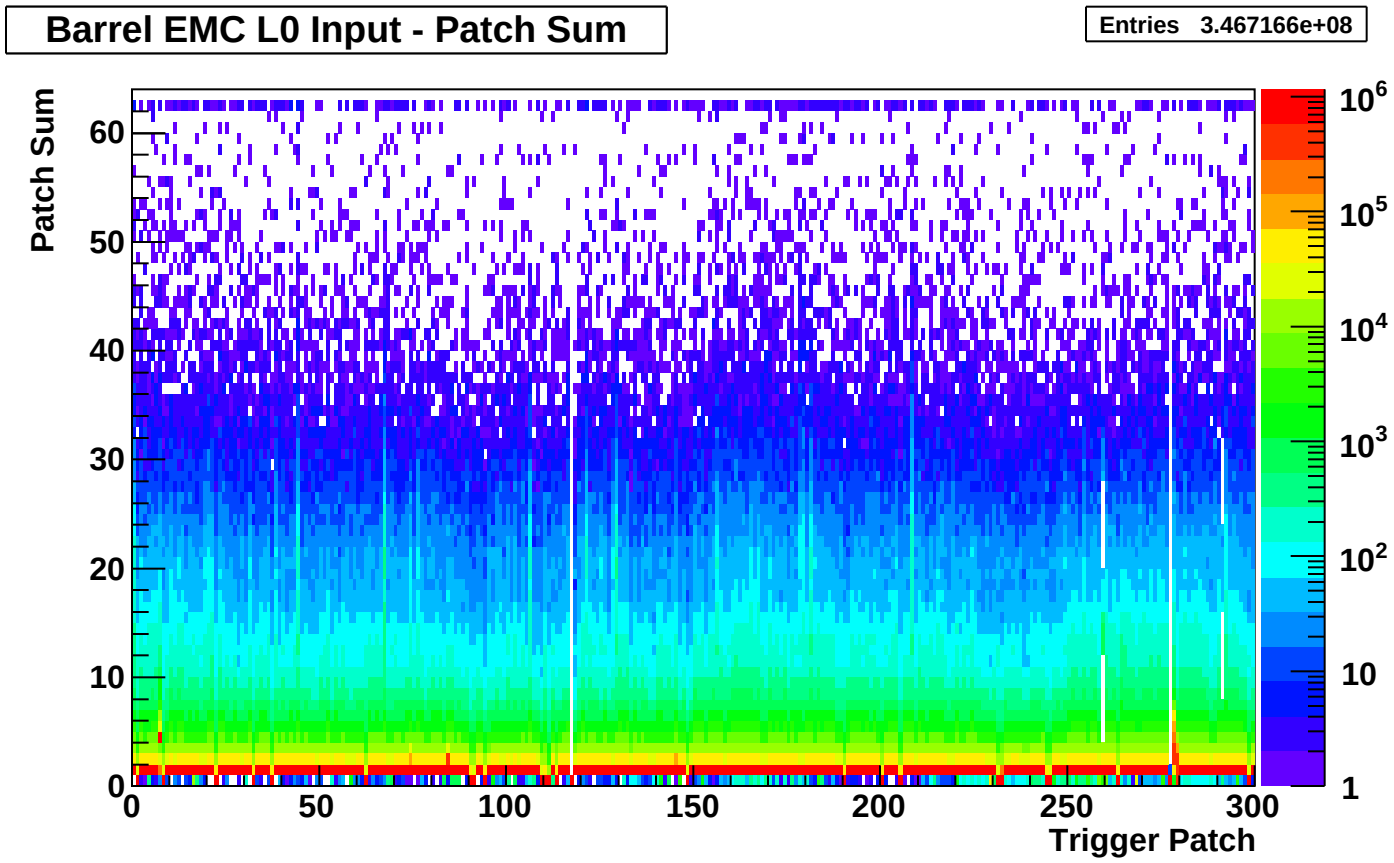

TPC Readout Mask (simulated)
Entries 322249

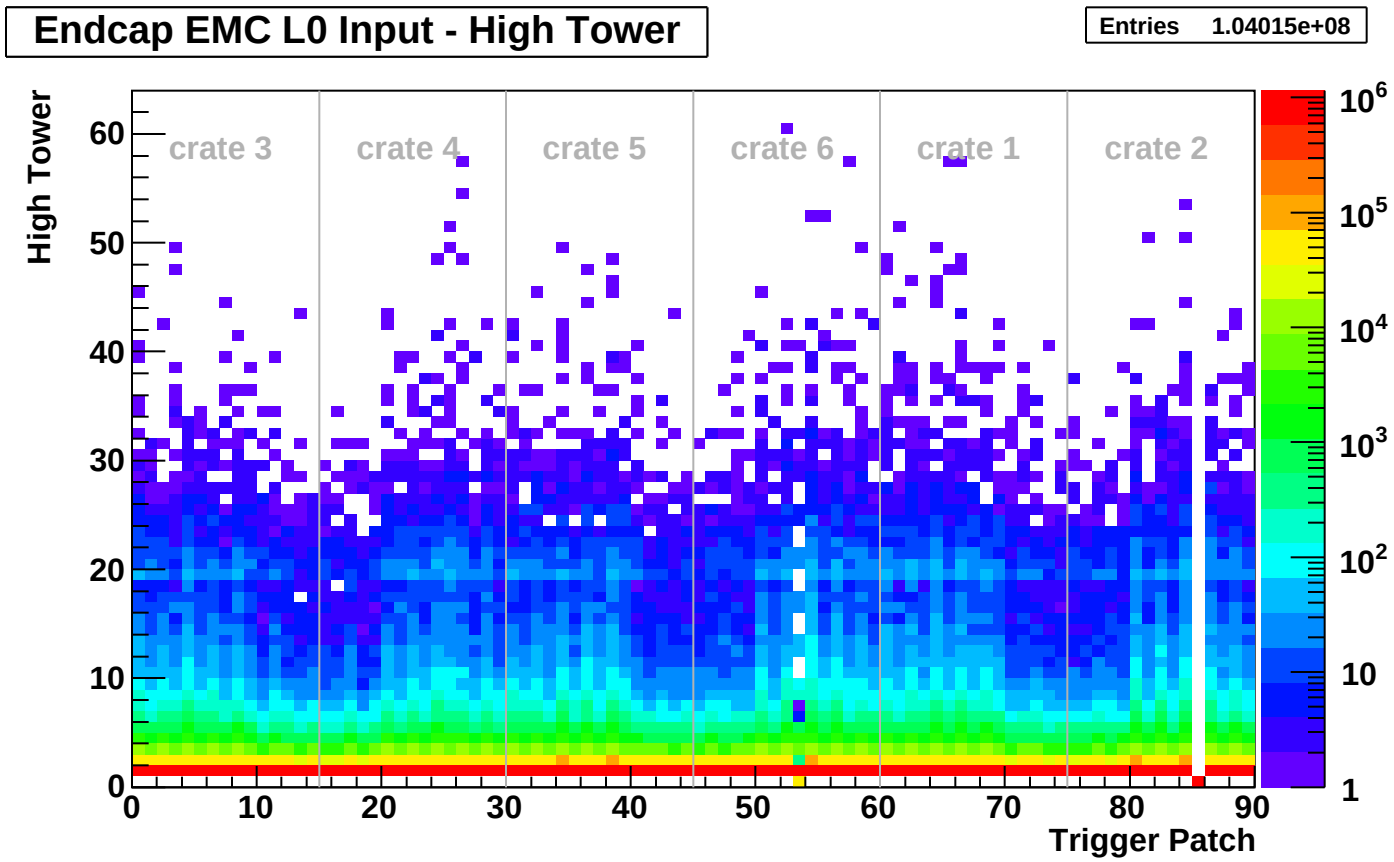

BunchId7Bit (all events)
Entries 1155722

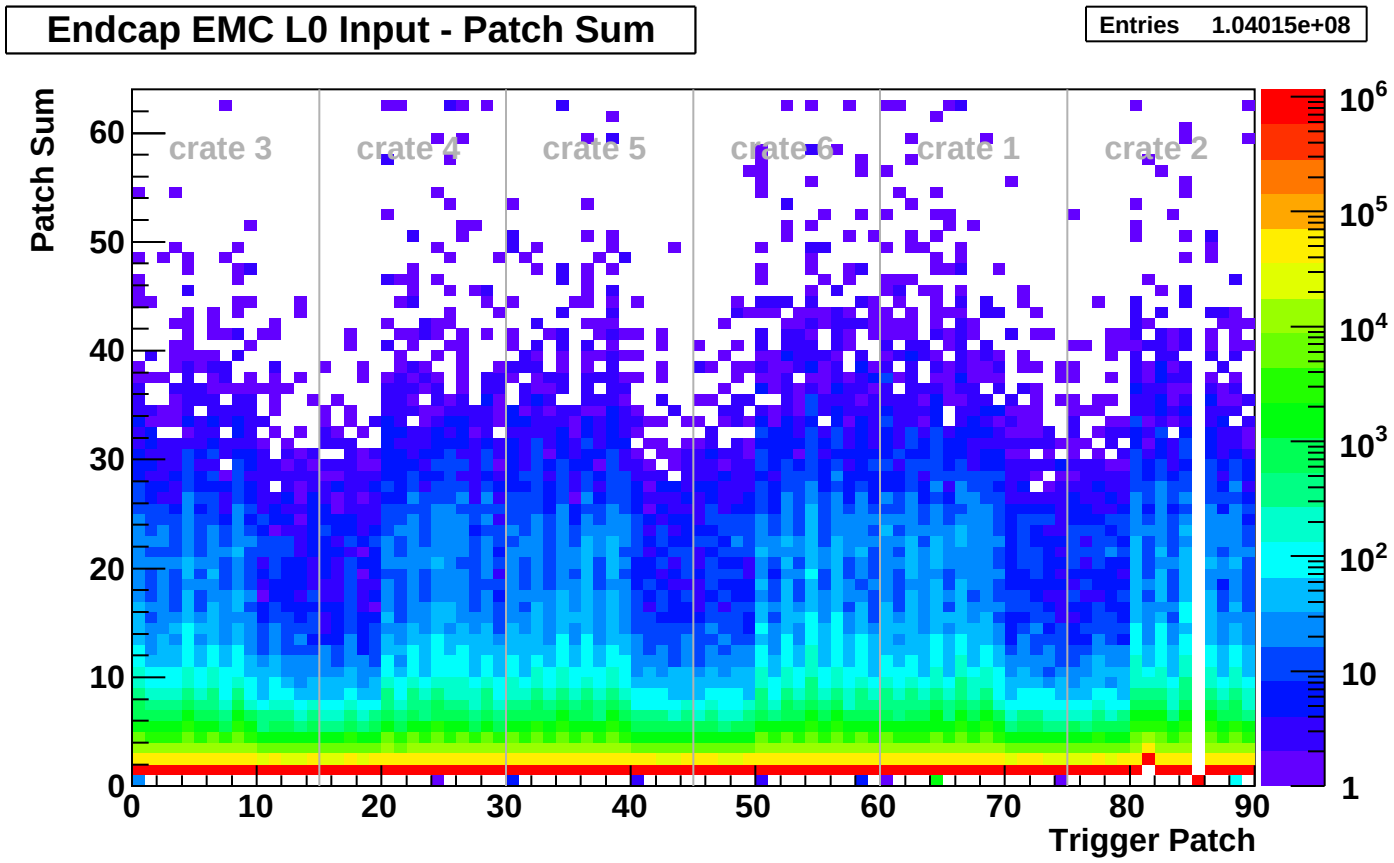

!Blue-Fill
Entries 77423

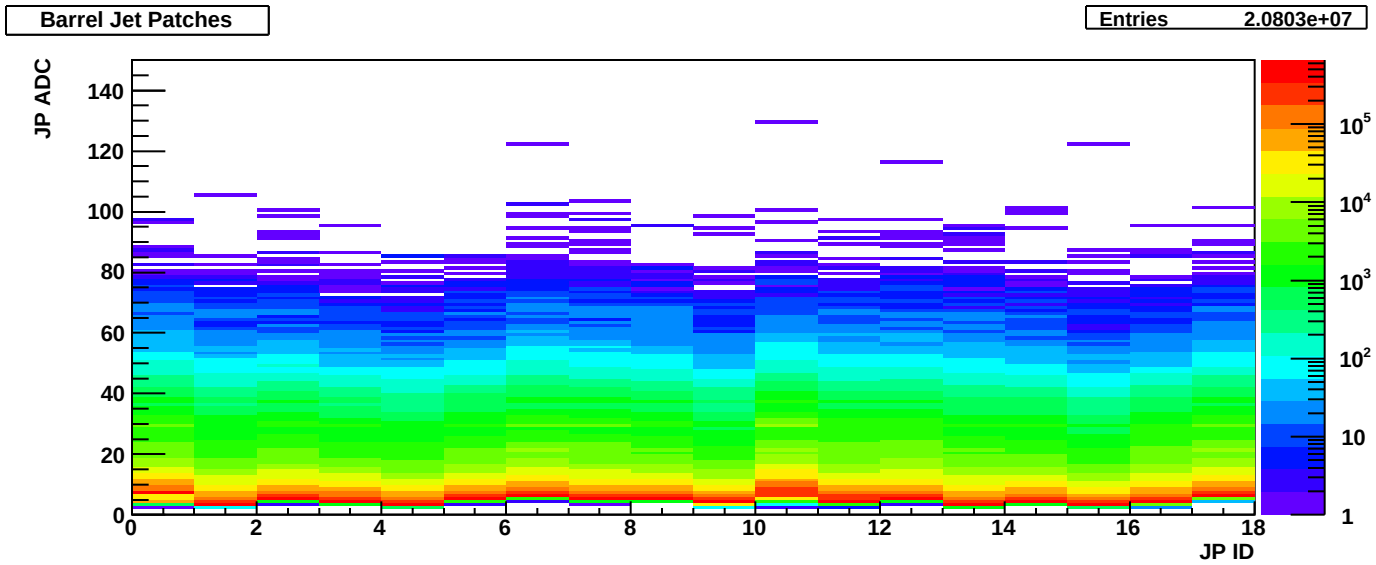

!Yellow-Fill
Entries 98728

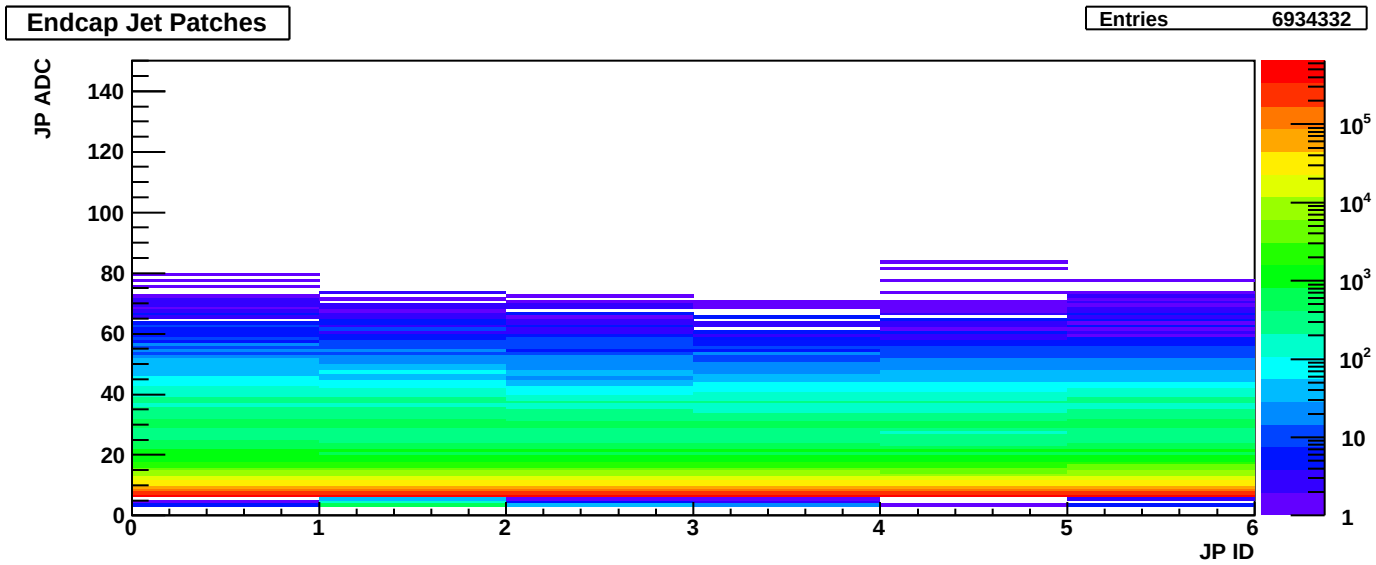

Barrel EMC L0 Input - High Tower
Entries 3.467166e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

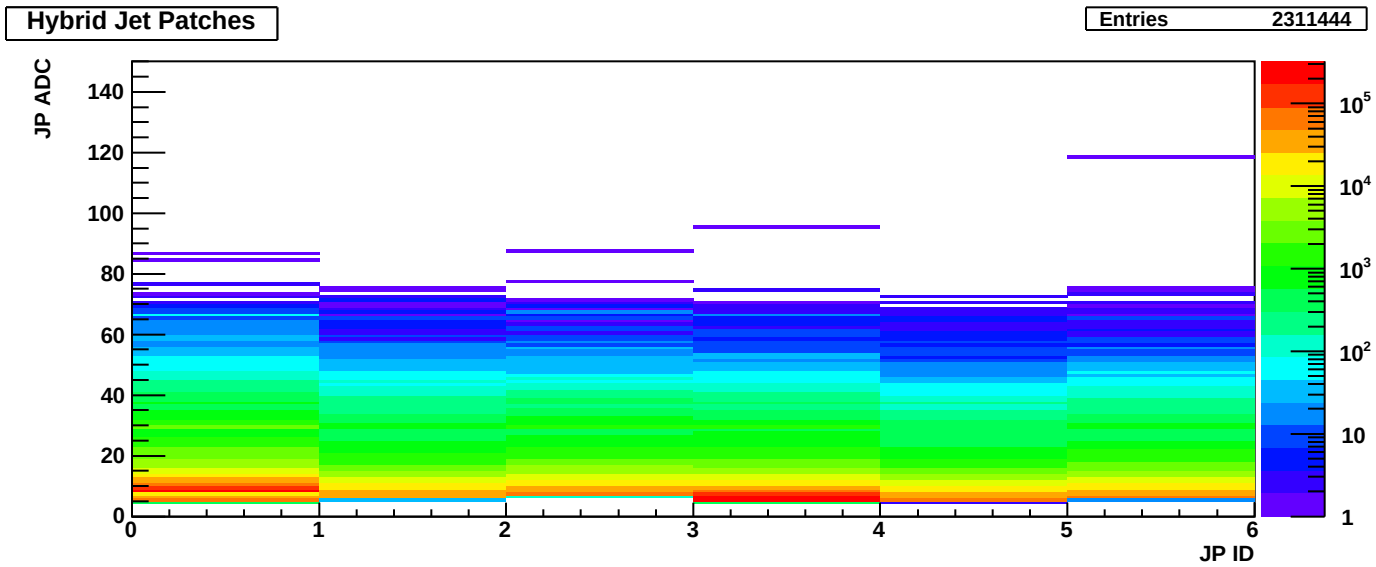

Barrel EMC L0 Input - Patch Sum
Entries 3.467166e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

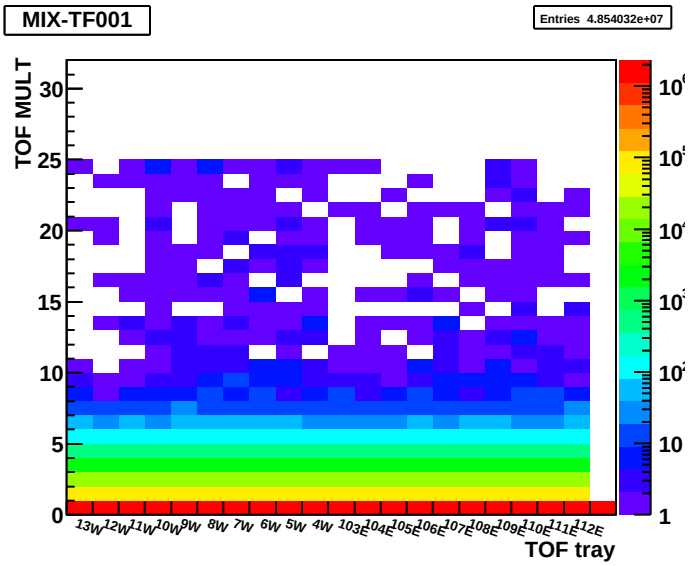

Endcap EMC L0 Input - High Tower
Entries 1.04015e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

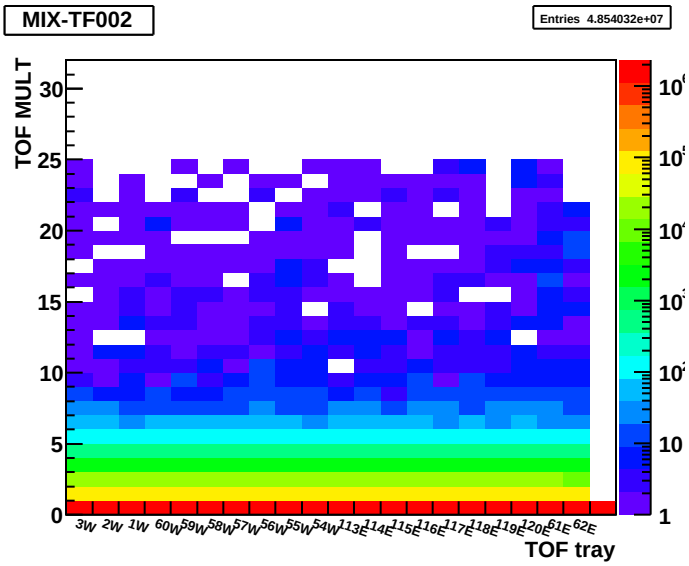

Endcap EMC L0 Input - Patch Sum
Entries 1.04015e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

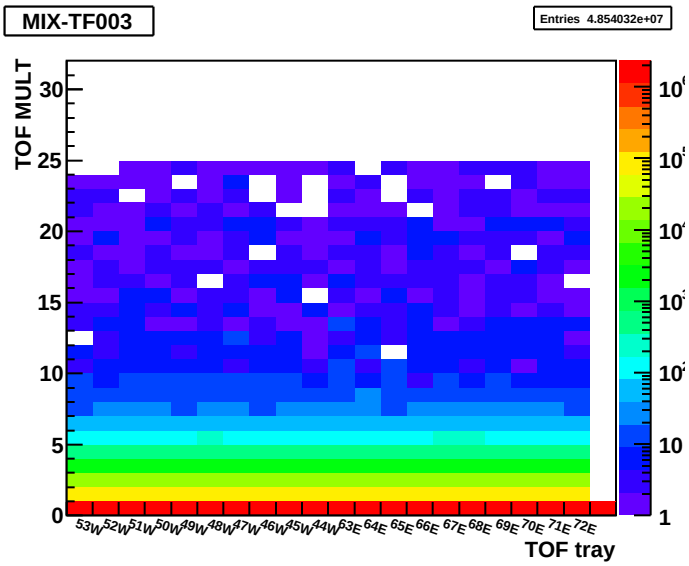

Barrel Jet Patches
Entries 2.0803e+07
JP ADC
JP ID

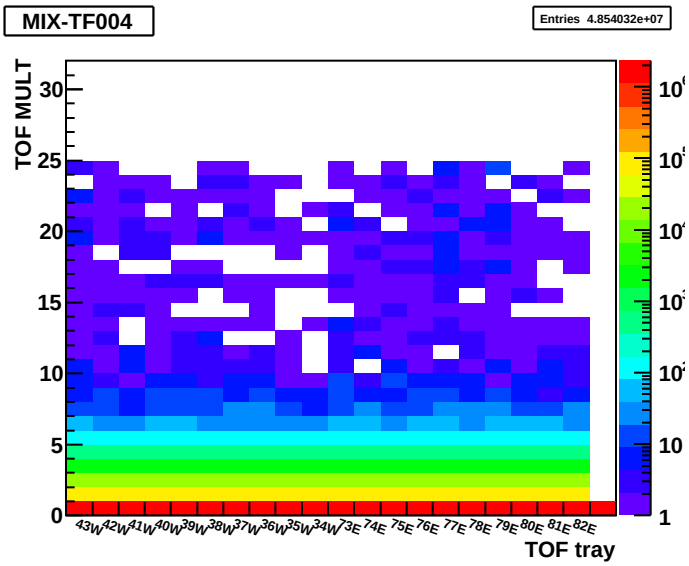

Endcap Jet Patches
Entries 6934332
JP ADC
JP ID

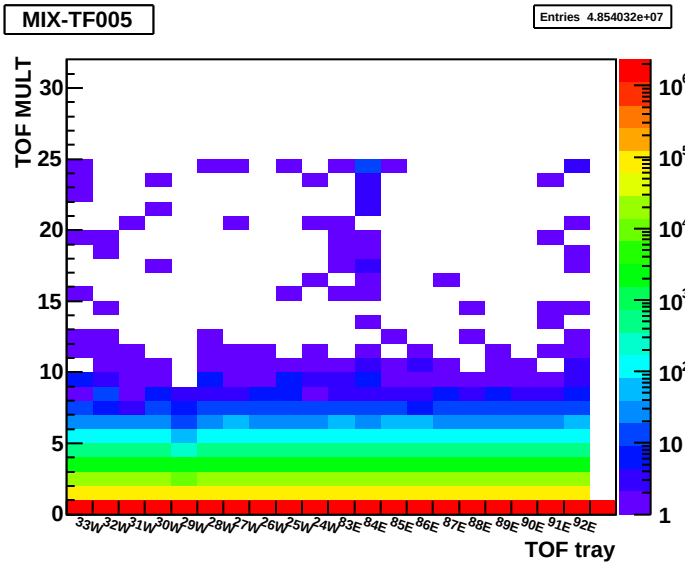

Hybrid Jet Patches
Entries 2311444
JP ADC
JP ID

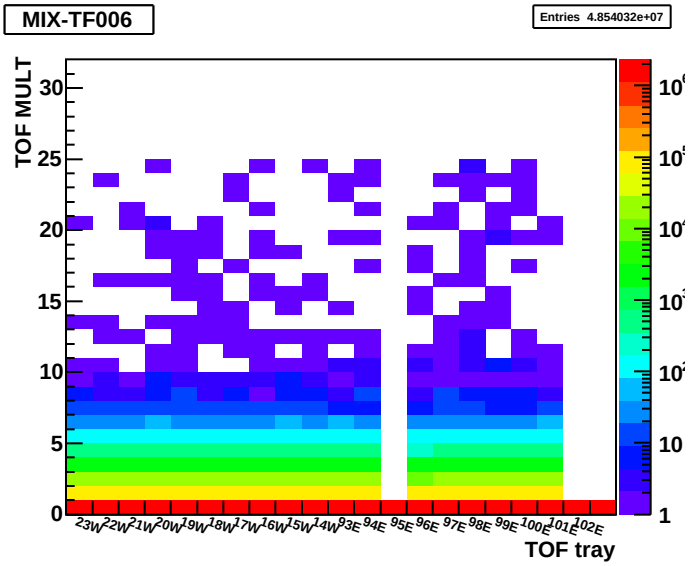

MIX-TF001
Entries 4.854032e+07
TOF MULT
TOF tray

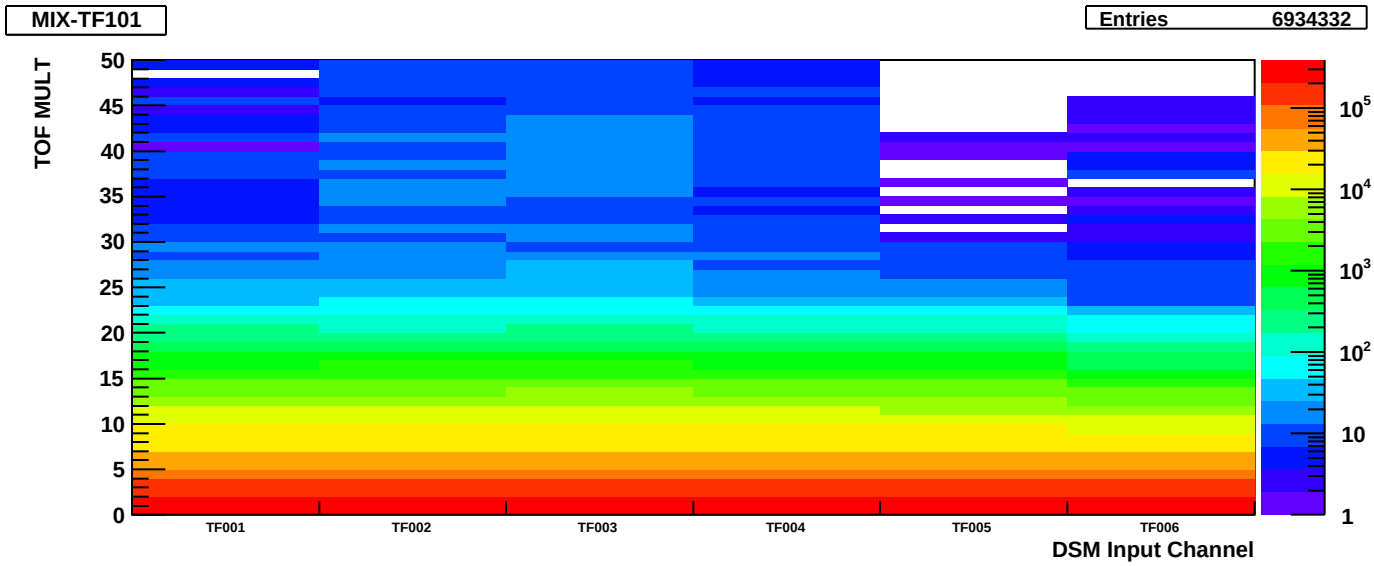

MIX-TF002
Entries 4.854032e+07
TOF MULT
TOF tray

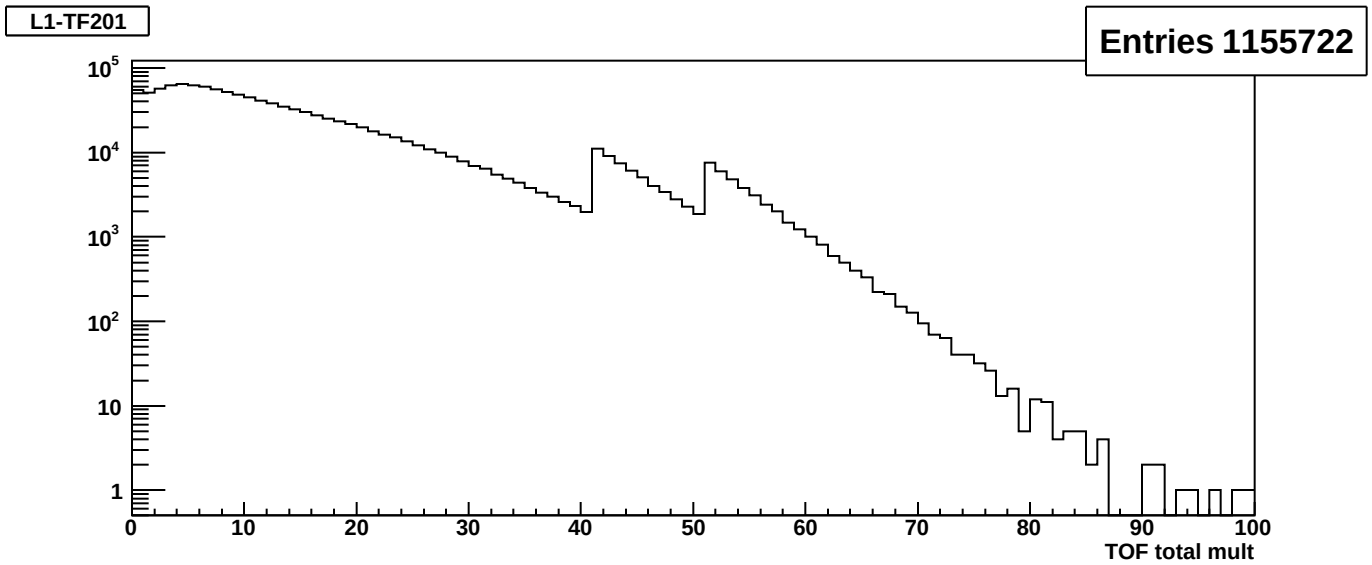

MIX-TF003
Entries 4.854032e+07
TOF MULT
TOF tray

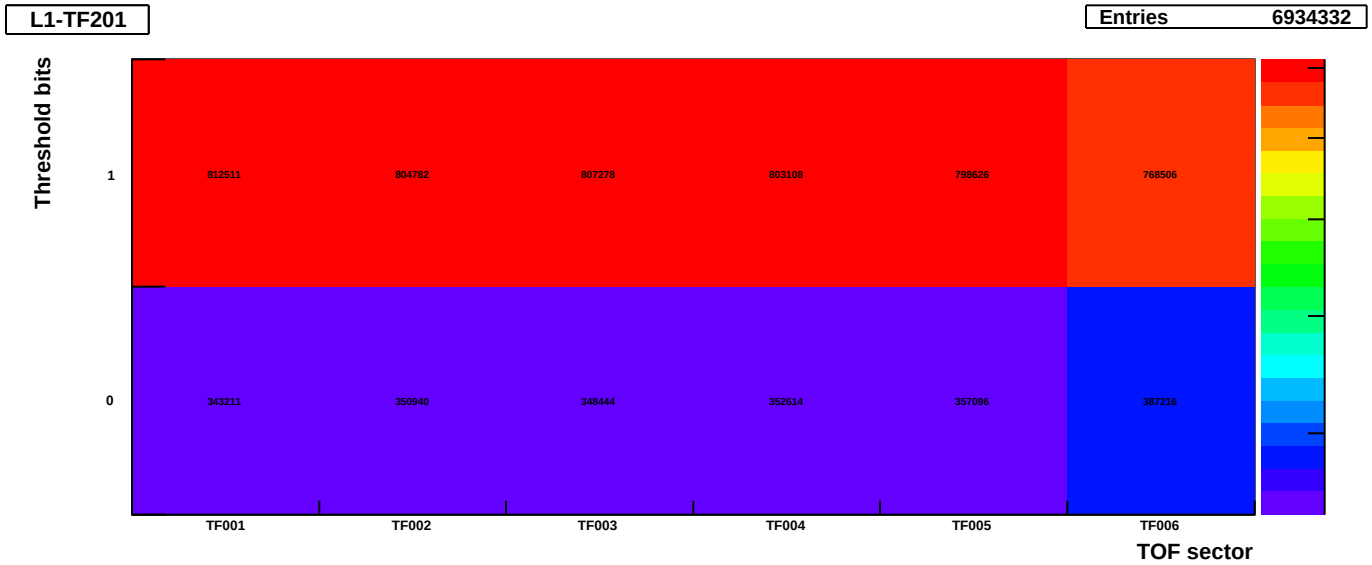

MIX-TF004
Entries 4.854032e+07
TOF MULT
TOF tray

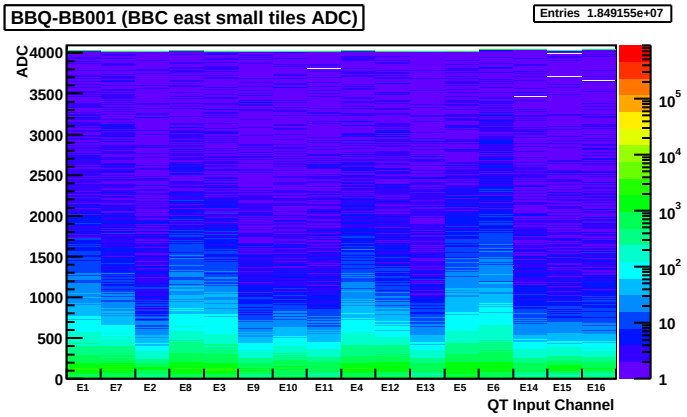

MIX-TF005
Entries 4.854032e+07
TOF MULT
TOF tray

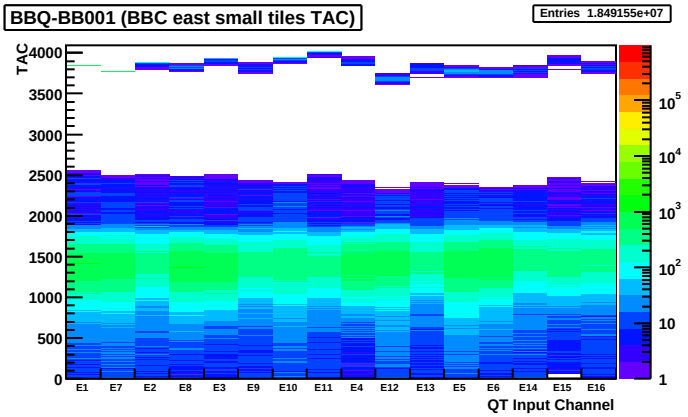

MIX-TF006
Entries 4.854032e+07
TOF MULT
TOF tray

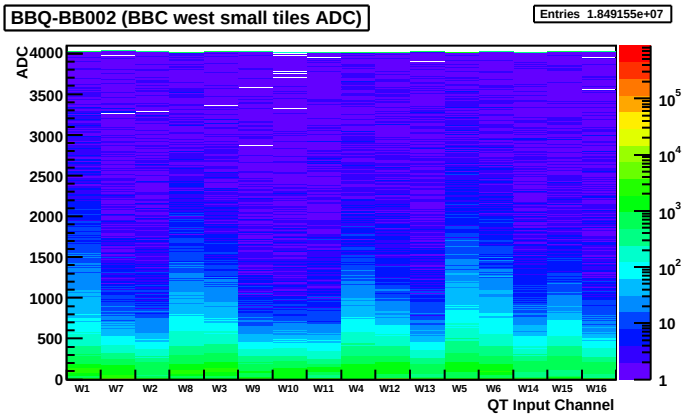

MIX-TF101
Entries 6934332
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

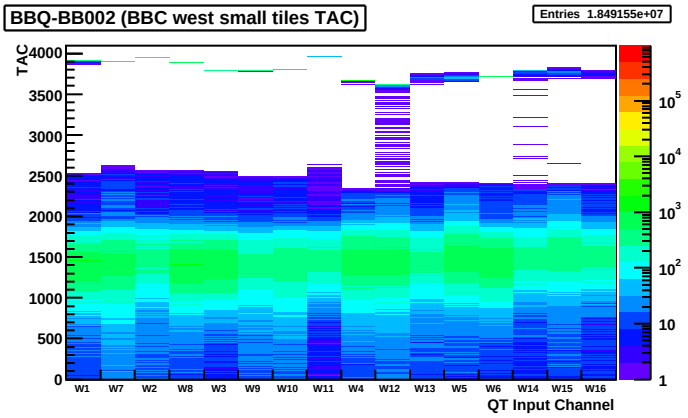

L1-TF201
Entries 1155722
TOF total mult

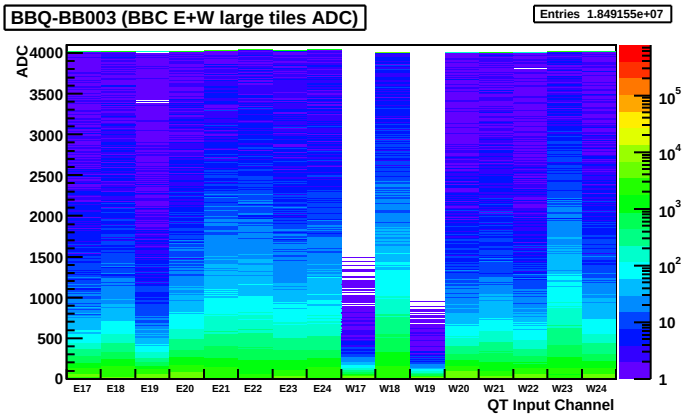

L1-TF201
Entries 6934332
Threshold bits
812511
804782
807278
803108
798626
768506
343211
350940
348444
352614
357096
387216
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

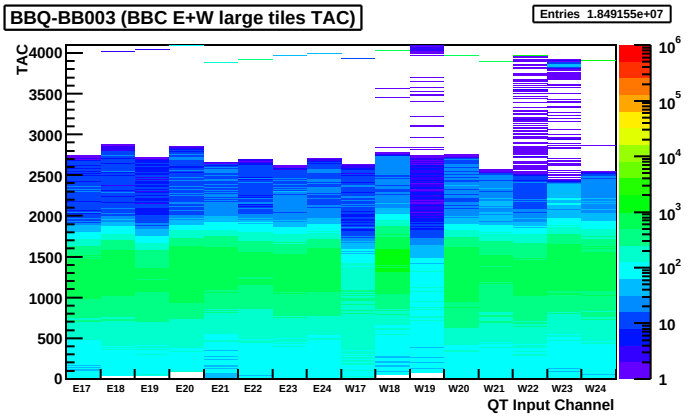

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.849155e+07
ADC
QT Input Channel

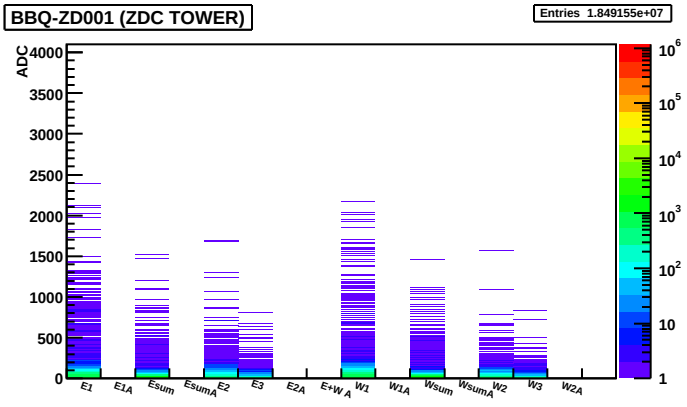

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.849155e+07
TAC
QT Input Channel

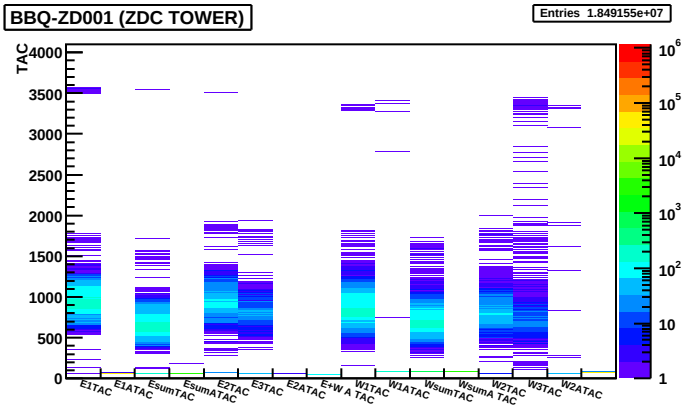

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.849155e+07
ADC
QT Input Channel

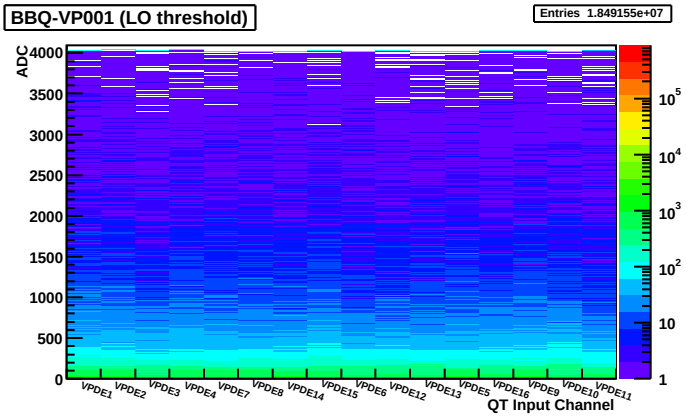

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.849155e+07
TAC
QT Input Channel

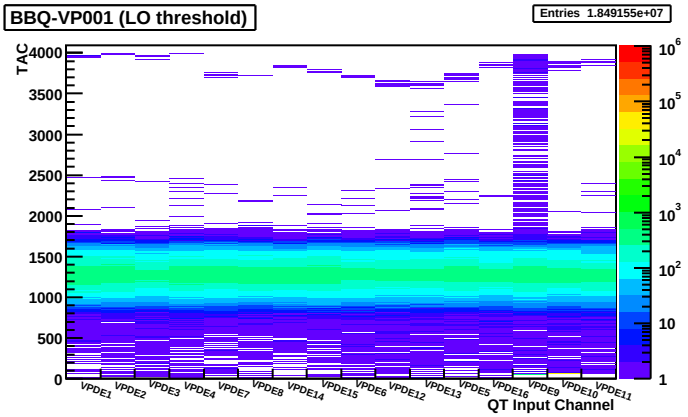

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.849155e+07
ADC
QT Input Channel

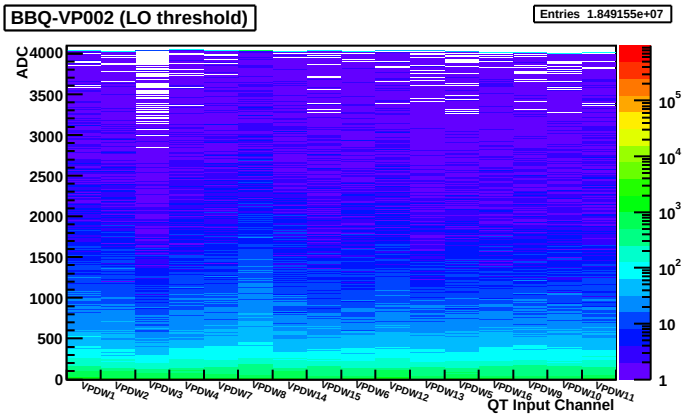

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.849155e+07
TAC
QT Input Channel

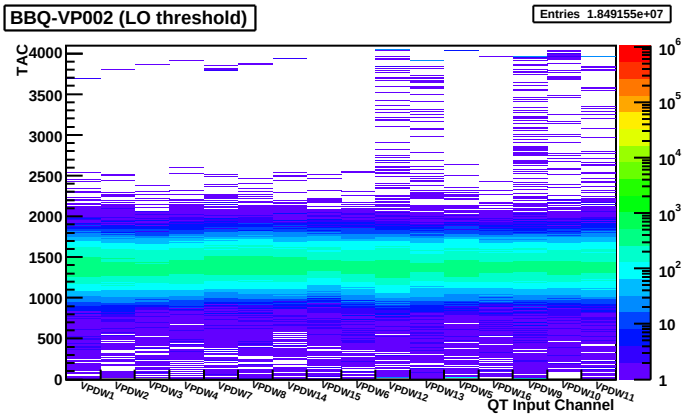

BBQ-ZD001 (ZDC TOWER)
Entries 1.849155e+07
ADC

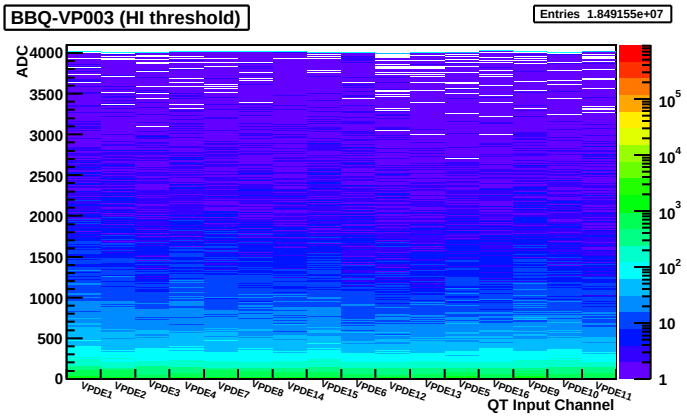

BBQ-ZD001 (ZDC TOWER)
Entries 1.849155e+07
TAC

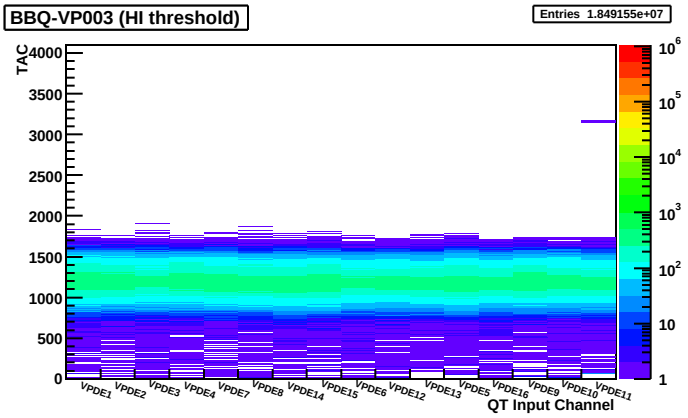

BBQ-VP001 (LO threshold)
Entries 1.849155e+07
ADC
QT Input Channel

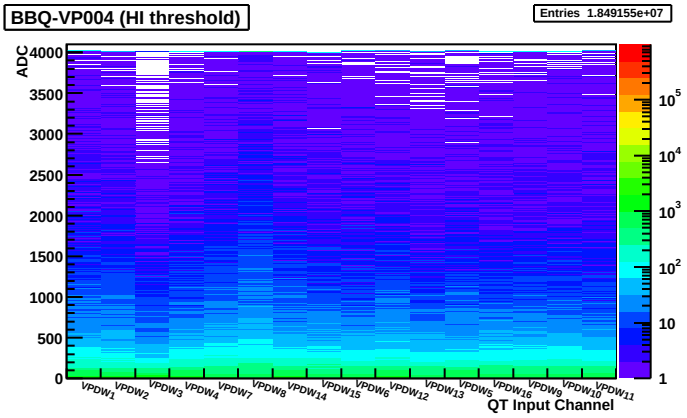

BBQ-VP001 (LO threshold)
Entries 1.849155e+07
TAC
QT Input Channel

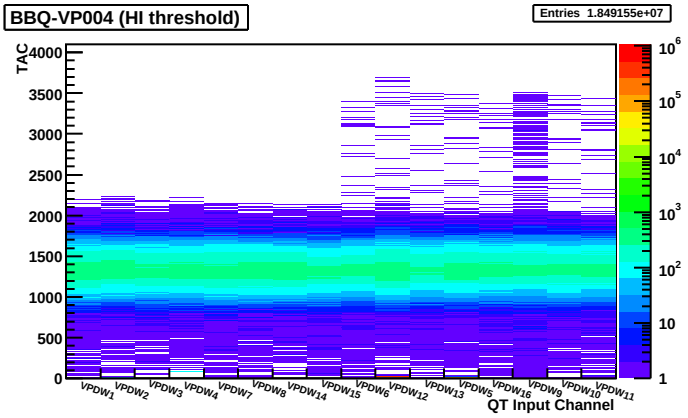

BBQ-VP002 (LO threshold)
Entries 1.849155e+07
ADC
QT Input Channel

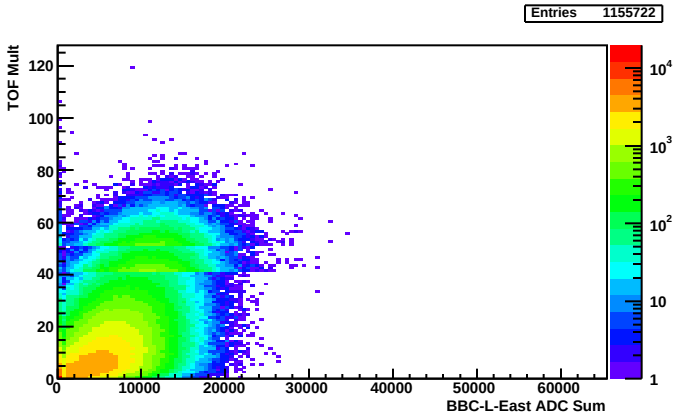

BBQ-VP002 (LO threshold)
Entries 1.849155e+07
TAC
QT Input Channel

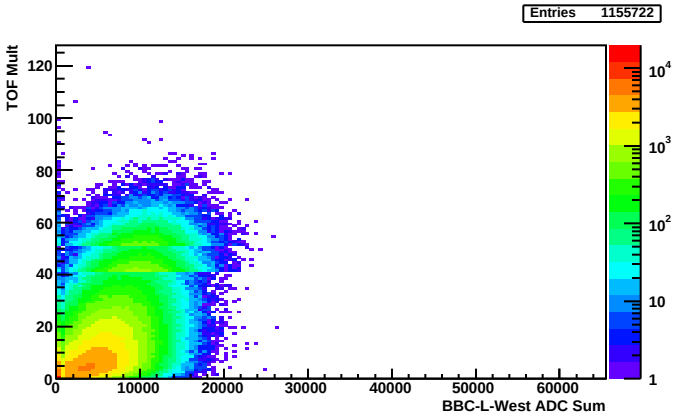

BBQ-VP003 (HI threshold)
Entries 1.849155e+07
ADC
QT Input Channel

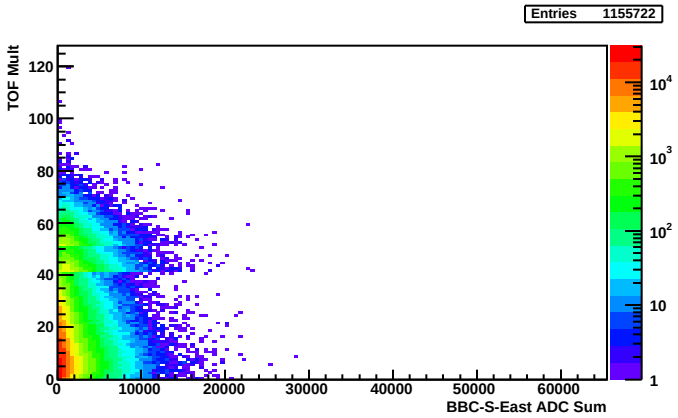

BBQ-VP003 (HI threshold)
Entries 1.849155e+07
TAC
QT Input Channel

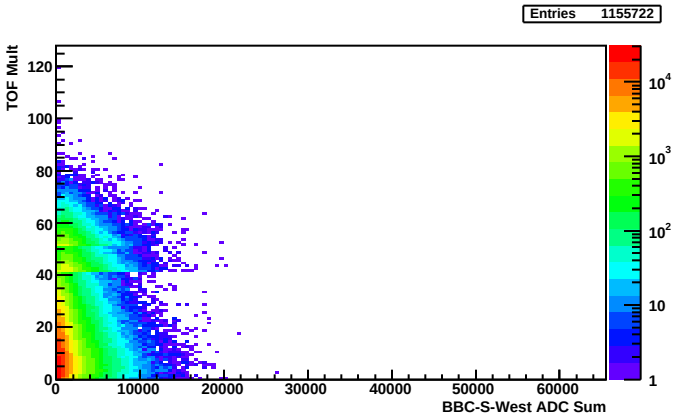

BBQ-VP004 (HI threshold)
Entries 1.849155e+07
ADC
QT Input Channel

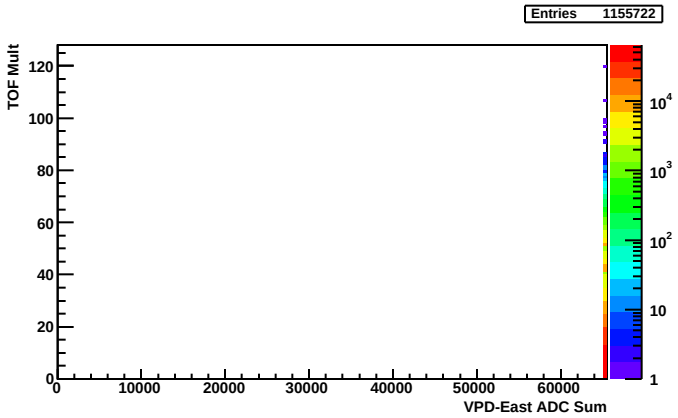

BBQ-VP004 (HI threshold)
Entries 1.849155e+07
TAC
QT Input Channel

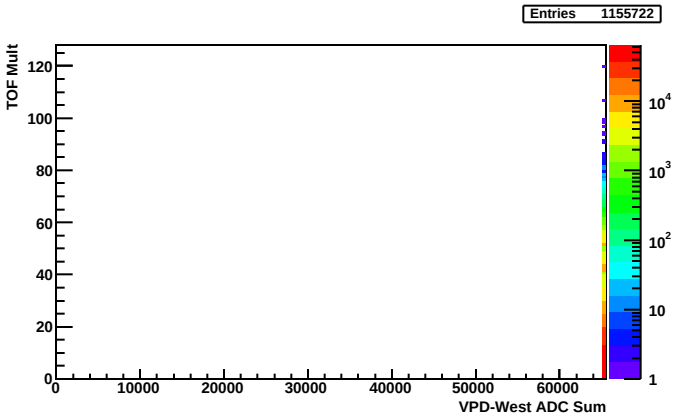

Entries 1155722
TOF Mult
BBC-L-East ADC Sum

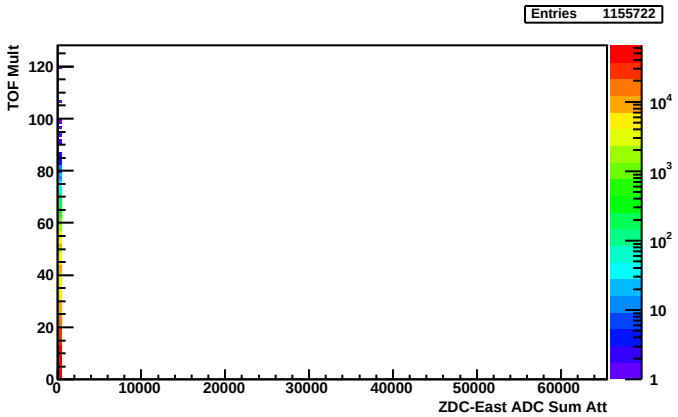

Entries 1155722
TOF Mult
BBC-L-West ADC Sum

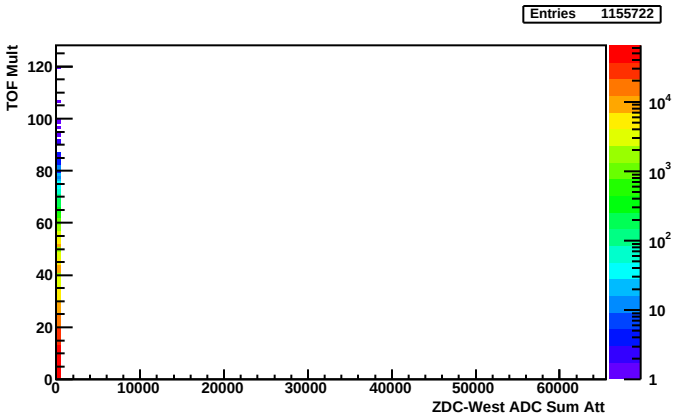

Entries 1155722
TOF Mult
BBC-S-East ADC Sum

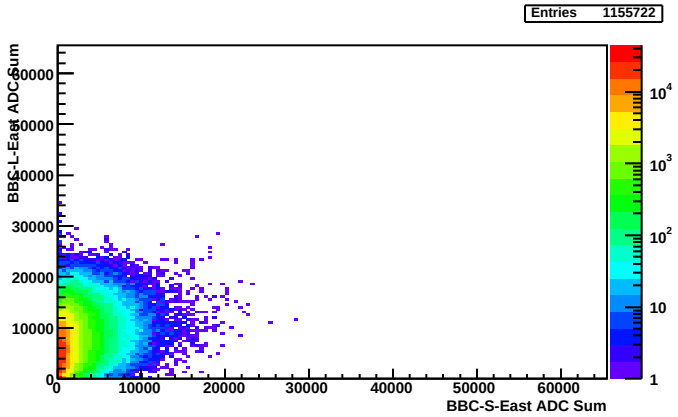

Entries 1155722
TOF Mult
BBC-S-West ADC Sum

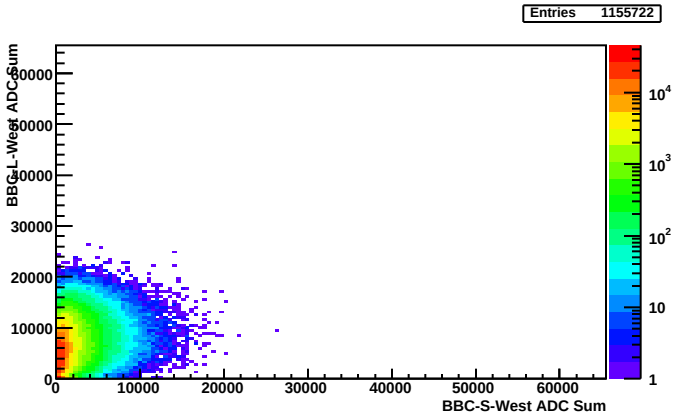

Entries 1155722
TOF Mult
VPD-East ADC Sum

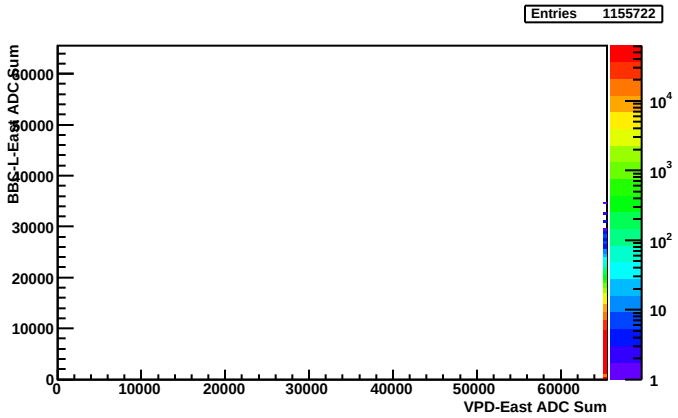

Entries 1155722
TOF Mult
VPD-West ADC Sum

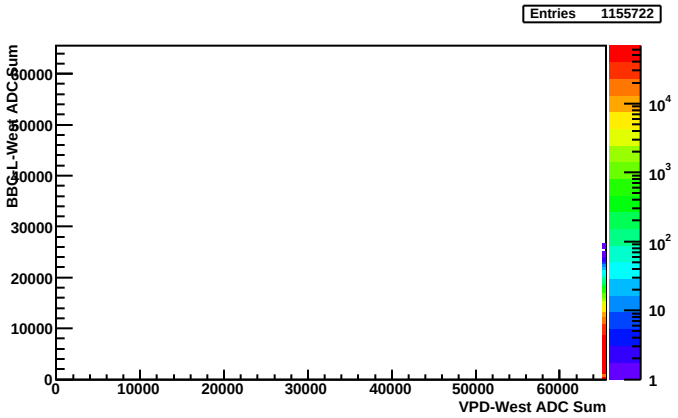

Entries 1155722
TOF Mult
ZDC-East ADC Sum Att

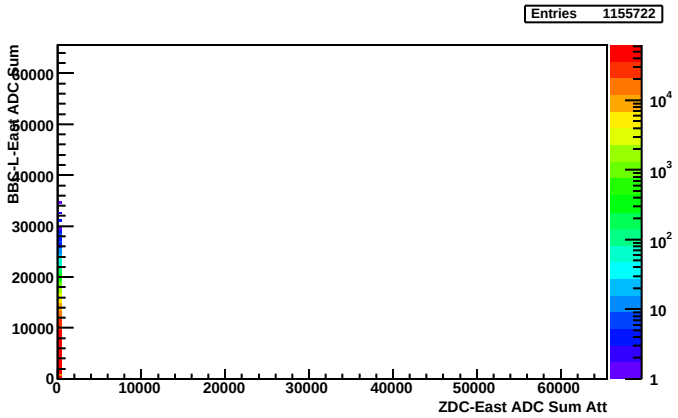

Entries 1155722
TOF Mult
ZDC-West ADC Sum Att

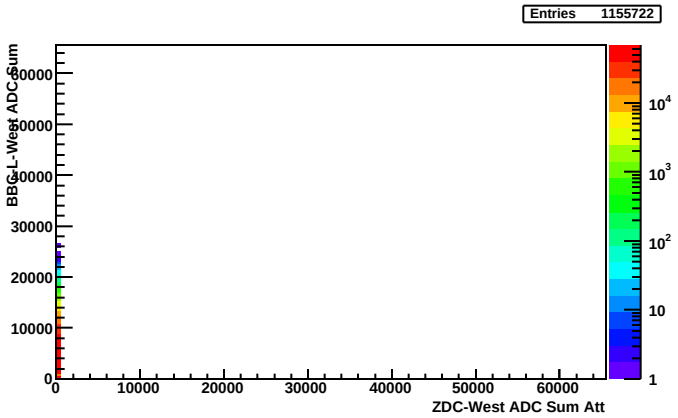

Entries 1155722
BBC-L-East ADC Sum
BBC-S-East ADC Sum

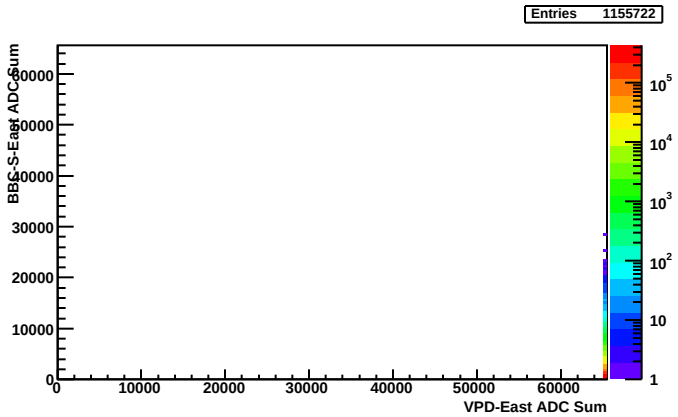

Entries 1155722
BBC-L-West ADC Sum
BBC-S-West ADC Sum

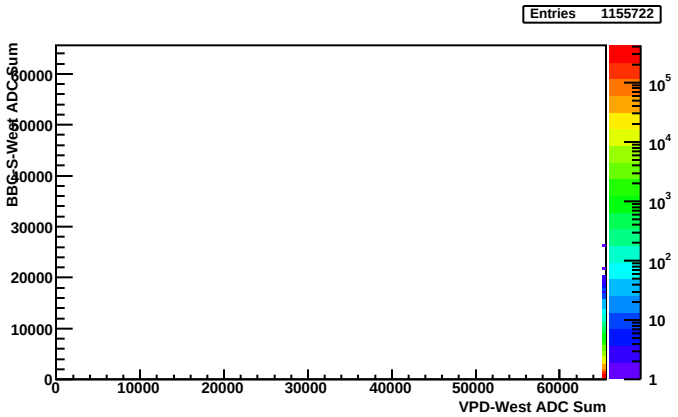

Entries 1155722
BBC-L-East ADC Sum
VPD-East ADC Sum

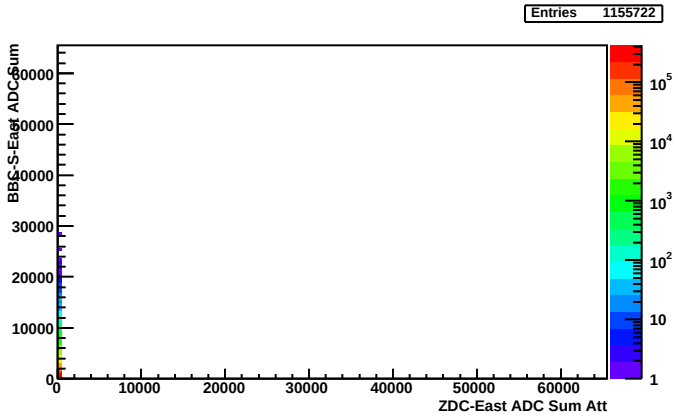

Entries 1155722
BBC-L-West ADC Sum
VPD-West ADC Sum

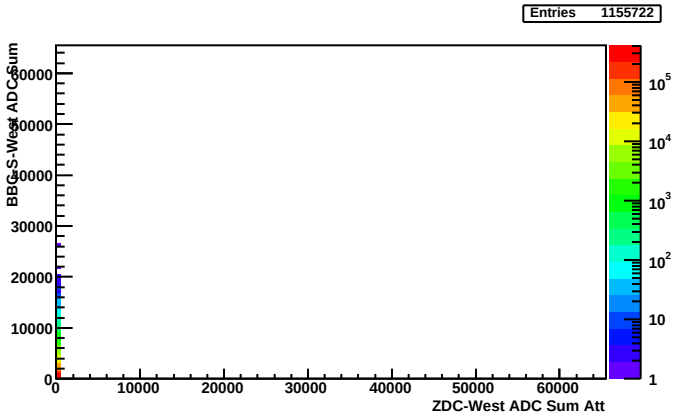

Entries 1155722
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

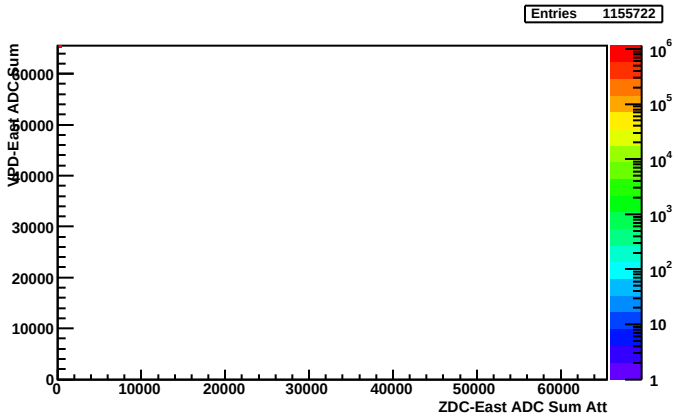

Entries 1155722
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

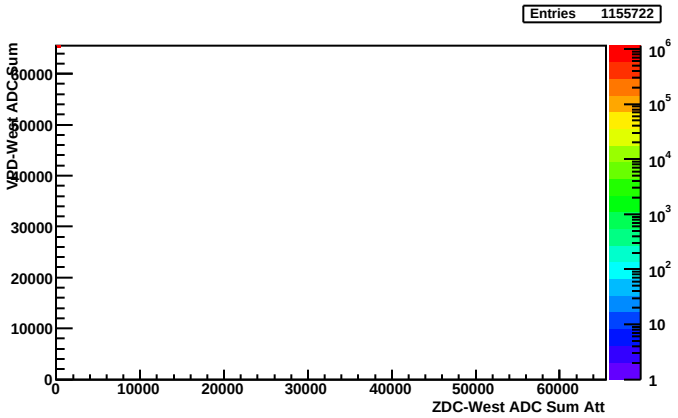

Entries 1155722
BBC-S-East ADC Sum
VPD-East ADC Sum

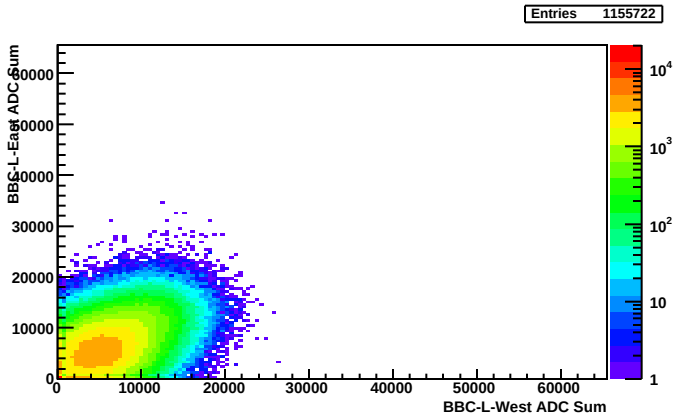

Entries 1155722
BBC-S-West ADC Sum
VPD-West ADC Sum

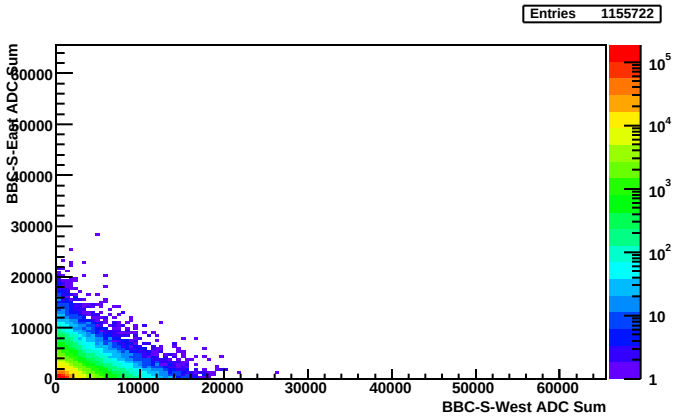

Entries 1155722
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

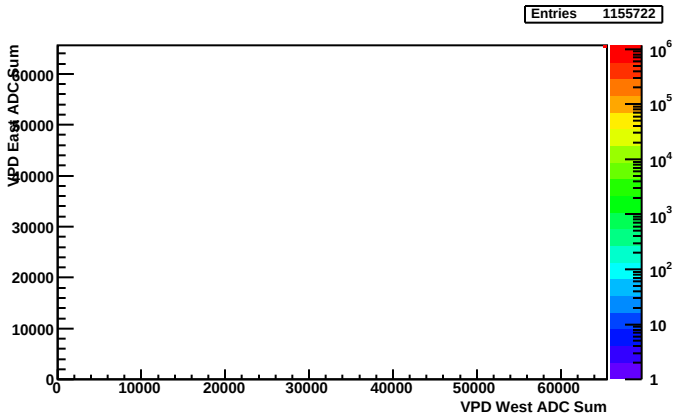

Entries 1155722
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

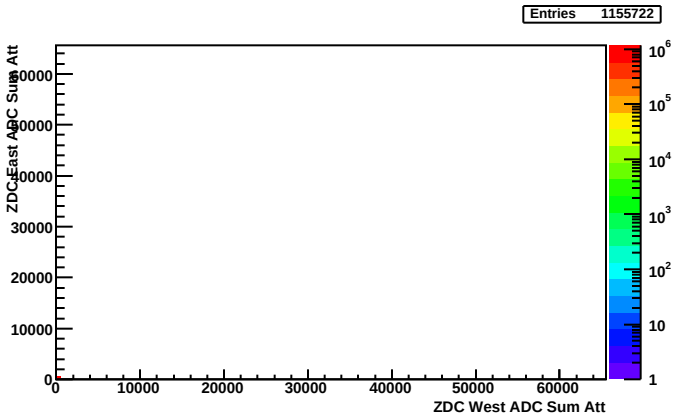

Entries 1155722
VPD-East ADC Sum
ZDC-East ADC Sum Att

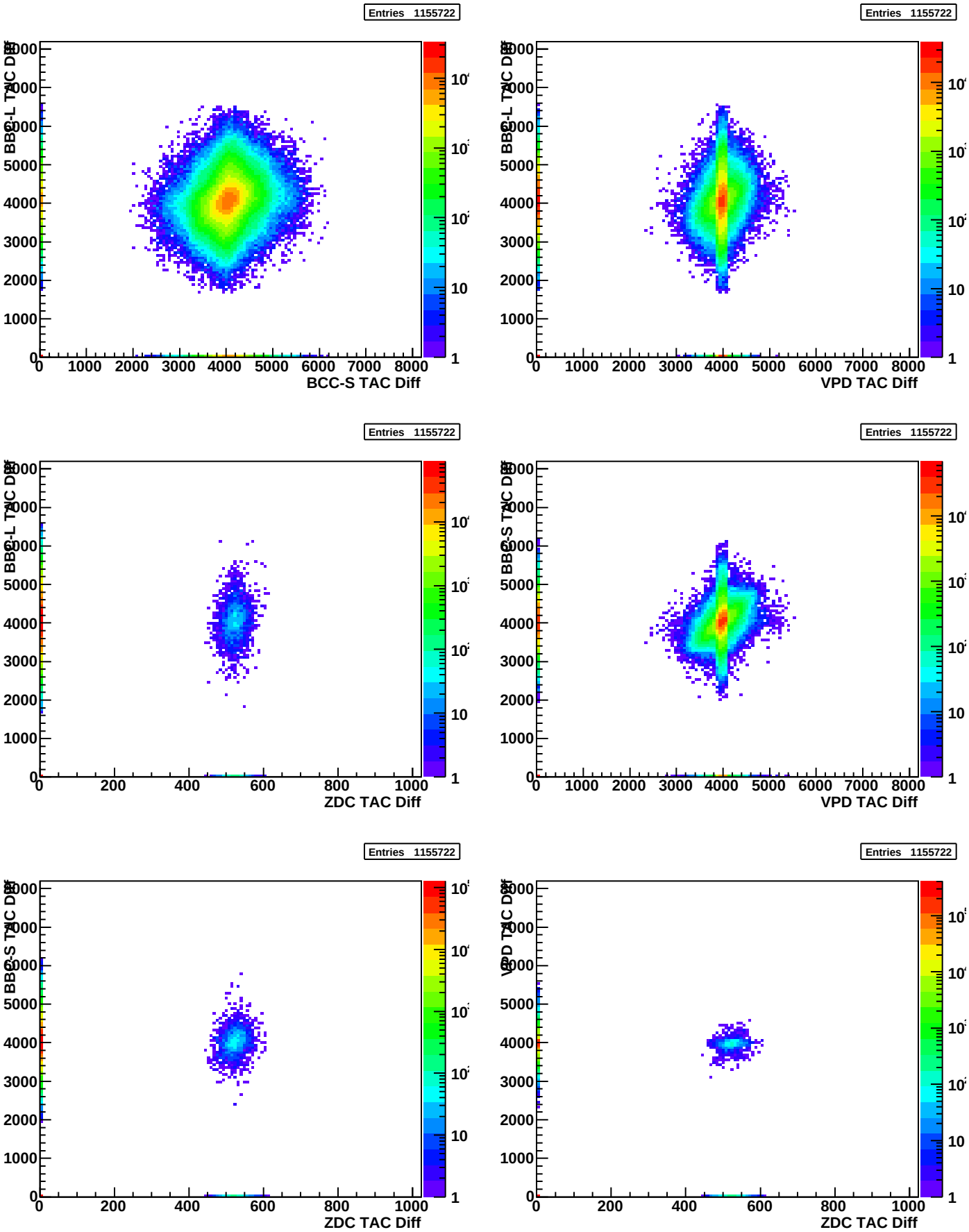

Entries 1155722
VPD-West ADC Sum
ZDC-West ADC Sum Att

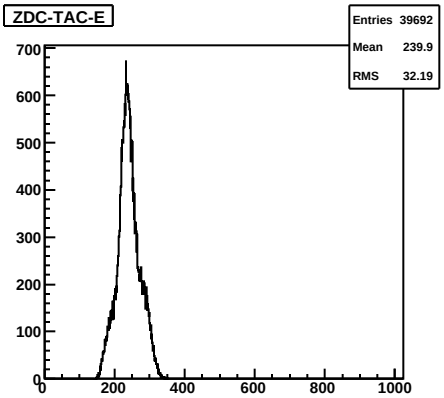

Entries 1155722
BBC-L-East ADC Sum
BBC-L-West ADC Sum

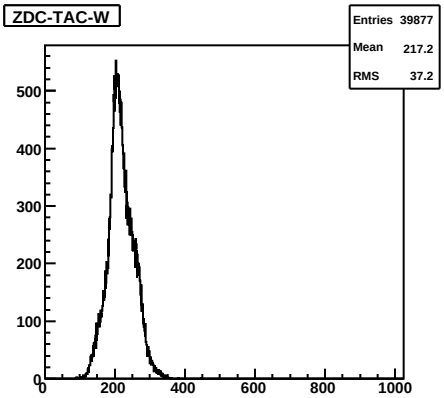

Entries 1155722
BBC-S-East ADC Sum
BBC-S-West ADC Sum

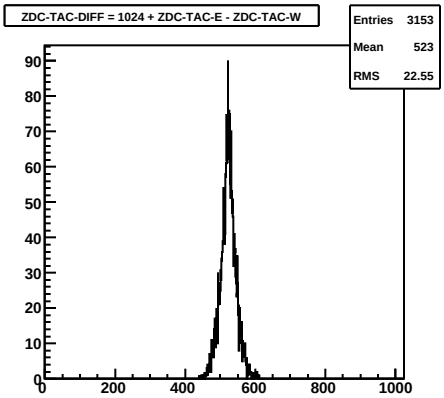

Entries 1155722
VPD East ADC Sum
VPD West ADC Sum

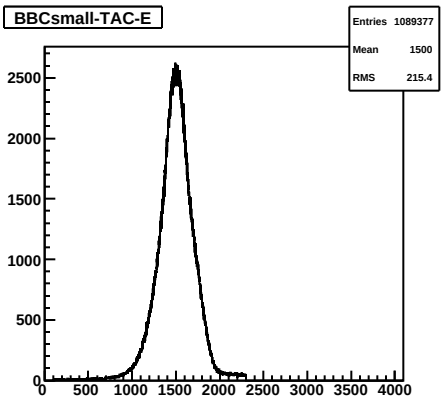

Entries 1155722
ZDC East ADC Sum Att
ZDC West ADC Sum Att

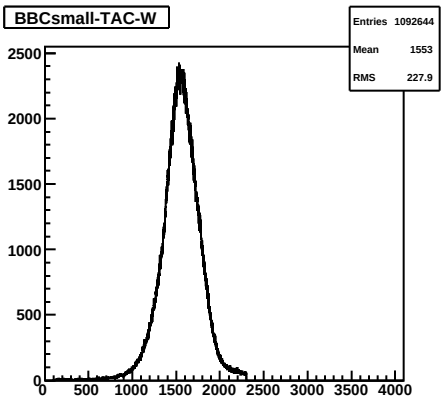

Entries 1155722
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1155722
BBC-L TAC Diff
VPD TAC Diff
Entries 1155722
BBC-L TAC Diff
ZDC TAC Diff
Entries 1155722
BBC-S TAC Diff
VPD TAC Diff
Entries 1155722
BBC-S TAC Diff
ZDC TAC Diff
Entries 1155722
VPD TAC Diff
ZDC TAC Diff

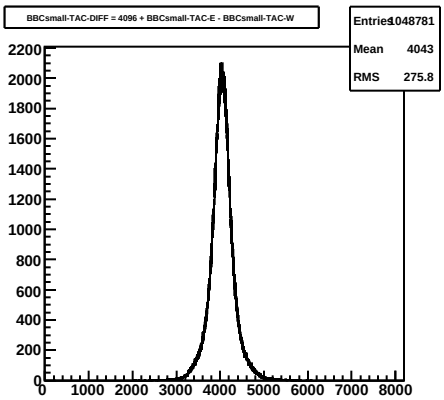

ZDC-TAC-E
Entries 39692
Mean 239.9
RMS 32.19

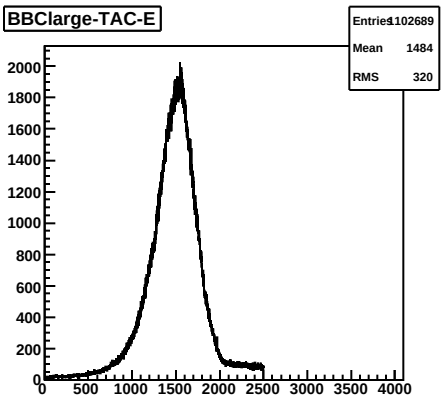

ZDC-TAC-W
Entries 39877
Mean 217.2
RMS 37.2

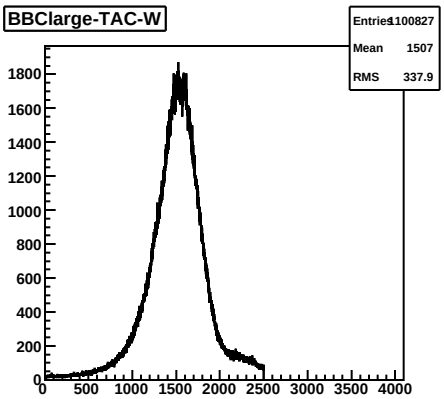

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3153
Mean 523
RMS 22.55

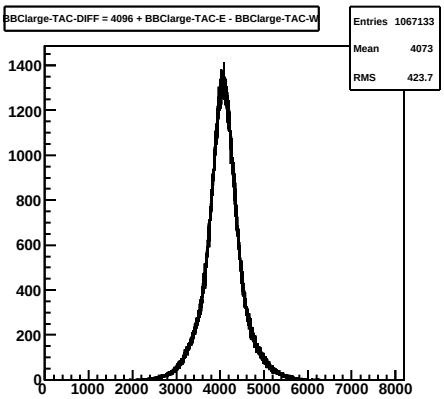

BBCsmall-TAC-E
Entries 1089377
Mean 1500
RMS 215.4

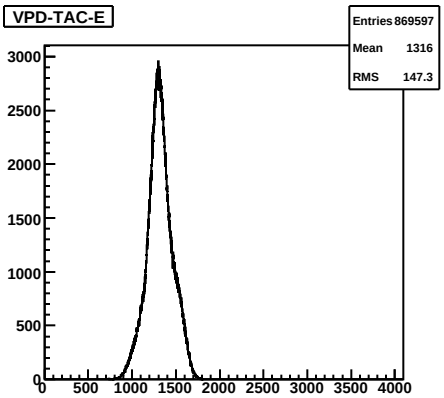

BBCsmall-TAC-W
Entries 1092644
Mean 1553
RMS 227.9

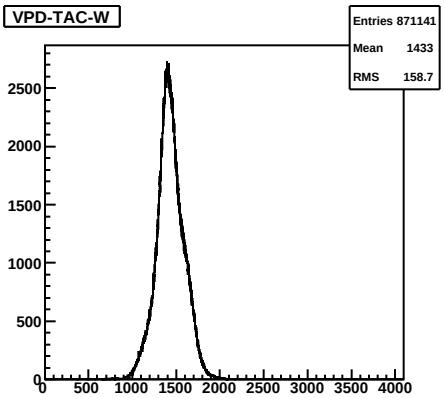

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1048781
Mean 4043
RMS 275.8

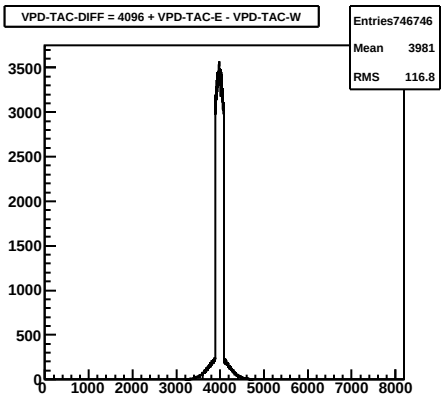

BBClarge-TAC-E
Entries 1102689
Mean 1484
RMS 320

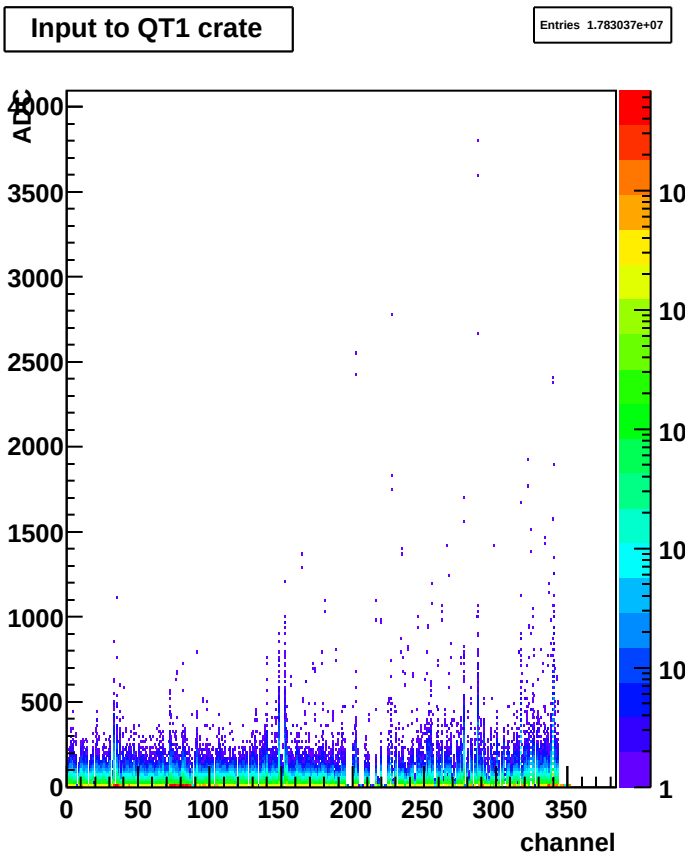

BBClarge-TAC-W
Entries 1100827
Mean 1507
RMS 337.9

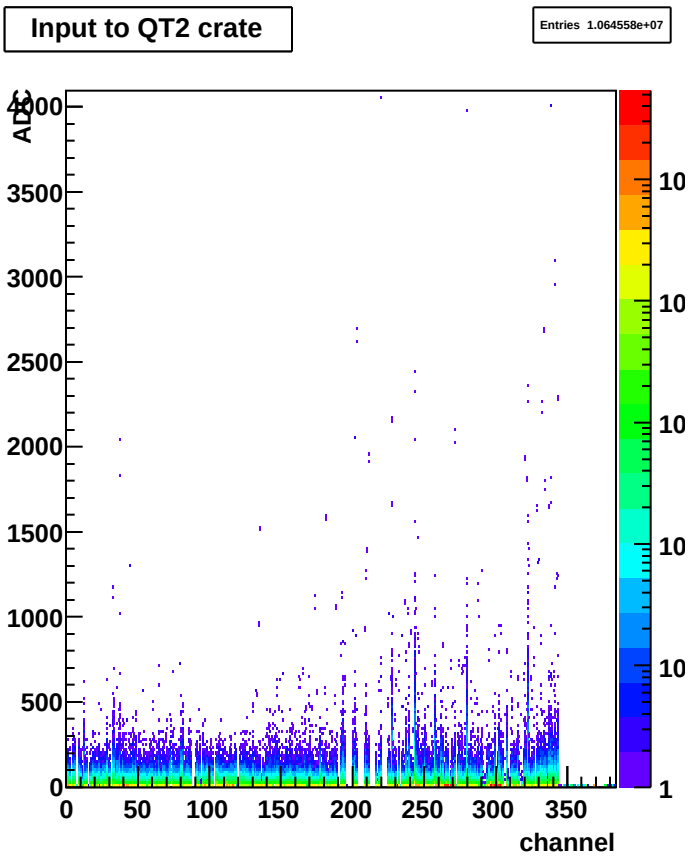

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1067133
Mean 4073
RMS 423.7

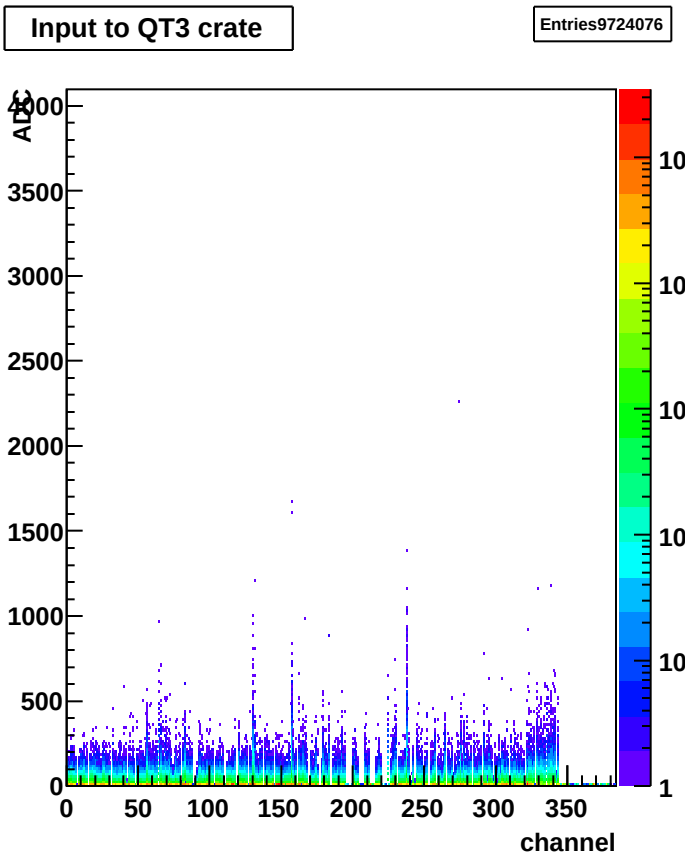

VPD-TAC-E
Entries 869597
Mean 1316
RMS 147.3

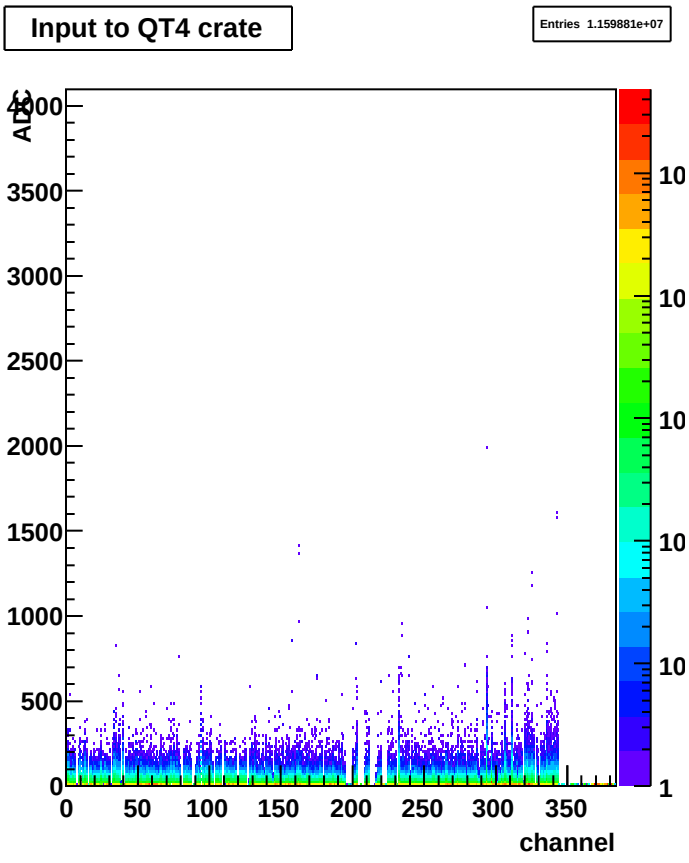

VPD-TAC-W
Entries 871141
Mean 1433
RMS 158.7

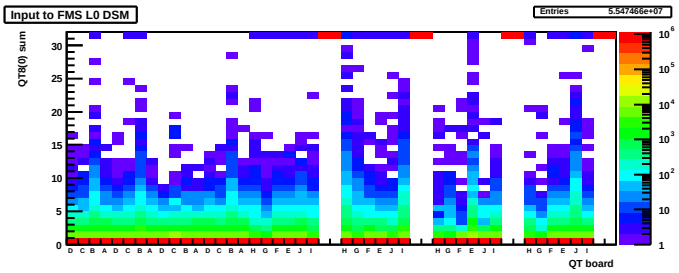

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 746746
Mean 3981
RMS 116.8

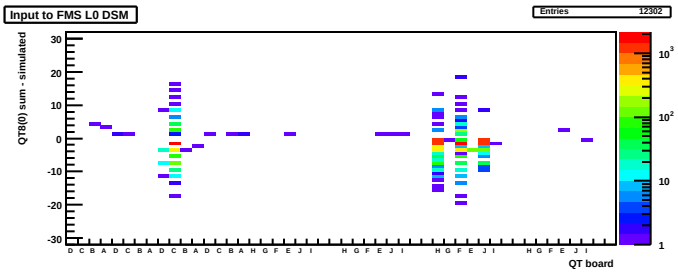

Input to QT1 crate
Entries 1.783037e+07
ADC
channel

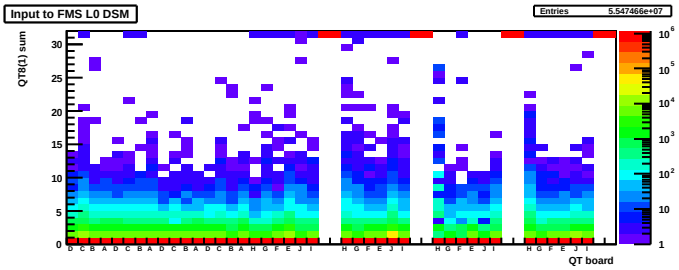

Input to QT2 crate
Entries 1.064558e+07
ADC
channel

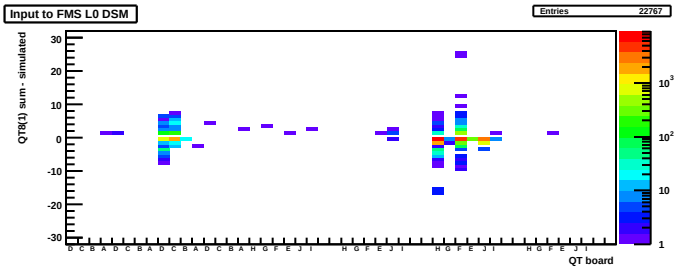

Input to QT3 crate
Entries9724076
ADC
channel

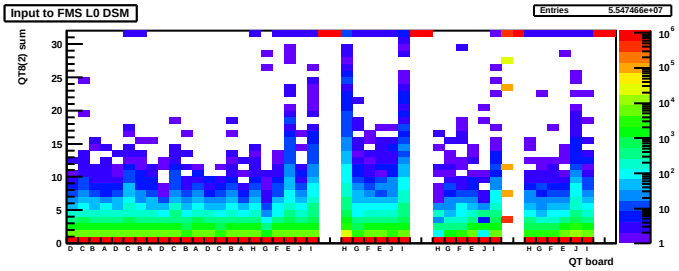

Input to QT4 crate
Entries 1.159881e+07
ADC
channel

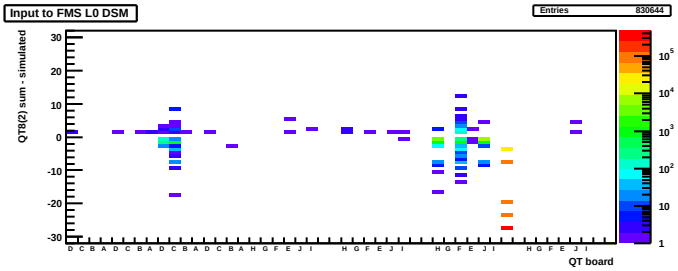

Input to FMS L0 DSM
Entries 5.547466e+07
QT8(0) sum
QT board

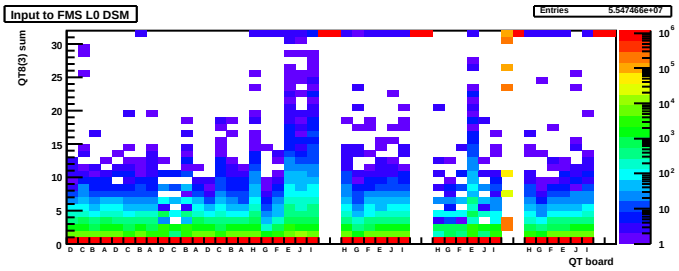

Input to FMS L0 DSM
Entries 12302
QT8(0) sum - simulated
QT board

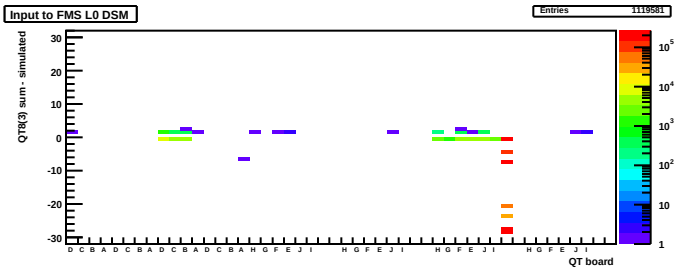

Input to FMS L0 DSM
Entries 5.547466e+07
QT8(1) sum
QT board

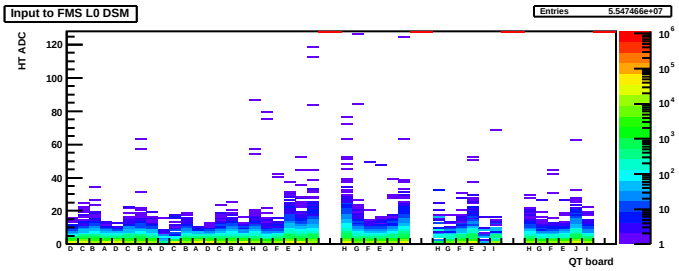

Input to FMS L0 DSM
Entries 22767
QT8(1) sum - simulated
QT board

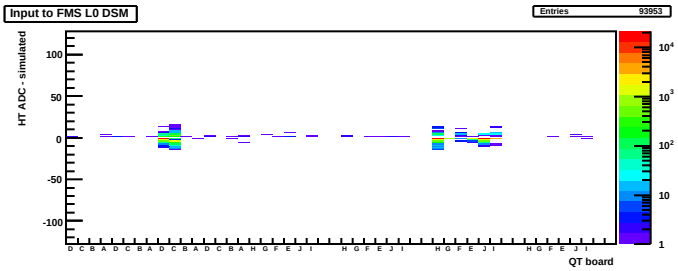

Input to FMS L0 DSM
Entries 5.547466e+07
QT8(2) sum
QT board

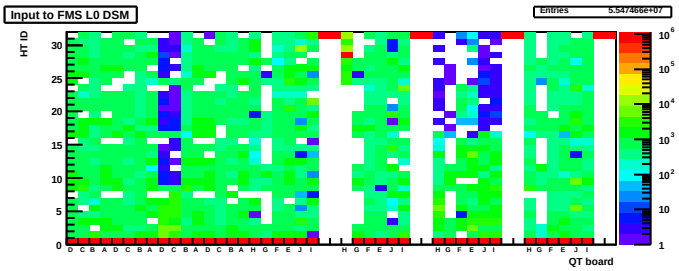

Input to FMS L0 DSM
Entries 830644
QT8(2) sum - simulated
QT board

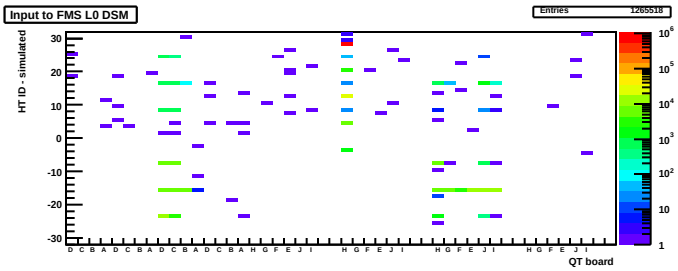

Input to FMS L0 DSM
Entries 5.547466e+07
QT8(3) sum
QT board

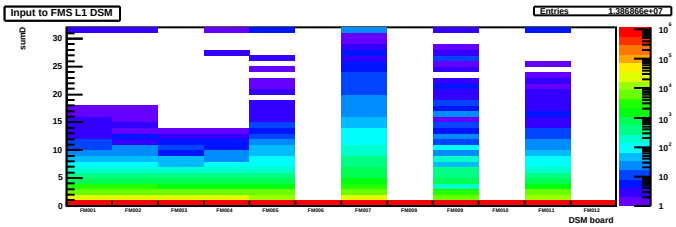

Input to FMS L0 DSM
Entries 1119581
QT8(3) sum - simulated
QT board

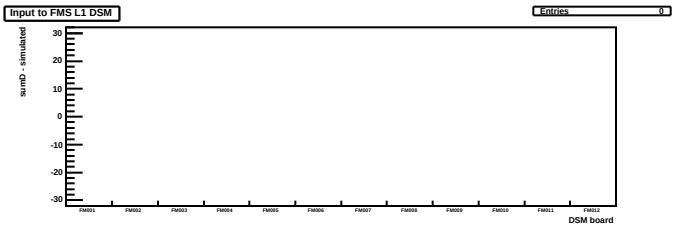

Input to FMS L0 DSM
Entries 5.547466e+07
HT ADC
QT board

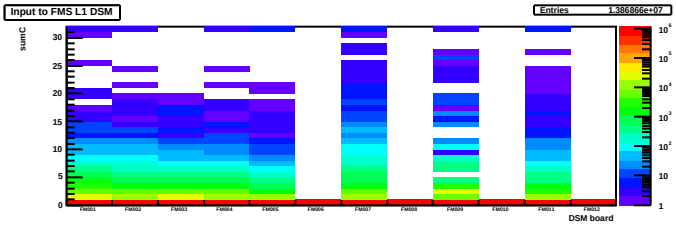

Input to FMS L0 DSM
Entries 93953
HT ADC - simulated
QT board

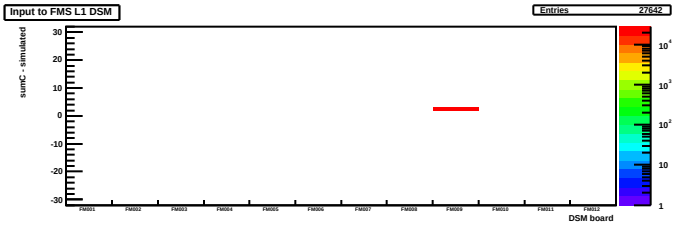

Input to FMS L0 DSM
Entries 5.547466e+07
HT ID
QT board

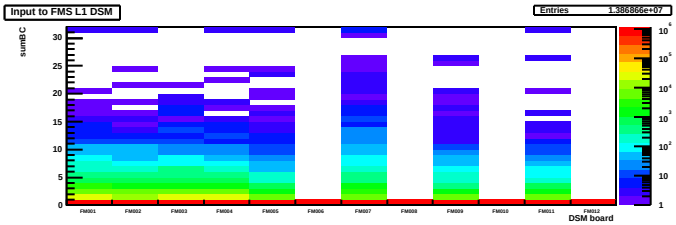

Input to FMS L0 DSM
Entries 1265518
HT ID - simulated
QT board

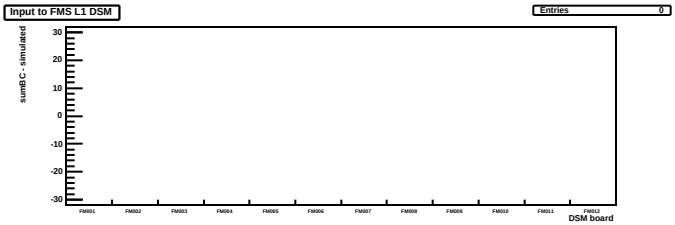

Input to FMS L1 DSM
Entries 1.386866e+07
sumD
DSM board

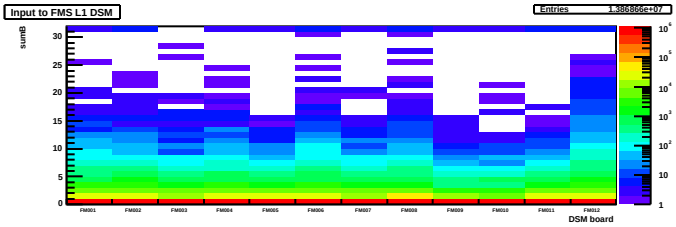

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

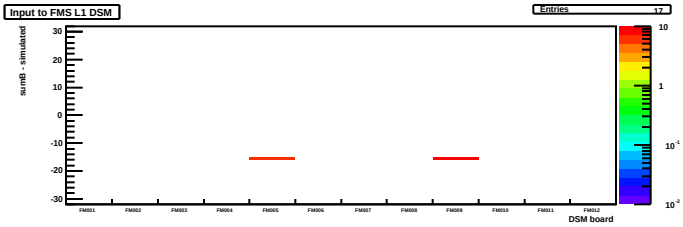

Input to FMS L1 DSM
Entries 1.386866e+07
sumC
DSM board

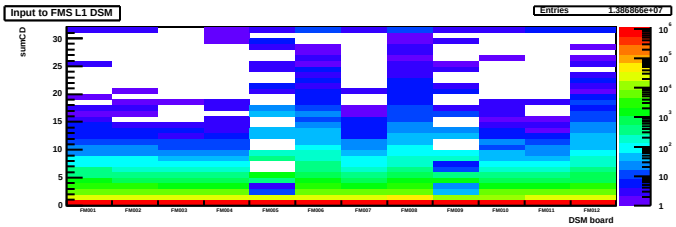

Input to FMS L1 DSM
Entries 27642
sumC - simulated
DSM board

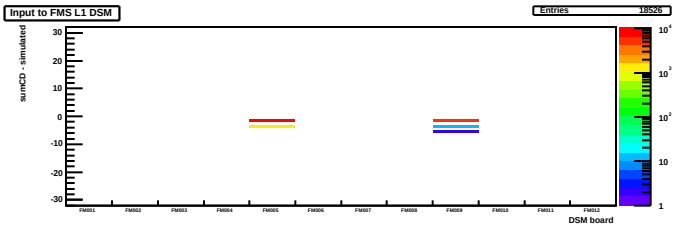

Input to FMS L1 DSM
Entries 1.386866e+07
sumBC
DSM board

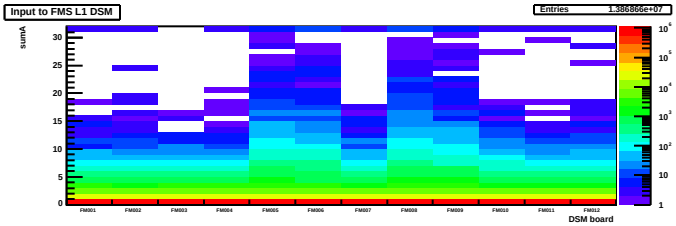

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

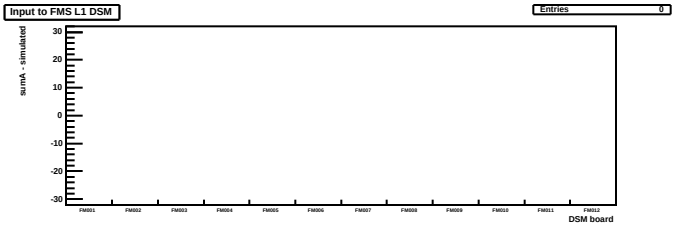

Input to FMS L1 DSM
Entries 1.386866e+07
sumB
DSM board

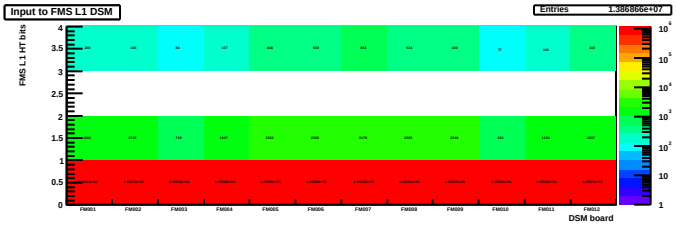

Input to FMS L1 DSM
Entries 17
sumB - simulated
DSM board

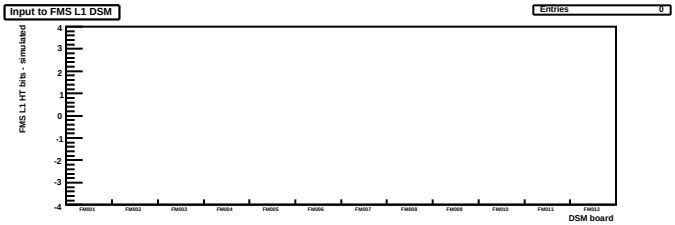

Input to FMS L1 DSM
Entries 1.386866e+07
sumCD
DSM board

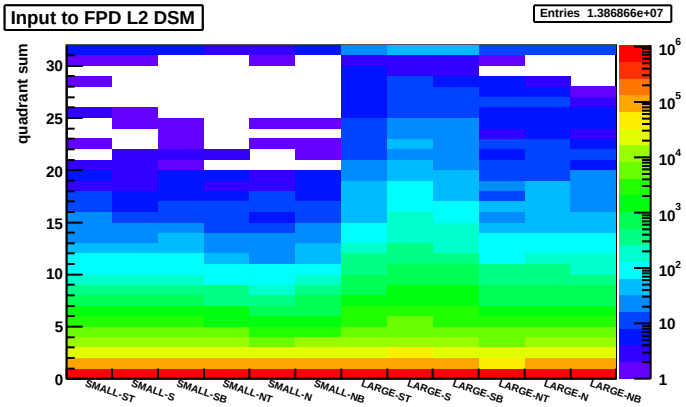

Input to FMS L1 DSM
Entries 18526
sumCD - simulated
DSM board

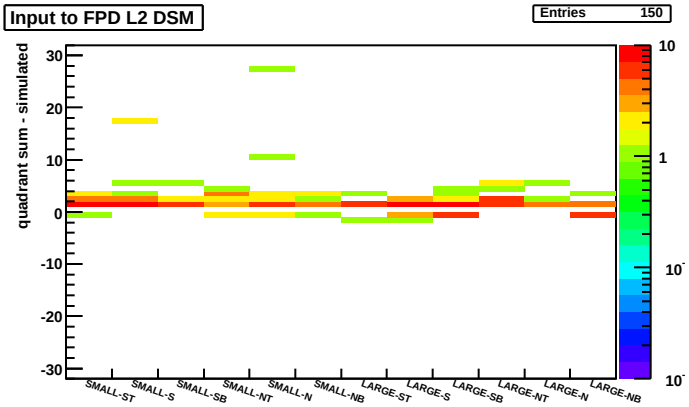

Input to FMS L1 DSM
Entries 1.386866e+07
sumA
DSM board

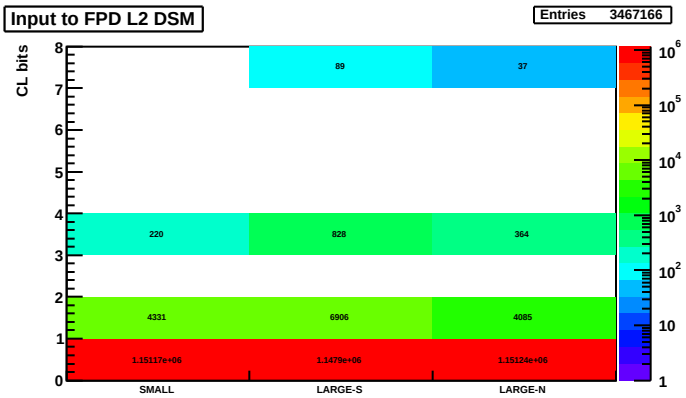

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

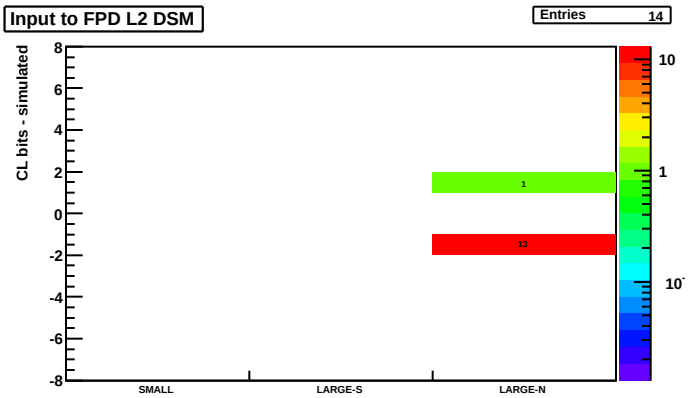

Input to FMS L1 DSM
Entries 1.386866e+07
FMS L1 HT bits
DSM board

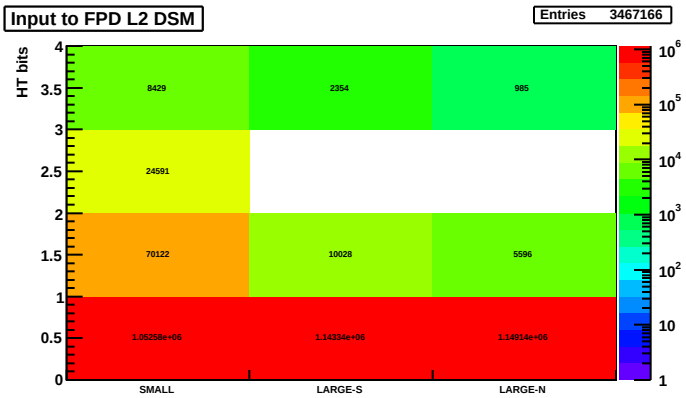

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

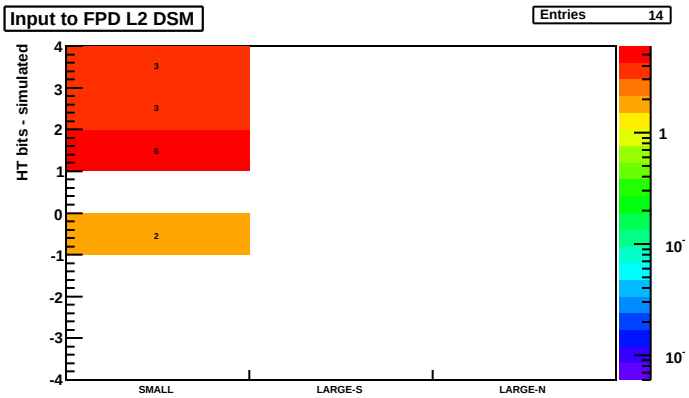

Input to FPD L2 DSM
Entries 1.386866e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

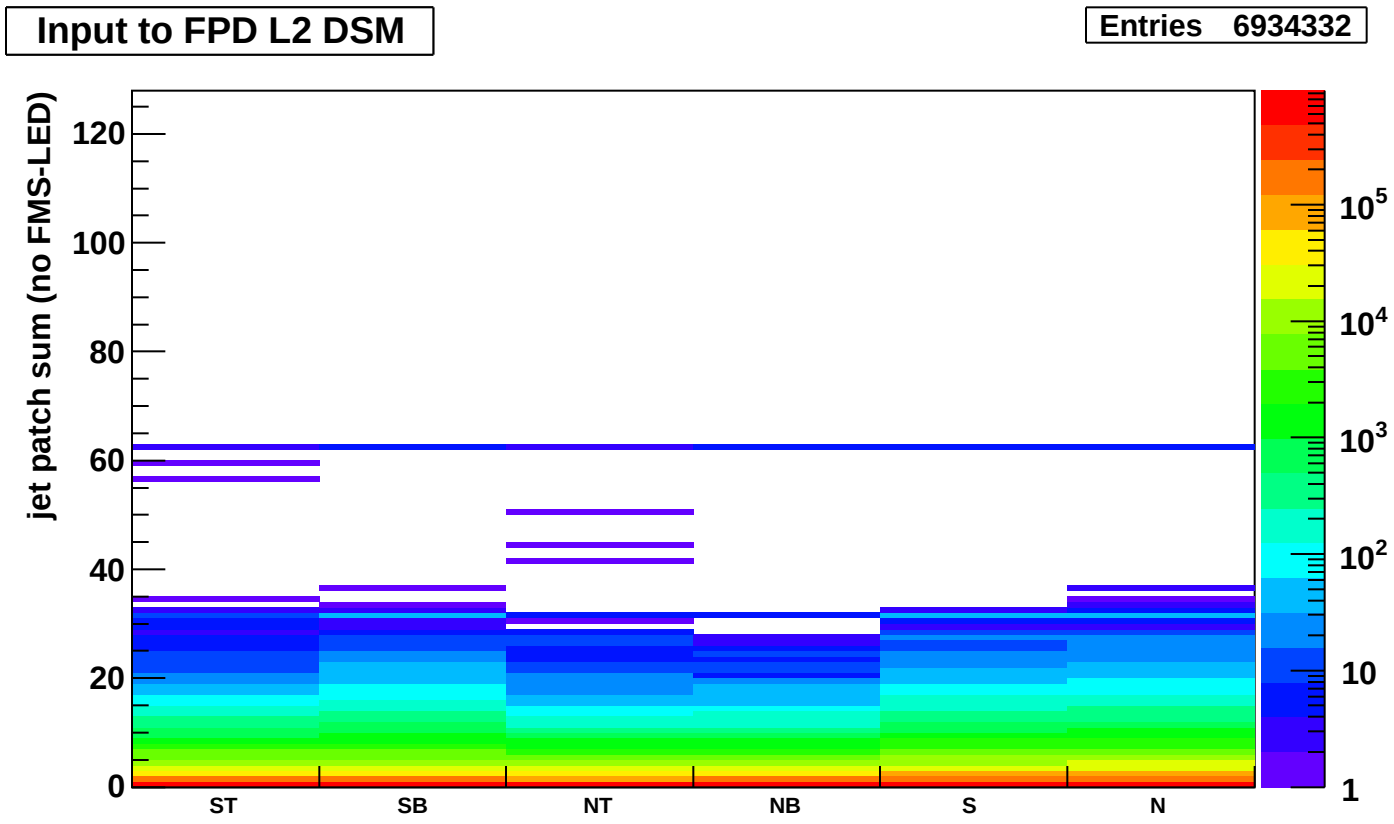

Input to FPD L2 DSM
Entries 150
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

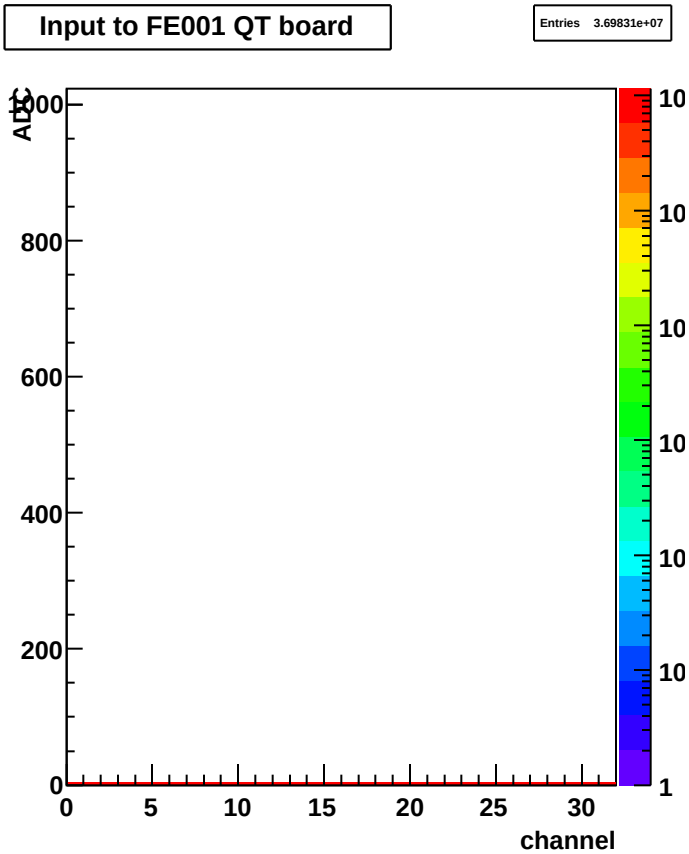

Input to FPD L2 DSM
Entries 3467166
CL bits
89
37
220
828
364
4331
6906
4085
1.15117e+06
1.1479e+06
1.15124e+06
SMALL
LARGE-S
LARGE-N

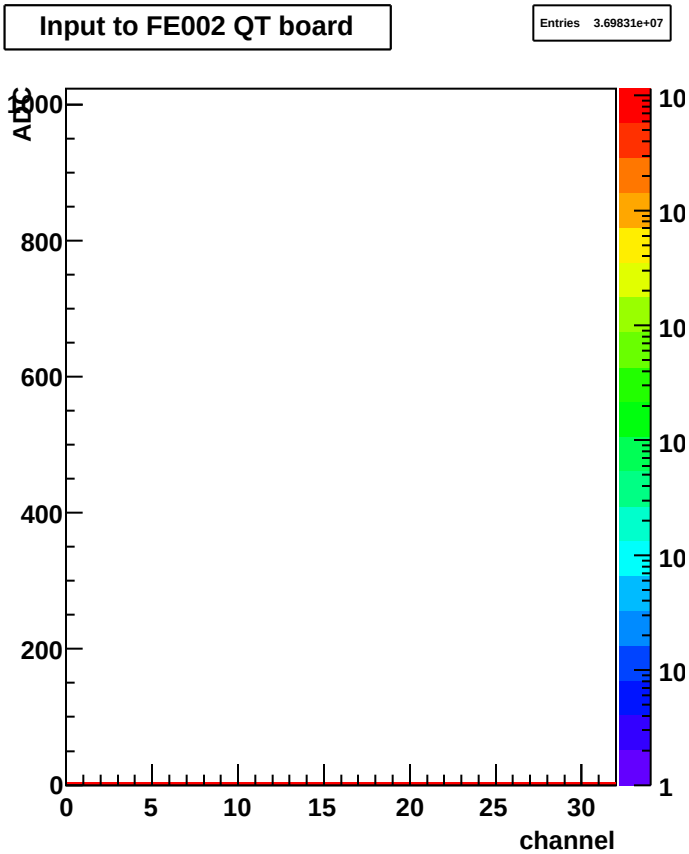

Input to FPD L2 DSM
Entries 14
CL bits - simulated
1
13
SMALL
LARGE-S
LARGE-N

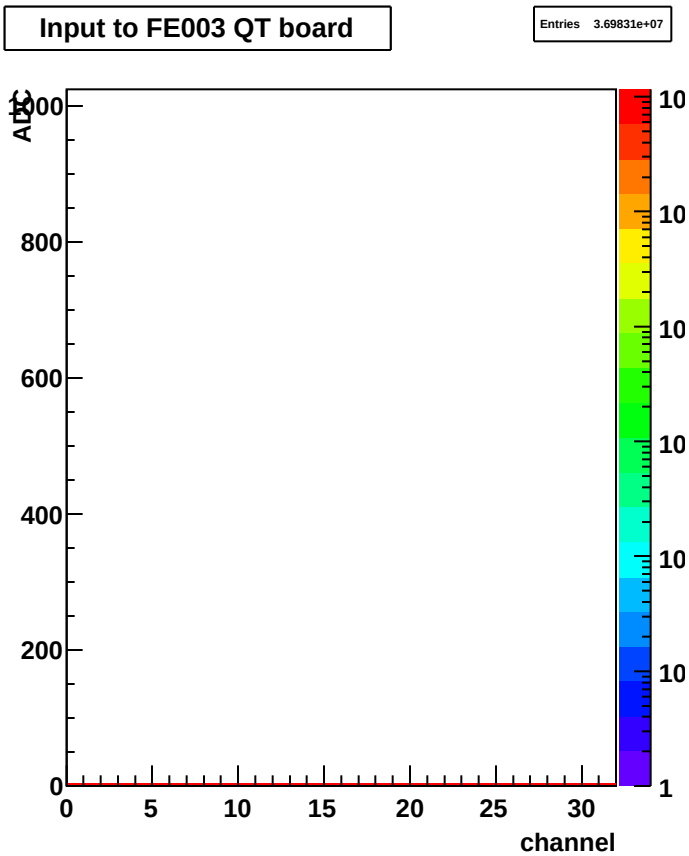

Input to FPD L2 DSM
Entries 3467166
HT bits
8429
2354
985
24591
70122
10028
5596
1.05258e+06
1.14334e+06
1.14914e+06
SMALL
LARGE-S
LARGE-N

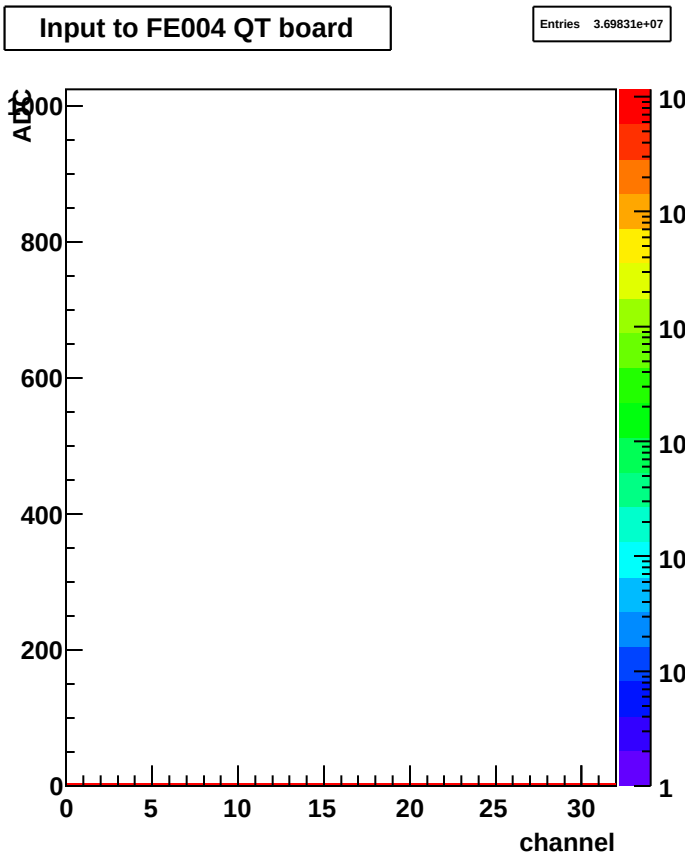

Input to FPD L2 DSM
Entries 14
HT bits - simulated
3
3
6
2
SMALL
LARGE-S
LARGE-N

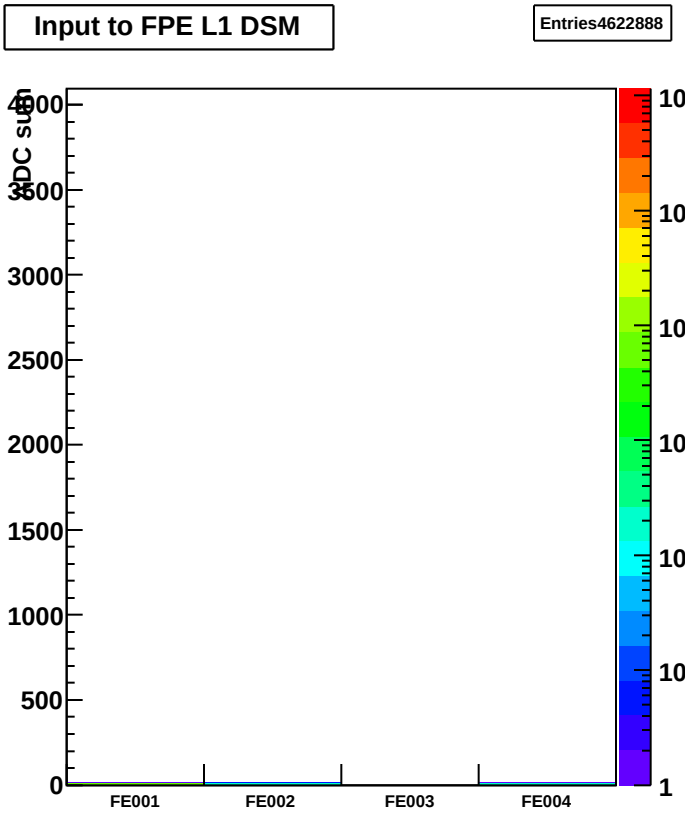

Input to FPD L2 DSM
Entries 6934332
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

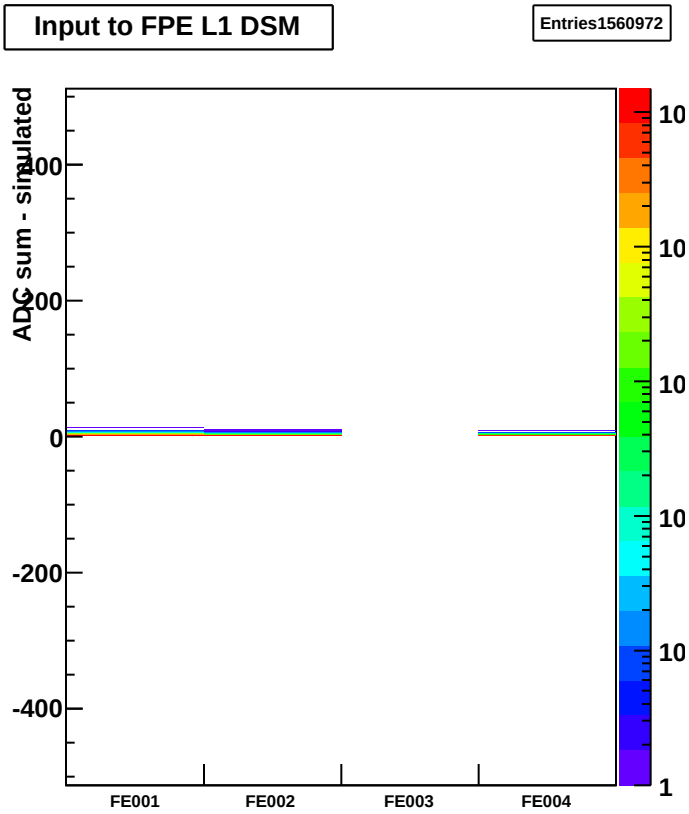

Input to FE001 QT board
Entries 3.69831e+07
ADC
channel

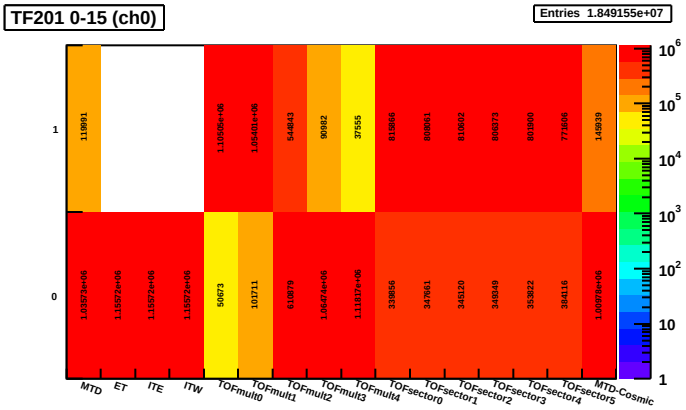

Input to FE002 QT board
Entries 3.69831e+07
ADC
channel

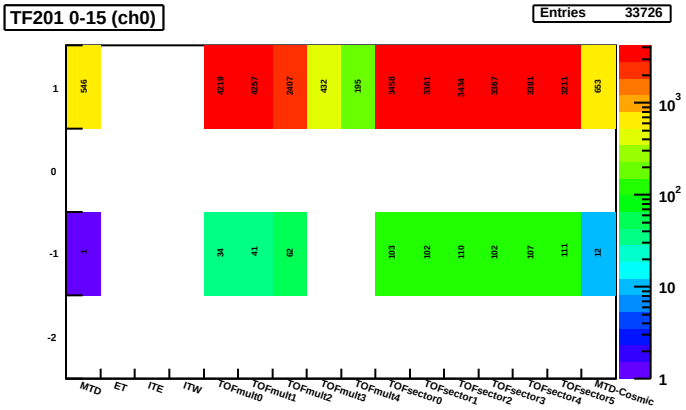

Input to FE003 QT board
Entries 3.69831e+07
ADC
channel


Input to FE004 QT board
Entries 3.69831e+07
ADC
channel


Input to FPE L1 DSM
Entries4622888
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries1560972
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.849155e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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Entries 33726
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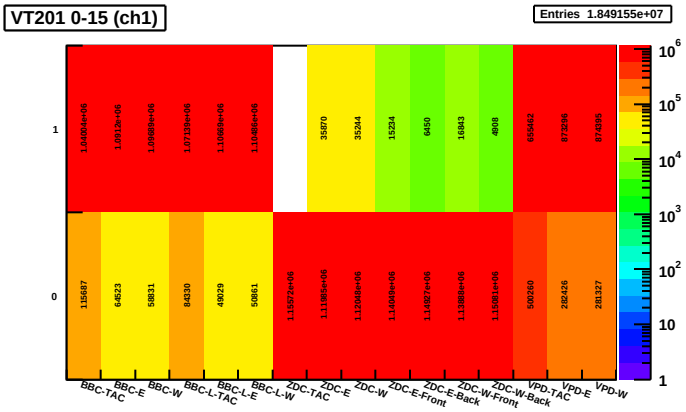
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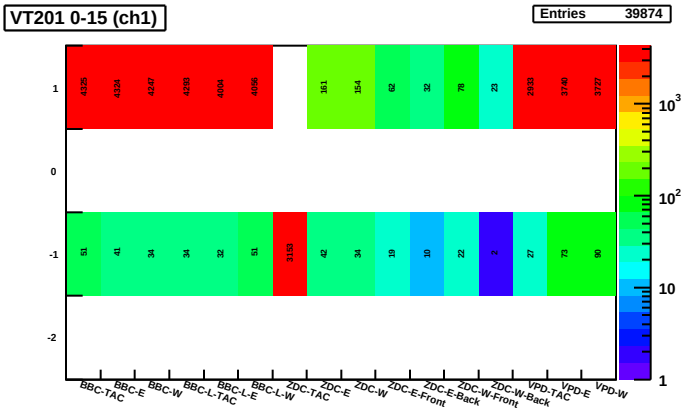
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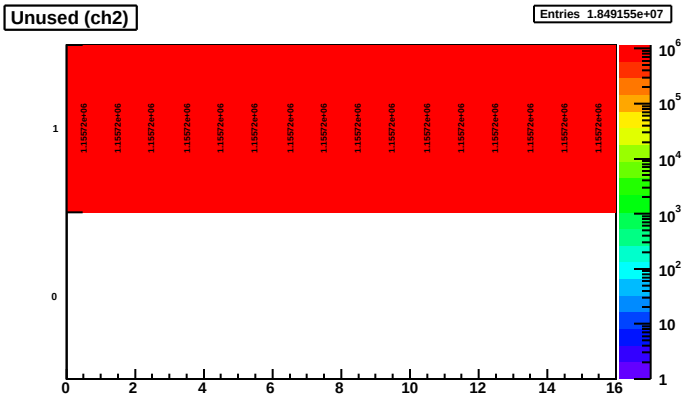
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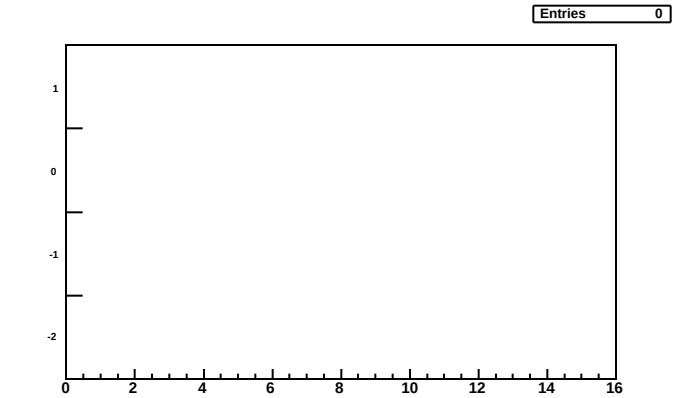
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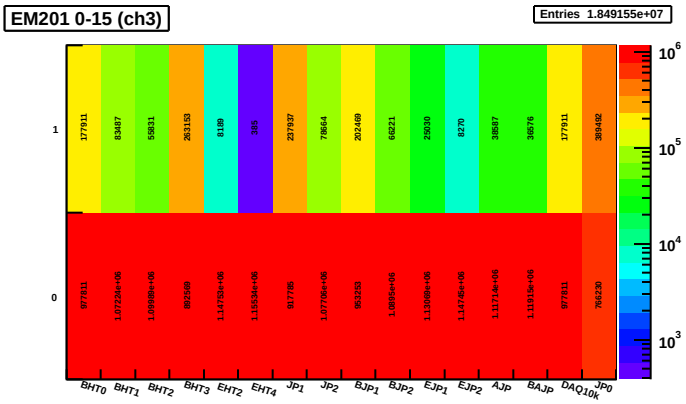
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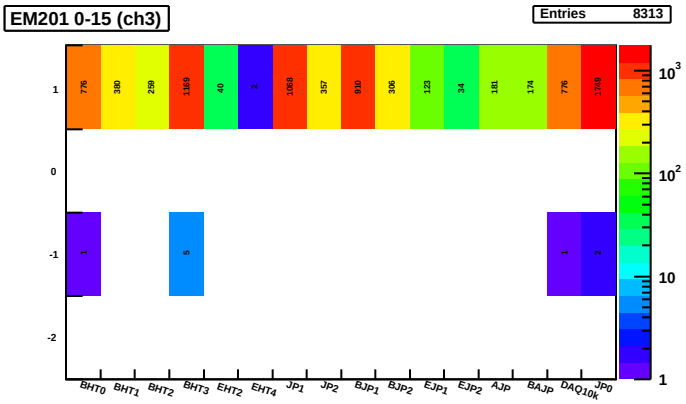
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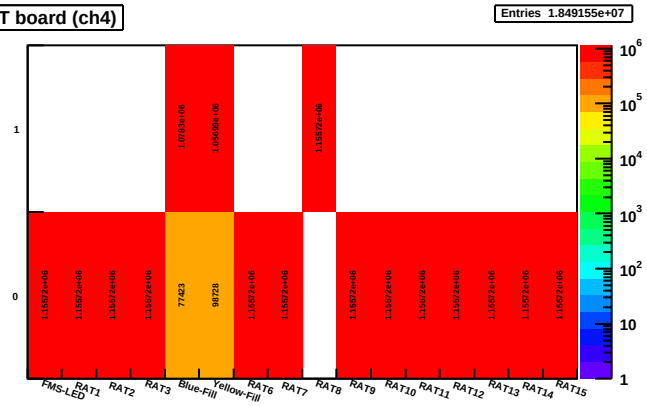
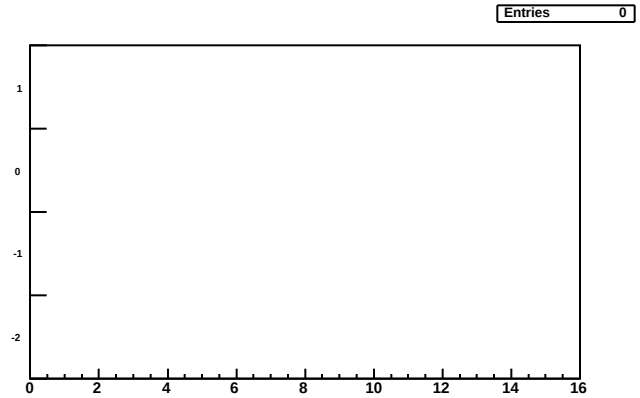
RAT board (ch4)





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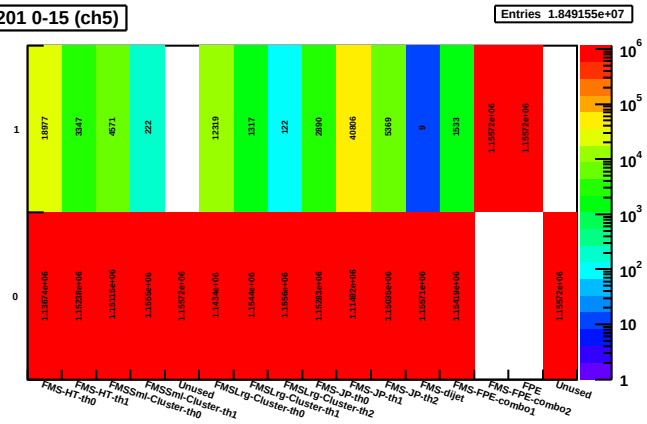

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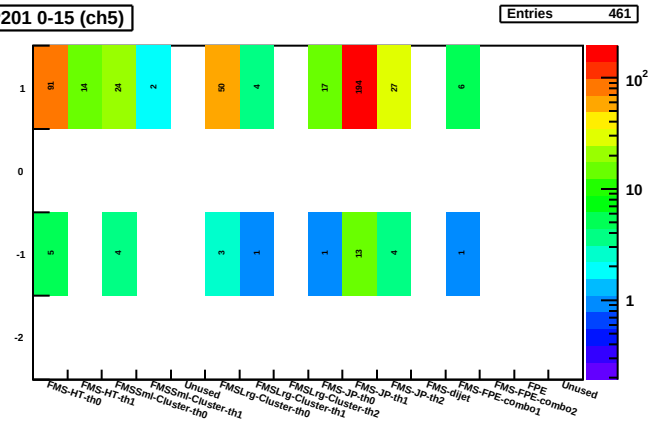
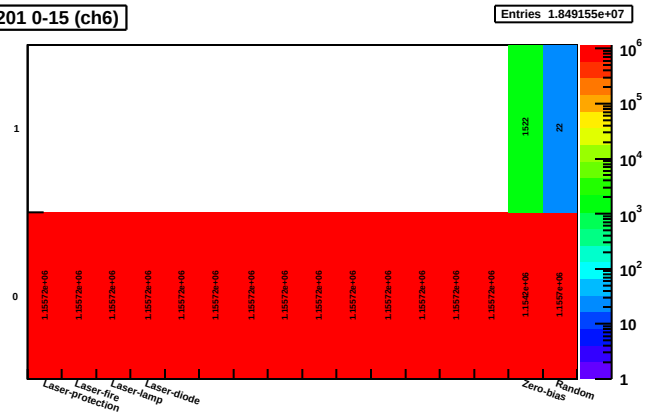
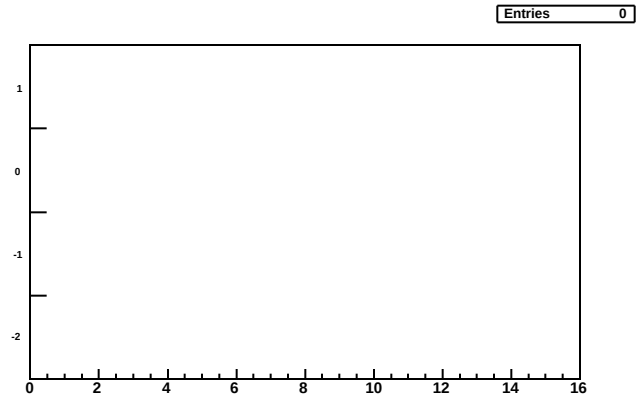
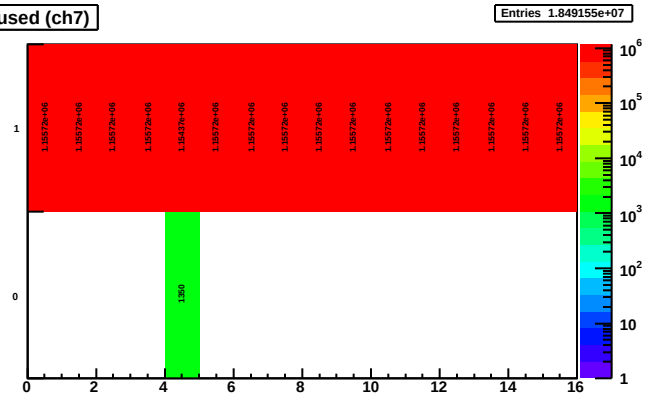
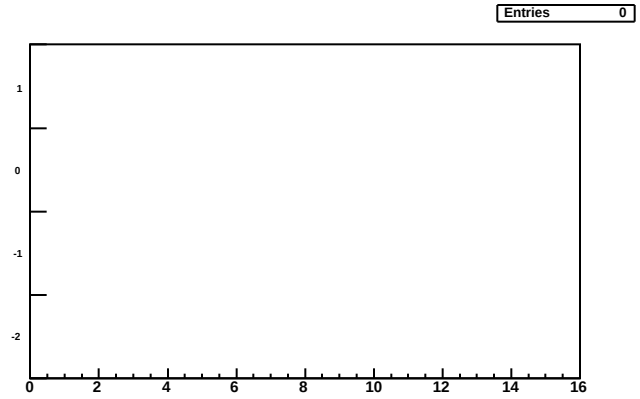


ST201 0-15 (ch6)





Unused (ch7)