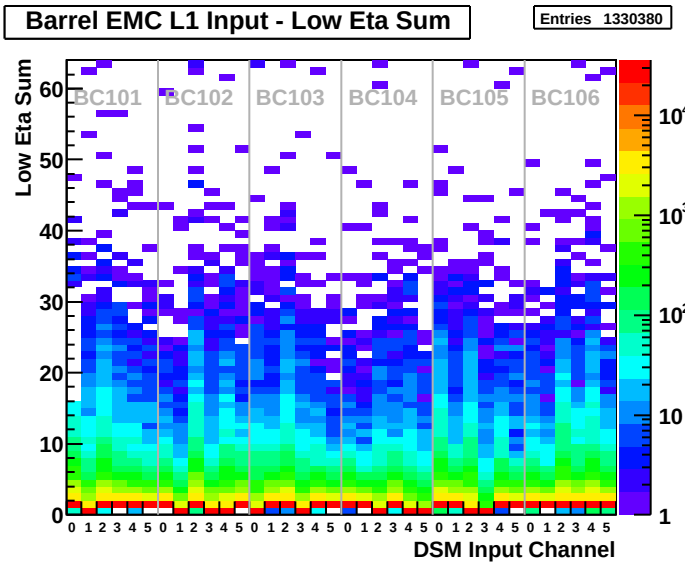
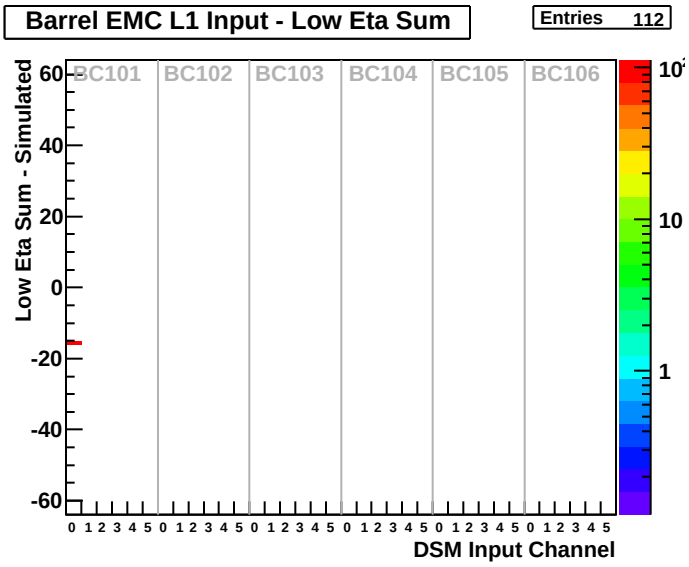
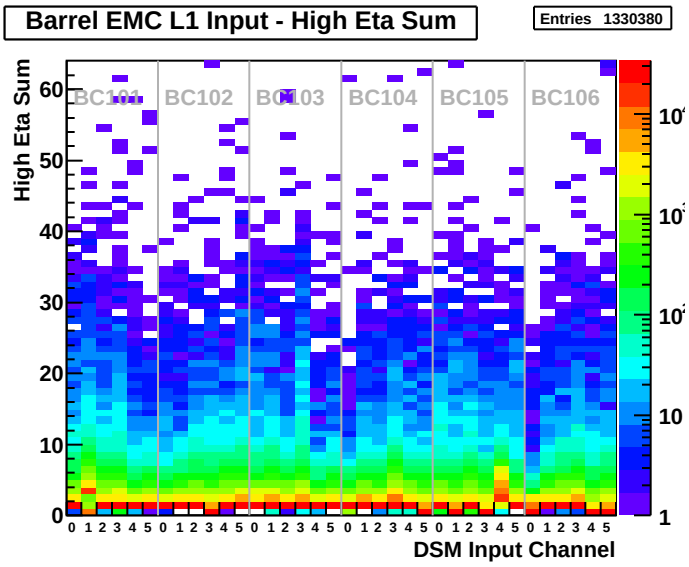
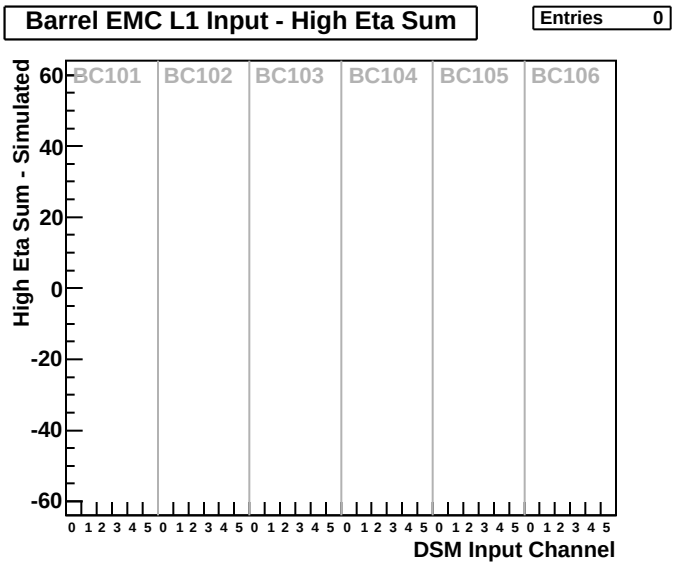
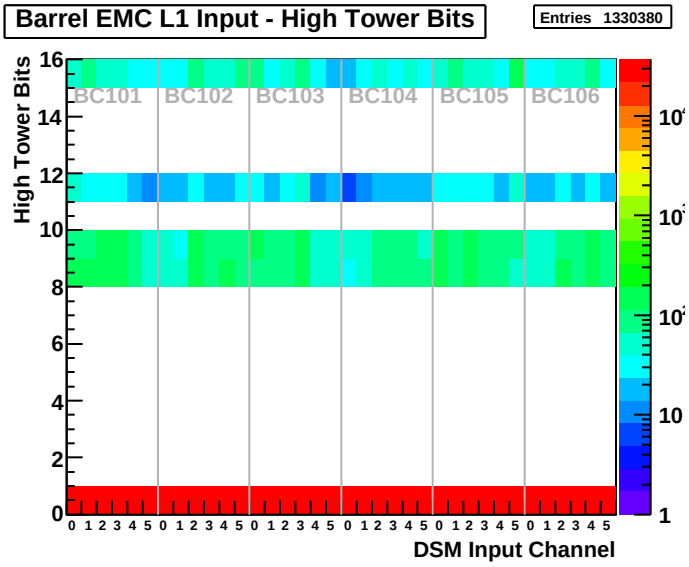
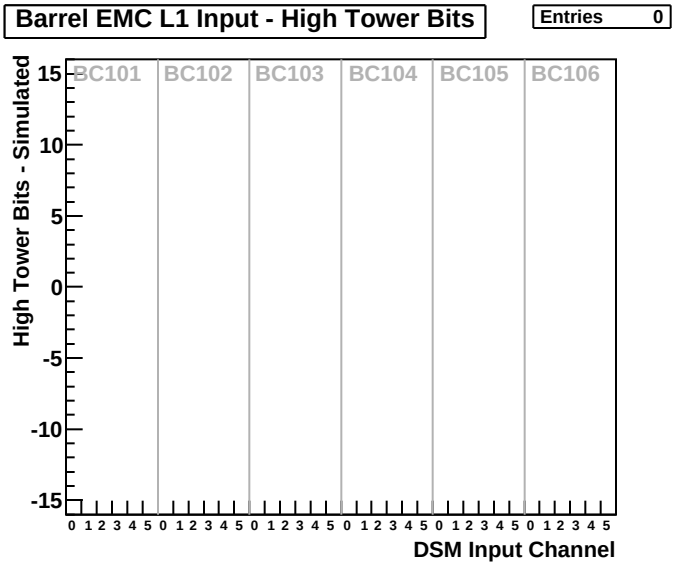

Barrel EMC L1 Input - Low Eta Sum
Entries 1330380
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

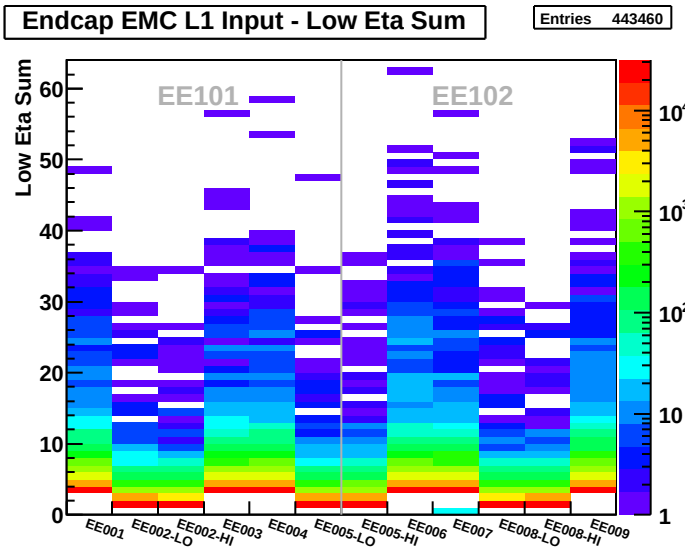

Barrel EMC L1 Input - Low Eta Sum
Entries 112
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

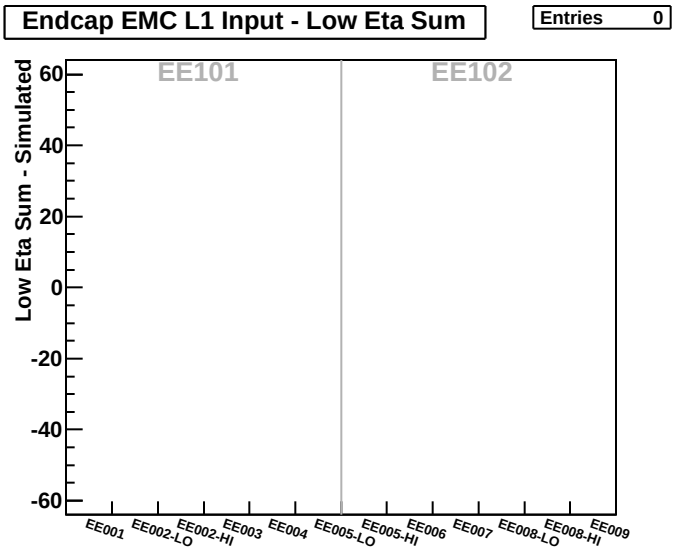

Barrel EMC L1 Input - High Eta Sum
Entries 1330380
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

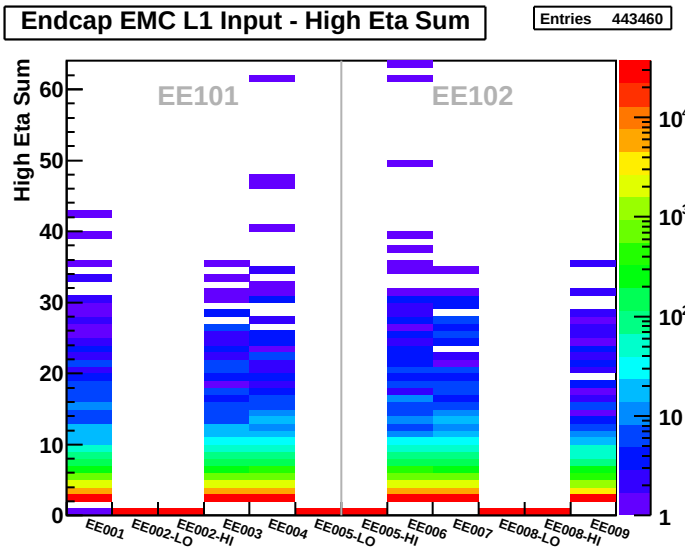

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

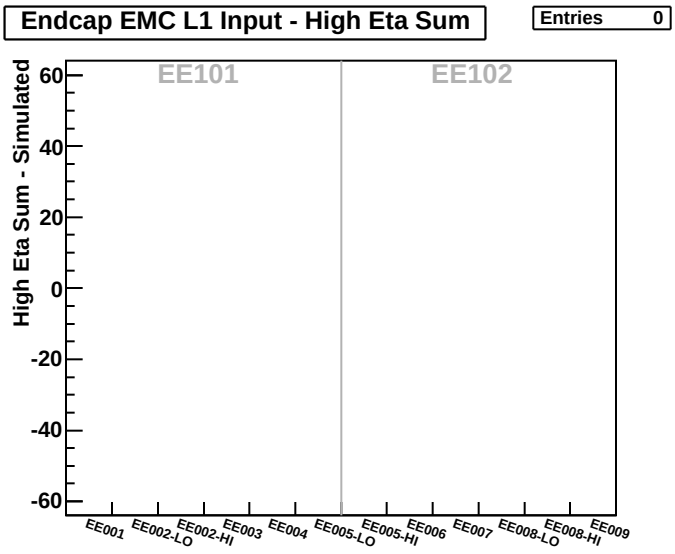

Barrel EMC L1 Input - High Tower Bits
Entries 1330380
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

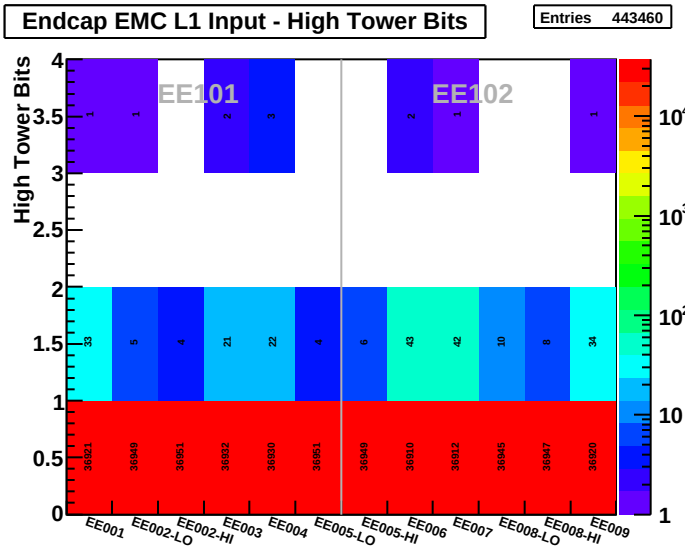

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

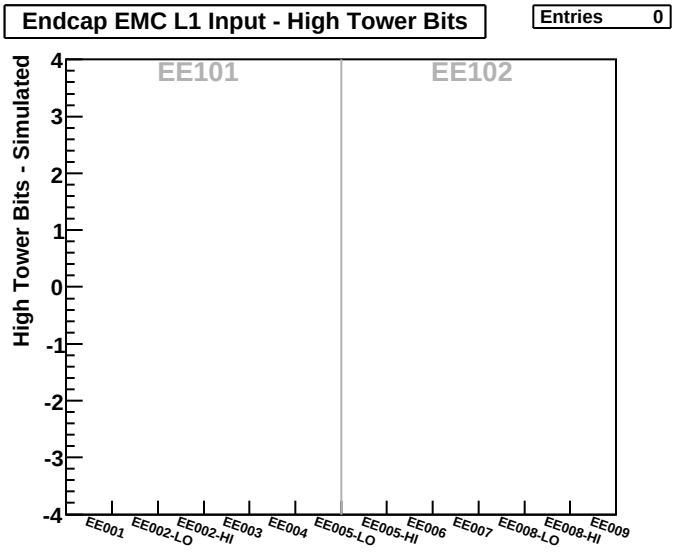

Endcap EMC L1 Input - Low Eta Sum
Entries 443460
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

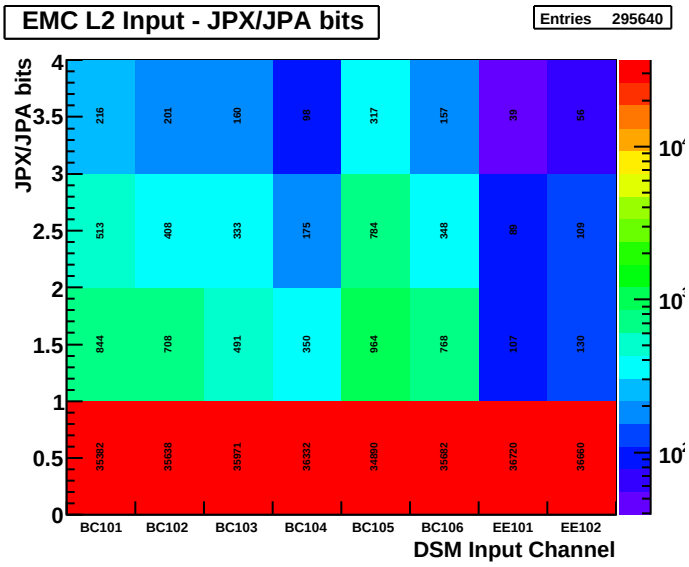

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

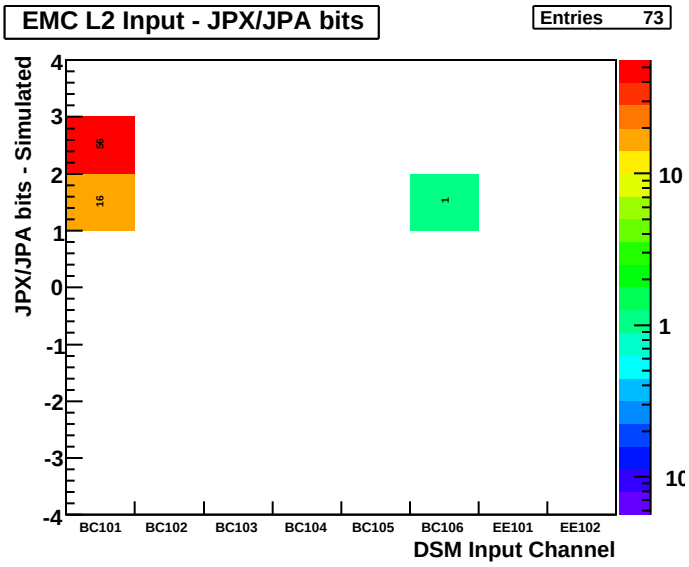

Endcap EMC L1 Input - High Eta Sum
Entries 443460
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

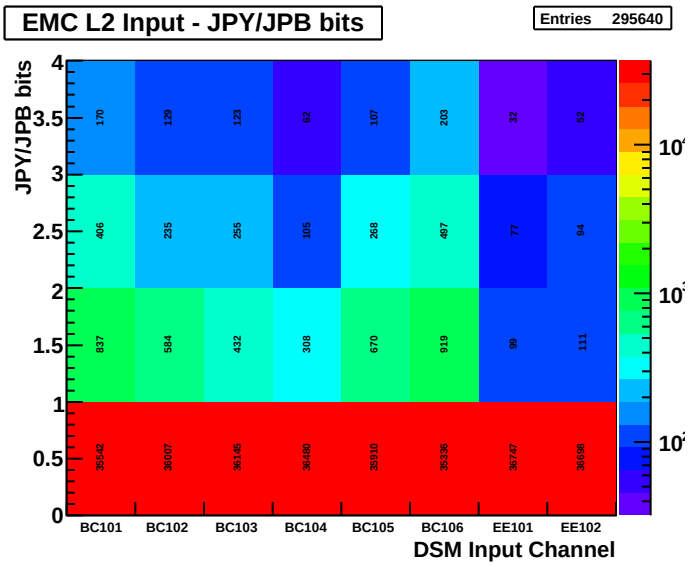

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

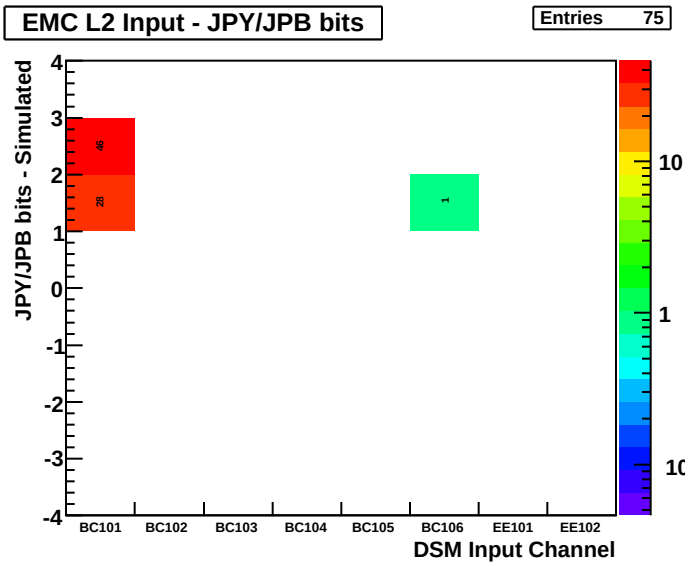

Endcap EMC L1 Input - High Tower Bits
Entries 443460
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

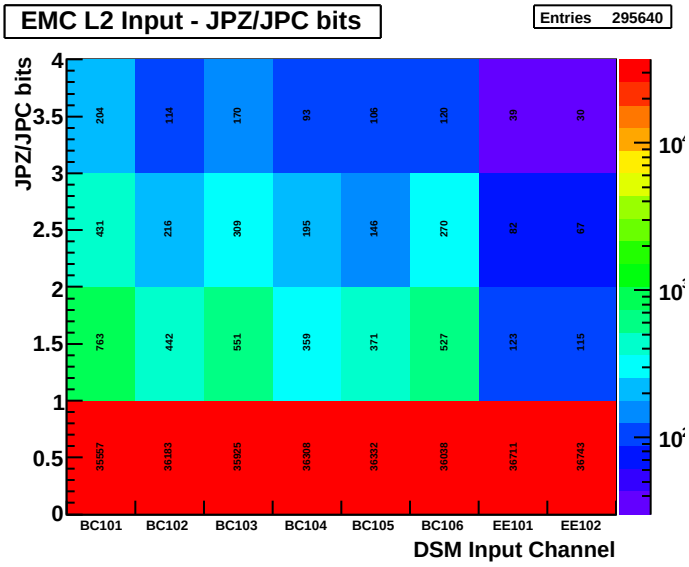

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

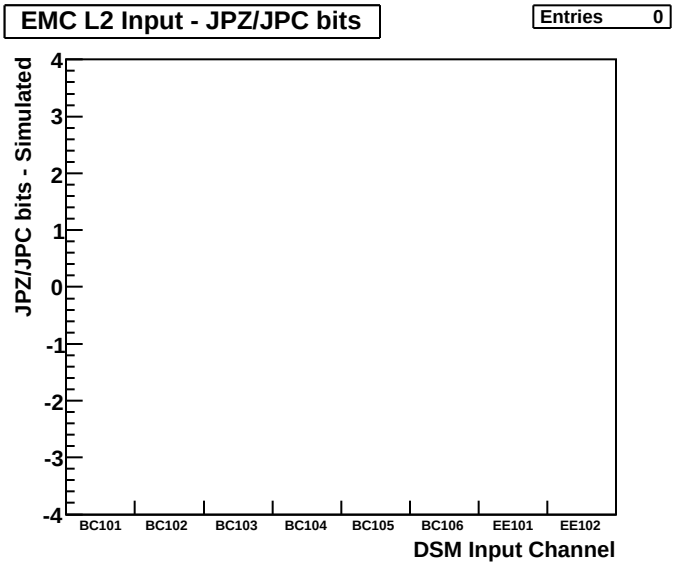

EMC L2 Input - JPX/JPA bits
Entries 295640
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
216 201 160 98 317 157 39 56
513 408 333 175 784 348 89 109
844 708 491 350 964 768 107 130
35382 35638 35971 36332 34890 35682 36720 36660

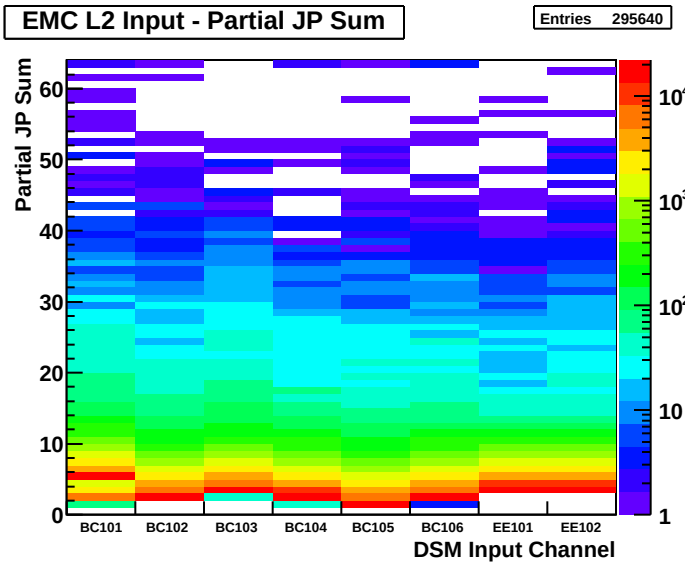

EMC L2 Input - JPX/JPA bits
Entries 73
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
56
16
1

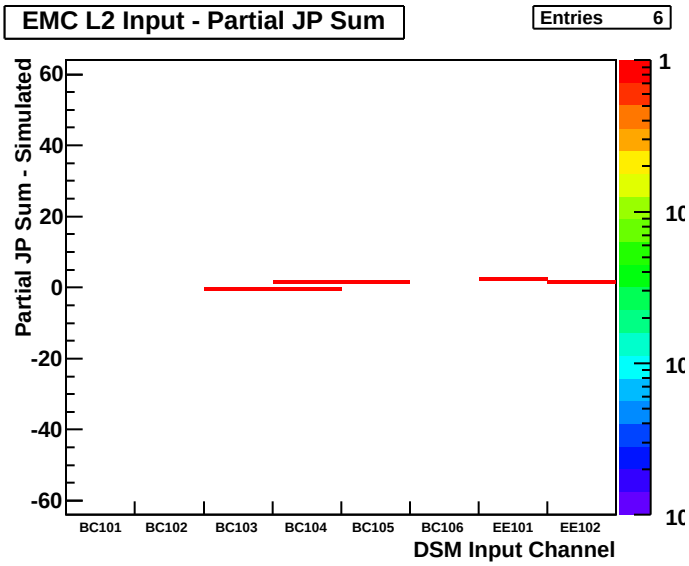

EMC L2 Input - JPY/JPB bits
Entries 295640
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
170 120 123 62 107 203 32 52
406 235 255 105 268 497 77 94
837 584 432 308 670 919 99 111
35542 36007 36146 36480 35910 35336 36747 36698

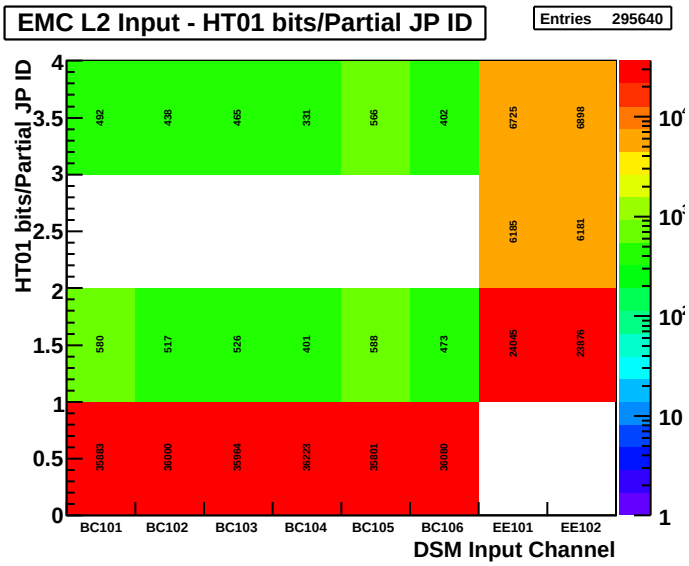

EMC L2 Input - JPY/JPB bits
Entries 75
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
46
28
1

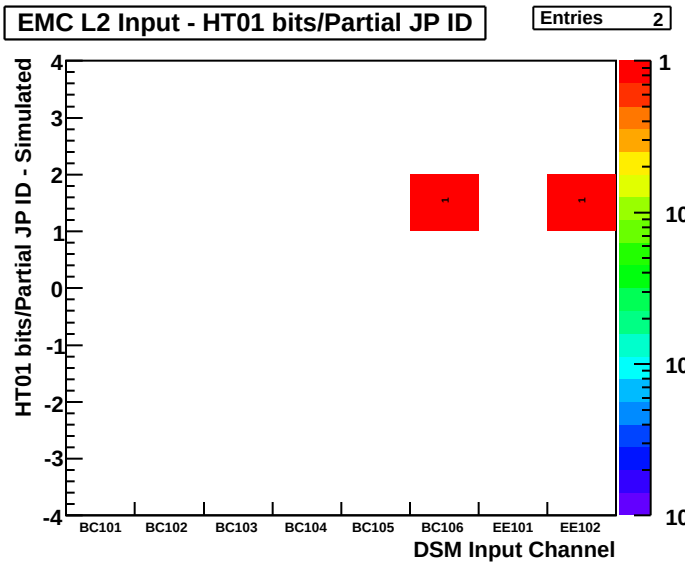

EMC L2 Input - JPZ/JPC bits
Entries 295640
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
204 114 170 93 106 120 39 30
431 216 309 195 146 270 82 67
763 442 551 359 371 527 123 115
35557 36183 35925 36308 36332 36038 36711 36743

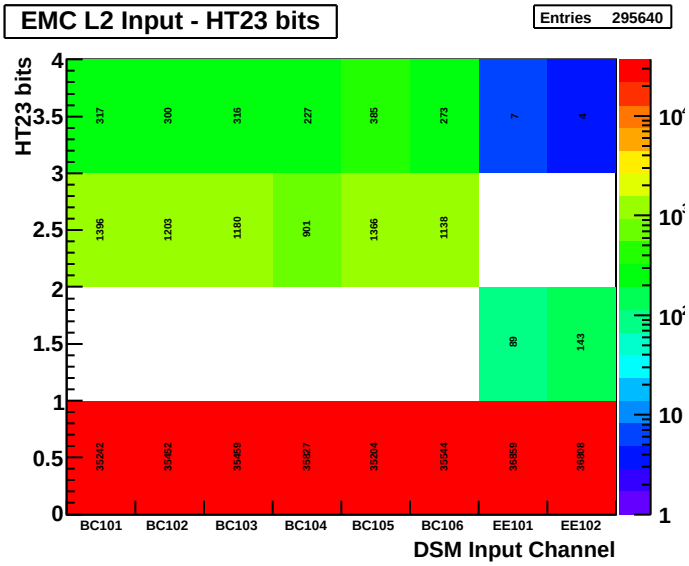

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

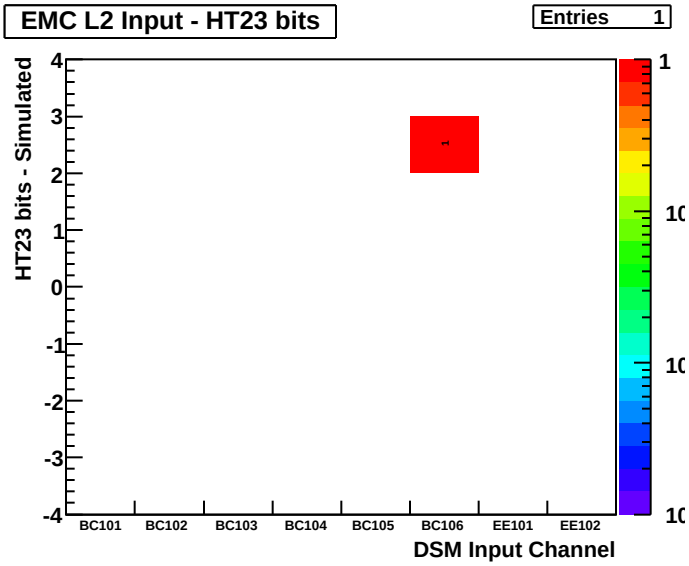

EMC L2 Input - Partial JP Sum
Entries 295640
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

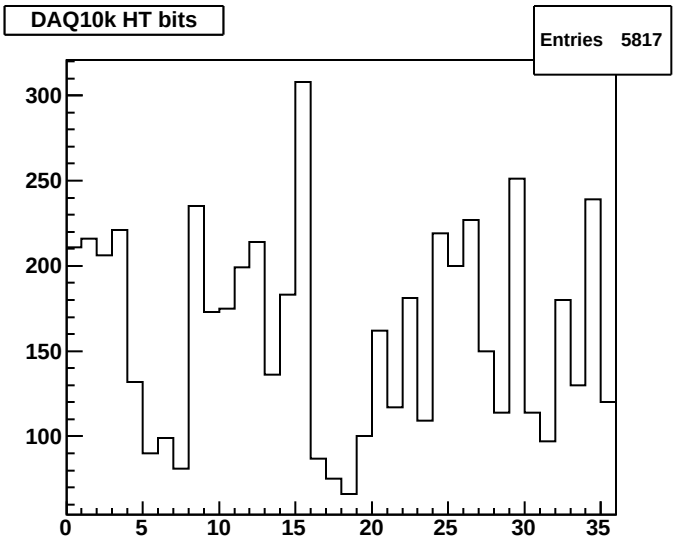

EMC L2 Input - Partial JP Sum
Entries 6
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

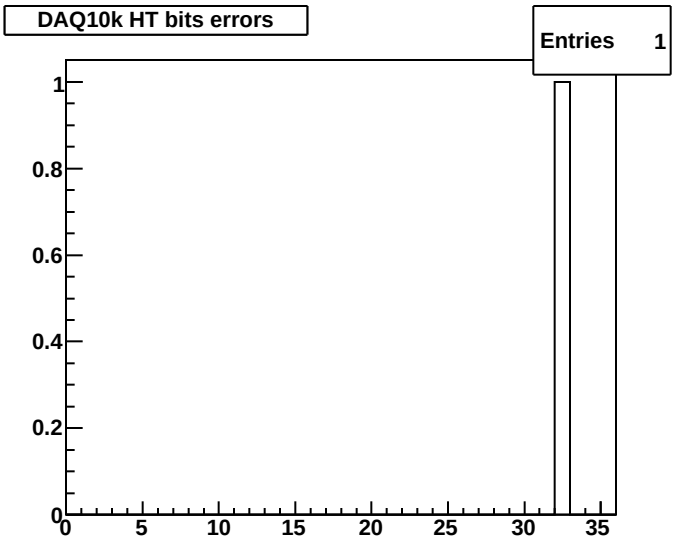

EMC L2 Input - HT01 bits/Partial JP ID
Entries 295640
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

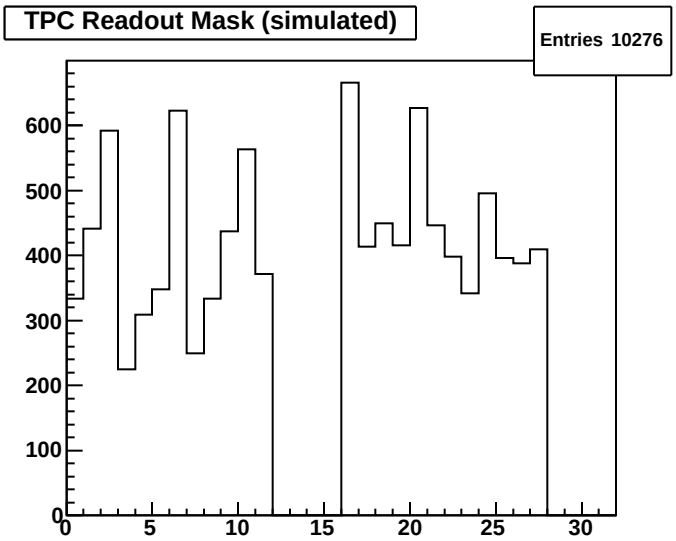

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

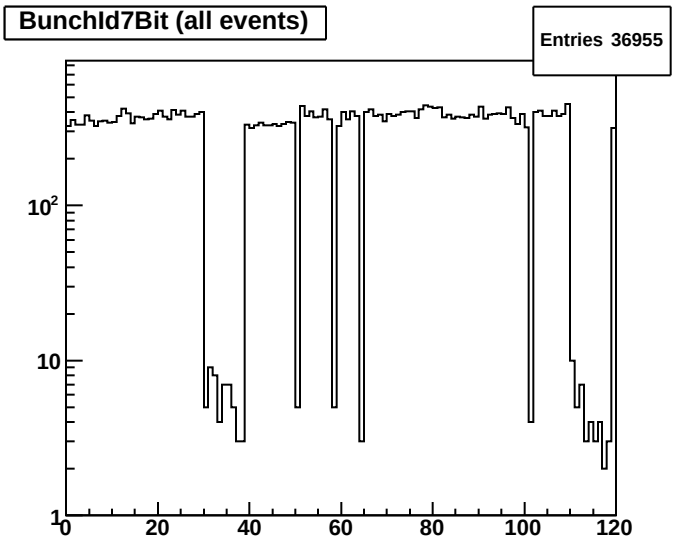

EMC L2 Input - HT23 bits
Entries 295640
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

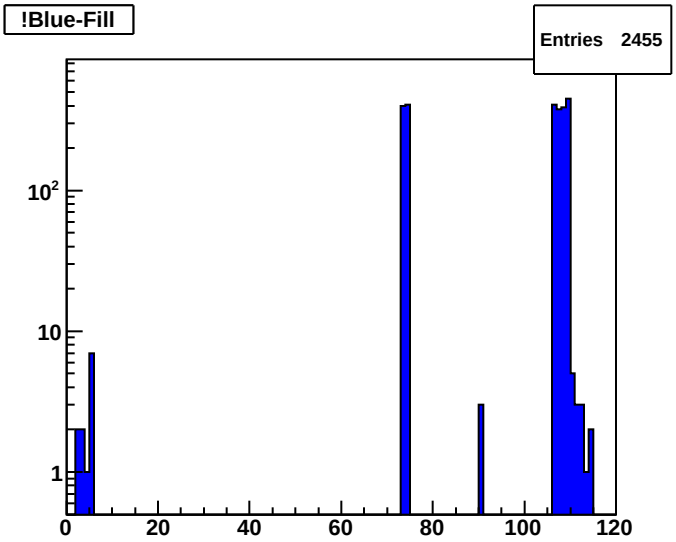

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

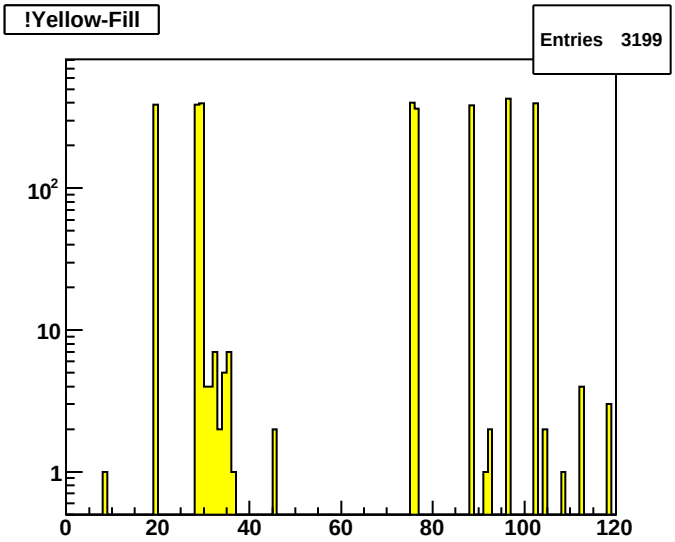

DAQ10k HT bits
Entries 5817

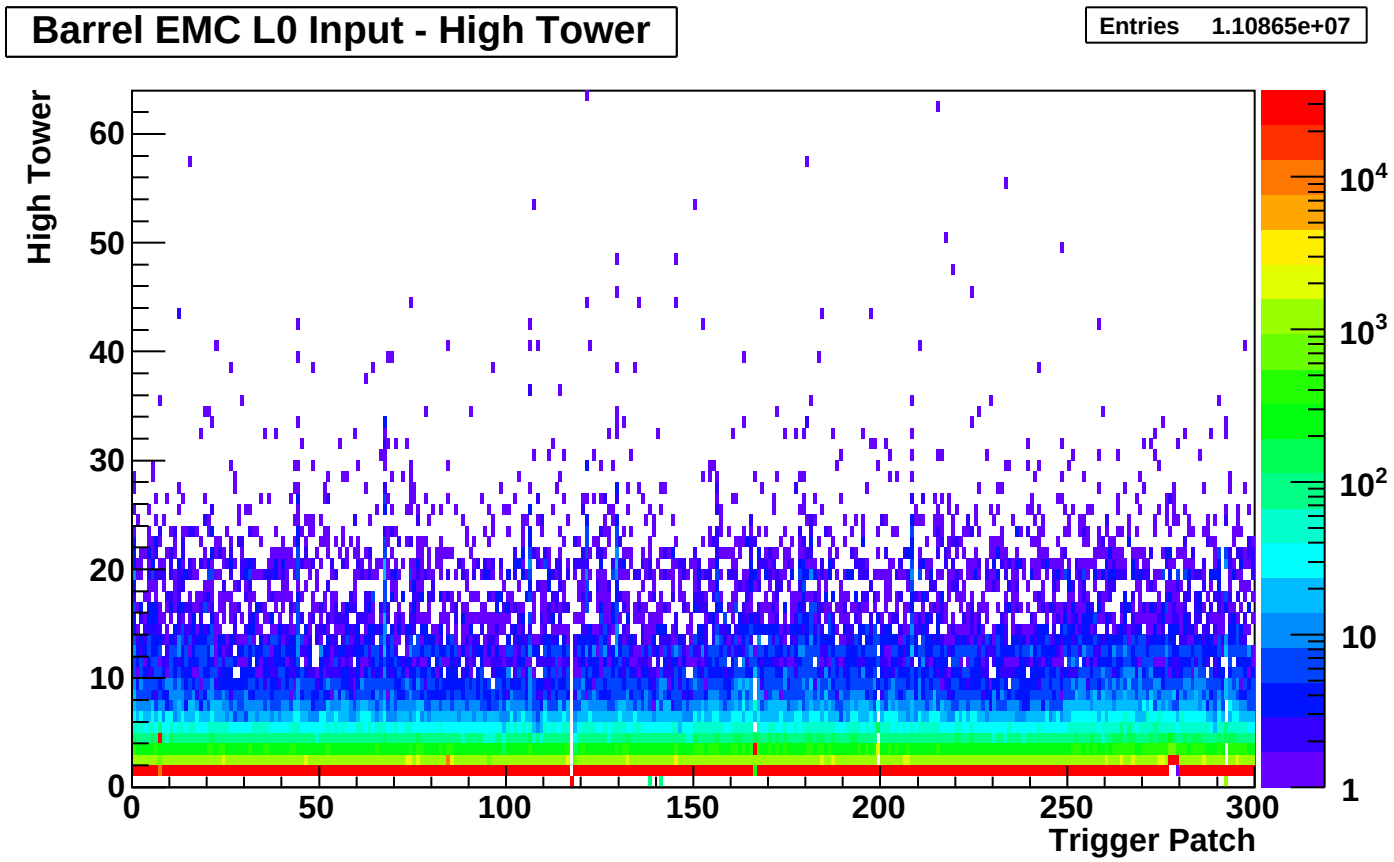

DAQ10k HT bits errors
Entries 1

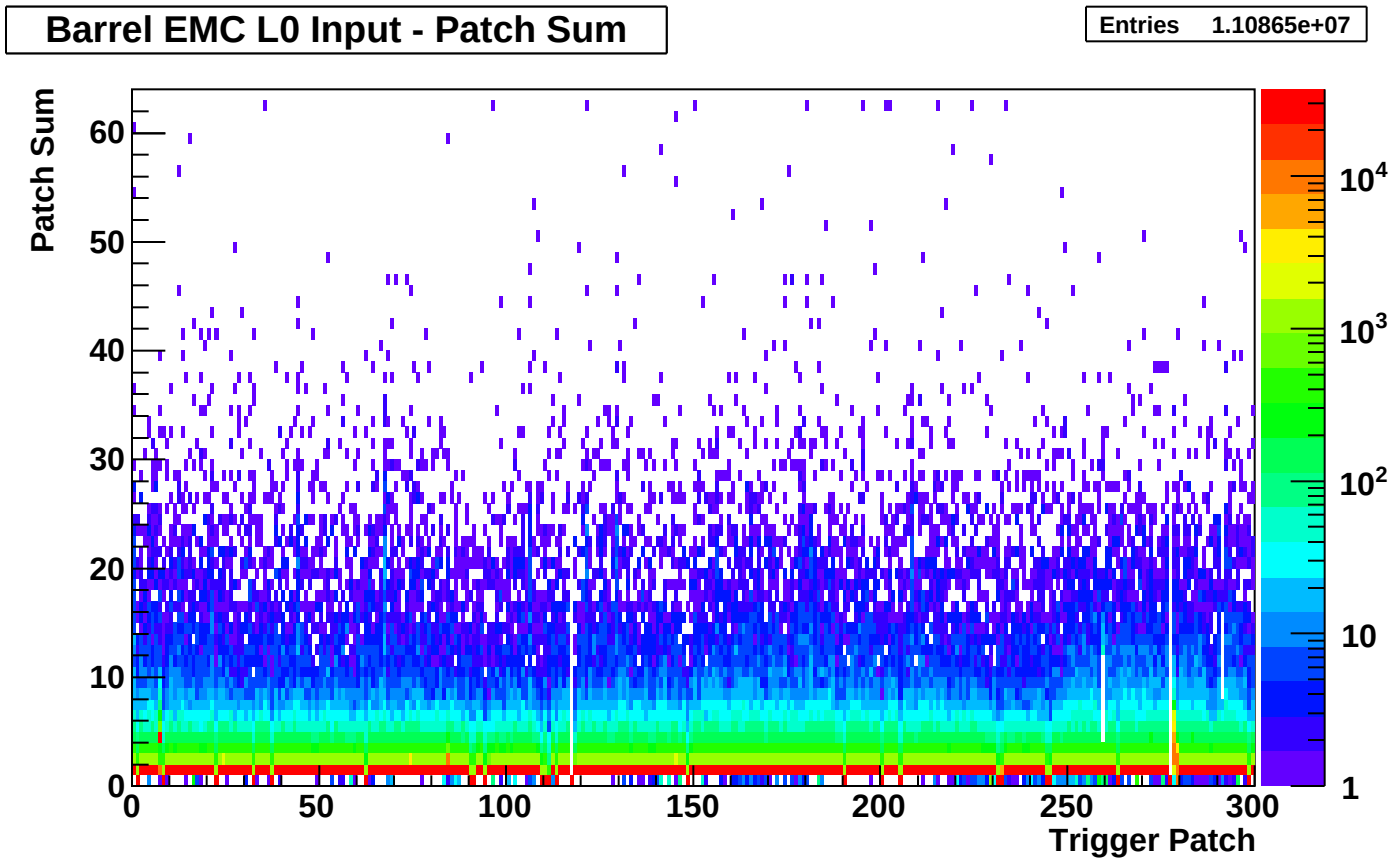

TPC Readout Mask (simulated)
Entries 10276

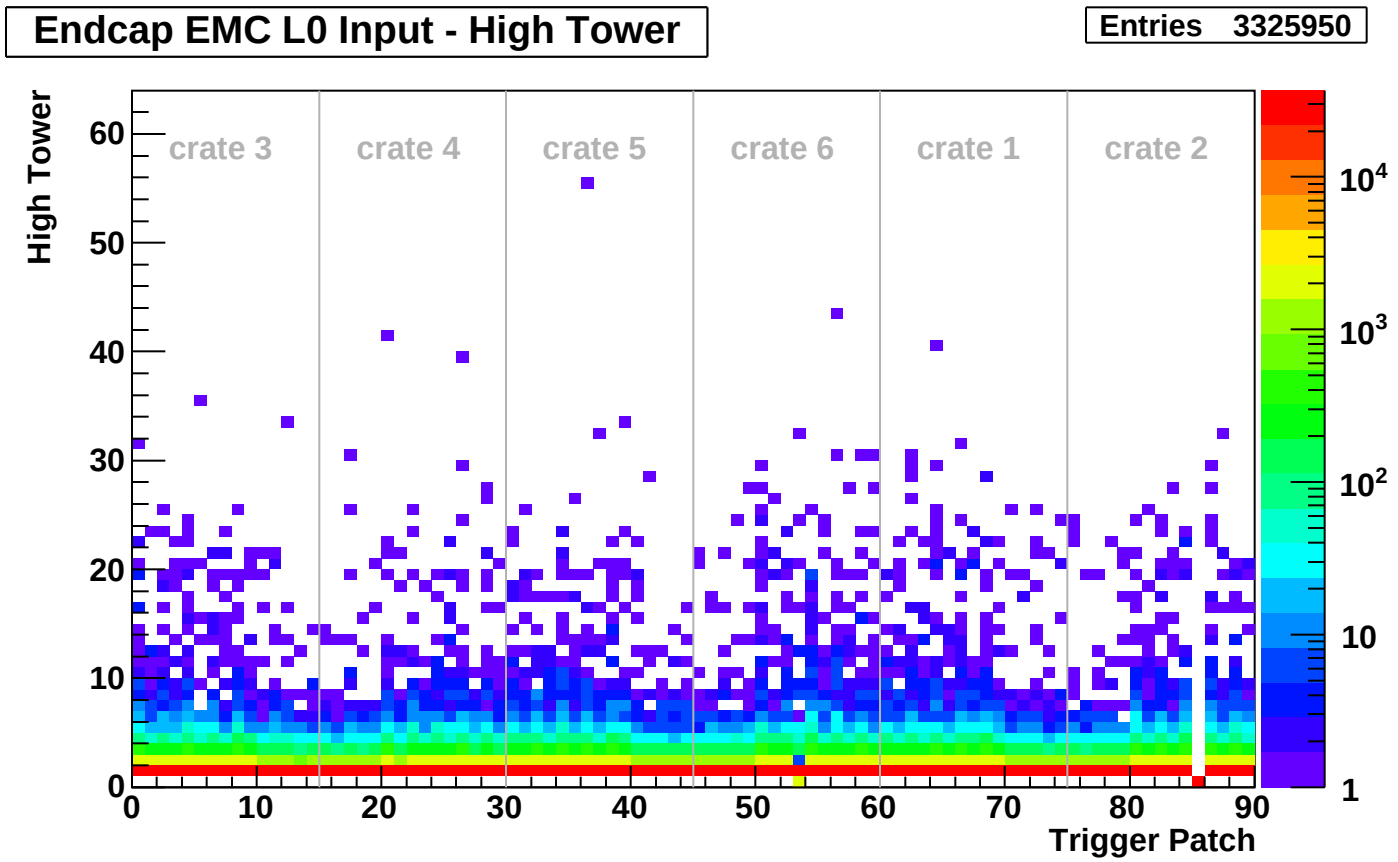

BunchId7Bit (all events)
Entries 36955

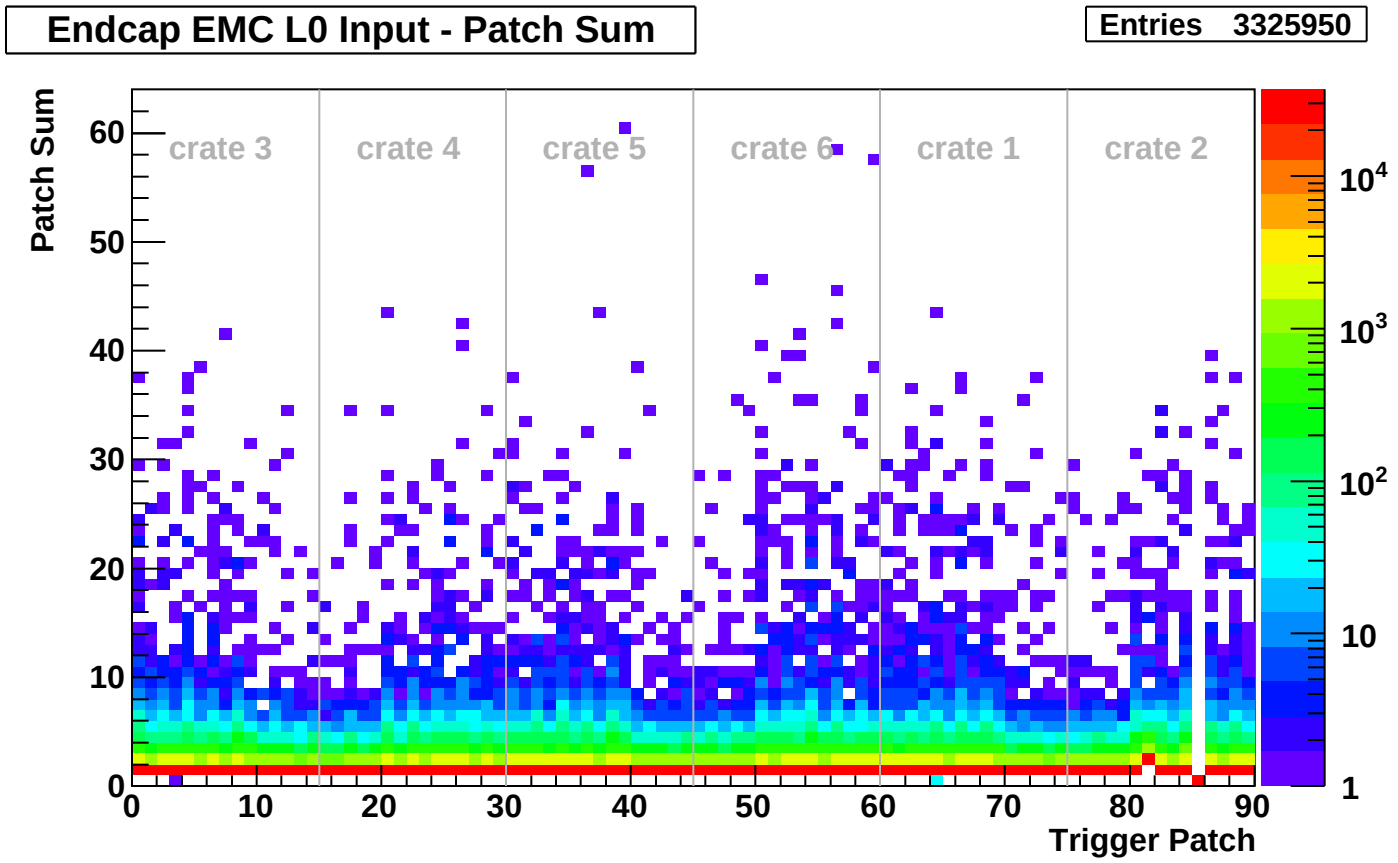

!Blue-Fill
Entries 2455

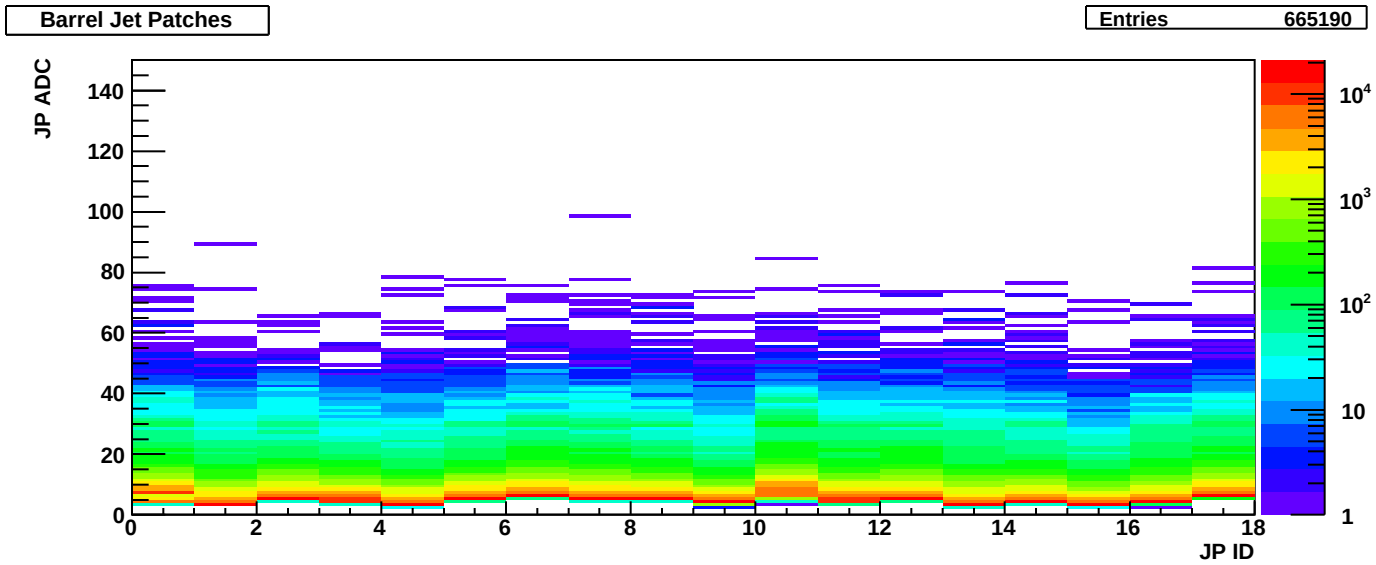

!Yellow-Fill
Entries 3199

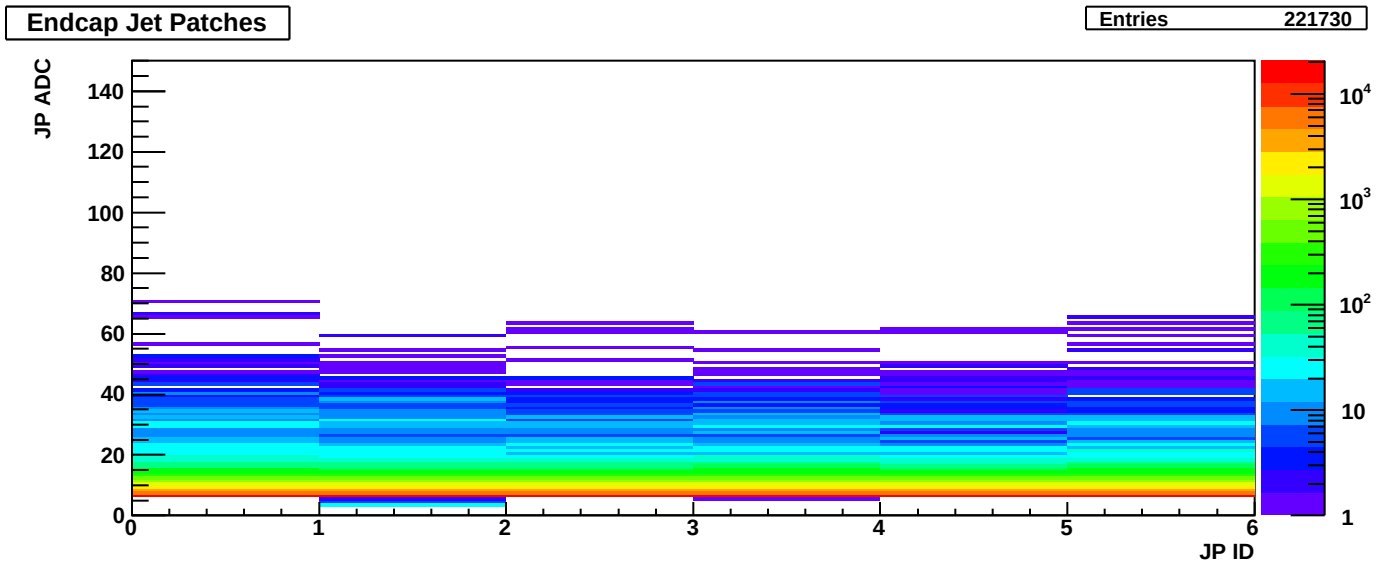

Barrel EMC L0 Input - High Tower
Entries 1.10865e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

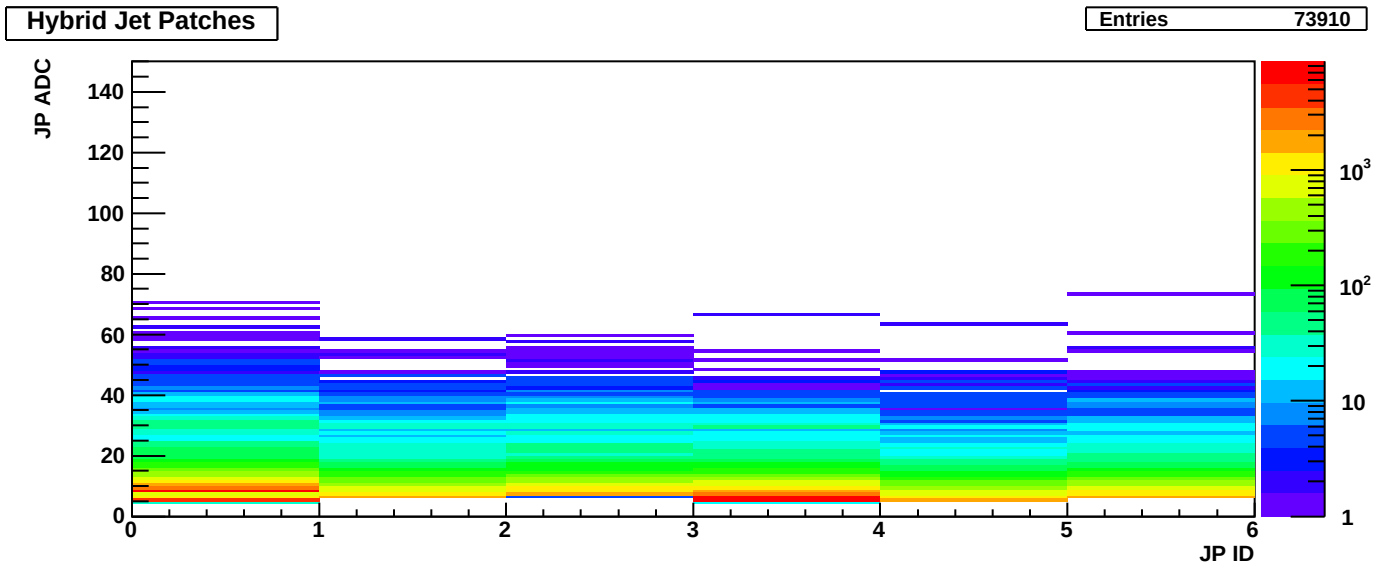

Barrel EMC L0 Input - Patch Sum
Entries 1.10865e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

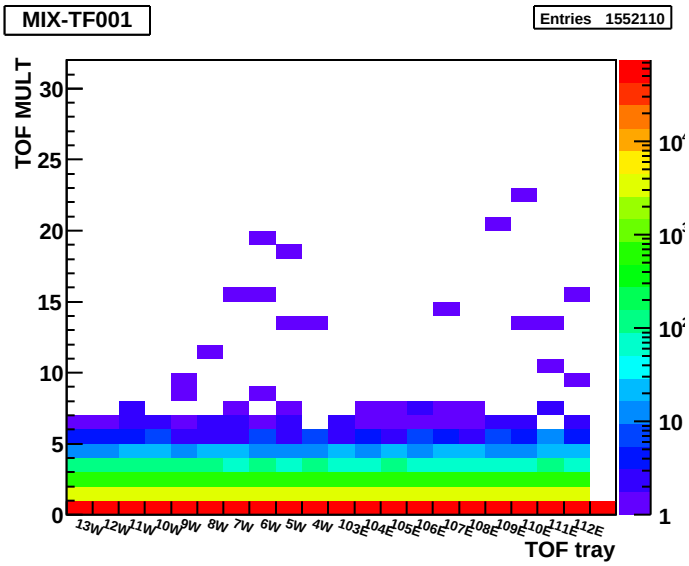

Endcap EMC L0 Input - High Tower
Entries 3325950
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

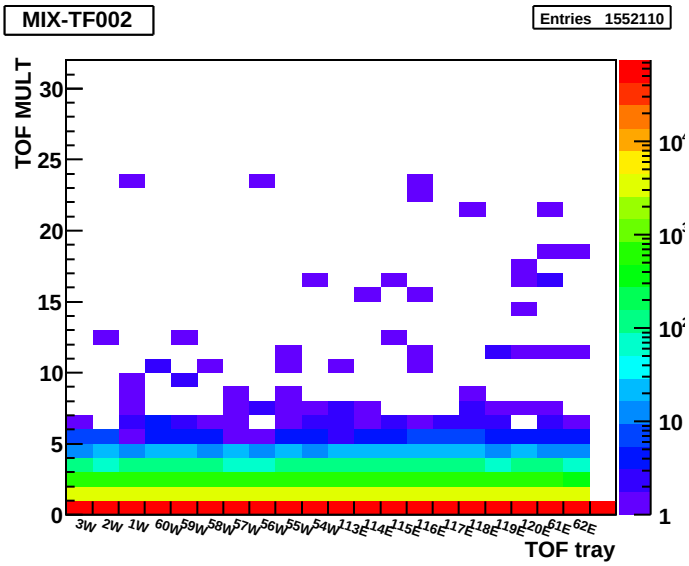

Endcap EMC L0 Input - Patch Sum
Entries 3325950
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

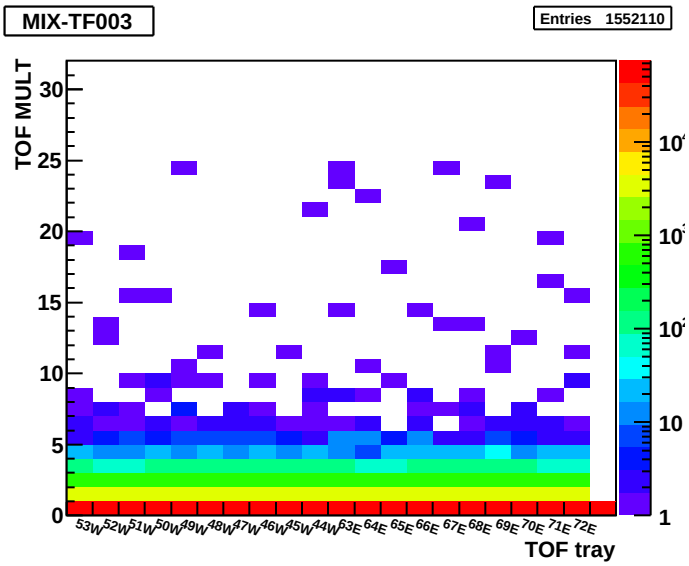

Barrel Jet Patches
Entries 665190
JP ADC
JP ID

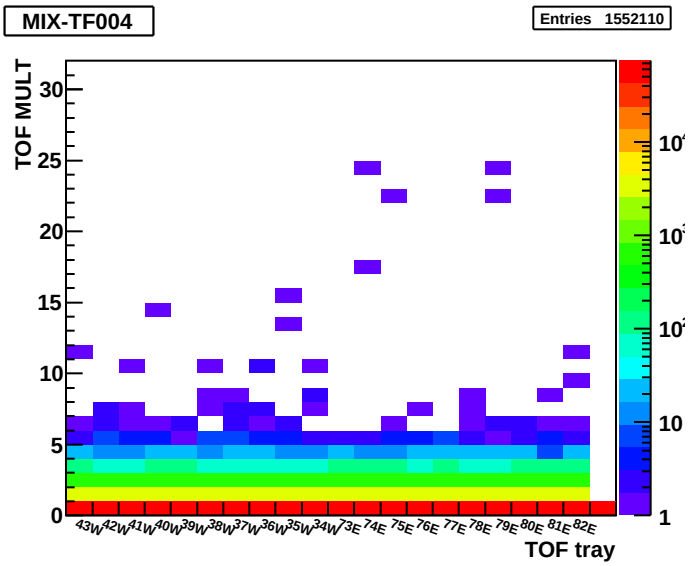

Endcap Jet Patches
Entries 221730
JP ADC
JP ID

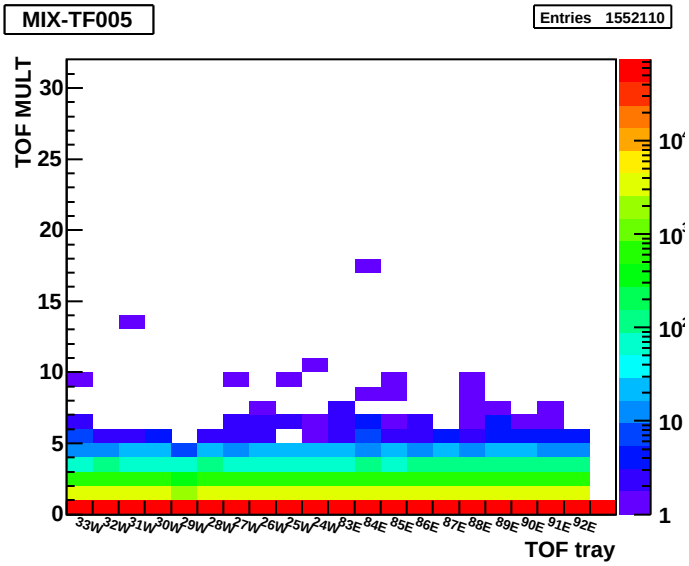

Hybrid Jet Patches
Entries 73910
JP ADC
JP ID

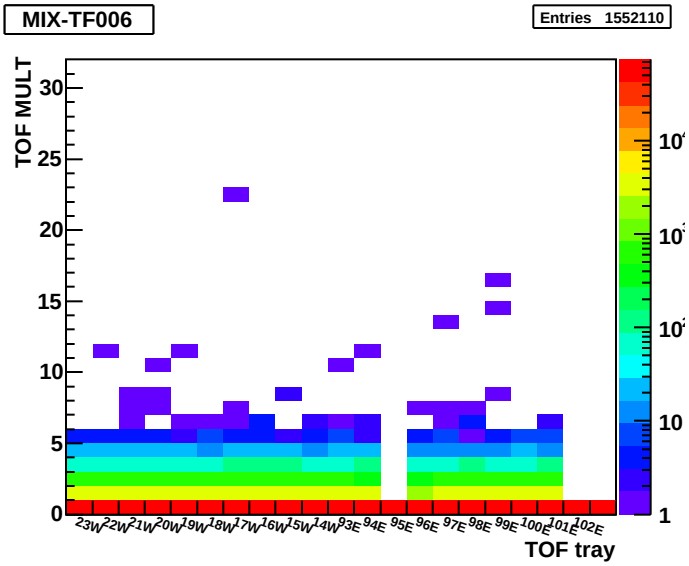

MIX-TF001
Entries 1552110
TOF MULT
TOF tray

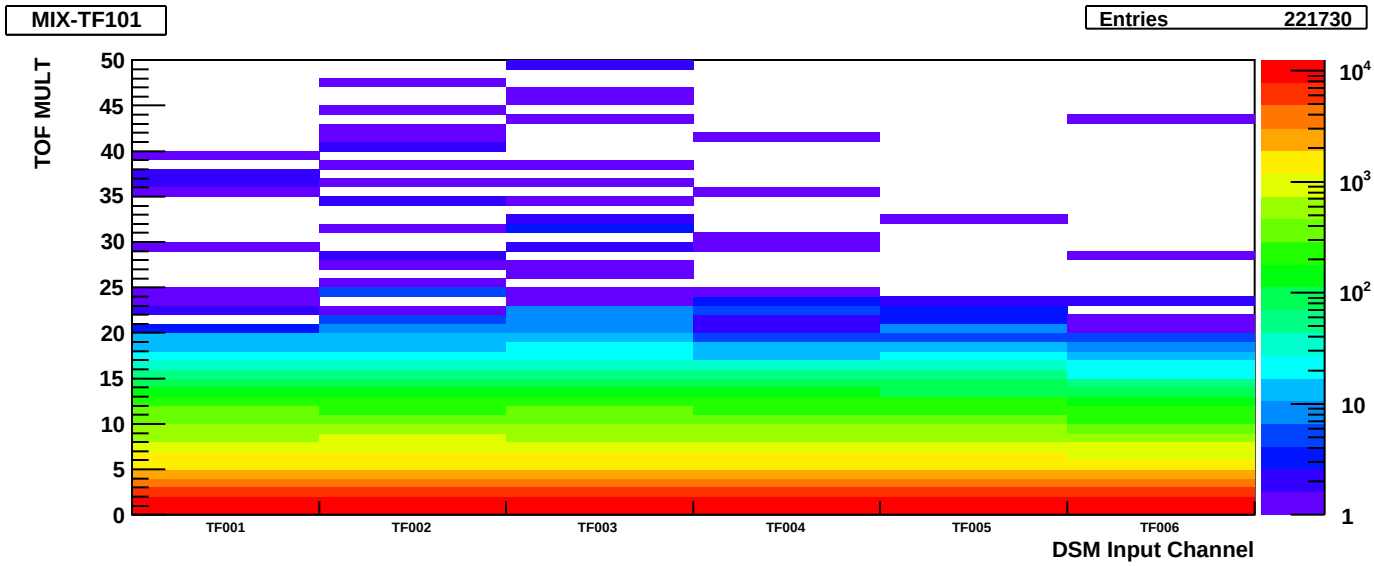

MIX-TF002
Entries 1552110
TOF MULT
TOF tray

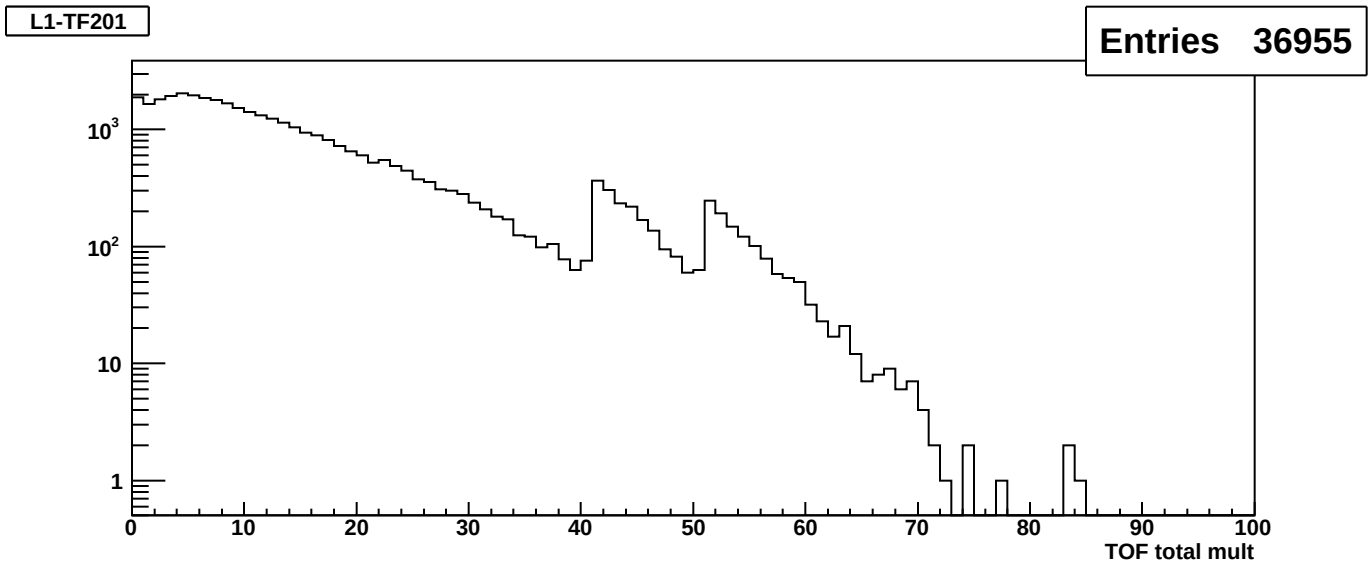

MIX-TF003
Entries 1552110
TOF MULT
TOF tray

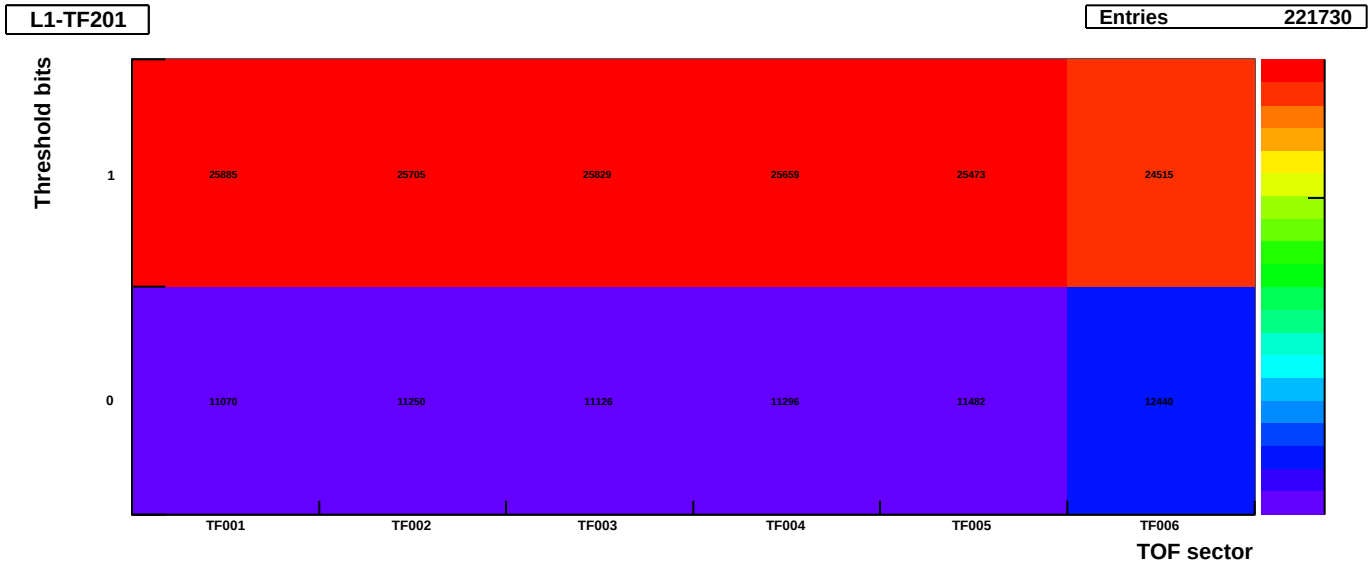

MIX-TF004
Entries 1552110
TOF MULT
TOF tray

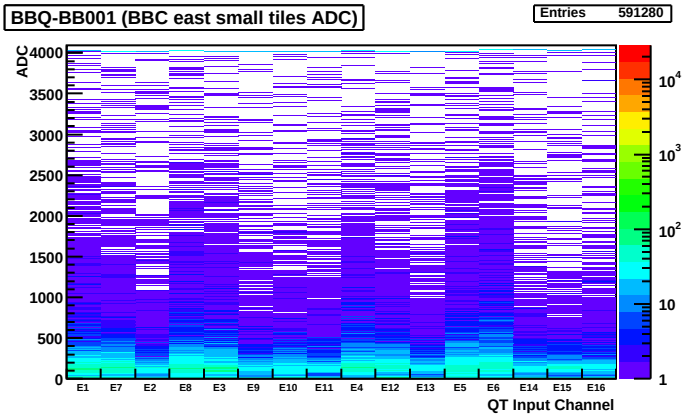

MIX-TF005
Entries 1552110
TOF MULT
TOF tray

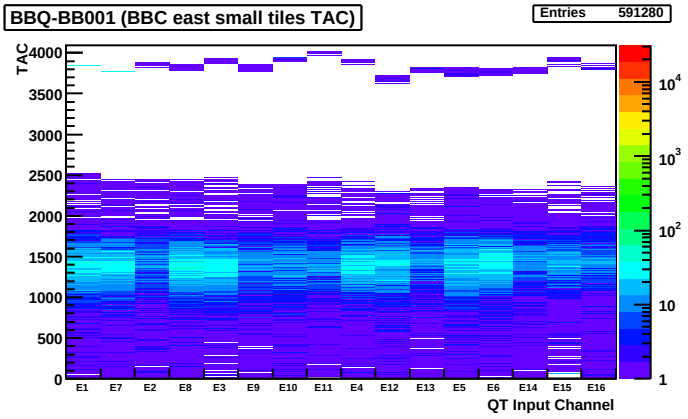

MIX-TF006
Entries 1552110
TOF MULT
TOF tray

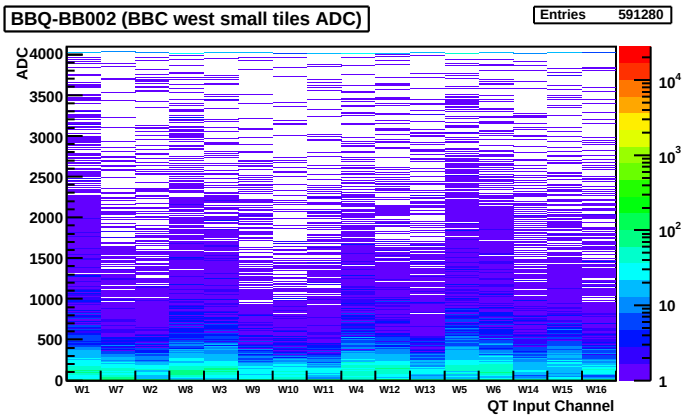

MIX-TF101
Entries 221730
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

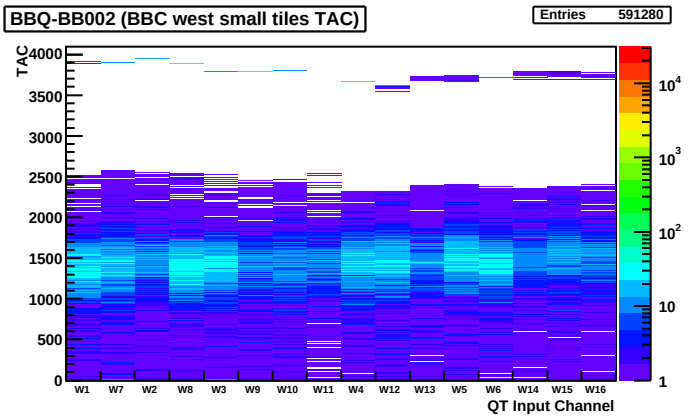

L1-TF201
Entries 36955
TOF total mult

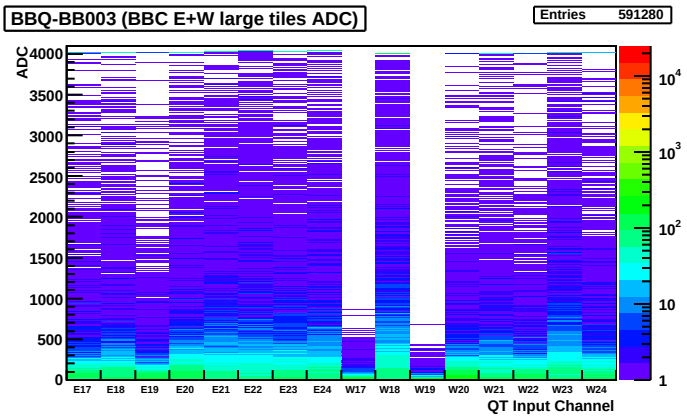

L1-TF201
Entries 221730
Threshold bits
25885
25705
25829
25659
25473
24515
11070
11250
11126
11296
11482
12440
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

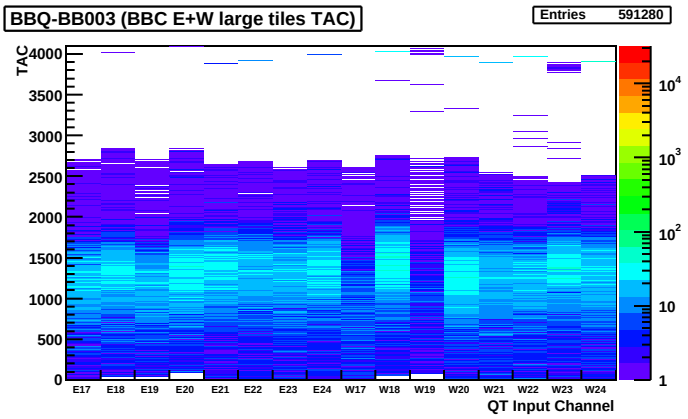

BBQ-BB001 (BBC east small tiles ADC)
Entries 591280
ADC
QT Input Channel

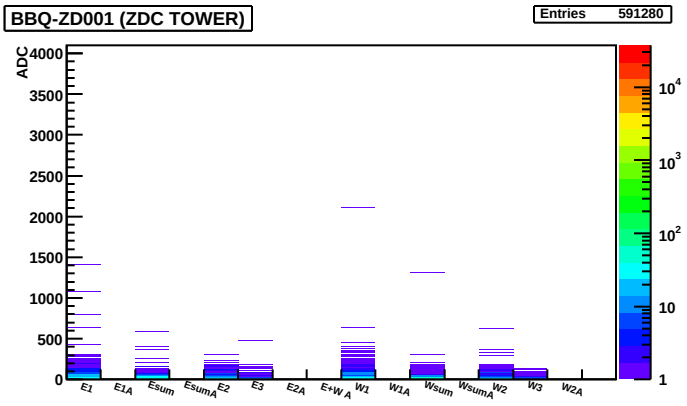

BBQ-BB001 (BBC east small tiles TAC)
Entries 591280
TAC
QT Input Channel

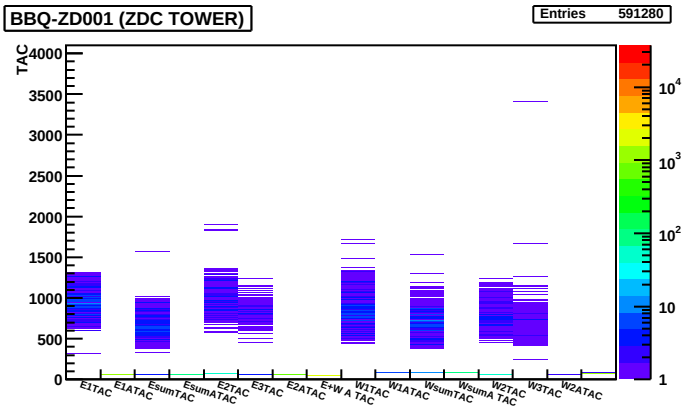

BBQ-BB002 (BBC west small tiles ADC)
Entries 591280
ADC
QT Input Channel

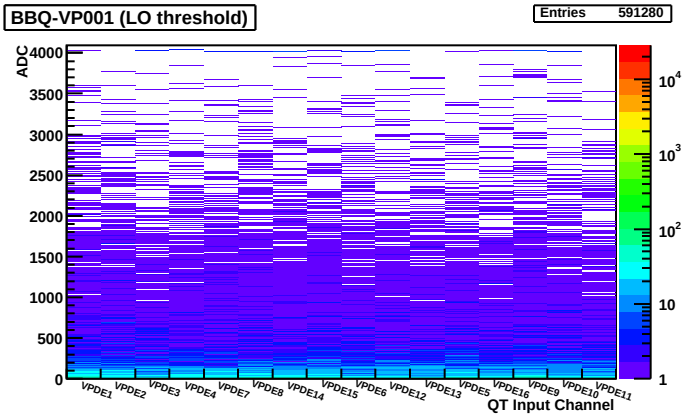

BBQ-BB002 (BBC west small tiles TAC)
Entries 591280
TAC
QT Input Channel

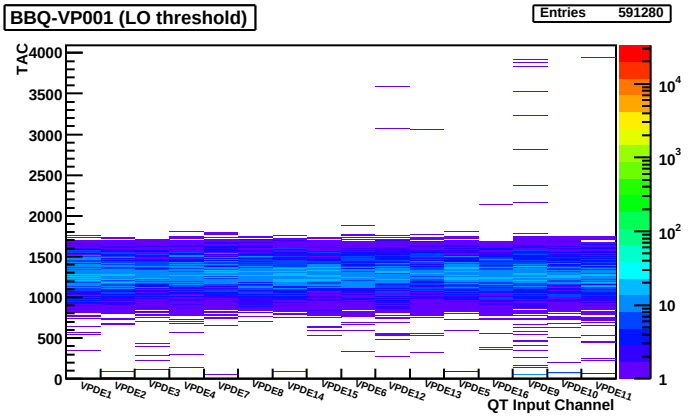

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 591280
ADC
QT Input Channel

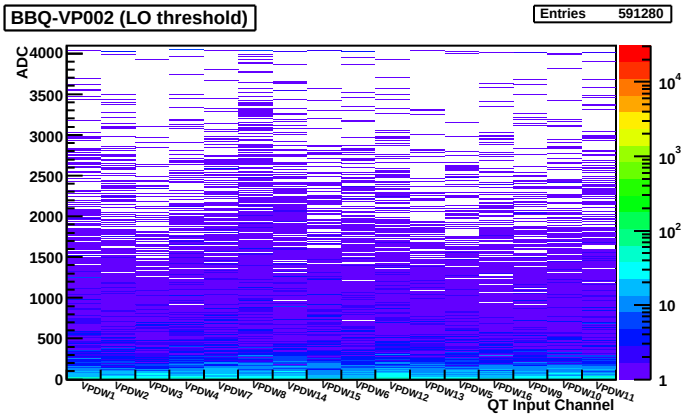

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 591280
TAC
QT Input Channel

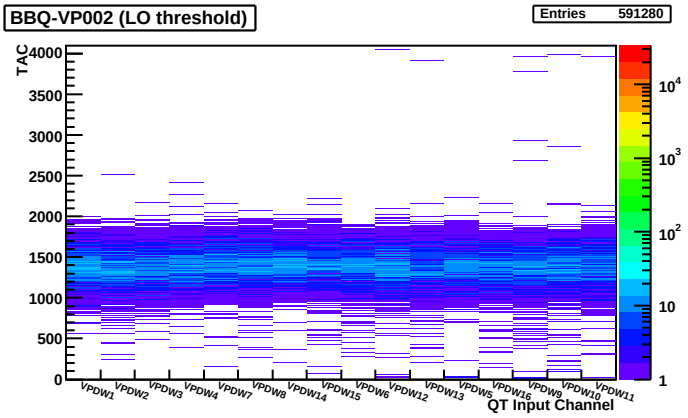

BBQ-ZD001 (ZDC TOWER)
Entries 591280
ADC

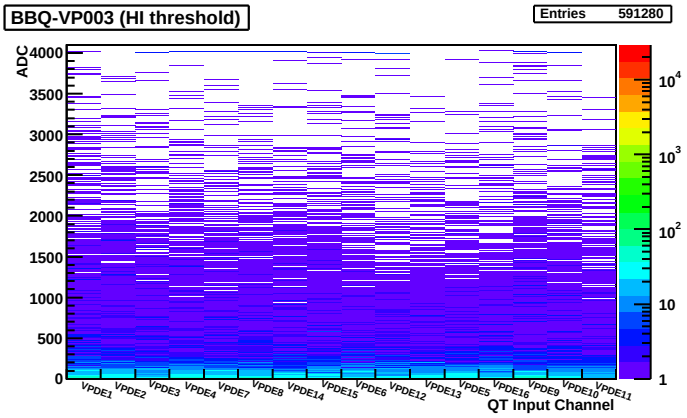

BBQ-ZD001 (ZDC TOWER)
Entries 591280
TAC

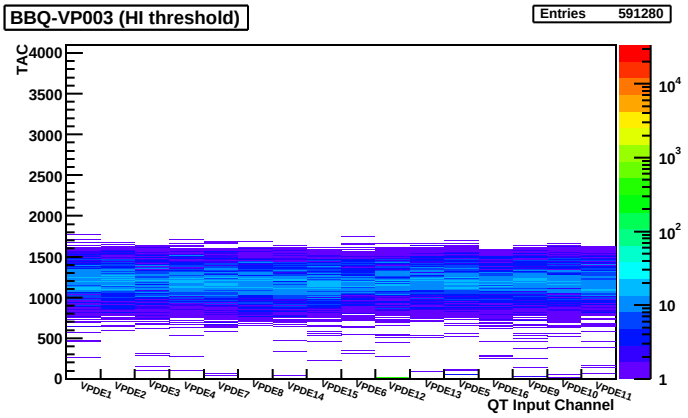

BBQ-VP001 (LO threshold)
Entries 591280
ADC
QT Input Channel

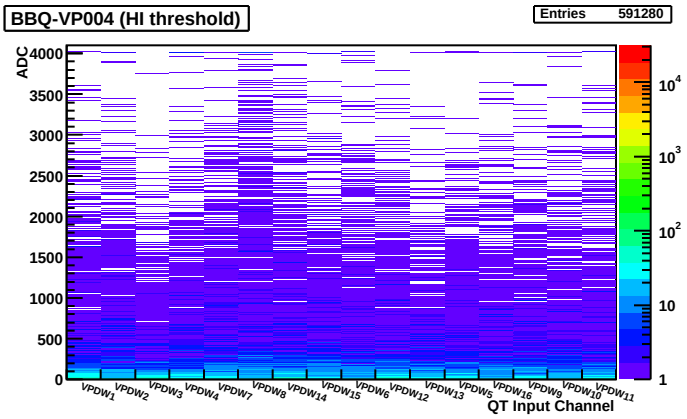

BBQ-VP001 (LO threshold)
Entries 591280
TAC
QT Input Channel

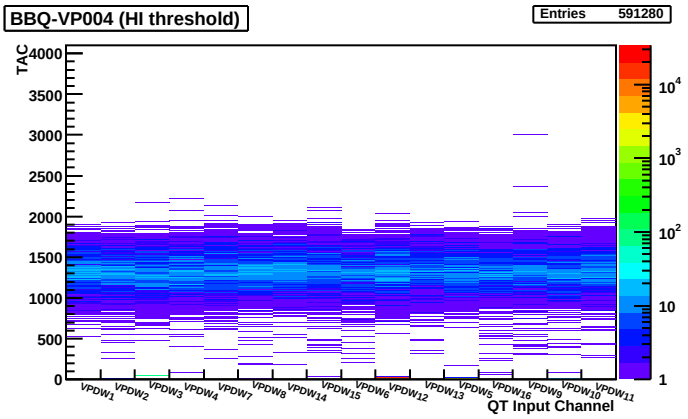

BBQ-VP002 (LO threshold)
Entries 591280
ADC
QT Input Channel

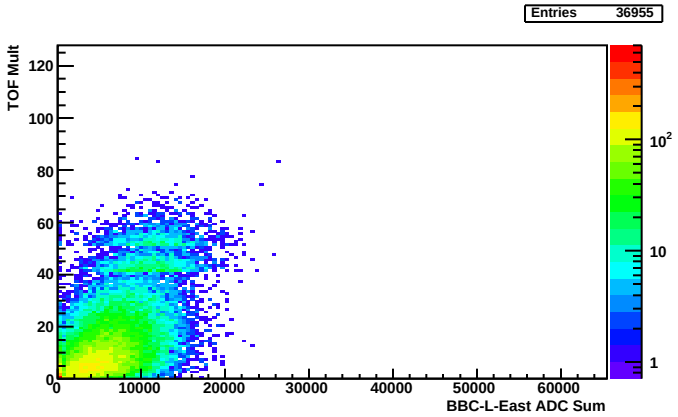

BBQ-VP002 (LO threshold)
Entries 591280
TAC
QT Input Channel

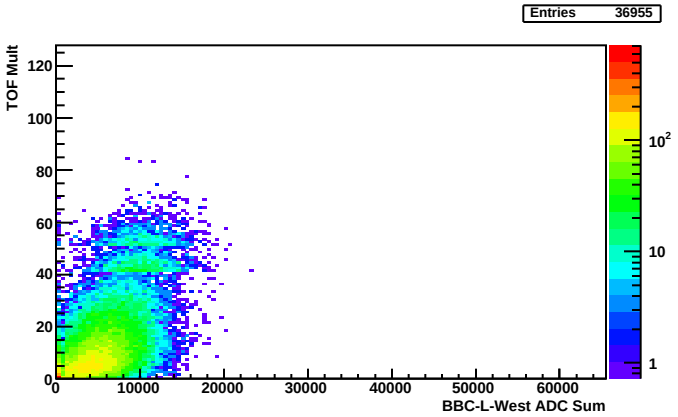

BBQ-VP003 (HI threshold)
Entries 591280
ADC
QT Input Channel

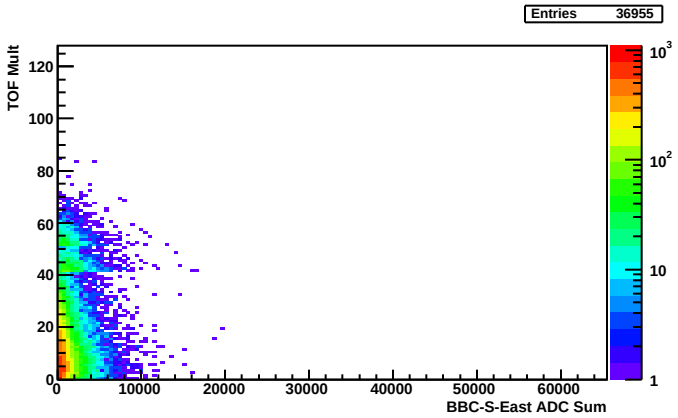

BBQ-VP003 (HI threshold)
Entries 591280
TAC
QT Input Channel

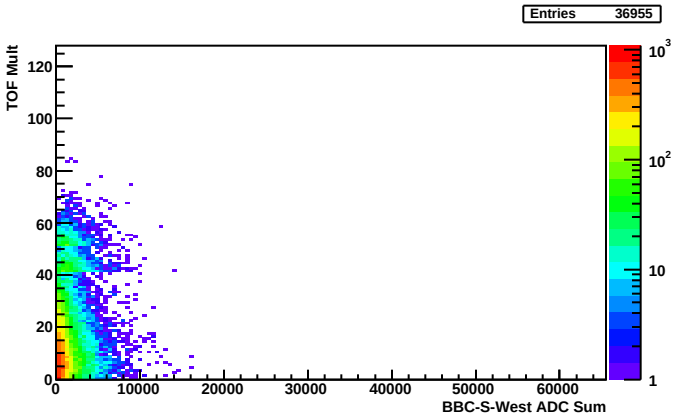

BBQ-VP004 (HI threshold)
Entries 591280
ADC
QT Input Channel

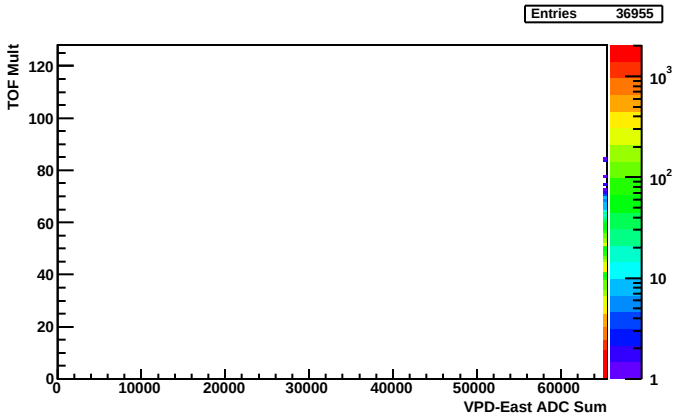

BBQ-VP004 (HI threshold)
Entries 591280
TAC
QT Input Channel

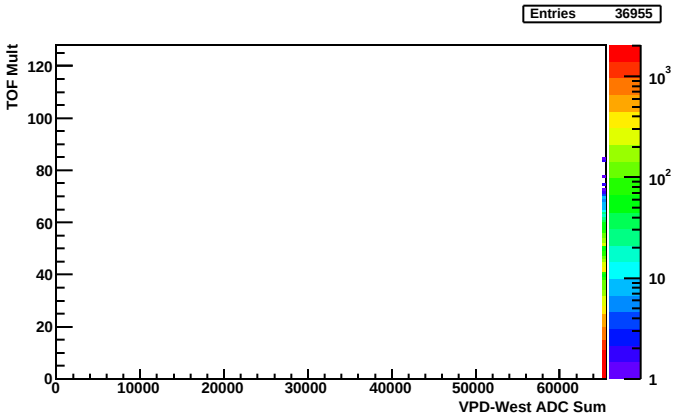

Entries 36955
TOF Mult
BBC-L-East ADC Sum

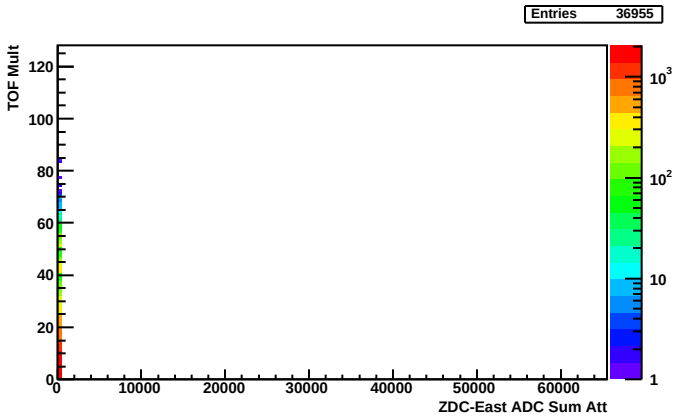

Entries 36955
TOF Mult
BBC-L-West ADC Sum

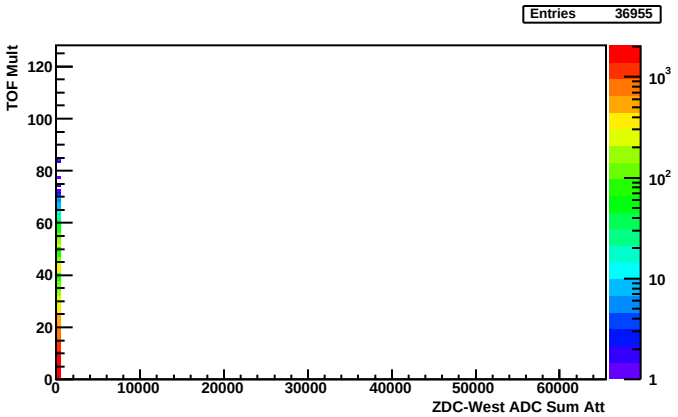

Entries 36955
TOF Mult
BBC-S-East ADC Sum

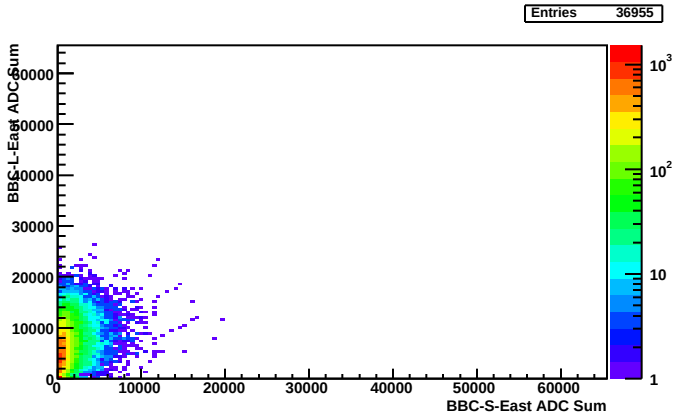

Entries 36955
TOF Mult
BBC-S-West ADC Sum

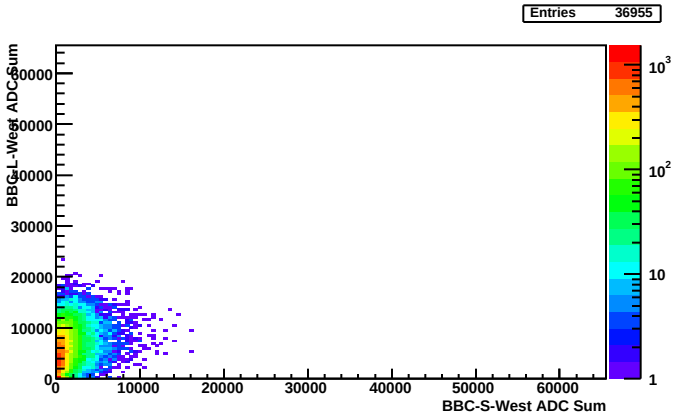

Entries 36955
TOF Mult
VPD-East ADC Sum

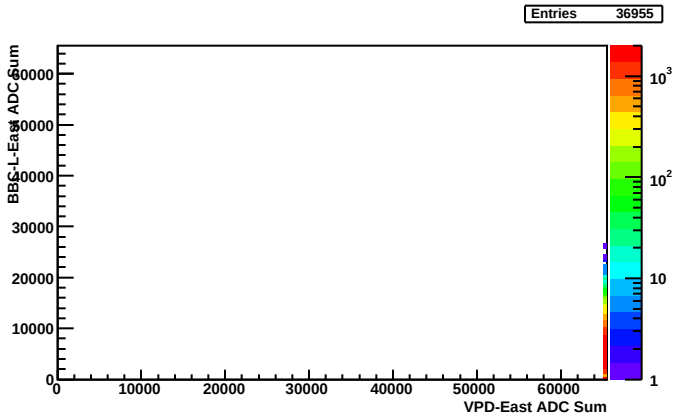

Entries 36955
TOF Mult
VPD-West ADC Sum

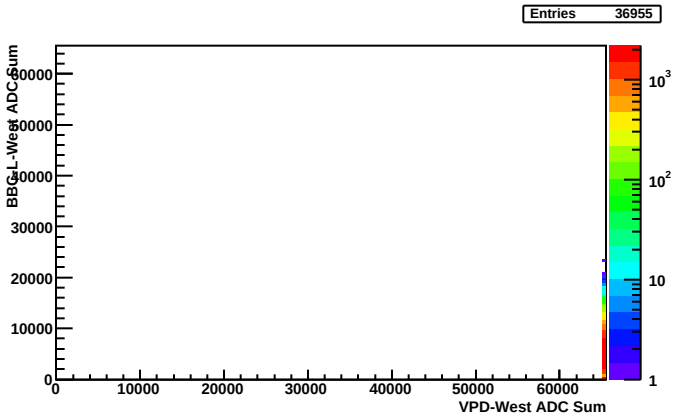

Entries 36955
TOF Mult
ZDC-East ADC Sum Att

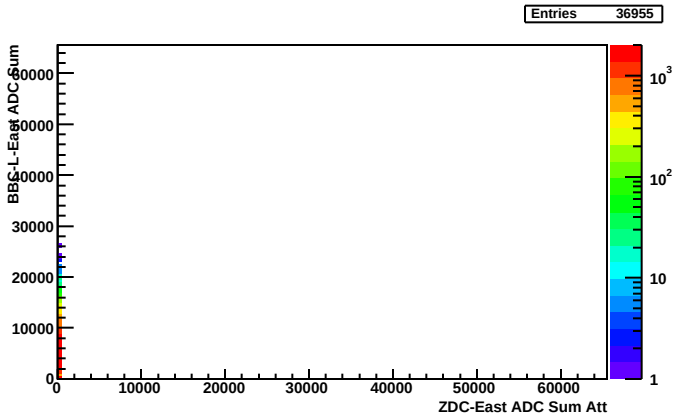

Entries 36955
TOF Mult
ZDC-West ADC Sum Att

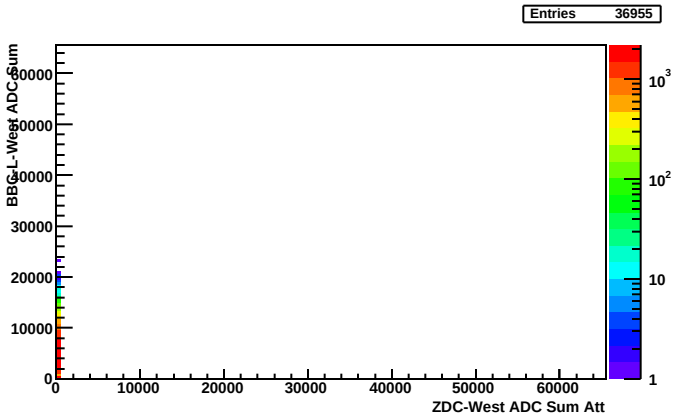

Entries 36955
BBC-L-East ADC Sum
BBC-S-East ADC Sum

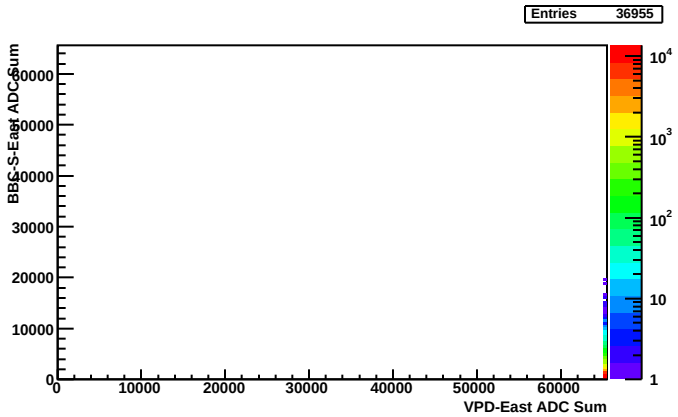

Entries 36955
BBC-L-West ADC Sum
BBC-S-West ADC Sum

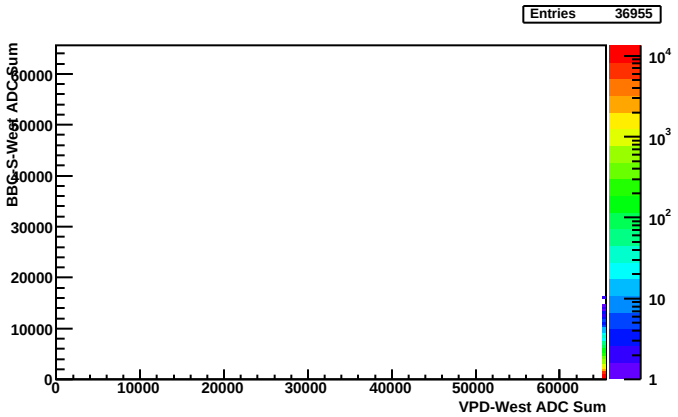

Entries 36955
BBC-L-East ADC Sum
VPD-East ADC Sum

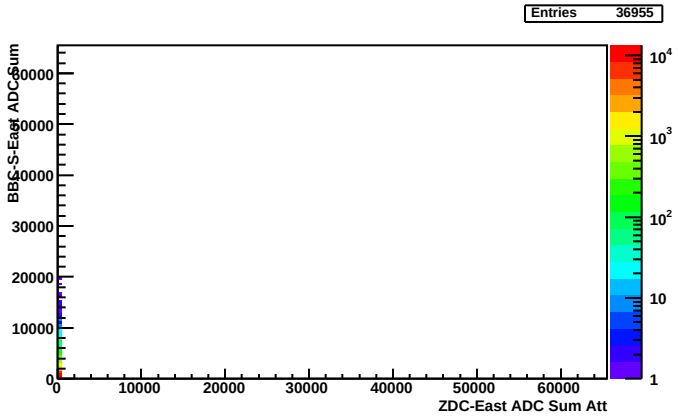

Entries 36955
BBC-L-West ADC Sum
VPD-West ADC Sum

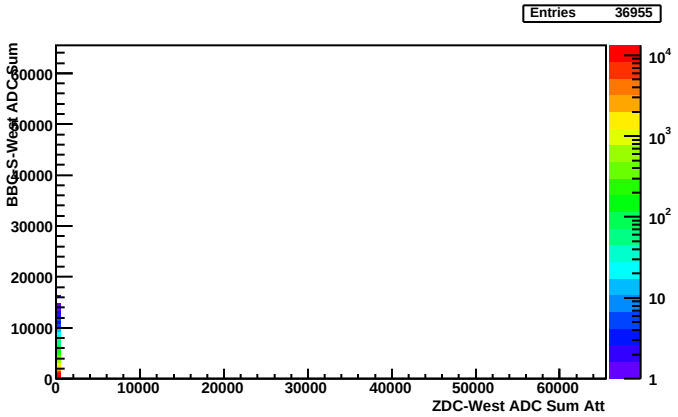

Entries 36955
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

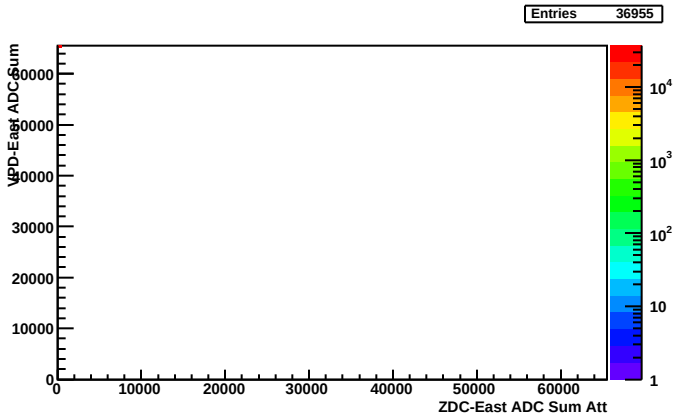

Entries 36955
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

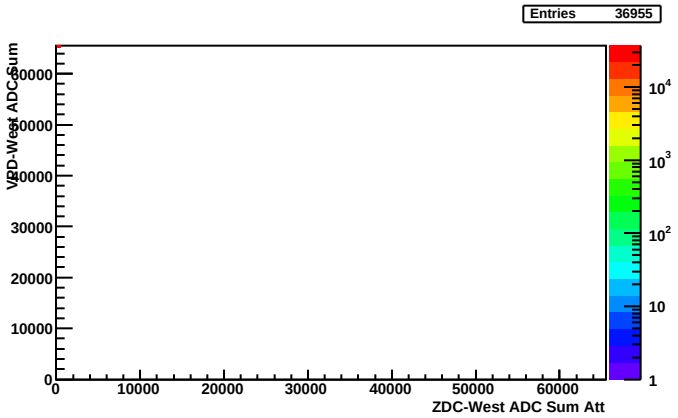

Entries 36955
BBC-S-East ADC Sum
VPD-East ADC Sum

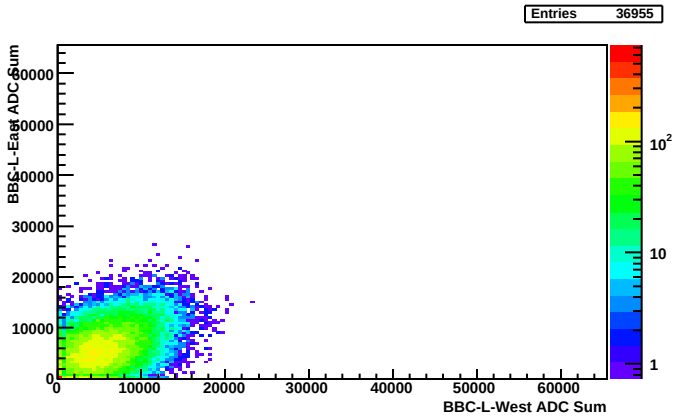

Entries 36955
BBC-S-West ADC Sum
VPD-West ADC Sum

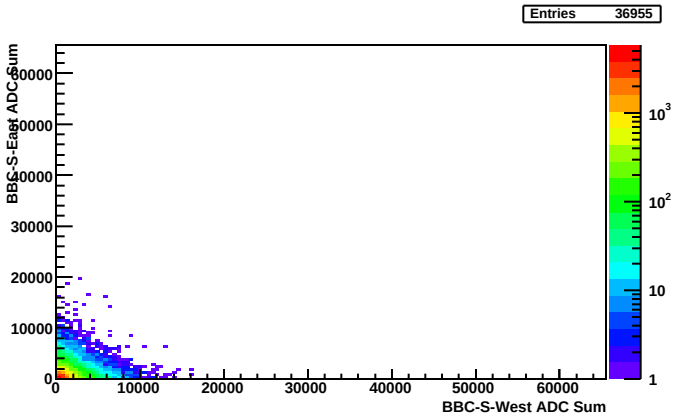

Entries 36955
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

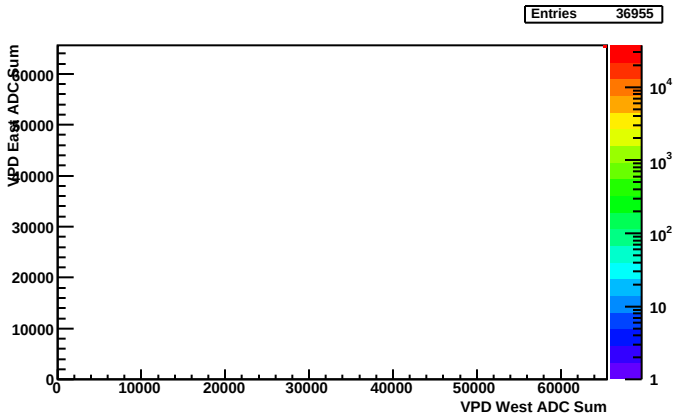

Entries 36955
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

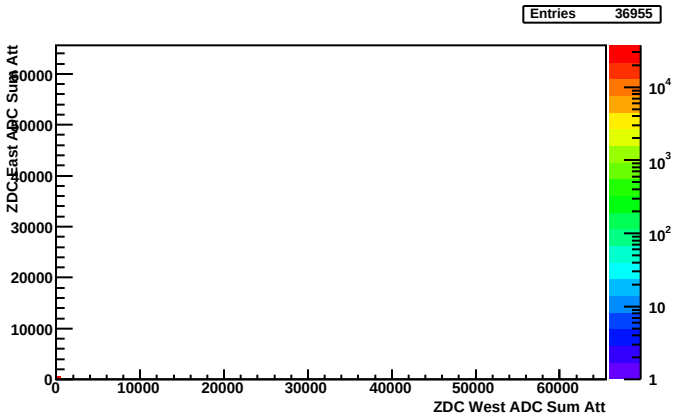

Entries 36955
VPD-East ADC Sum
ZDC-East ADC Sum Att

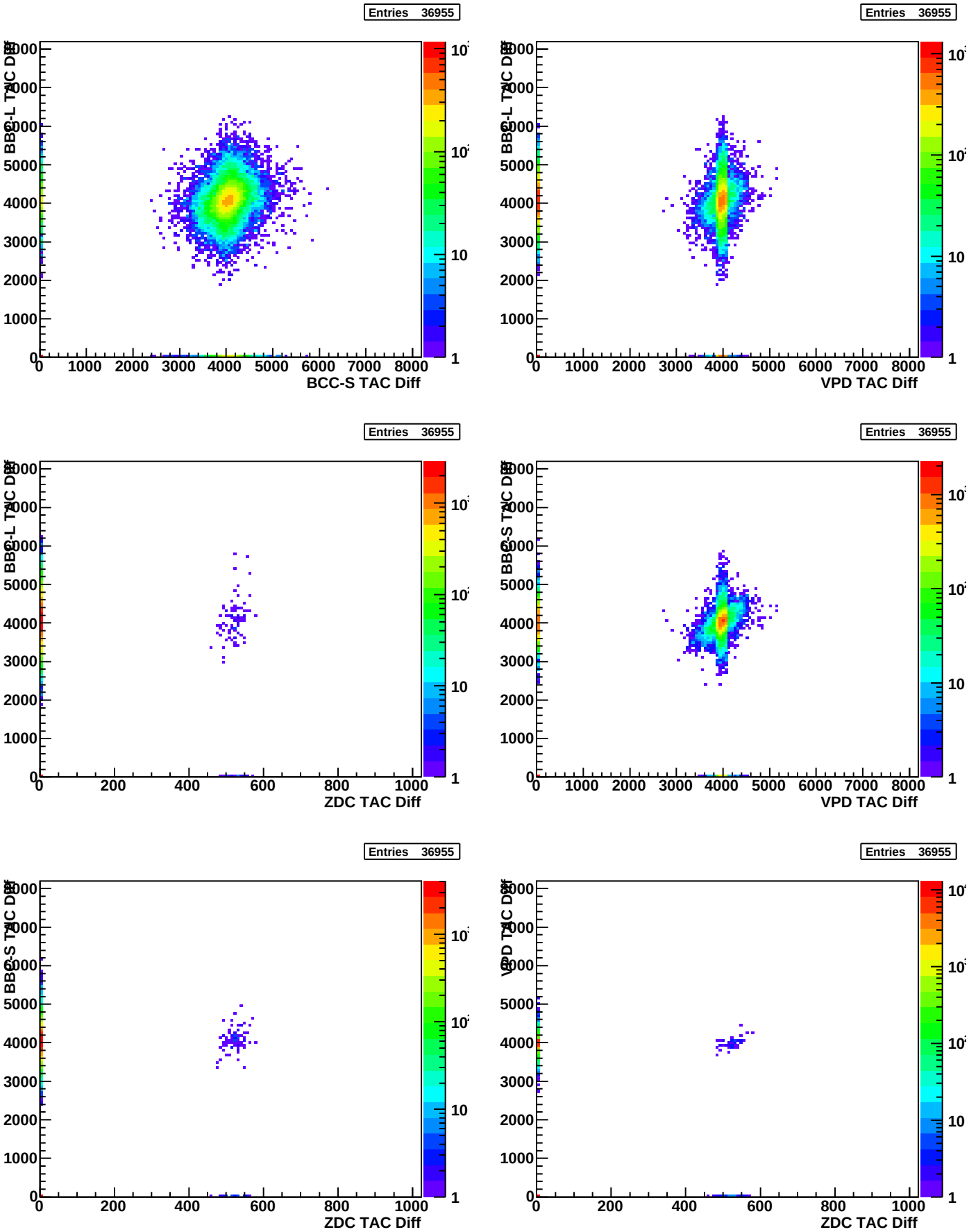

Entries 36955
VPD-West ADC Sum
ZDC-West ADC Sum Att

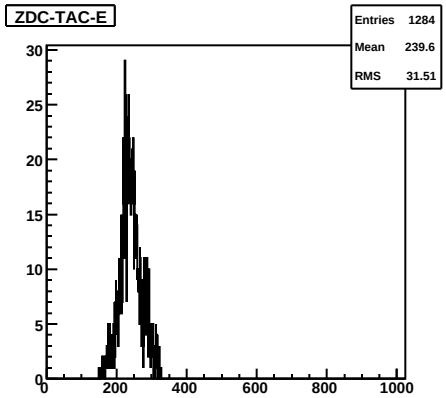

Entries 36955
BBC-L-East ADC Sum
BBC-L-West ADC Sum

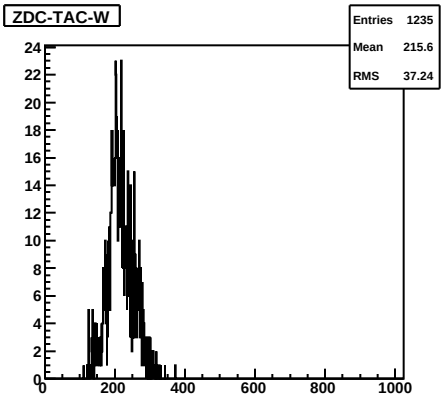

Entries 36955
BBC-S-East ADC Sum
BBC-S-West ADC Sum

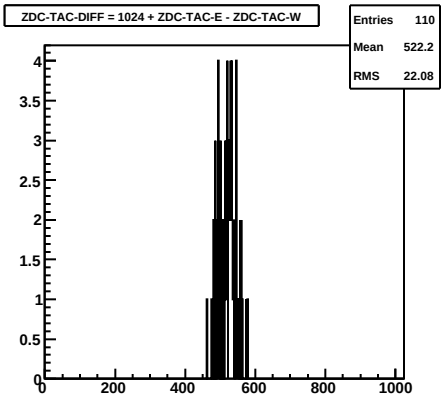

Entries 36955
VPD East ADC Sum
VPD West ADC Sum

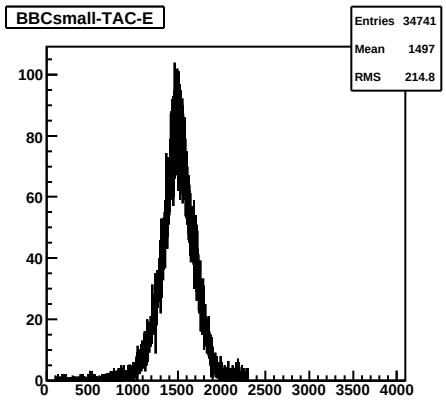

Entries 36955
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

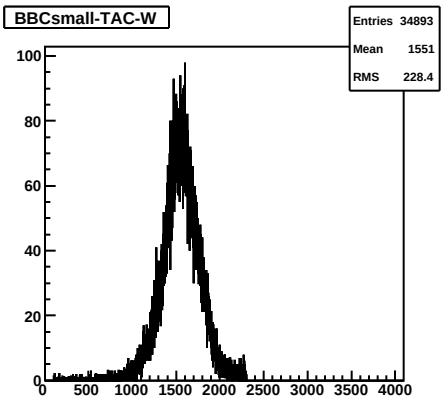

Entries 36955
BBC-L TAC Diff
BCC-S TAC Diff
Entries 36955
BBC-L TAC Diff
VPD TAC Diff
Entries 36955
BBC-L TAC Diff
ZDC TAC Diff
Entries 36955
BBC-S TAC Diff
VPD TAC Diff
Entries 36955
BBC-S TAC Diff
ZDC TAC Diff
Entries 36955
VPD TAC Diff
ZDC TAC Diff

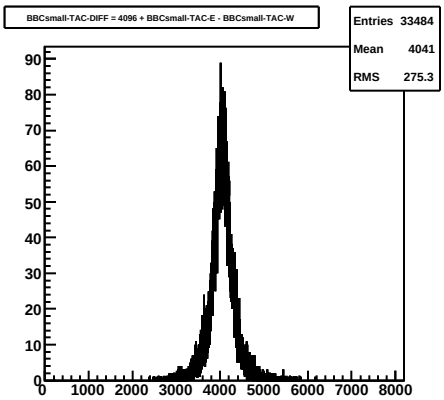

ZDC-TAC-E
Entries 1284
Mean 239.6
RMS 31.51

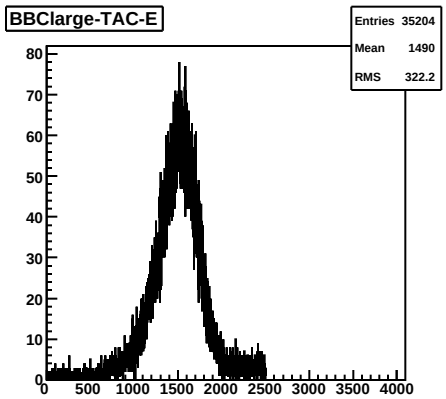

ZDC-TAC-W
Entries 1235
Mean 215.6
RMS 37.24

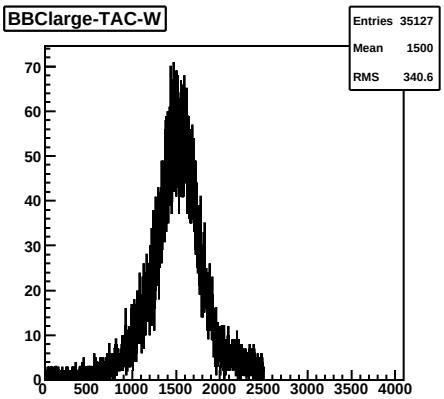

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 110
Mean 522.2
RMS 22.08

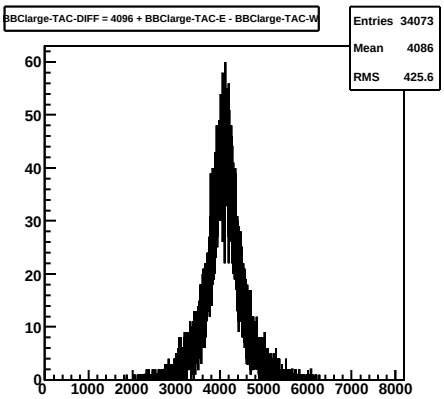

BBCsmall-TAC-E
Entries 34741
Mean 1497
RMS 214.8

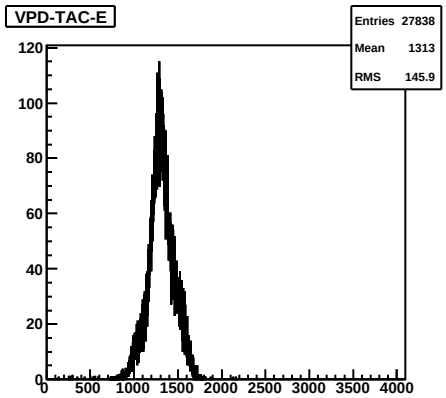

BBCsmall-TAC-W
Entries 34893
Mean 1551
RMS 228.4

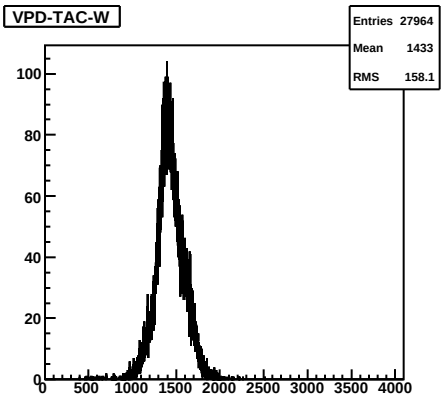

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 33484
Mean 4041
RMS 275.3

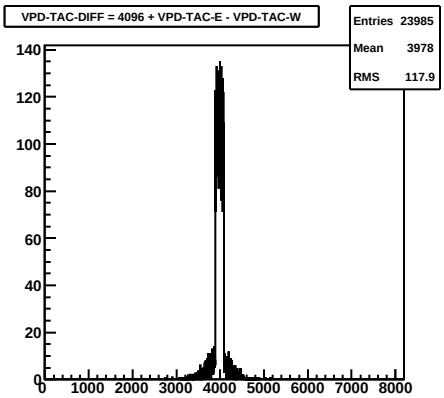

BBClarge-TAC-E
Entries 35204
Mean 1490
RMS 322.2

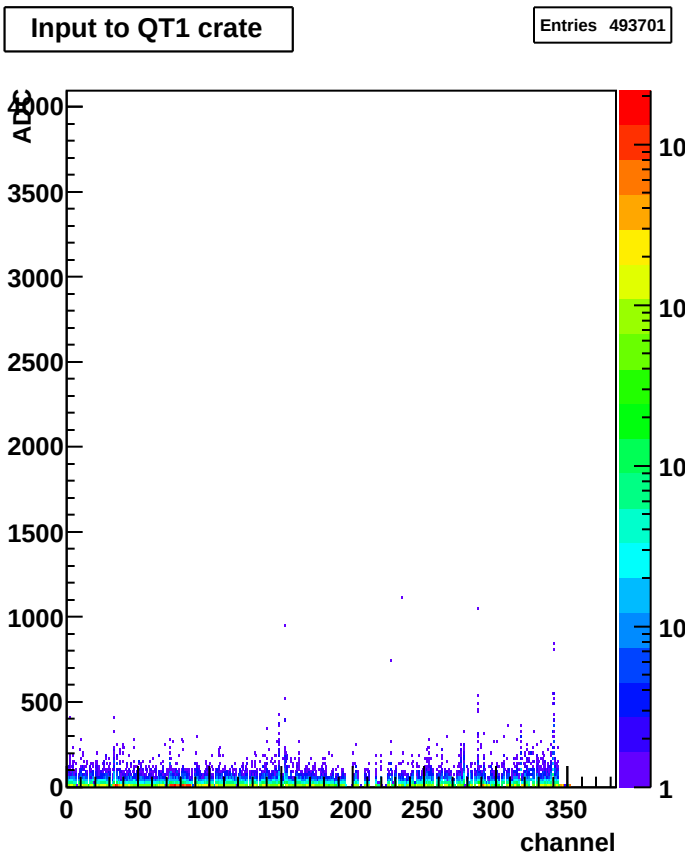

BBClarge-TAC-W
Entries 35127
Mean 1500
RMS 340.6

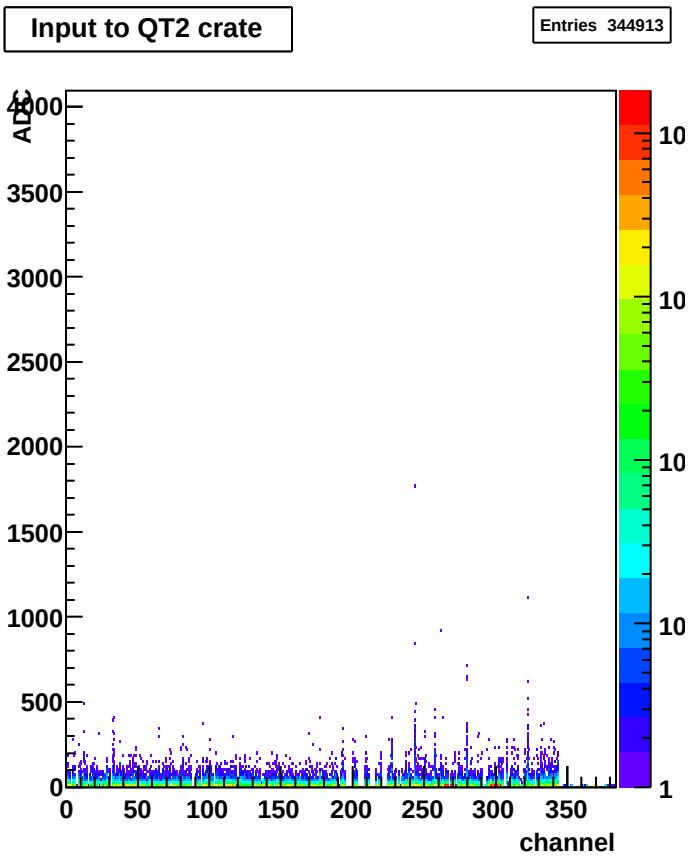

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 34073
Mean 4086
RMS 425.6

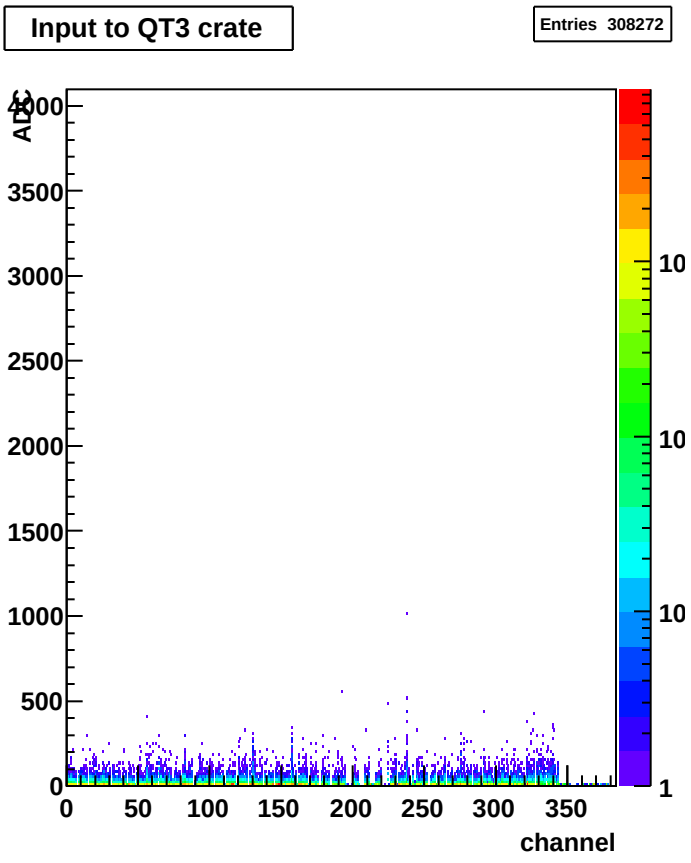

VPD-TAC-E
Entries 27838
Mean 1313
RMS 145.9

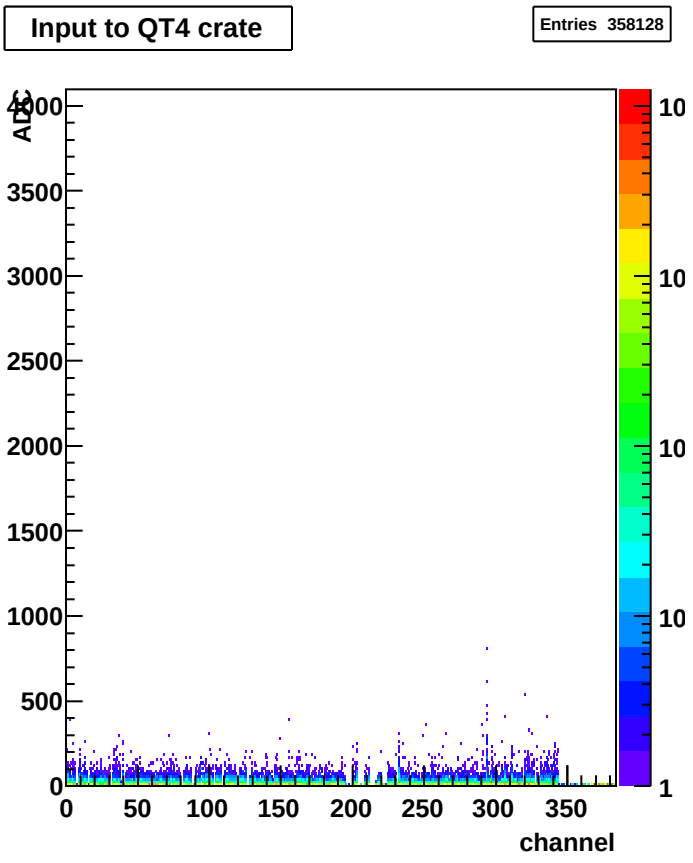

VPD-TAC-W
Entries 27964
Mean 1433
RMS 158.1

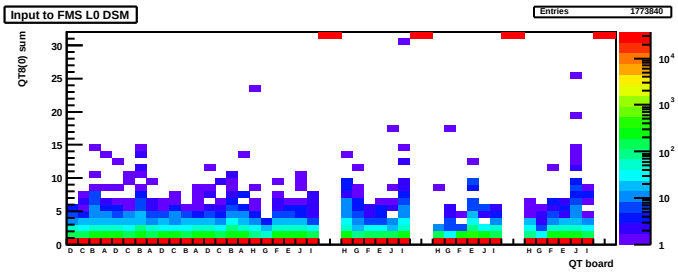

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 23985
Mean 3978
RMS 117.9

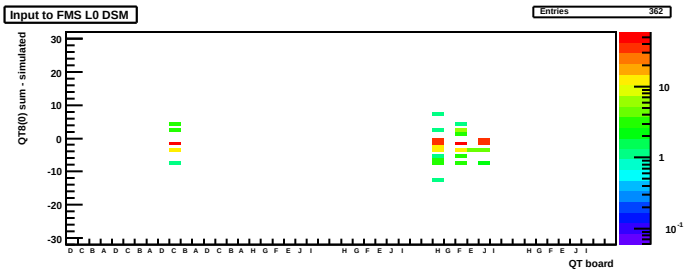

Input to QT1 crate
Entries 493701
ADC
channel

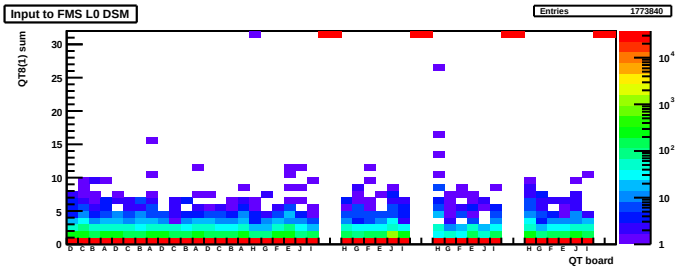

Input to QT2 crate
Entries 344913
ADC
channel

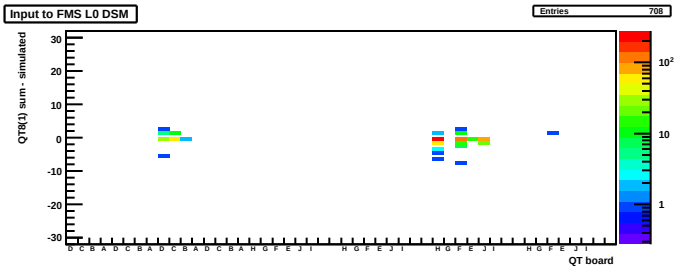

Input to QT3 crate
Entries 308272
ADC
channel

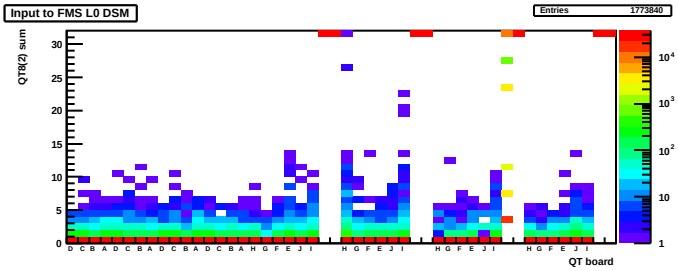

Input to QT4 crate
Entries 358128
ADC
channel

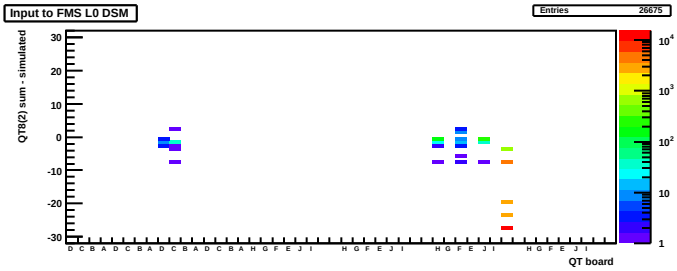

Input to FMS L0 DSM
Entries 1773840
QT8(0) sum
QT board

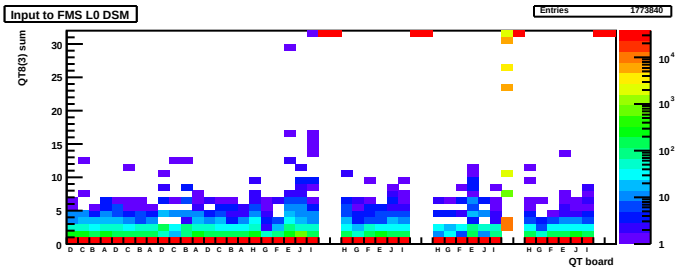

Input to FMS L0 DSM
Entries 362
QT8(0) sum - simulated
QT board

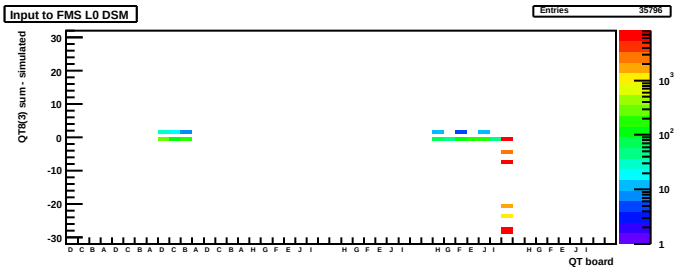

Input to FMS L0 DSM
Entries 1773840
QT8(1) sum
QT board

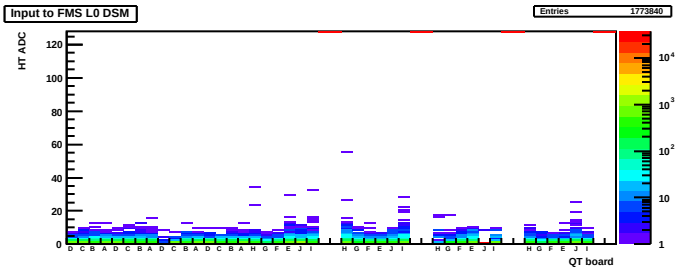

Input to FMS L0 DSM
Entries 708
QT8(1) sum - simulated
QT board

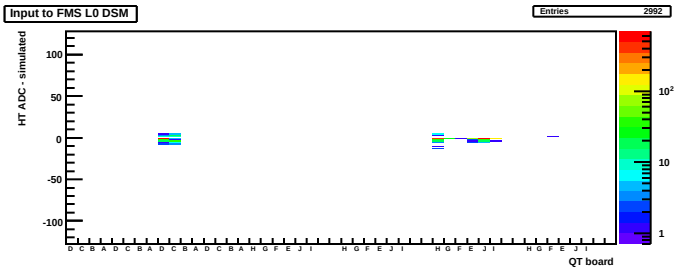

Input to FMS L0 DSM
Entries 1773840
QT8(2) sum
QT board

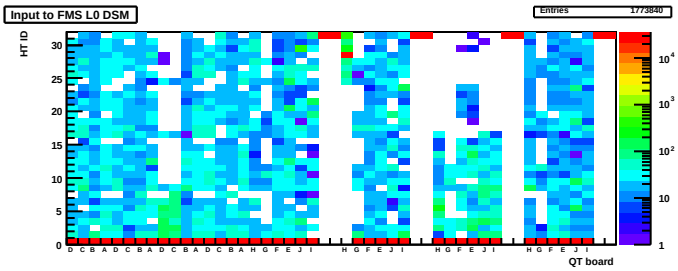

Input to FMS L0 DSM
Entries 26675
QT8(2) sum - simulated
QT board

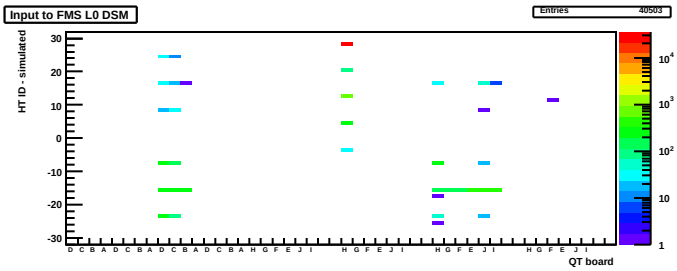

Input to FMS L0 DSM
Entries 1773840
QT8(3) sum
QT board

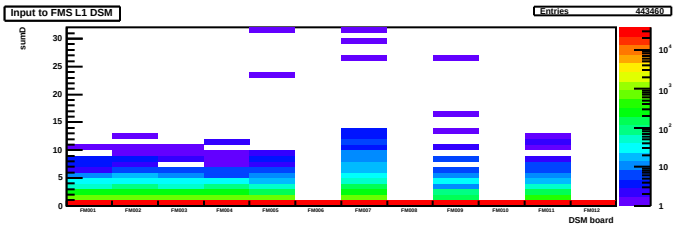

Input to FMS L0 DSM
Entries 35796
QT8(3) sum - simulated
QT board

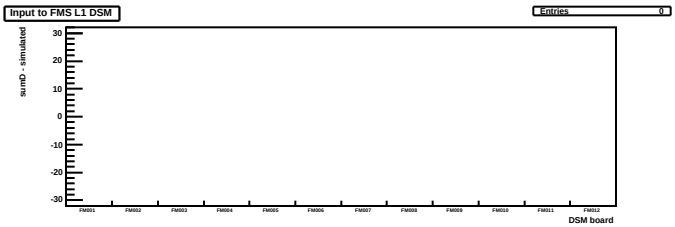

Input to FMS L0 DSM
Entries 1773840
HT ADC
QT board

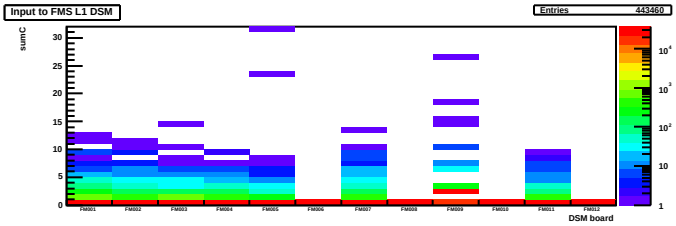

Input to FMS L0 DSM
Entries 2992
HT ADC - simulated
QT board

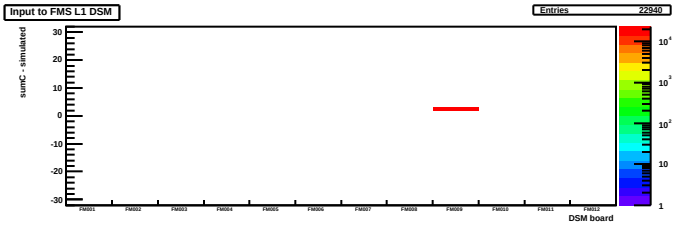

Input to FMS L0 DSM
Entries 1773840
HT ID
QT board

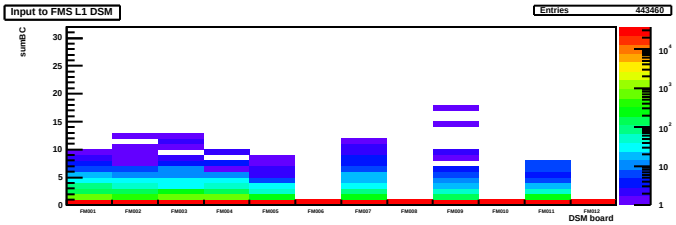

Input to FMS L0 DSM
Entries 40503
HT ID - simulated
QT board

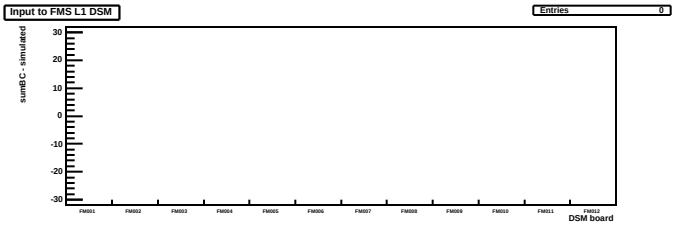

Input to FMS L1 DSM
Entries 443460
sumD
DSM board

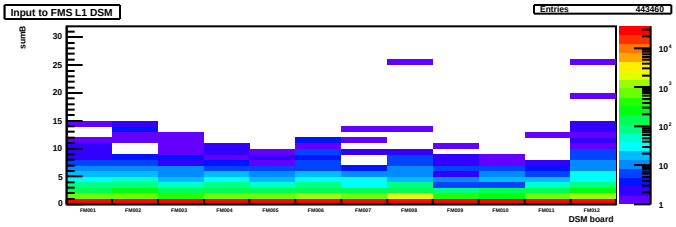

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

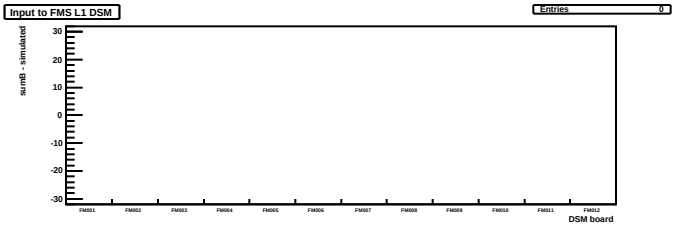

Input to FMS L1 DSM
Entries 443460
sumC
DSM board

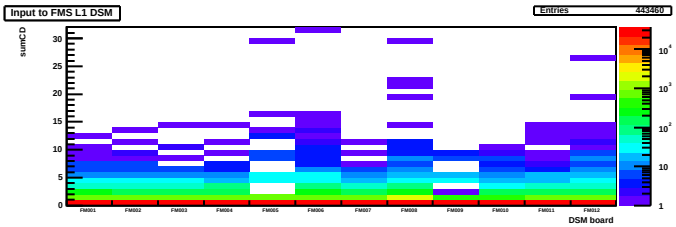

Input to FMS L1 DSM
Entries 22940
sumC - simulated
DSM board

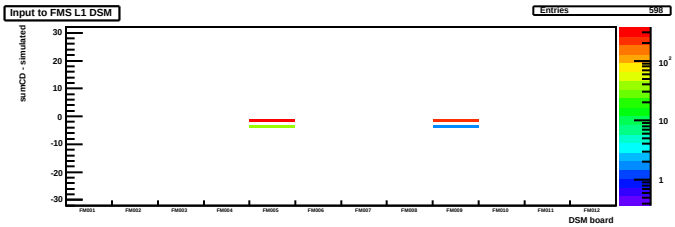

Input to FMS L1 DSM
Entries 443460
sumBC
DSM board

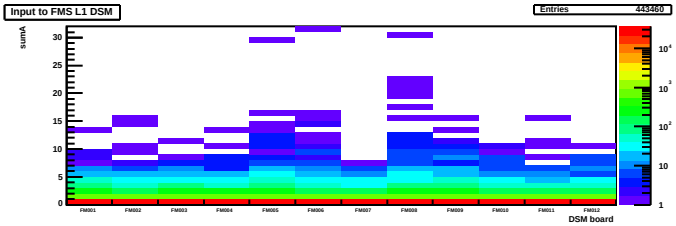

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

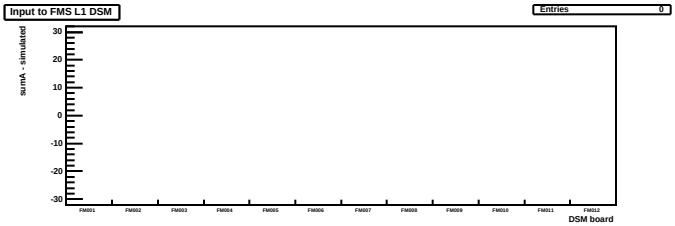

Input to FMS L1 DSM
Entries 443460
sumB
DSM board

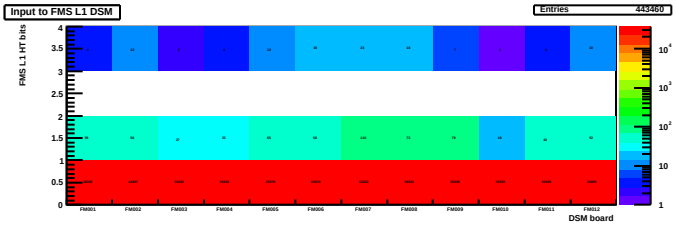

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

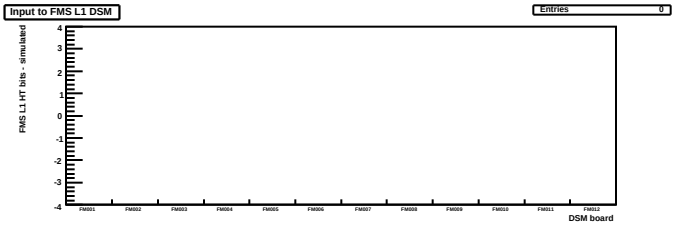

Input to FMS L1 DSM
Entries 443460
sumCD
DSM board

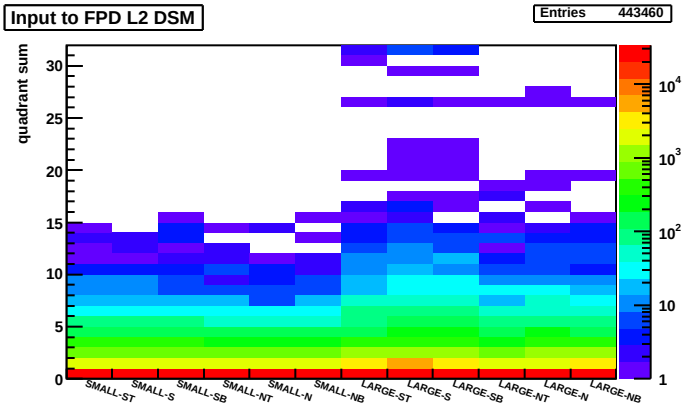

Input to FMS L1 DSM
Entries 598
sumCD - simulated
DSM board

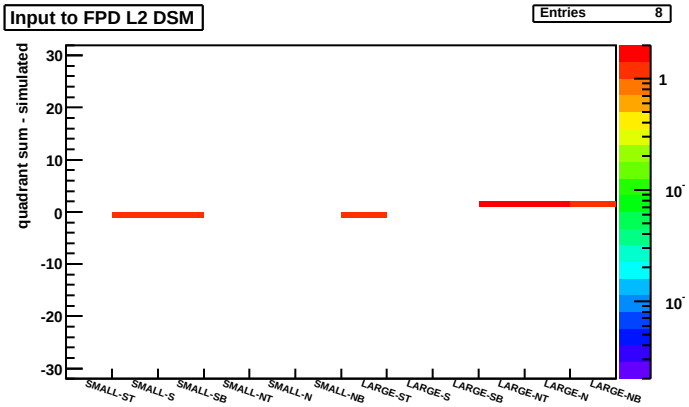

Input to FMS L1 DSM
Entries 443460
sumA
DSM board

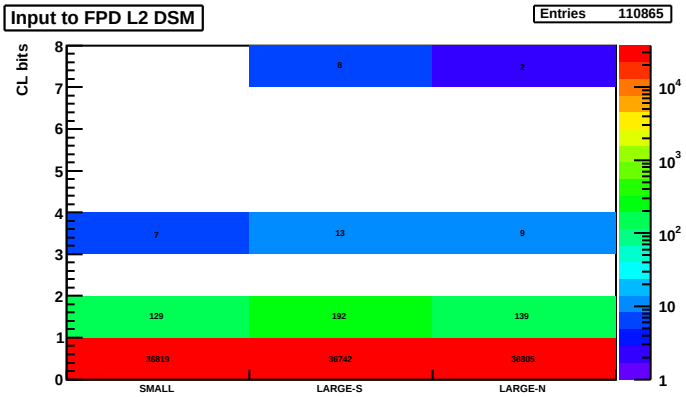

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

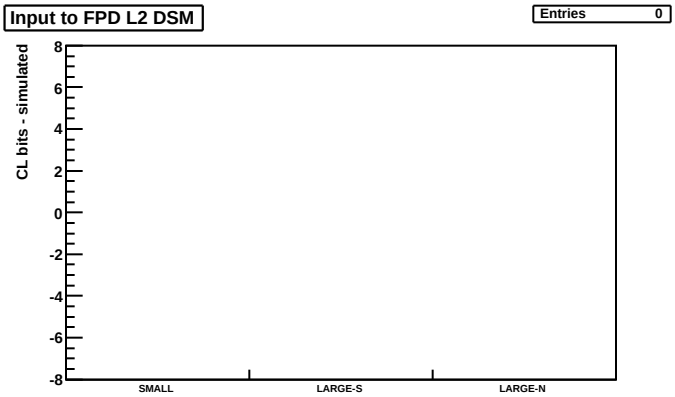

Input to FMS L1 DSM
Entries 443460
FMS L1 HT bits
DSM board

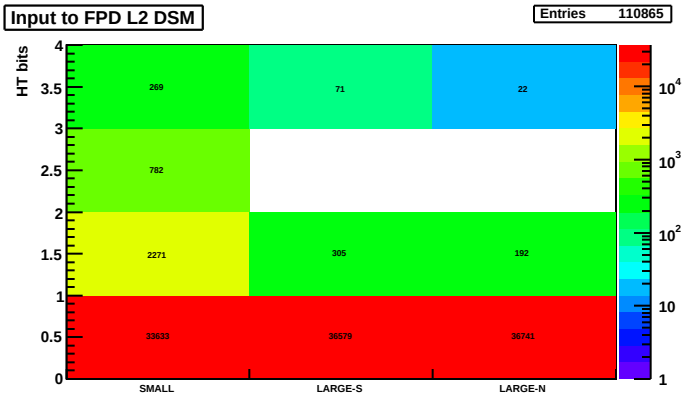

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

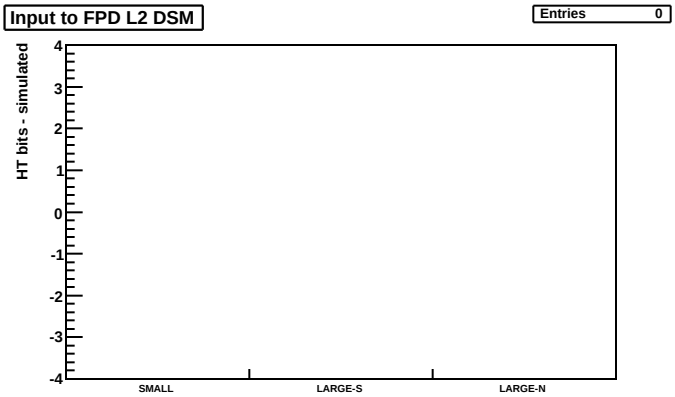

Input to FPD L2 DSM
Entries 443460
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

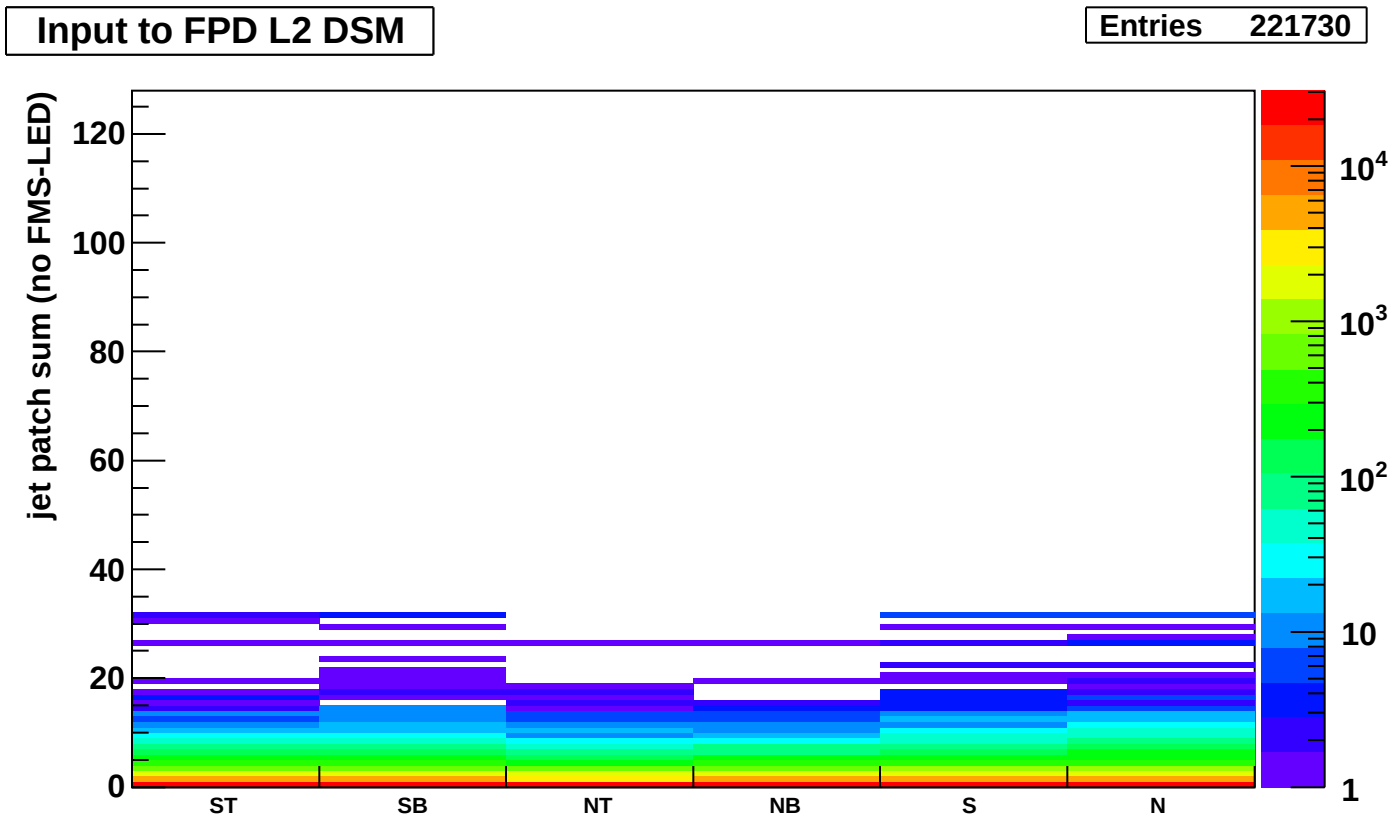

Input to FPD L2 DSM
Entries 8
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

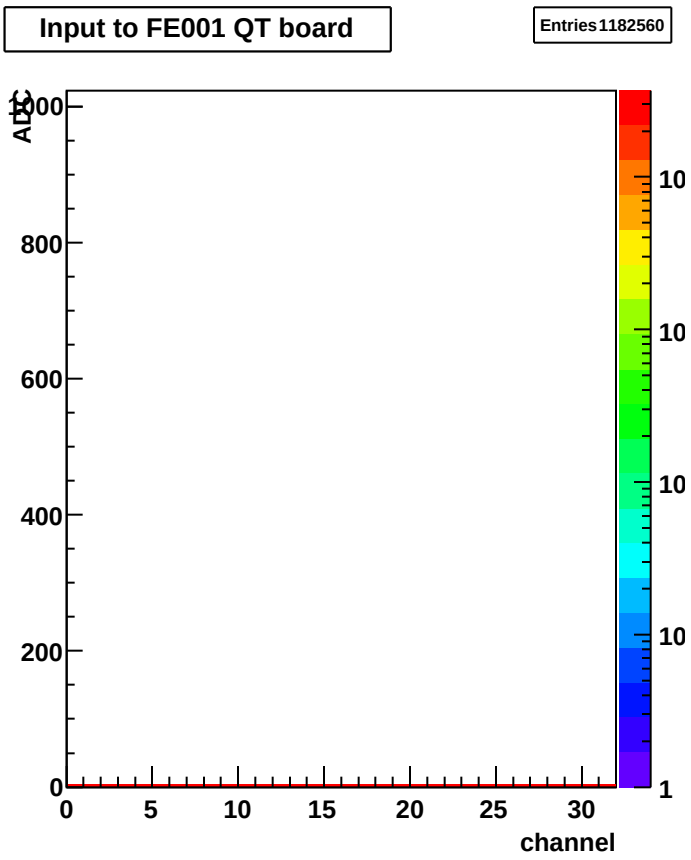

Input to FPD L2 DSM
Entries 110865
CL bits
8
2
7
13
9
129
192
139
36819
36742
36805
SMALL
LARGE-S
LARGE-N

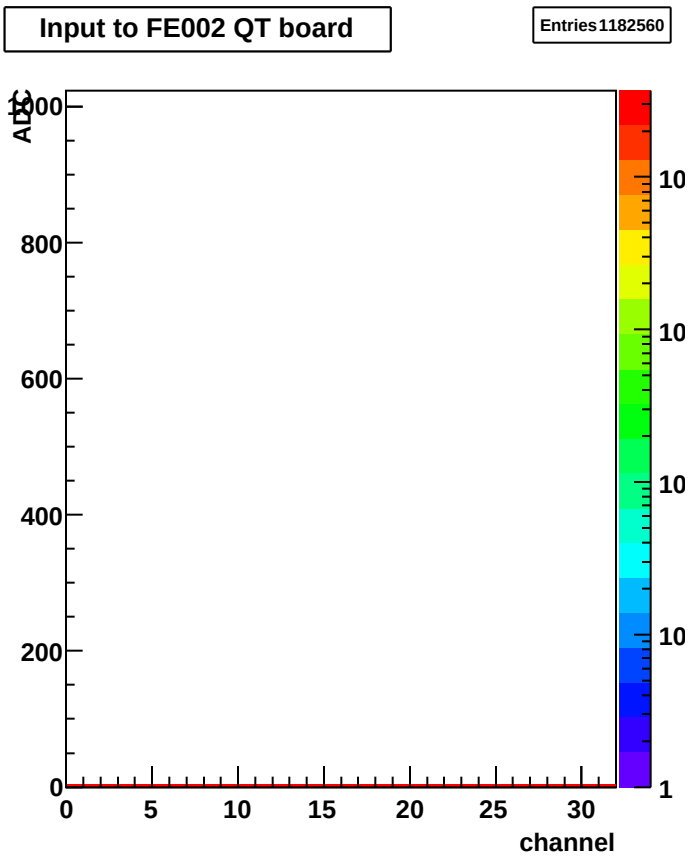

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

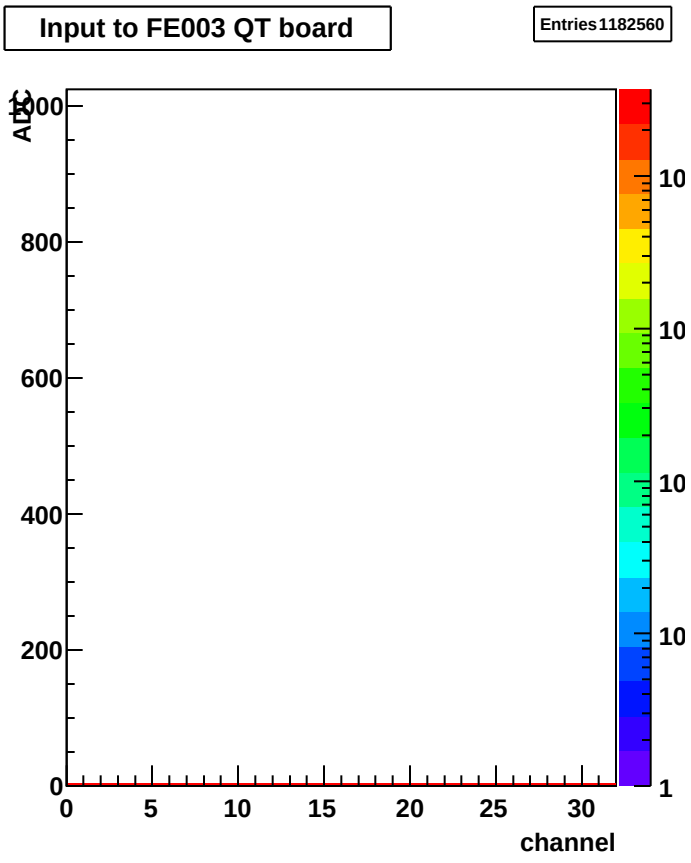

Input to FPD L2 DSM
Entries 110865
HT bits
269
71
22
782
2271
305
192
33633
36579
36741
SMALL
LARGE-S
LARGE-N

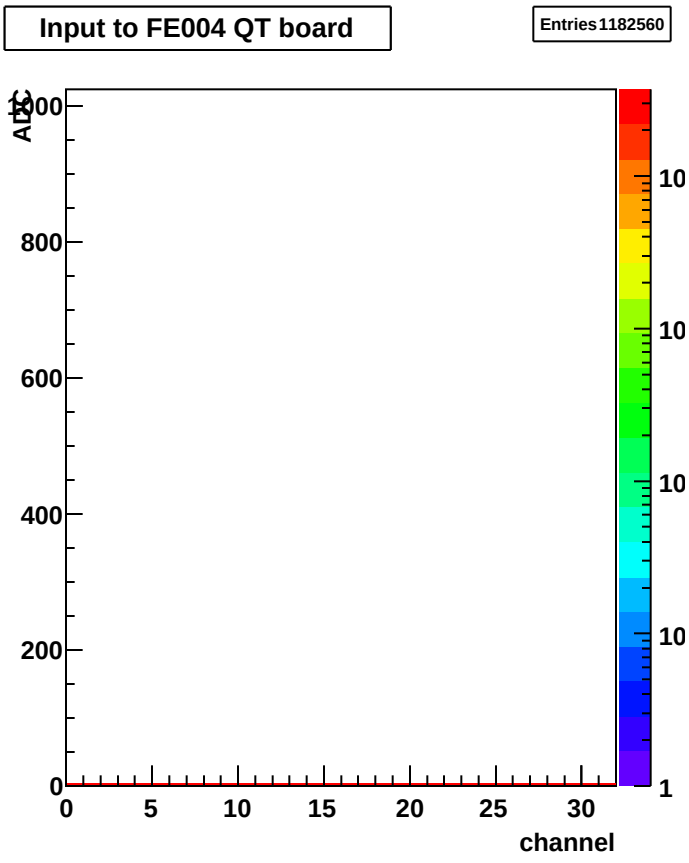

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

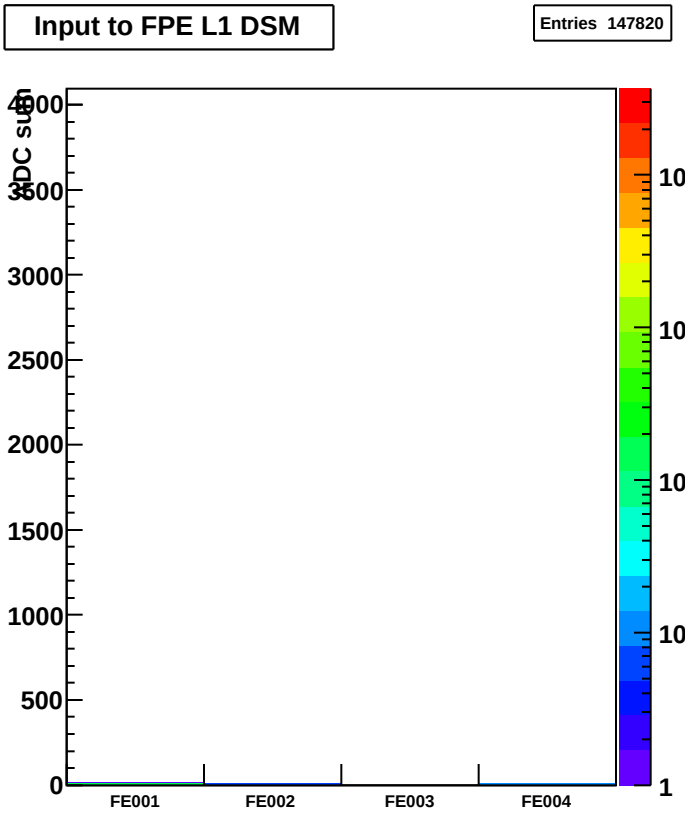

Input to FPD L2 DSM
Entries 221730
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

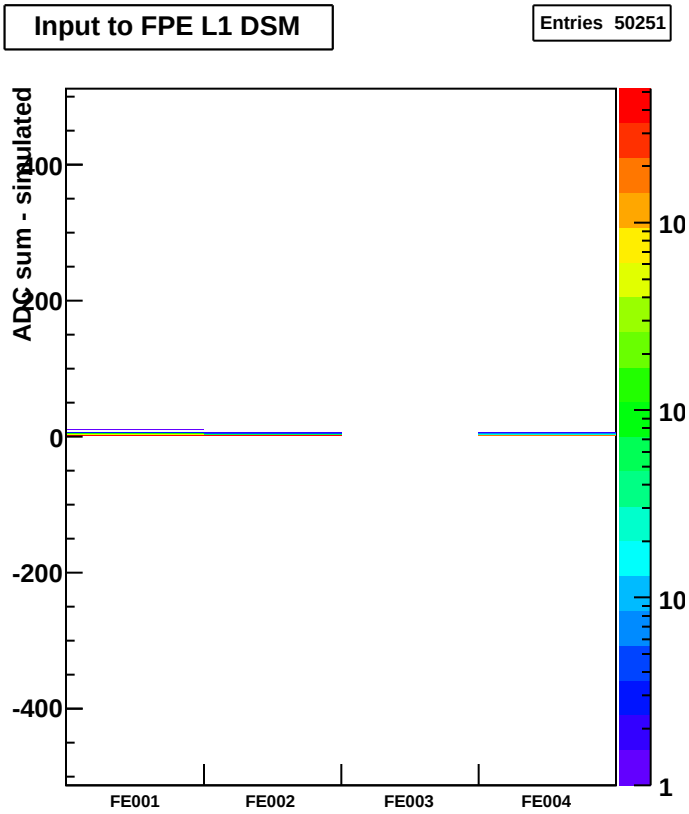

Input to FE001 QT board
Entries 1182560
ADC
channel

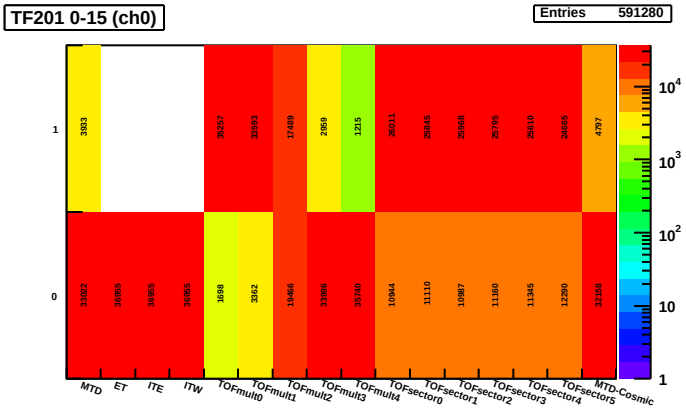

Input to FE002 QT board
Entries 1182560
ADC
channel

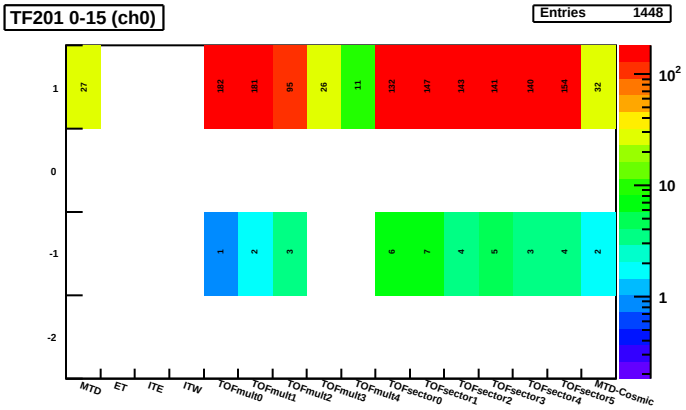

Input to FE003 QT board
Entries 1182560
ADC
channel

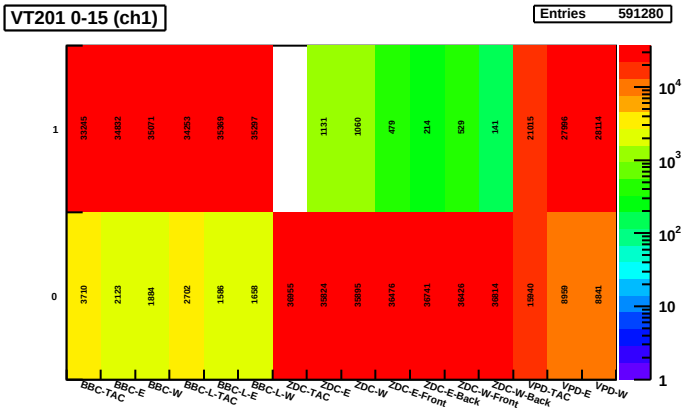

Input to FE004 QT board
Entries 1182560
ADC
channel

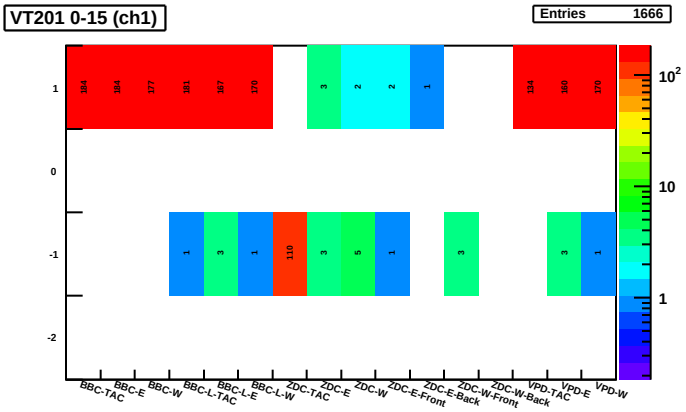

Input to FPE L1 DSM
Entries 147820
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

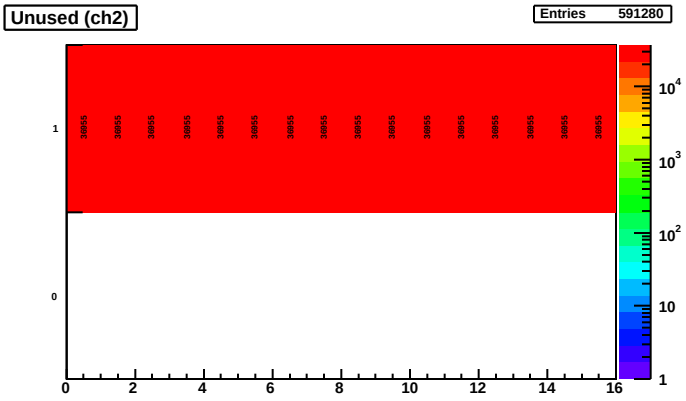

Input to FPE L1 DSM
Entries 50251
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


TF201 0-15 (ch0)
Entries 591280
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 1448
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 591280
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1666
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
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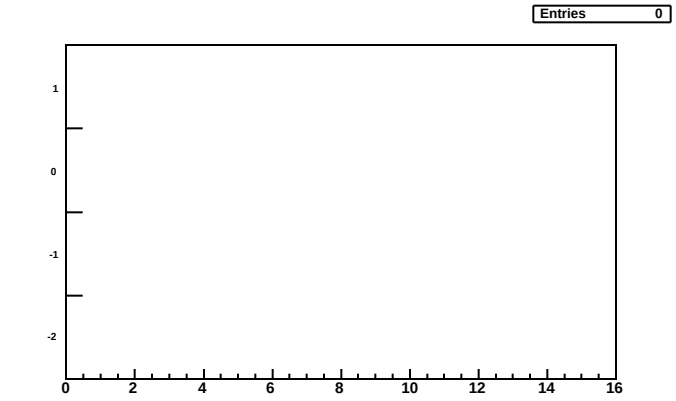
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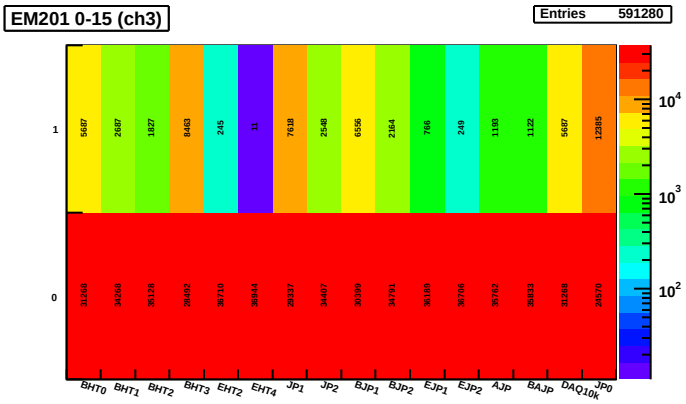
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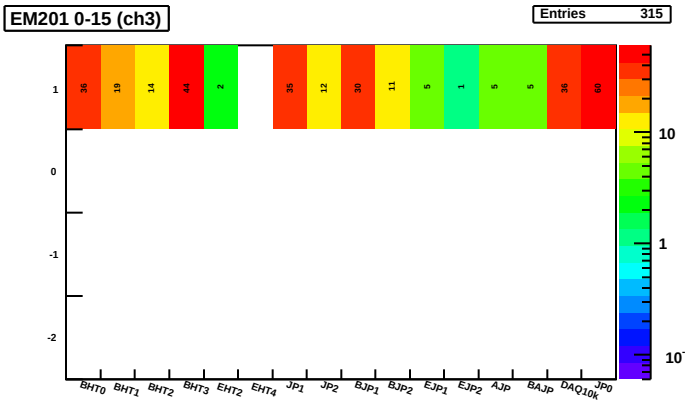
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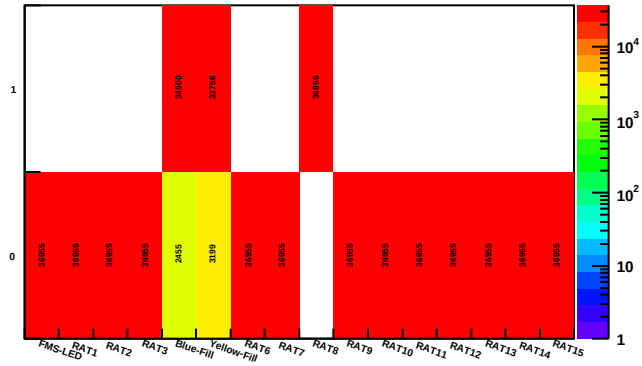
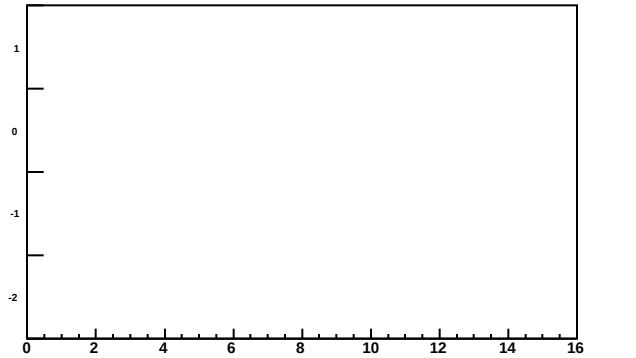
RAT board (ch4)





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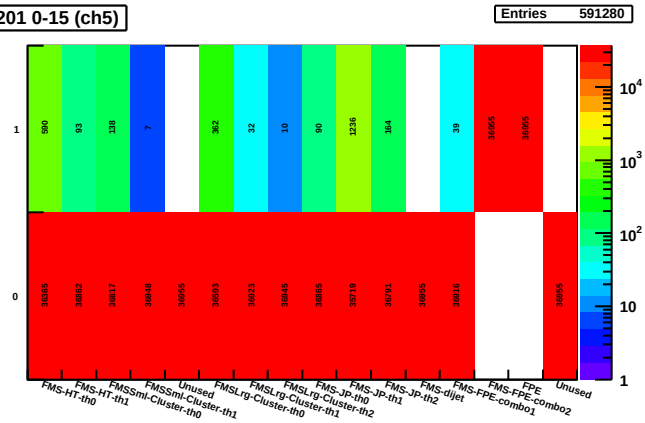

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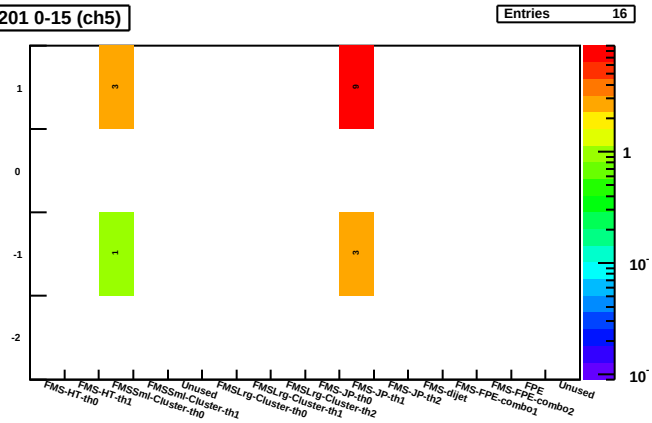

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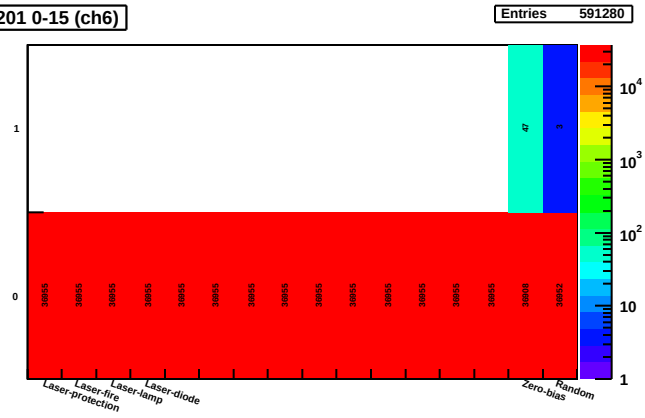

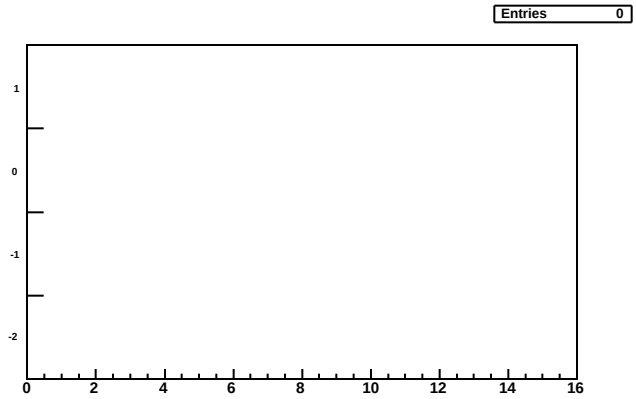
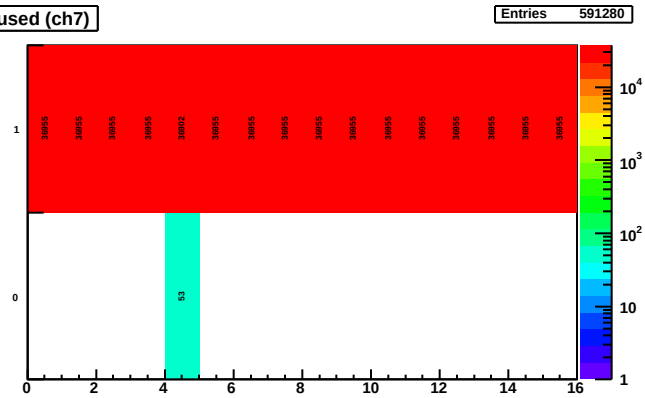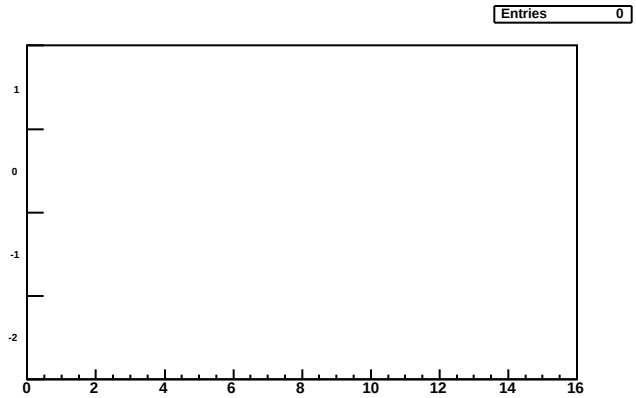
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