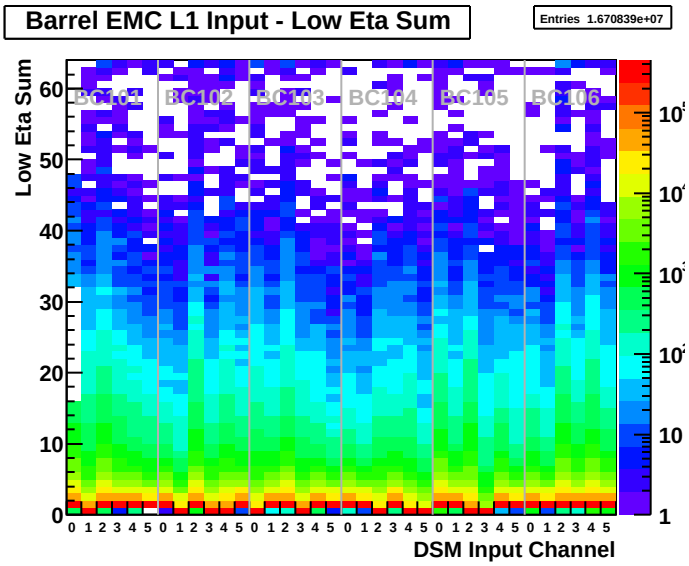
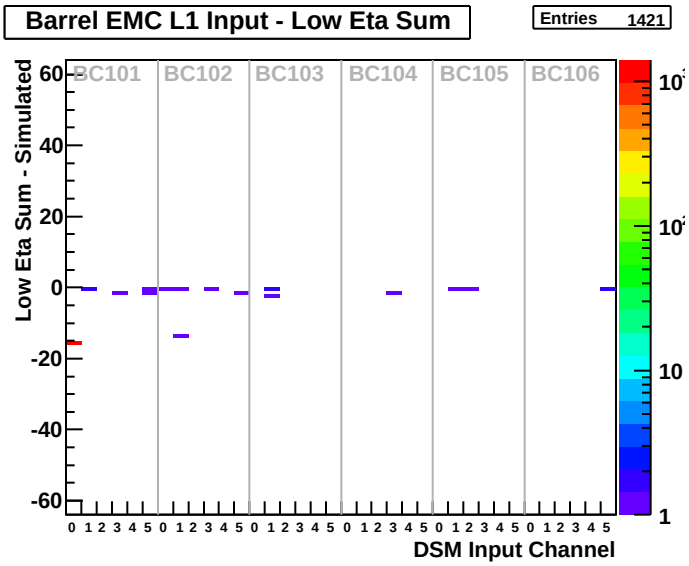
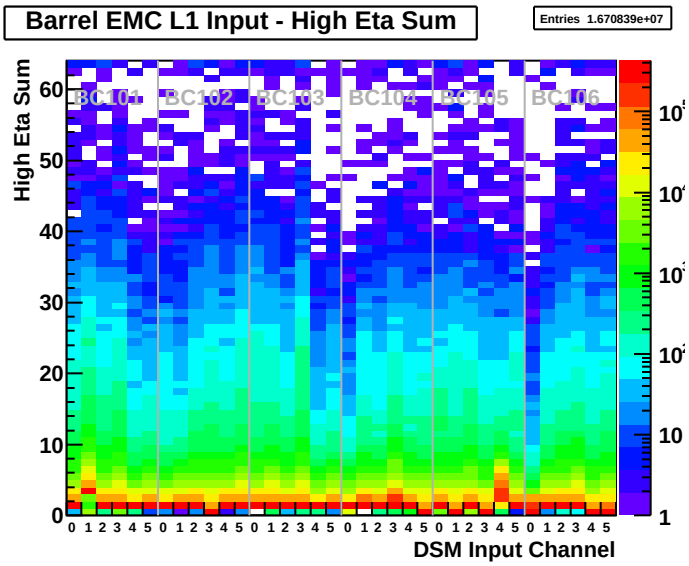
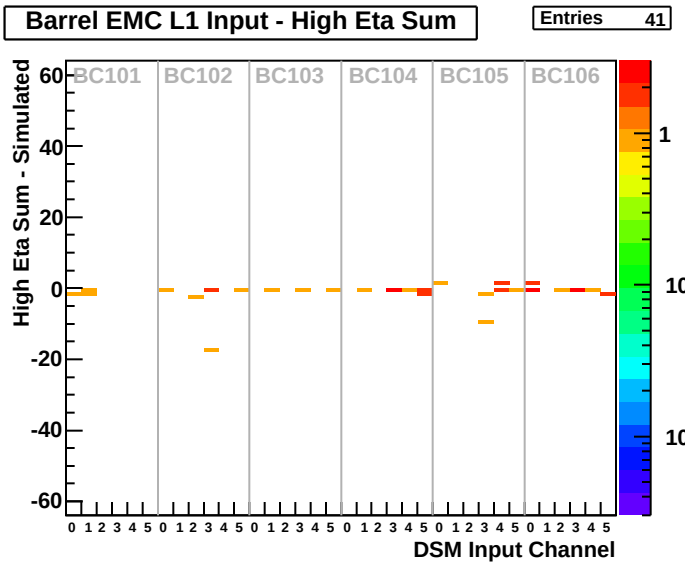
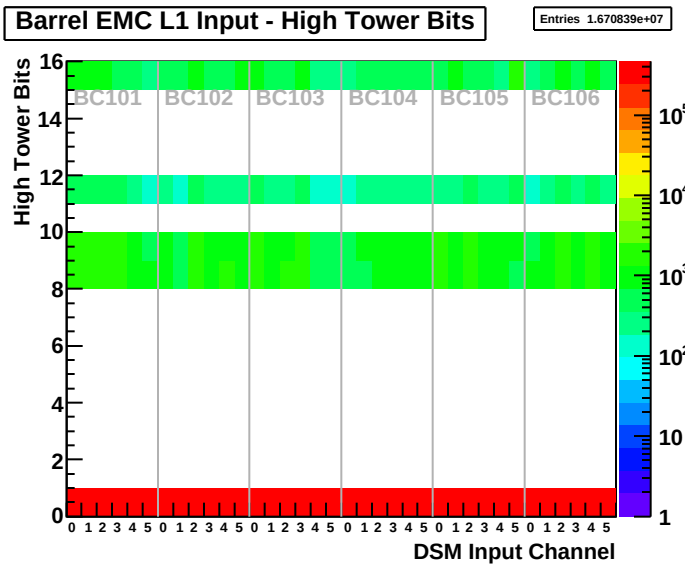
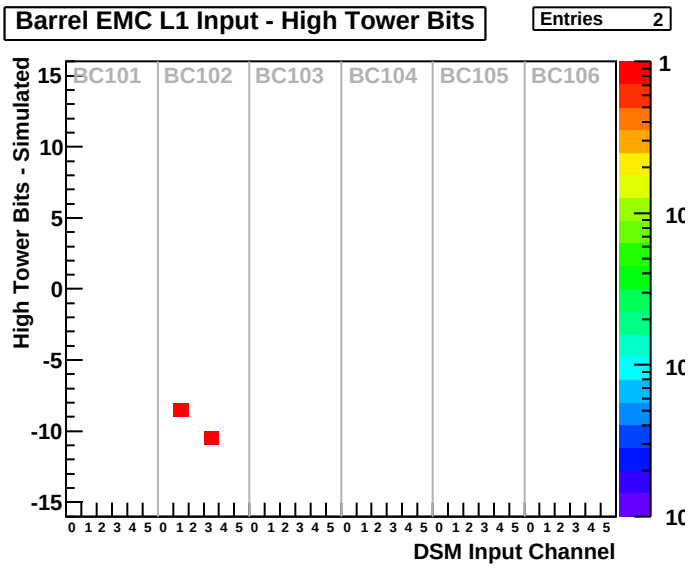

Barrel EMC L1 Input - Low Eta Sum
Entries 1.670839e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

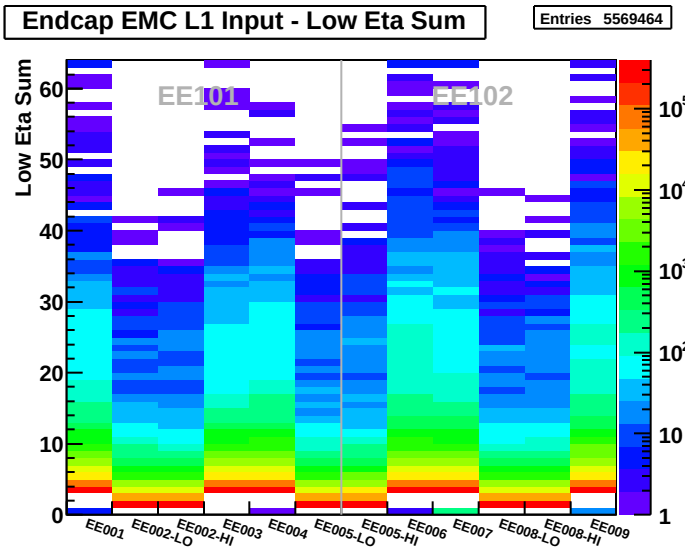

Barrel EMC L1 Input - Low Eta Sum
Entries 1421
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

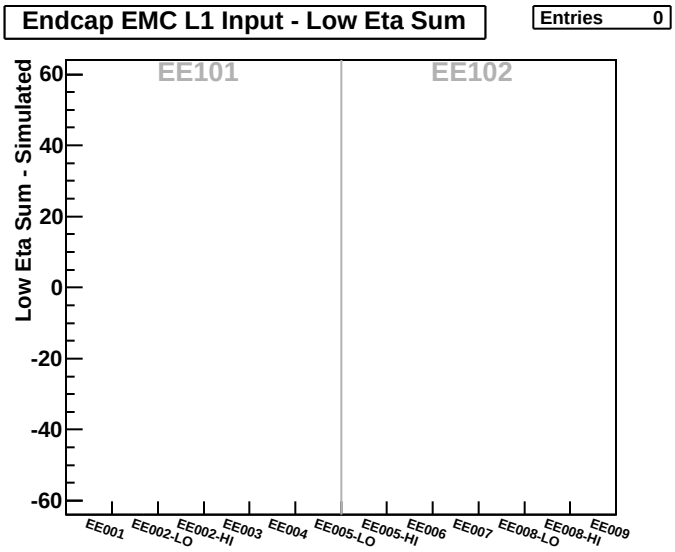

Barrel EMC L1 Input - High Eta Sum
Entries 1.670839e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

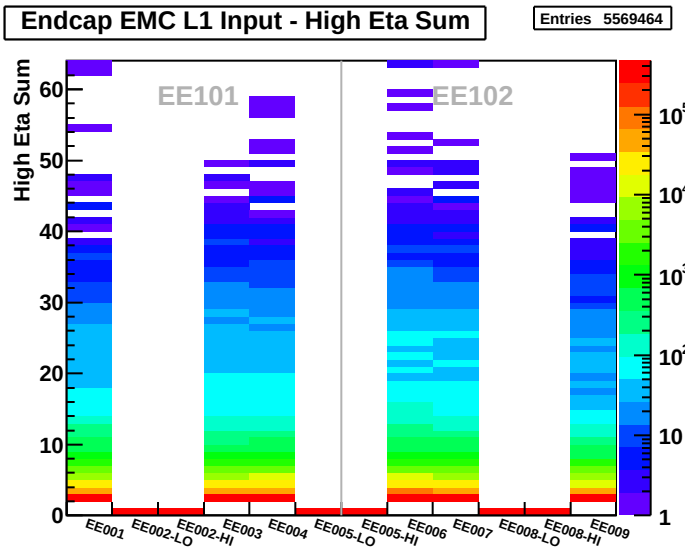

Barrel EMC L1 Input - High Eta Sum
Entries 41
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

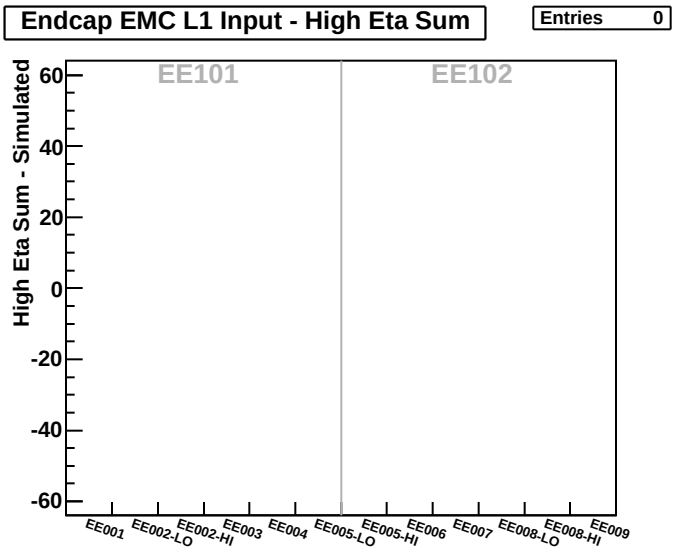

Barrel EMC L1 Input - High Tower Bits
Entries 1.670839e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

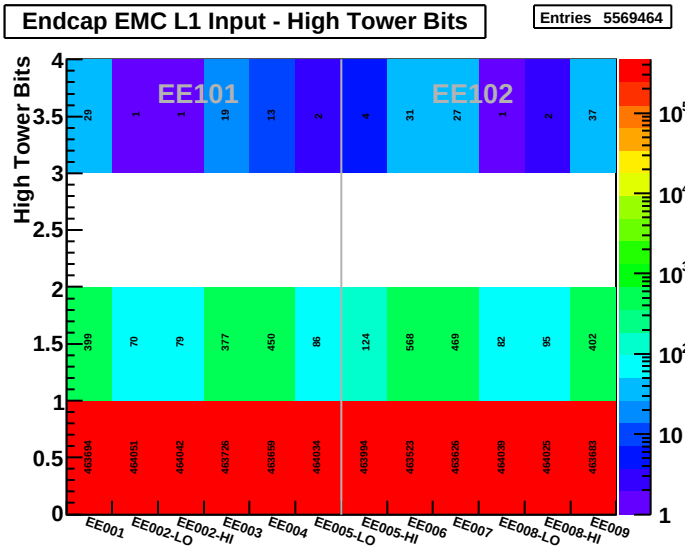

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

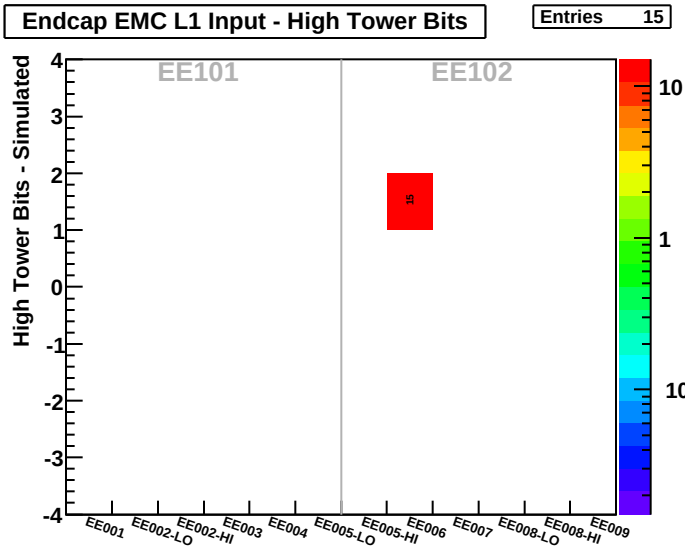

Endcap EMC L1 Input - Low Eta Sum
Entries 5569464
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

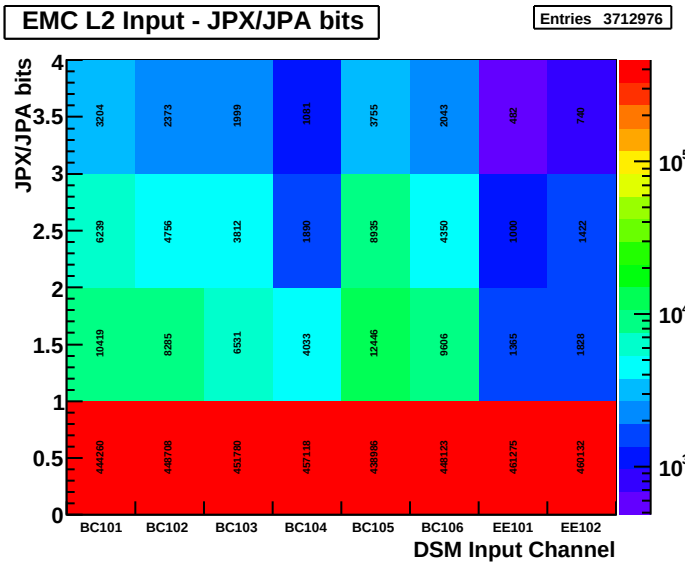

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

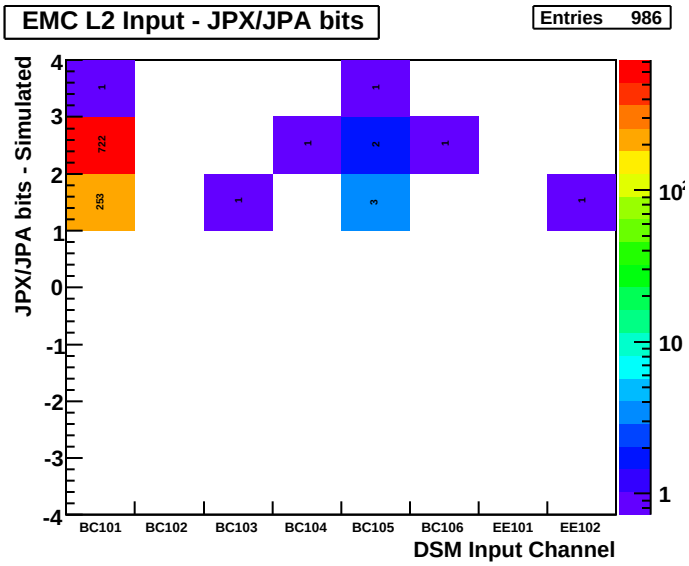

Endcap EMC L1 Input - High Eta Sum
Entries 5569464
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

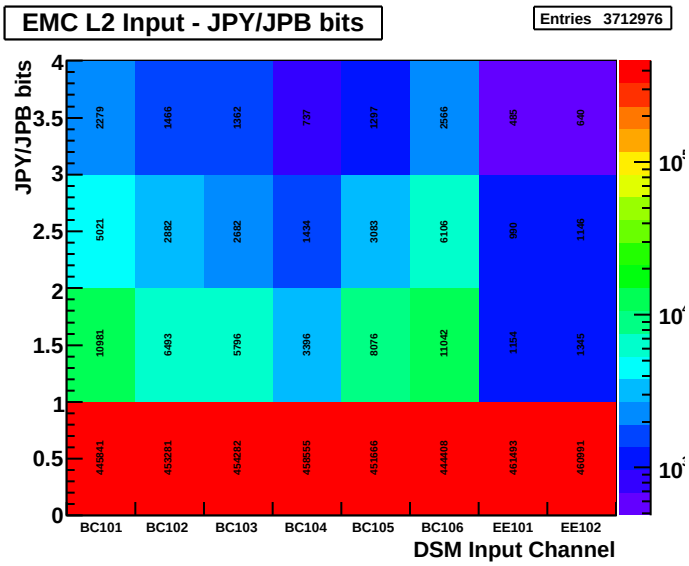

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

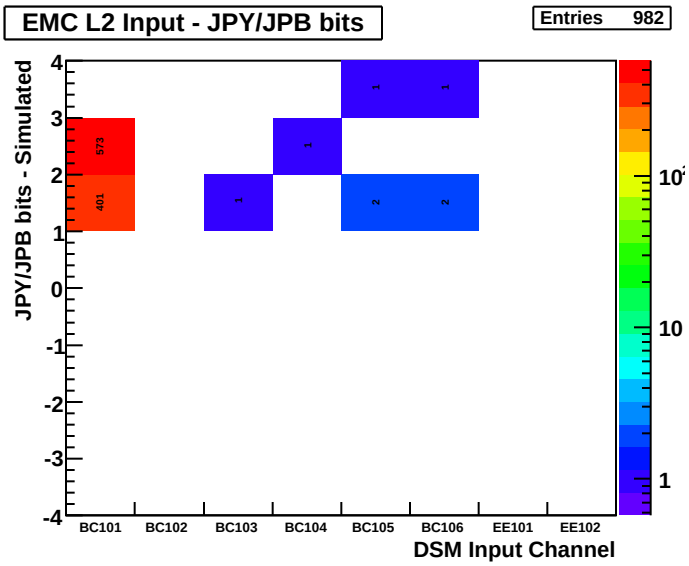

Endcap EMC L1 Input - High Tower Bits
Entries 5569464
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

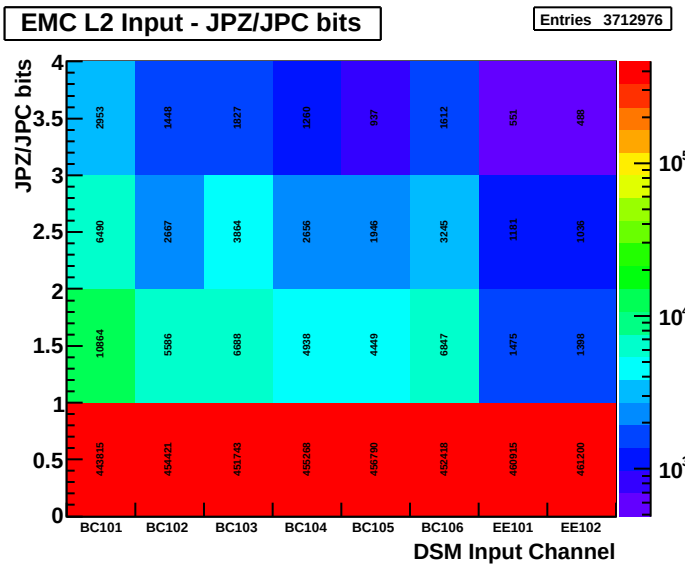

Endcap EMC L1 Input - High Tower Bits
Entries 15
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

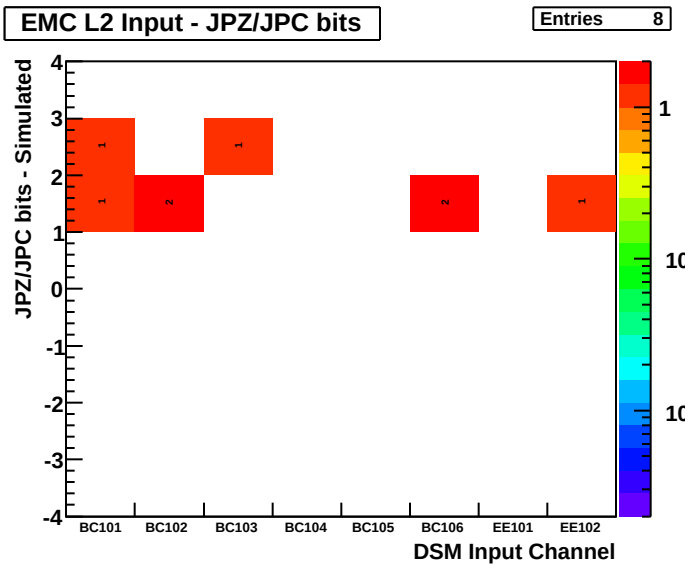

EMC L2 Input - JPX/JPA bits
Entries 3712976
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3204 2373 1999 1081 3755 2043 482 740
6239 4756 3812 1890 8935 4350 1000 1422
10419 8285 6531 4033 12446 9606 1365 1828
444260 448708 451780 457118 439986 448123 461275 460132

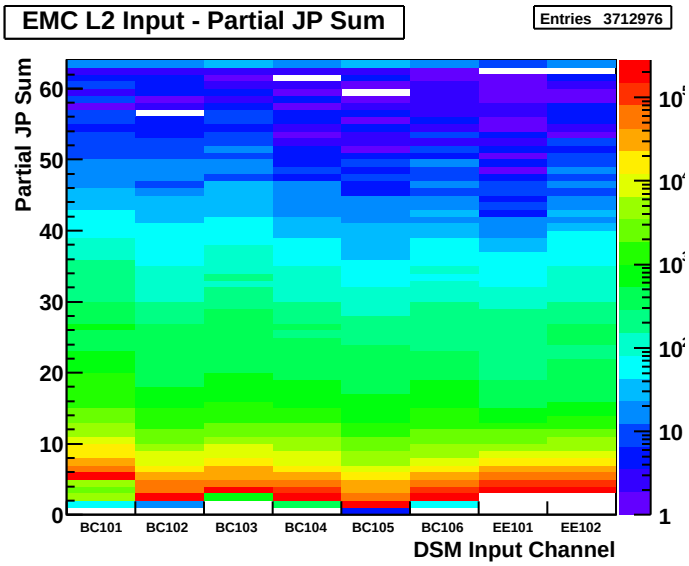

EMC L2 Input - JPX/JPA bits
Entries 986
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1
722 1 2 1
253 1 3 1

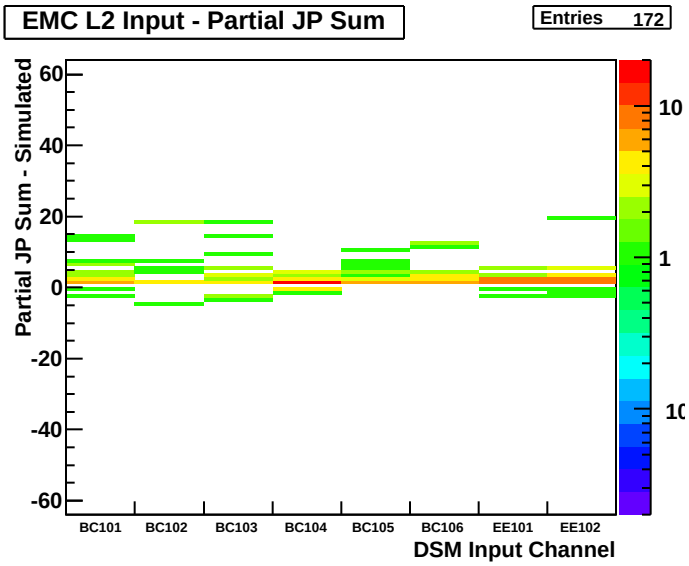

EMC L2 Input - JPY/JPB bits
Entries 3712976
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2279 1466 1362 737 1297 2566 485 640
5021 2882 2682 1434 3083 6106 990 1146
10981 6493 5706 3396 8076 11042 1154 1345
448841 452281 454282 458555 451666 444408 461493 460991

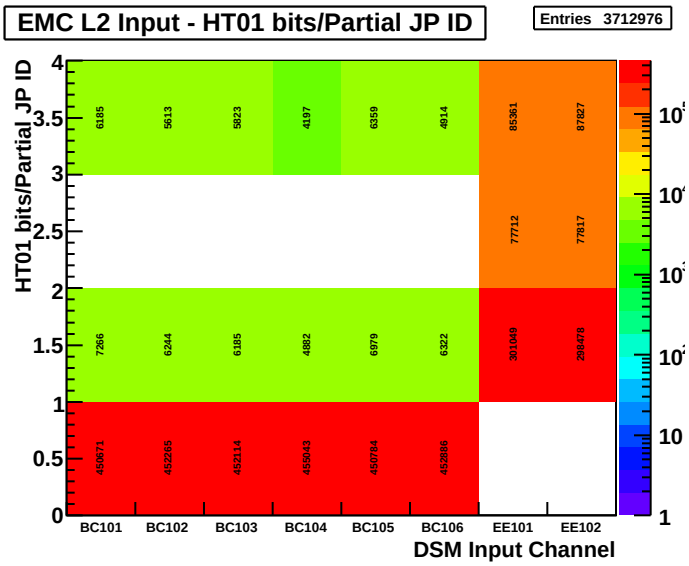

EMC L2 Input - JPY/JPB bits
Entries 982
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1
573 1
401 1 2 2

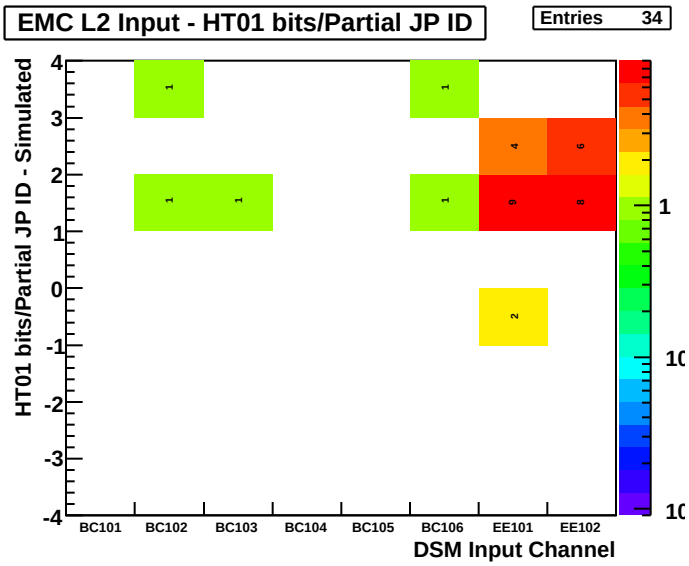

EMC L2 Input - JPZ/JPC bits
Entries 3712976
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2953 1448 1827 1260 937 1612 531 488
6490 2667 3864 2656 1946 3245 1181 1036
10864 5586 6688 4938 4449 6847 1475 1398
443915 454421 451743 455268 456790 452418 460915 461200

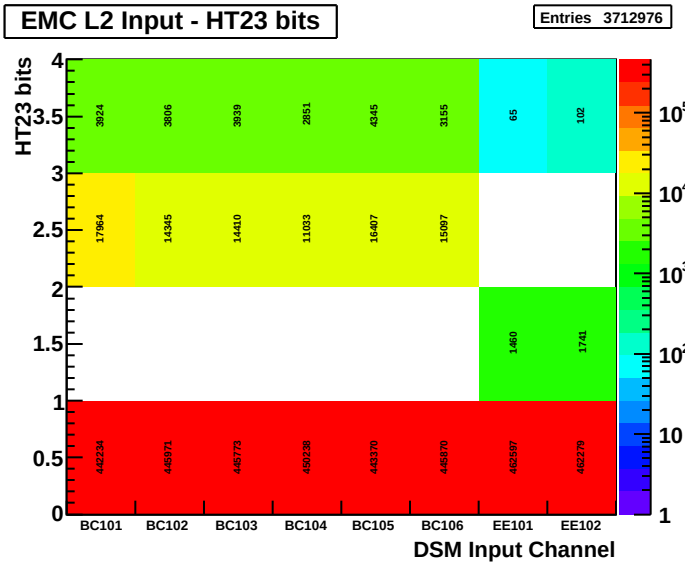

EMC L2 Input - JPZ/JPC bits
Entries 8
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1
1 2 2 1

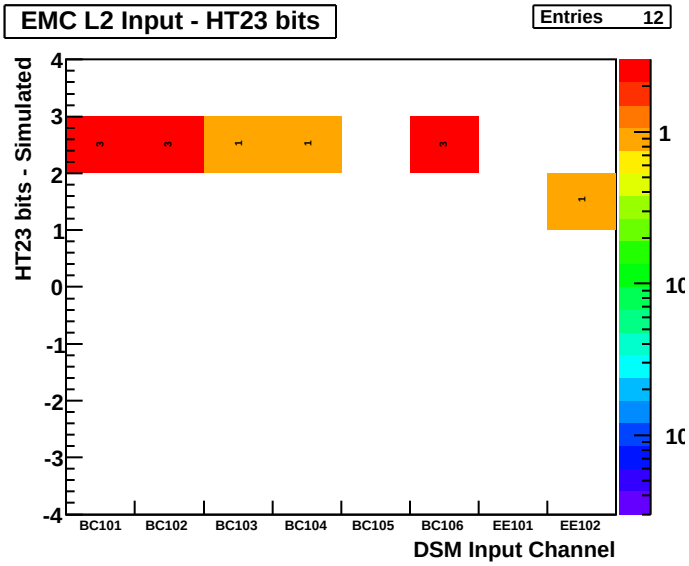

EMC L2 Input - Partial JP Sum
Entries 3712976
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

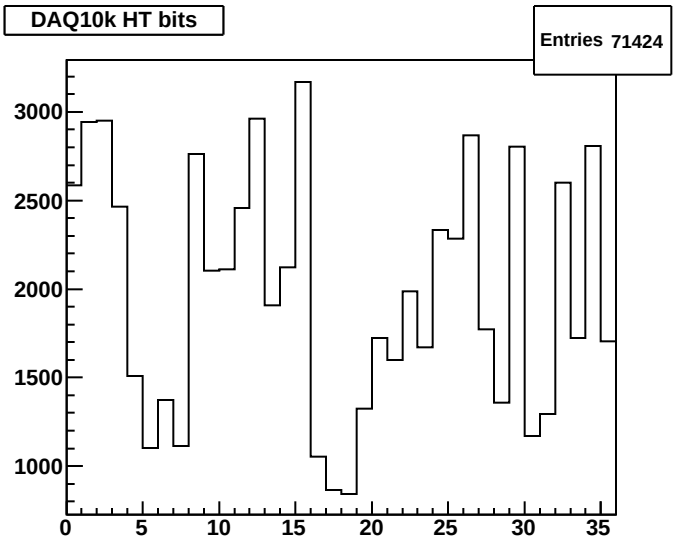

EMC L2 Input - Partial JP Sum
Entries 172
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

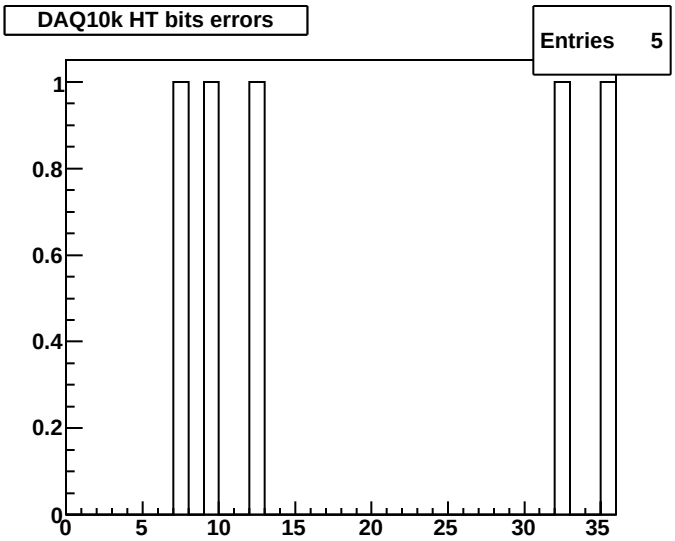

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3712976
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

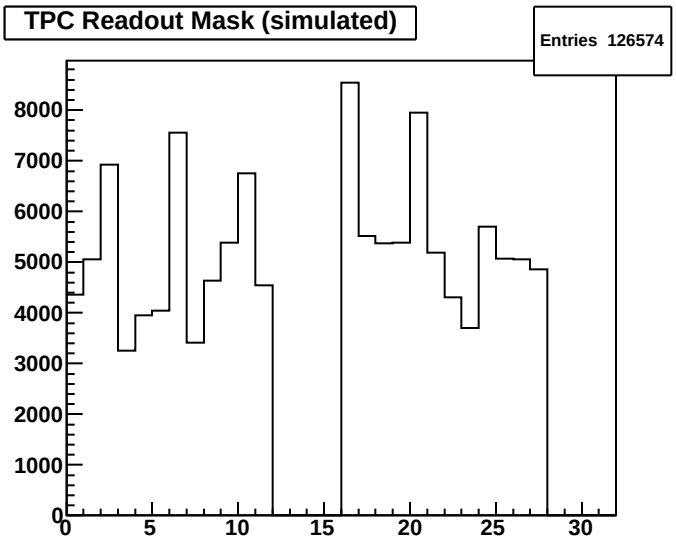

EMC L2 Input - HT01 bits/Partial JP ID
Entries 34
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

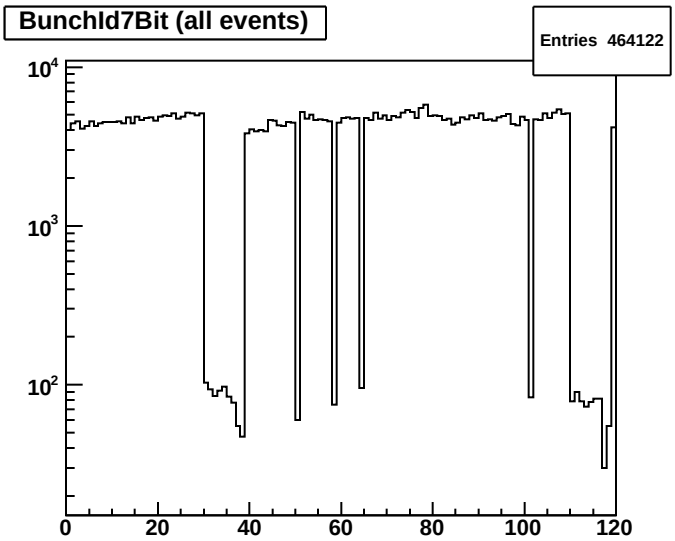

EMC L2 Input - HT23 bits
Entries 3712976
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

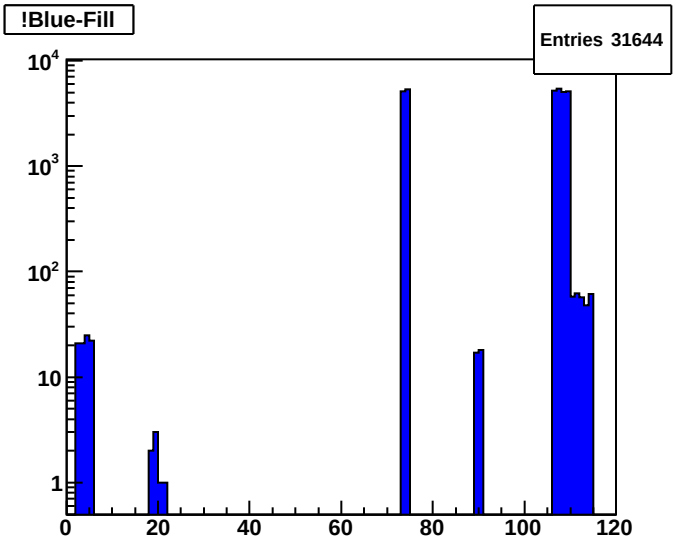

EMC L2 Input - HT23 bits
Entries 12
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

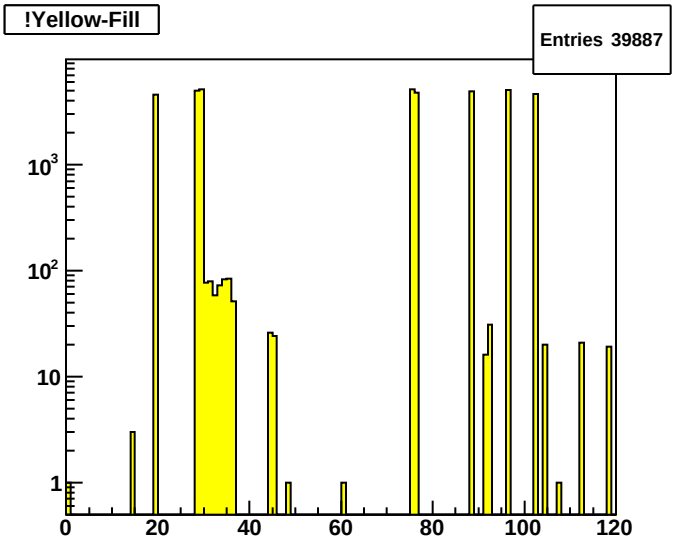

DAQ10k HT bits
Entries 71424

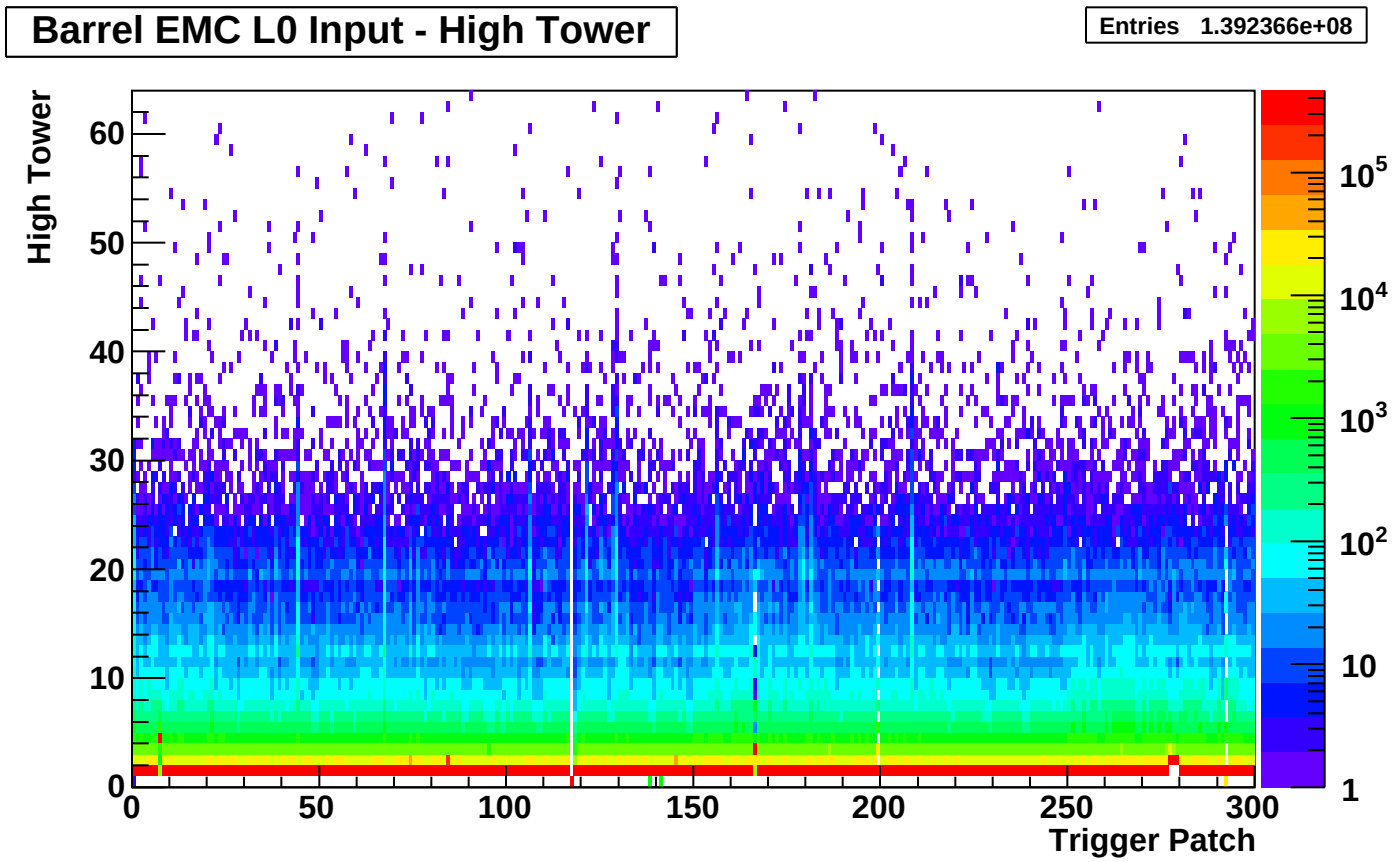

DAQ10k HT bits errors
Entries 5

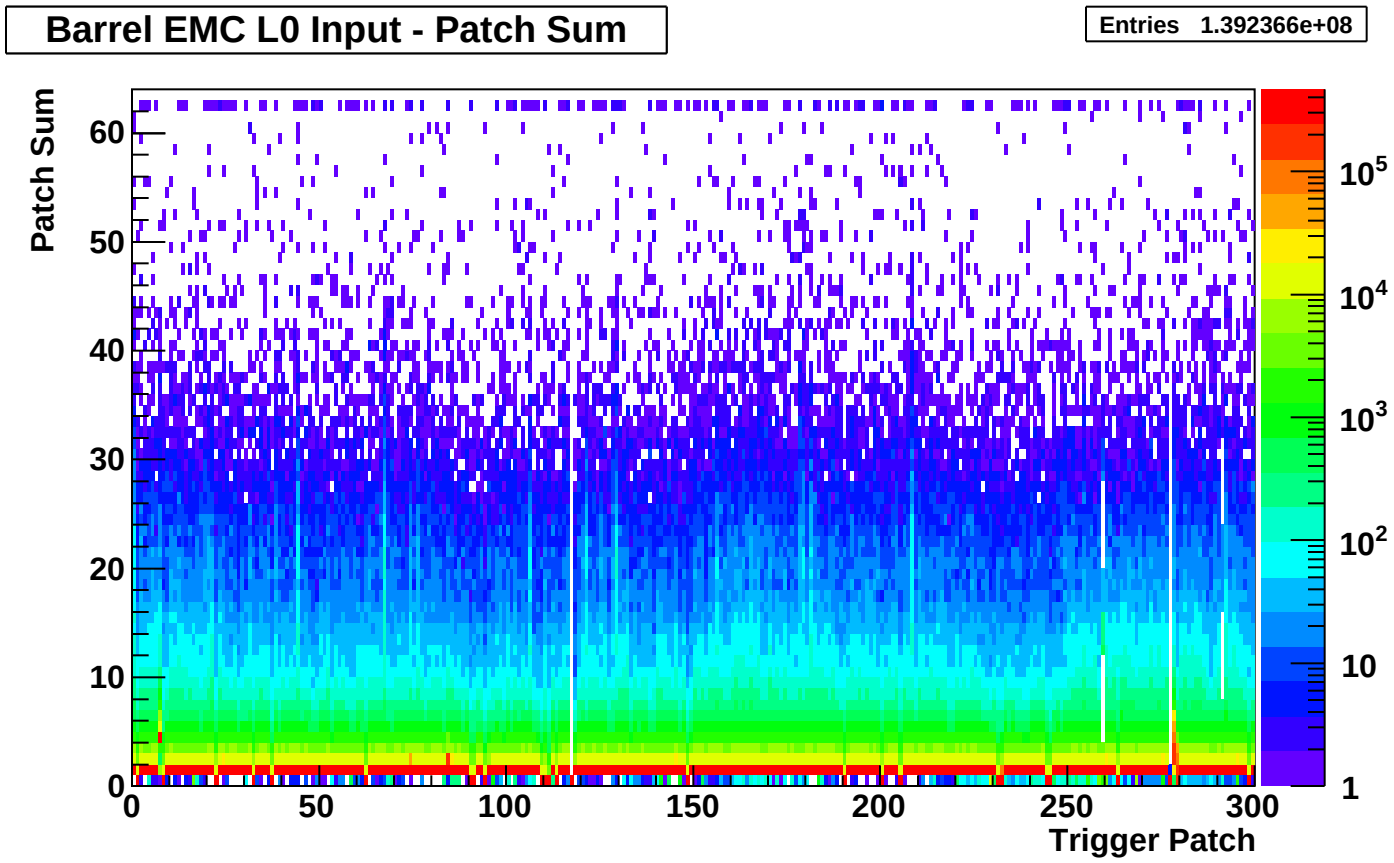

TPC Readout Mask (simulated)
Entries 126574

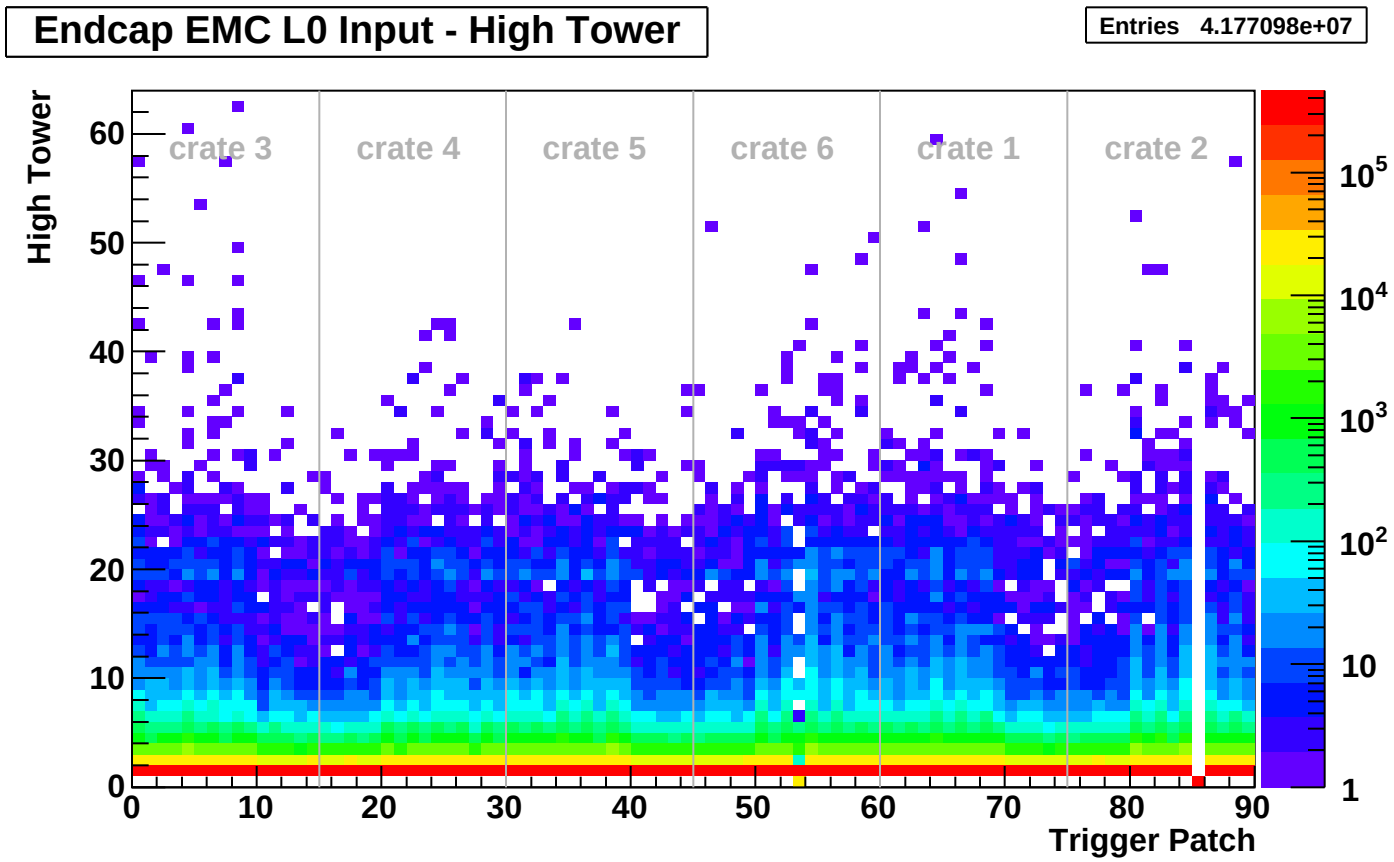

BunchId7Bit (all events)
Entries 464122

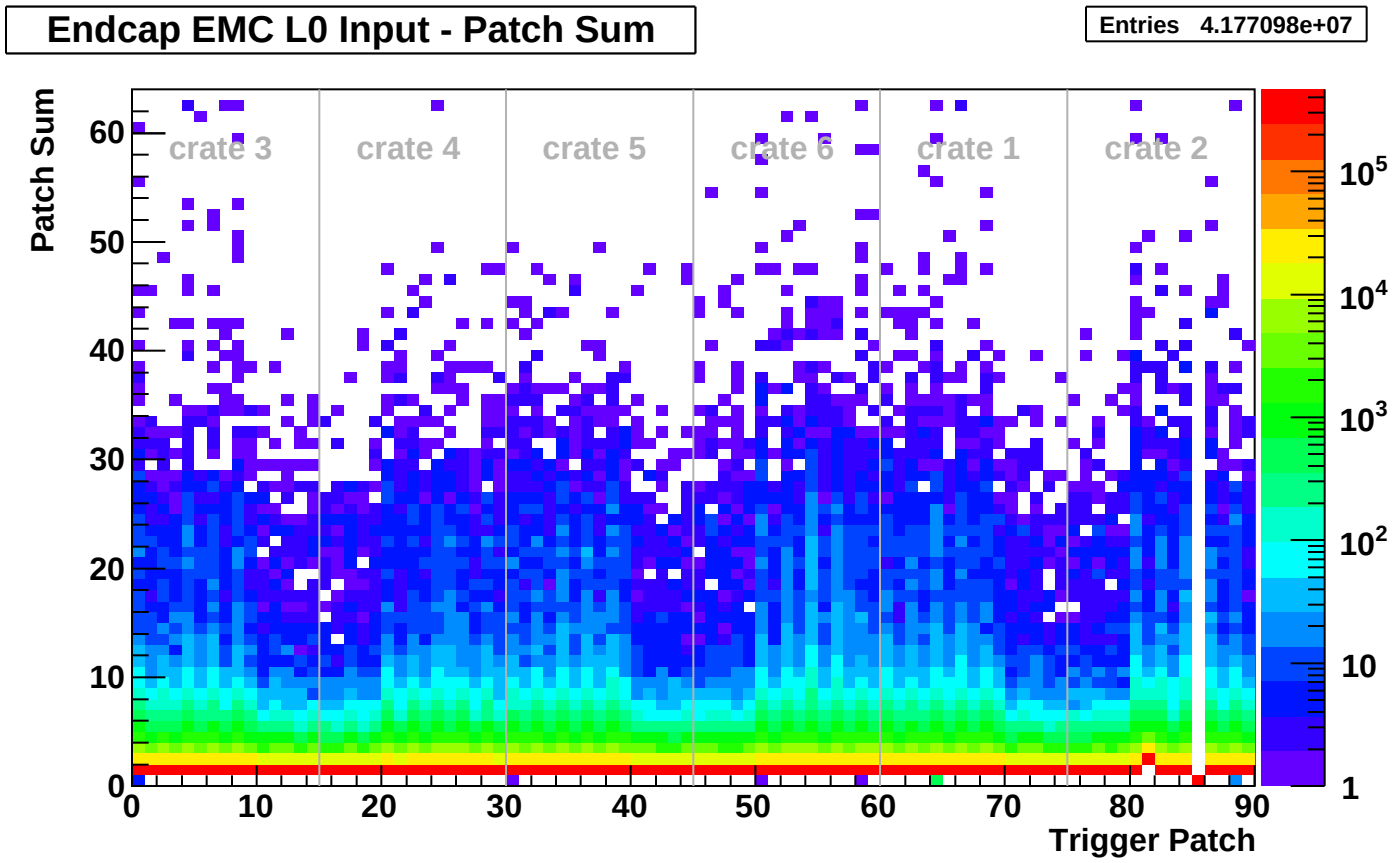

!Blue-Fill
Entries 31644

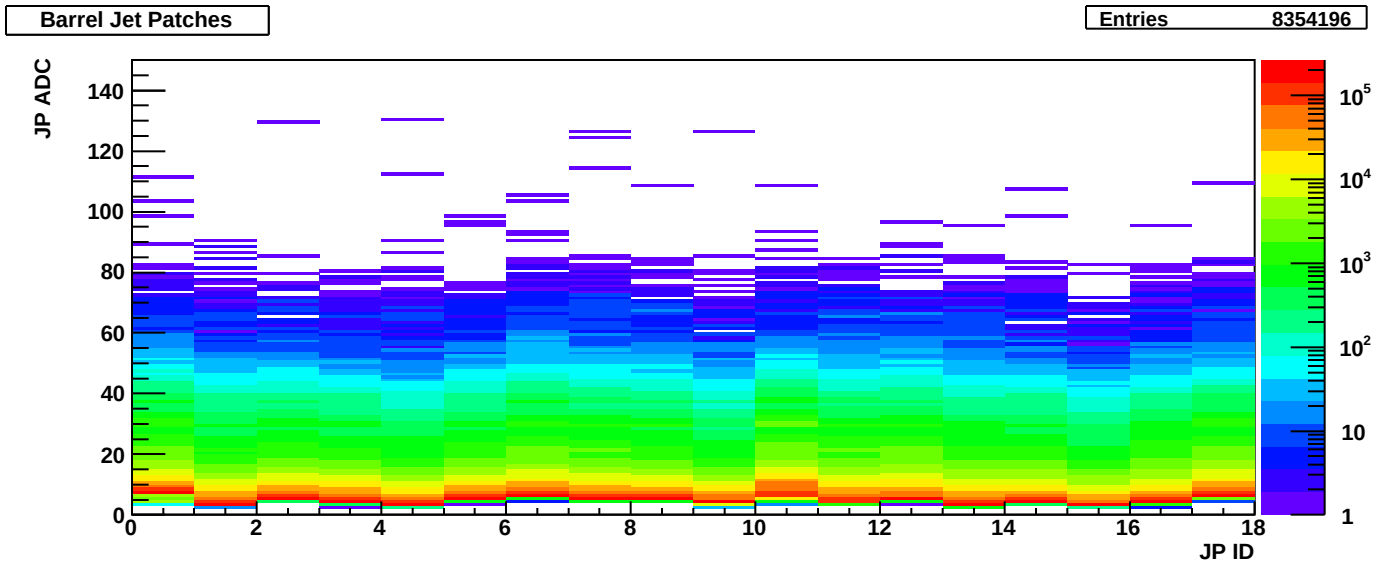

!Yellow-Fill
Entries 39887

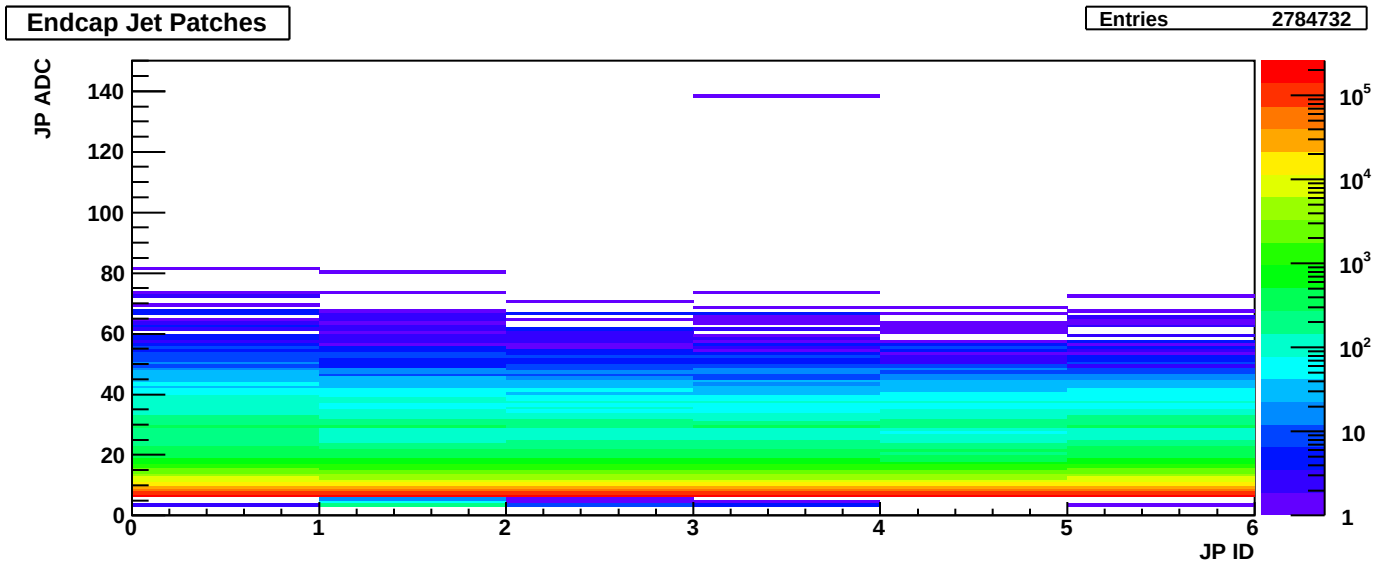

Barrel EMC L0 Input - High Tower
Entries 1.392366e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

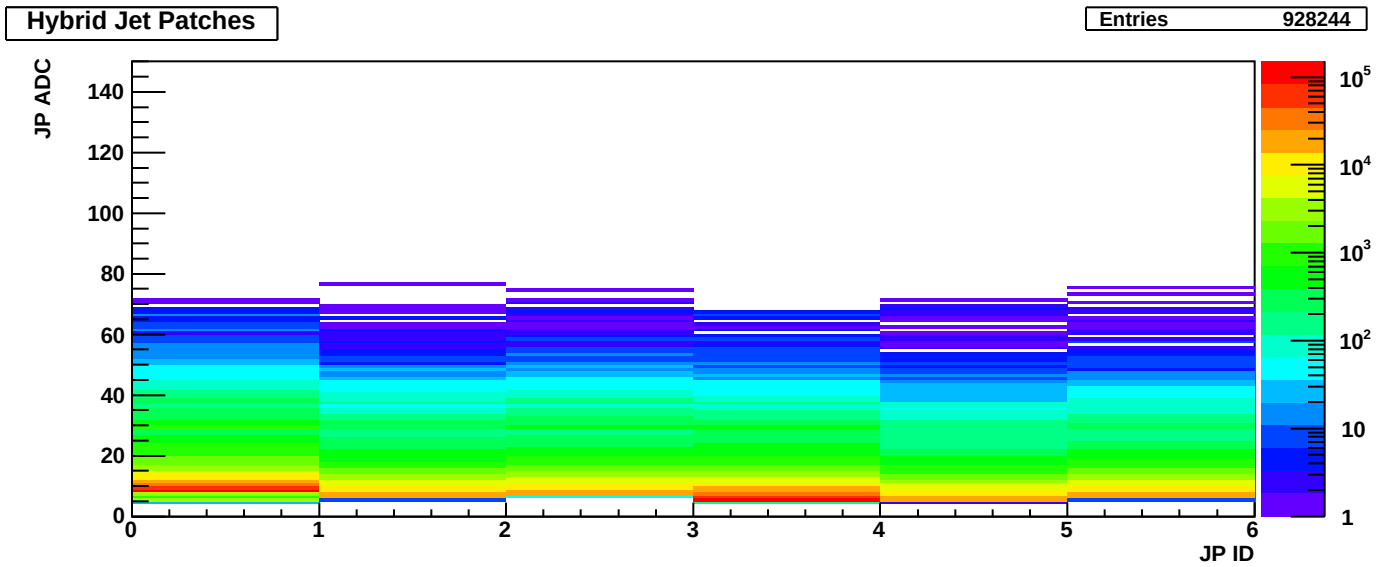

Barrel EMC L0 Input - Patch Sum
Entries 1.392366e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

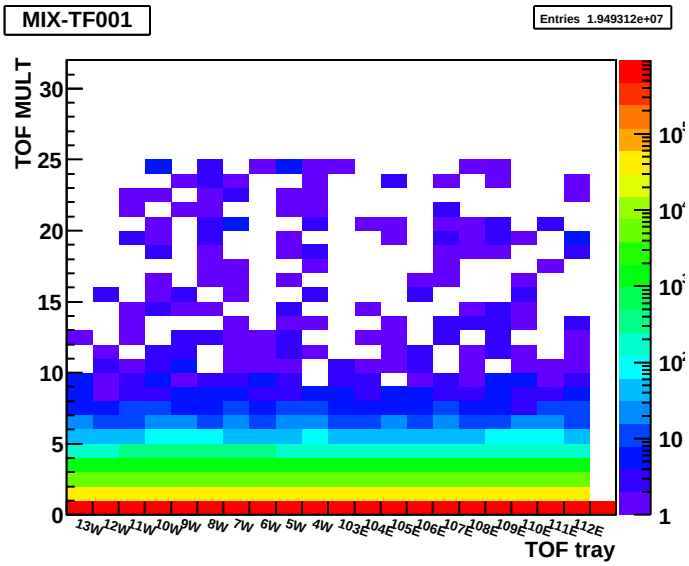

Endcap EMC L0 Input - High Tower
Entries 4.177098e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

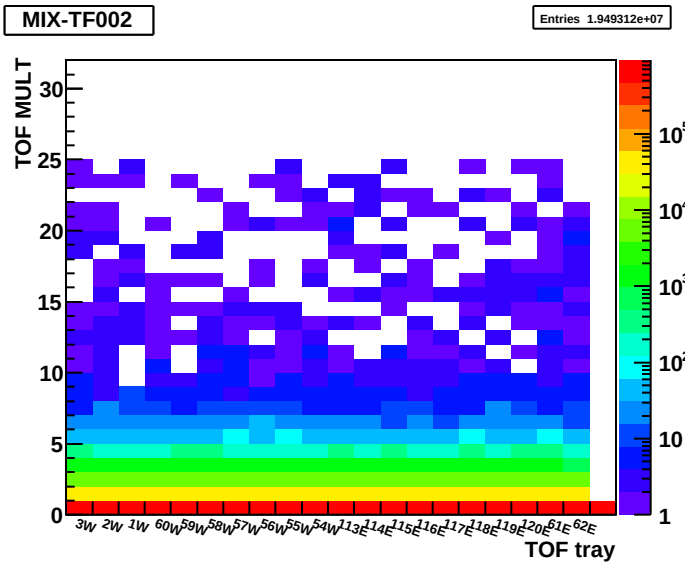

Endcap EMC L0 Input - Patch Sum
Entries 4.177098e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

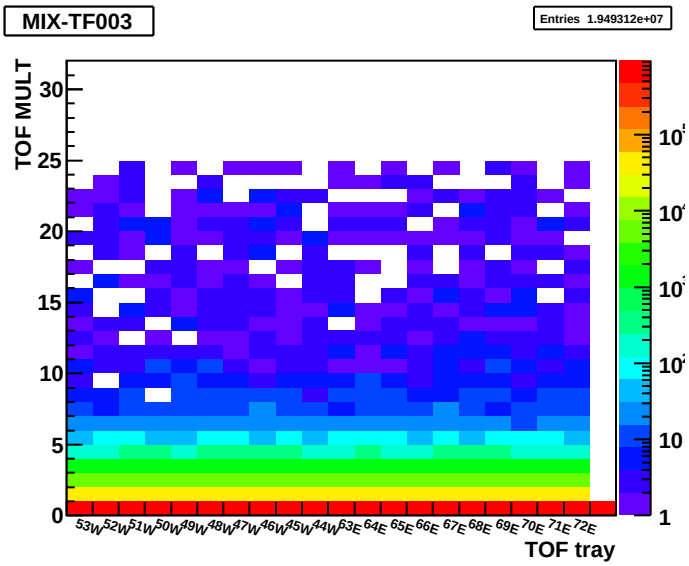

Barrel Jet Patches
Entries 8354196
JP ADC
JP ID

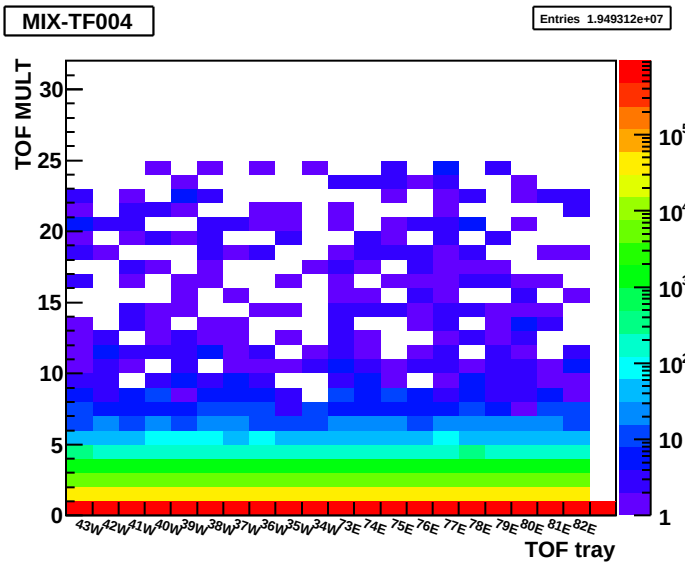

Endcap Jet Patches
Entries 2784732
JP ADC
JP ID

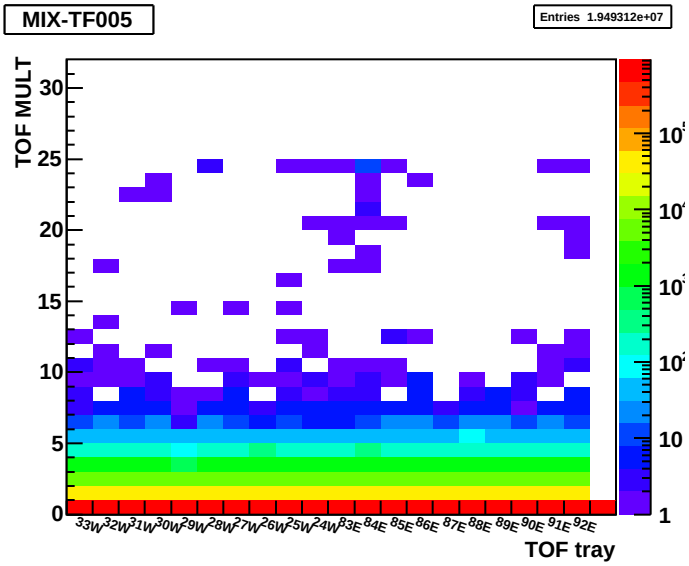

Hybrid Jet Patches
Entries 928244
JP ADC
JP ID

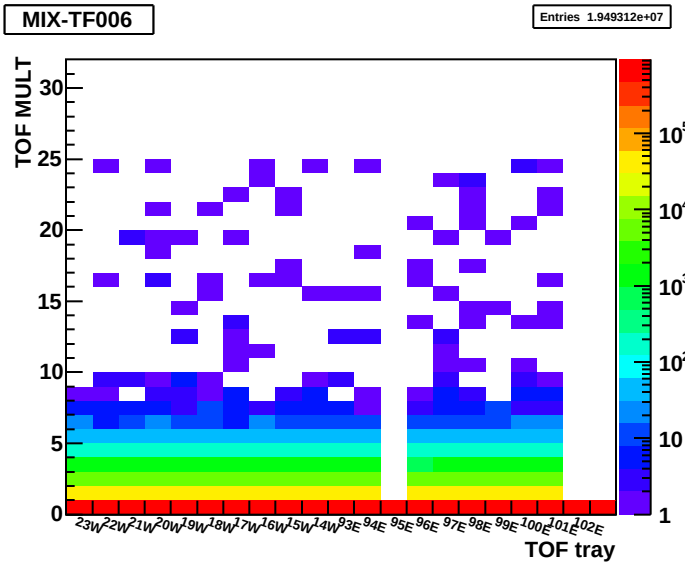

MIX-TF001
Entries 1.949312e+07
TOF MULT
TOF tray

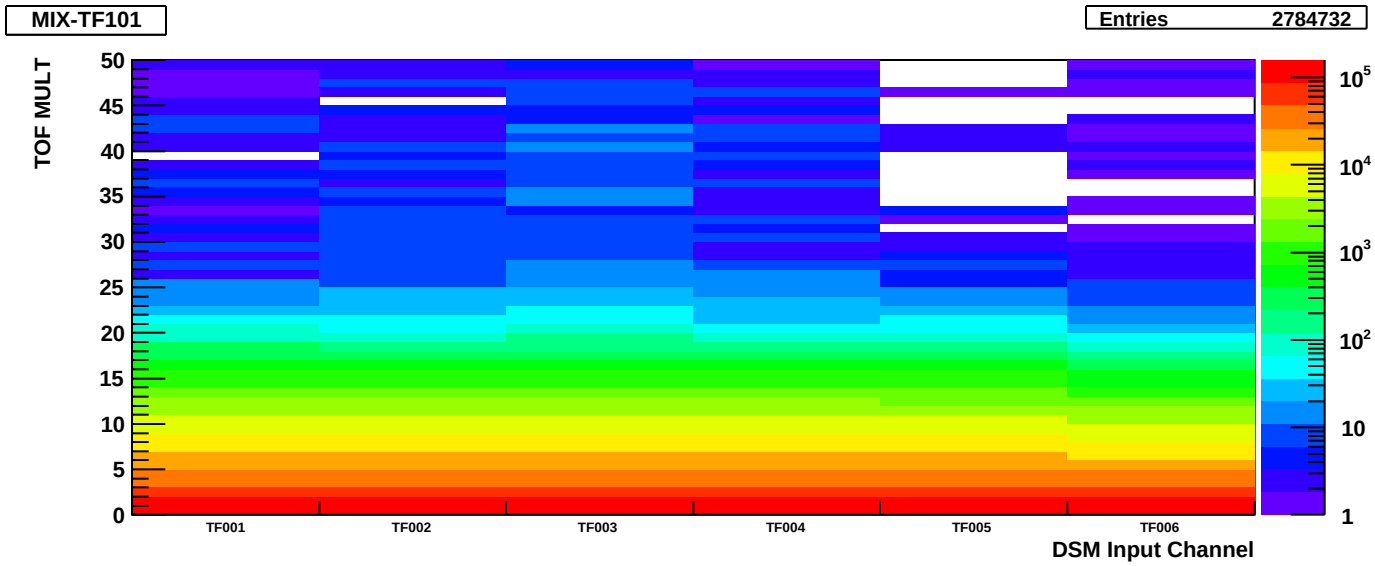

MIX-TF002
Entries 1.949312e+07
TOF MULT
TOF tray

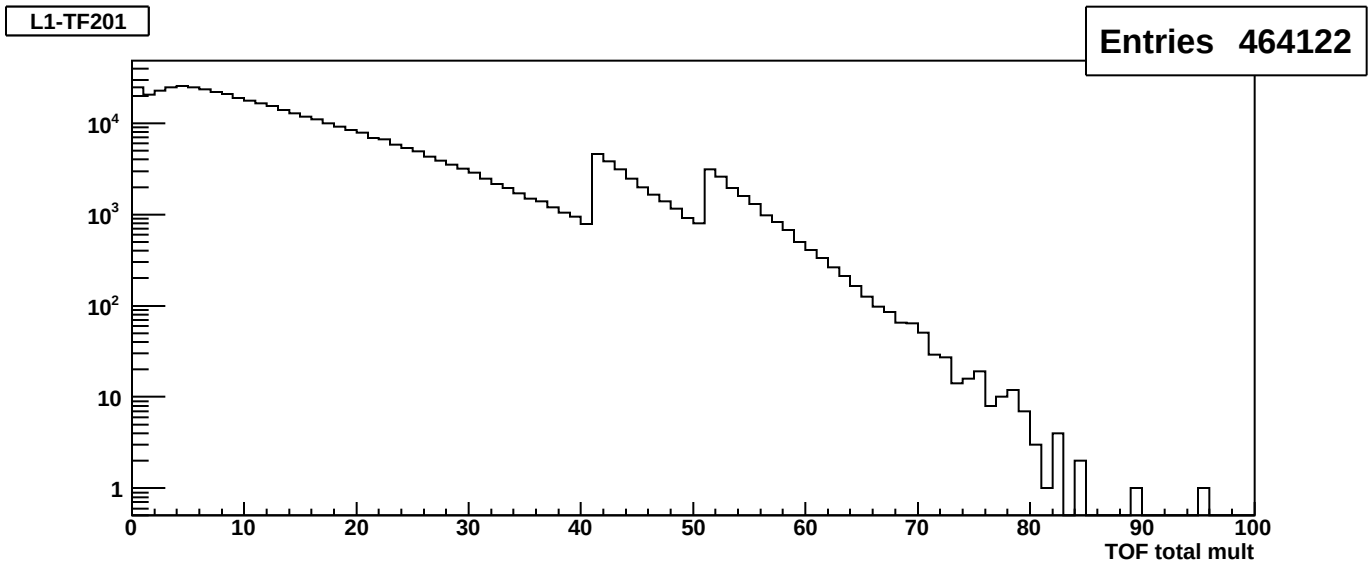

MIX-TF003
Entries 1.949312e+07
TOF MULT
TOF tray

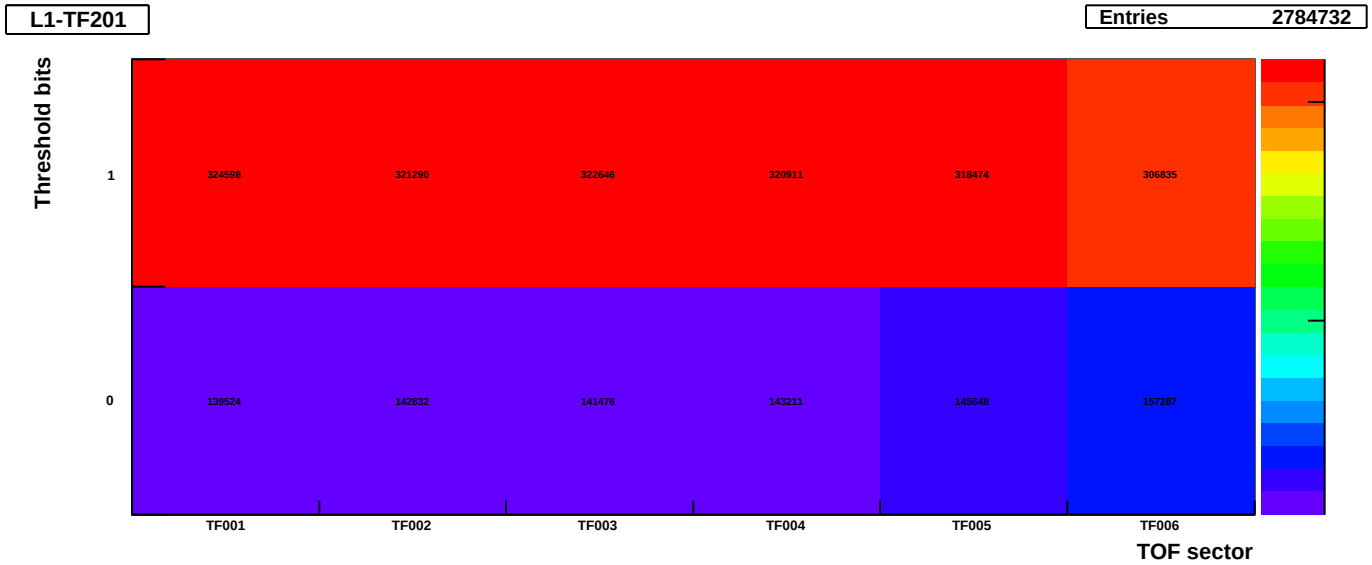

MIX-TF004
Entries 1.949312e+07
TOF MULT
TOF tray

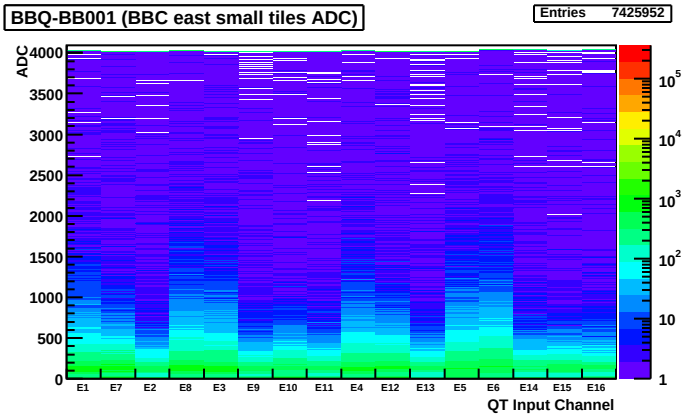

MIX-TF005
Entries 1.949312e+07
TOF MULT
TOF tray

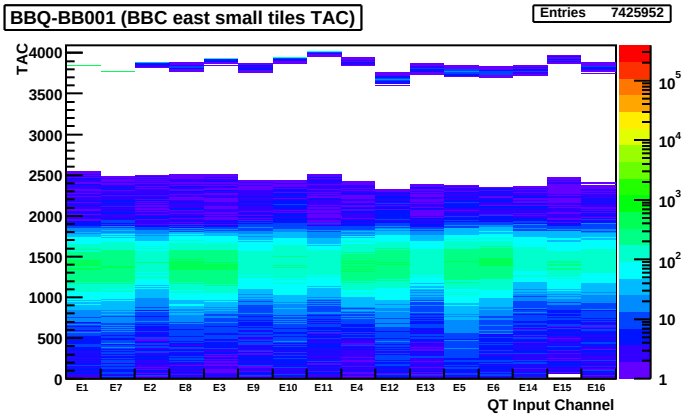

MIX-TF006
Entries 1.949312e+07
TOF MULT
TOF tray

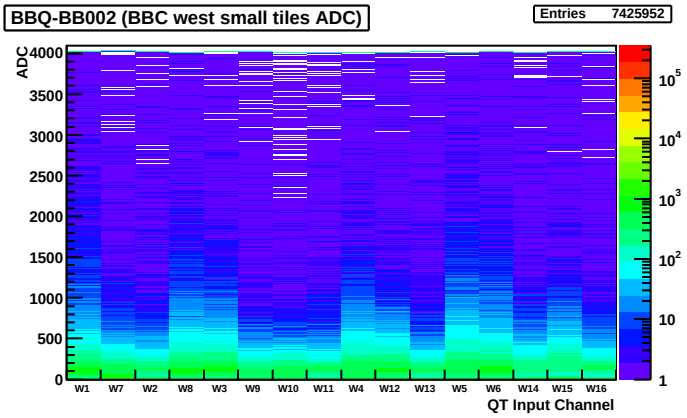

MIX-TF101
Entries 2784732
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

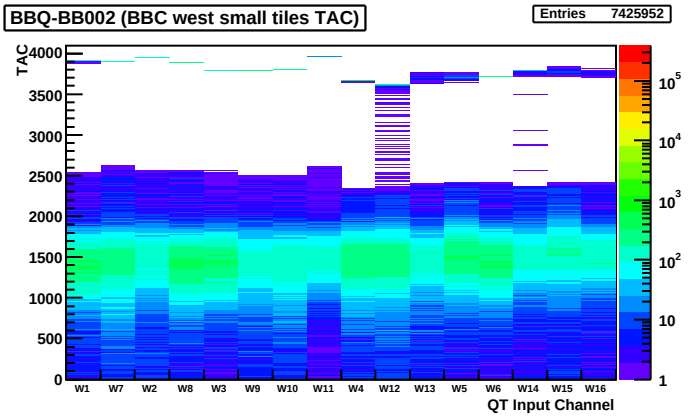

L1-TF201
Entries 464122
TOF total mult

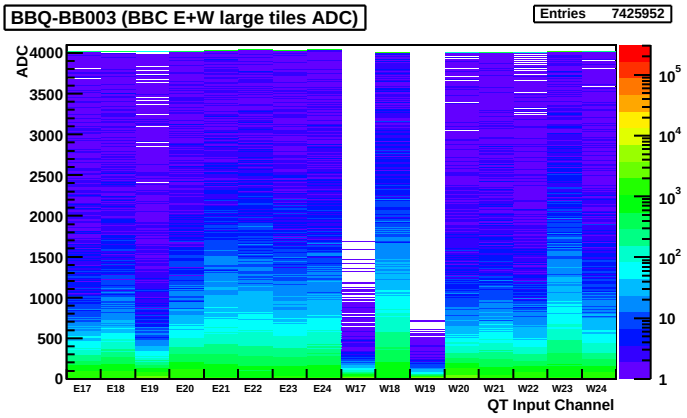

L1-TF201
Entries 2784732
Threshold bits
324598
321290
322646
320911
318474
306835
139524
142832
141476
143211
145648
157287
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

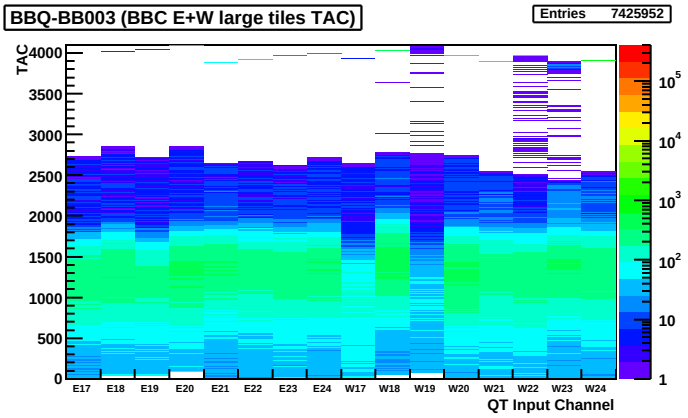

BBQ-BB001 (BBC east small tiles ADC)
Entries 7425952
ADC
QT Input Channel

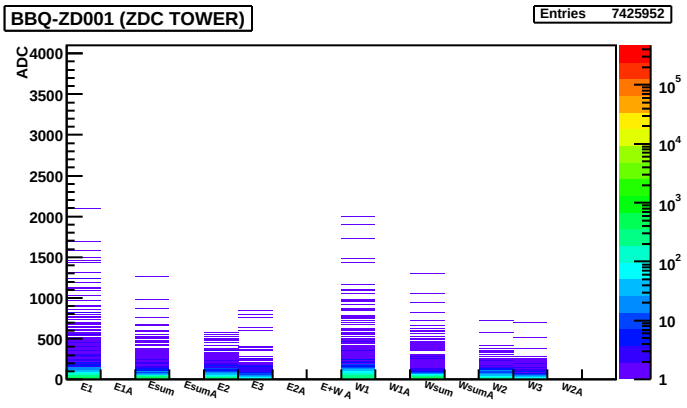

BBQ-BB001 (BBC east small tiles TAC)
Entries 7425952
TAC
QT Input Channel

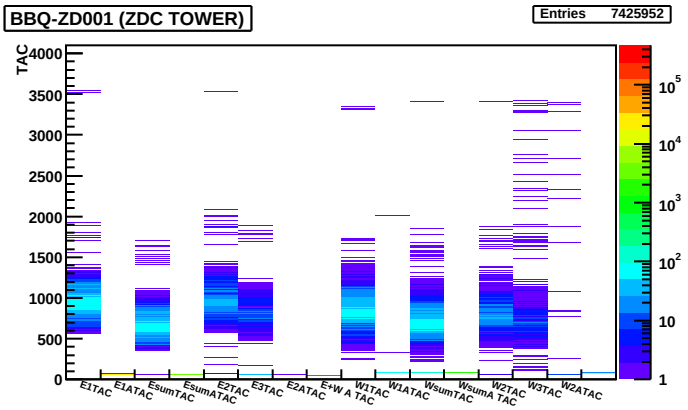

BBQ-BB002 (BBC west small tiles ADC)
Entries 7425952
ADC
QT Input Channel

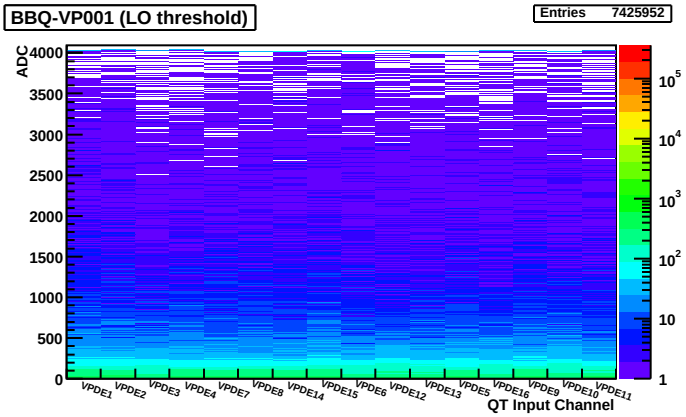

BBQ-BB002 (BBC west small tiles TAC)
Entries 7425952
TAC
QT Input Channel

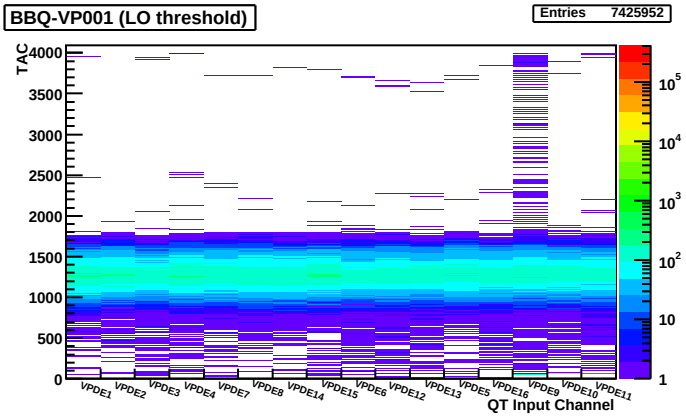

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7425952
ADC
QT Input Channel

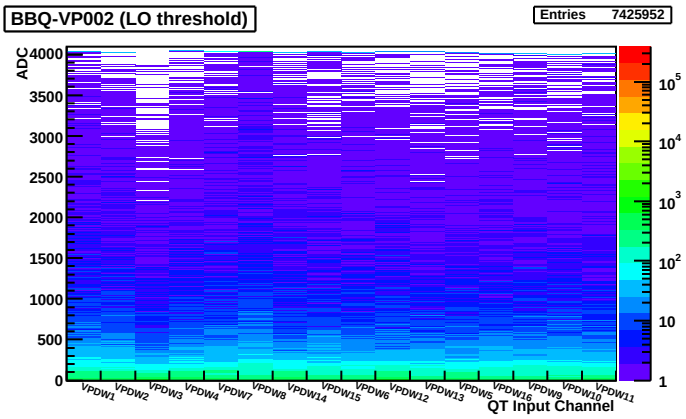

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7425952
TAC
QT Input Channel

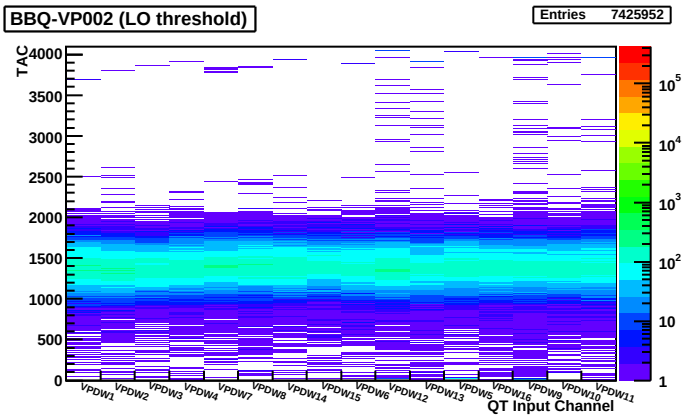

BBQ-ZD001 (ZDC TOWER)
Entries 7425952
ADC

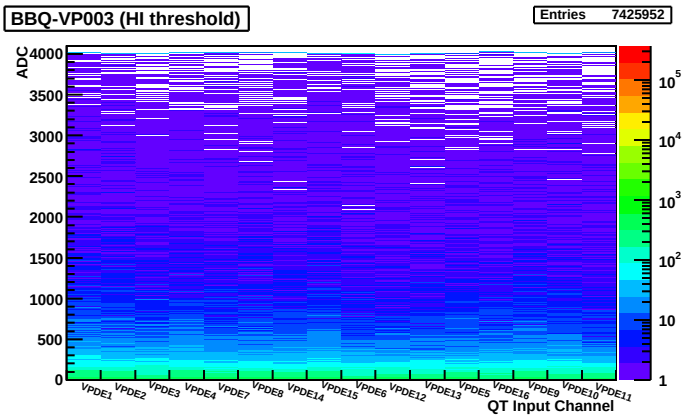

BBQ-ZD001 (ZDC TOWER)
Entries 7425952
TAC

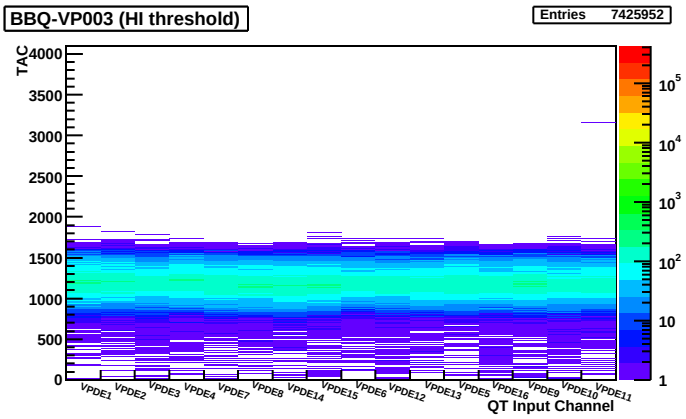

BBQ-VP001 (LO threshold)
Entries 7425952
ADC
QT Input Channel

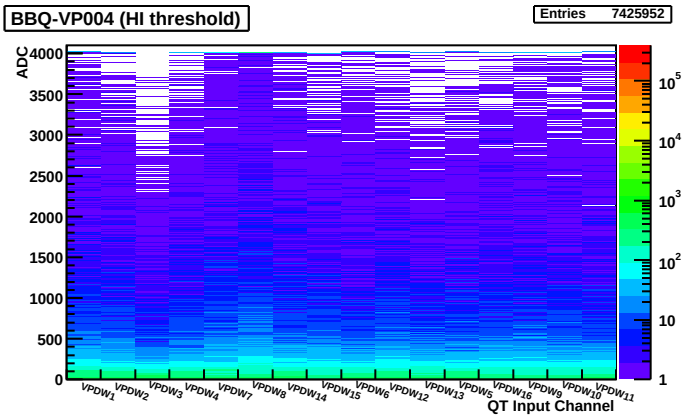

BBQ-VP001 (LO threshold)
Entries 7425952
TAC
QT Input Channel

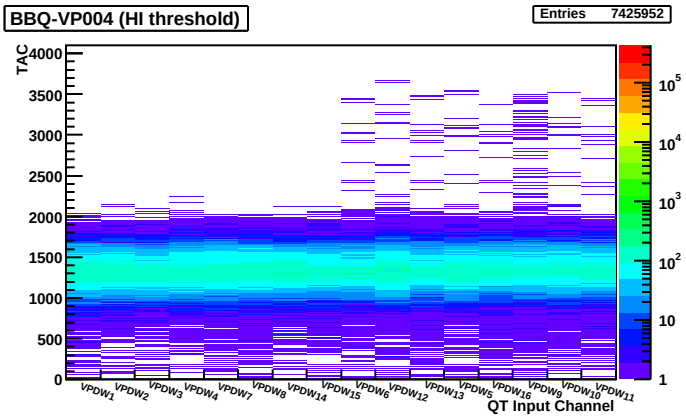

BBQ-VP002 (LO threshold)
Entries 7425952
ADC
QT Input Channel

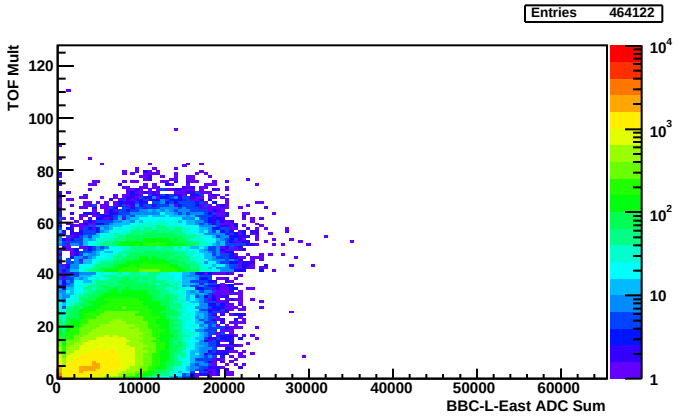

BBQ-VP002 (LO threshold)
Entries 7425952
TAC
QT Input Channel

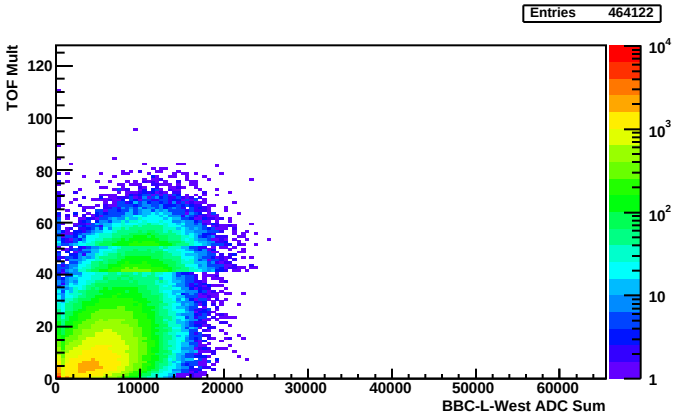

BBQ-VP003 (HI threshold)
Entries 7425952
ADC
QT Input Channel

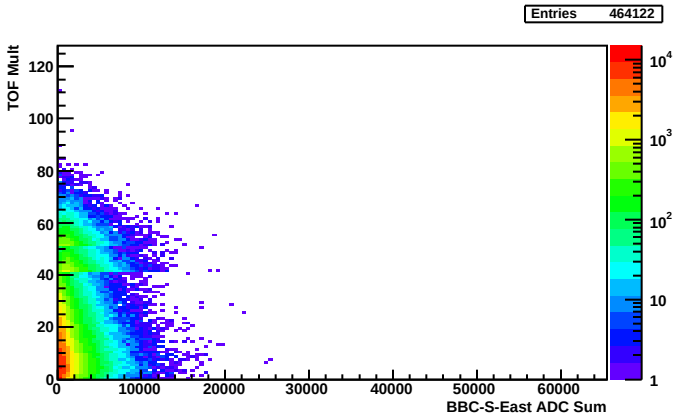

BBQ-VP003 (HI threshold)
Entries 7425952
TAC
QT Input Channel

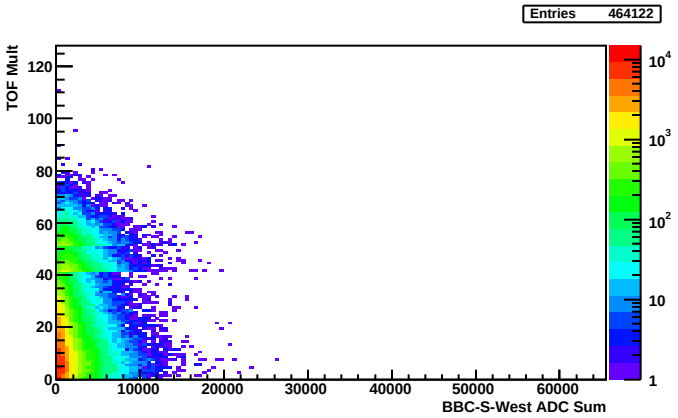

BBQ-VP004 (HI threshold)
Entries 7425952
ADC
QT Input Channel

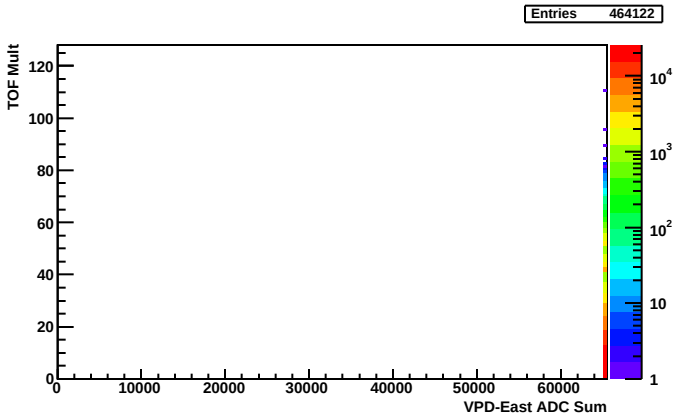

BBQ-VP004 (HI threshold)
Entries 7425952
TAC
QT Input Channel

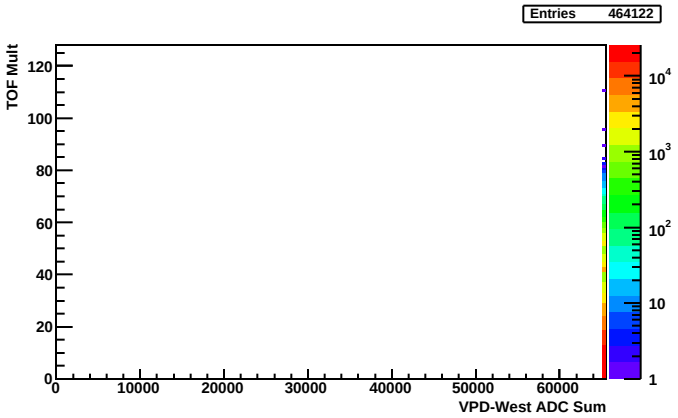

Entries 464122
TOF Mult
BBC-L-East ADC Sum

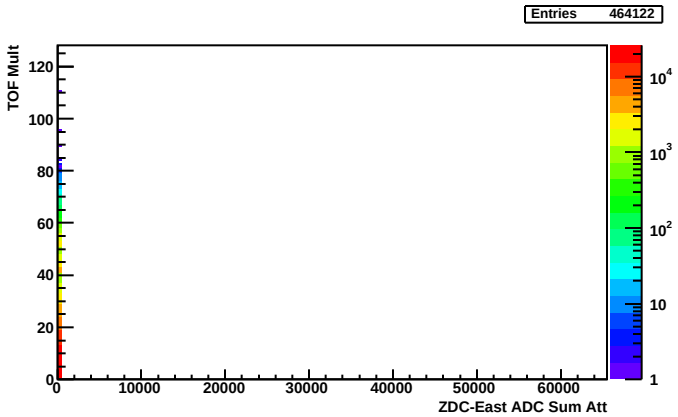

Entries 464122
TOF Mult
BBC-L-West ADC Sum

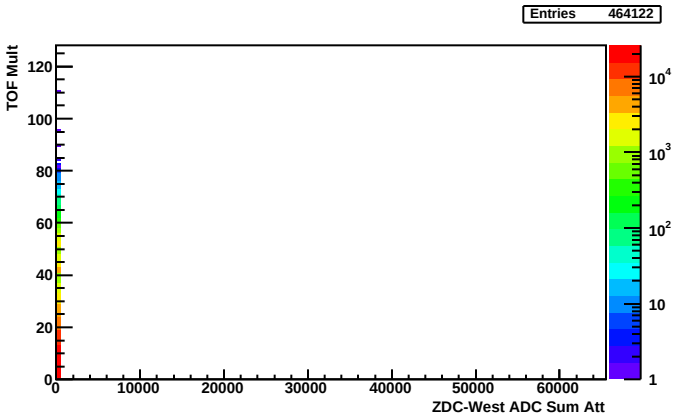

Entries 464122
TOF Mult
BBC-S-East ADC Sum

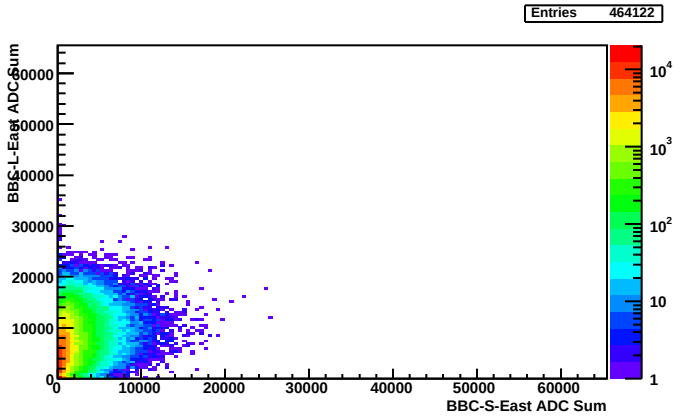

Entries 464122
TOF Mult
BBC-S-West ADC Sum

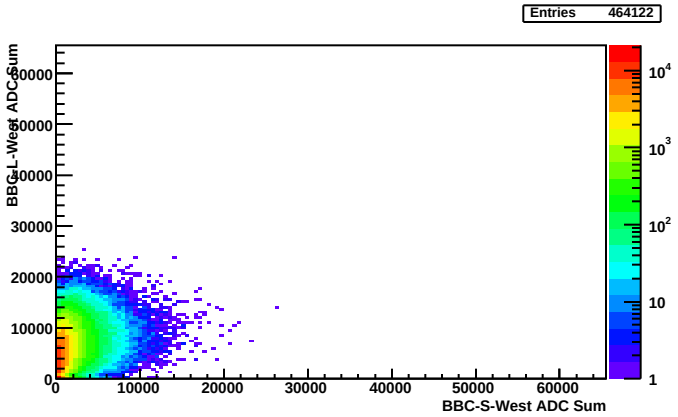

Entries 464122
TOF Mult
VPD-East ADC Sum

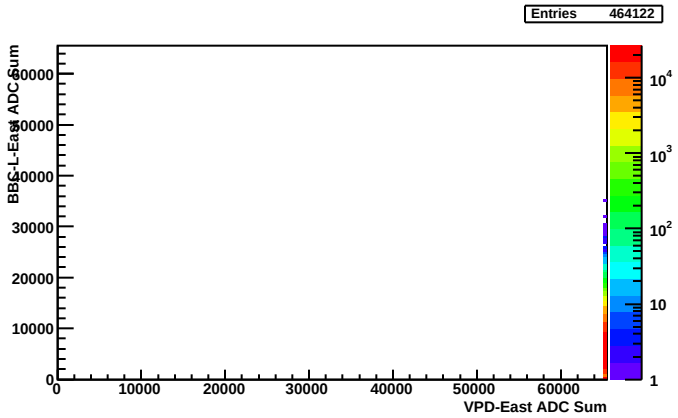

Entries 464122
TOF Mult
VPD-West ADC Sum

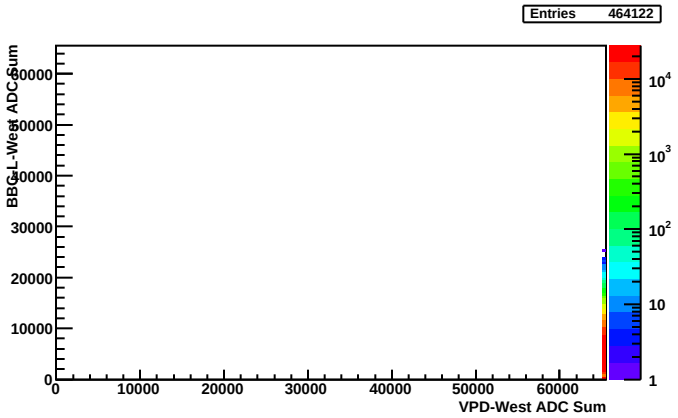

Entries 464122
TOF Mult
ZDC-East ADC Sum Att

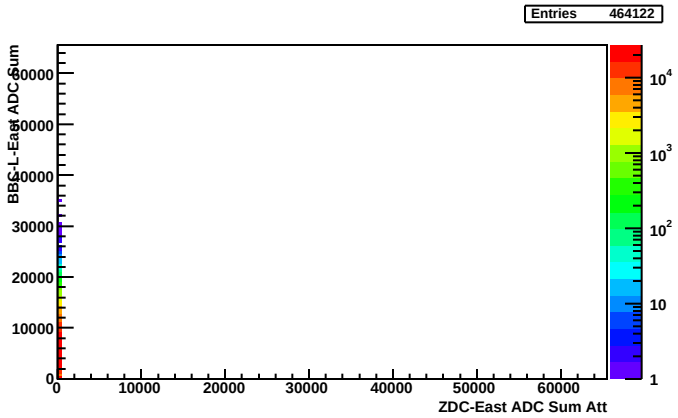

Entries 464122
TOF Mult
ZDC-West ADC Sum Att

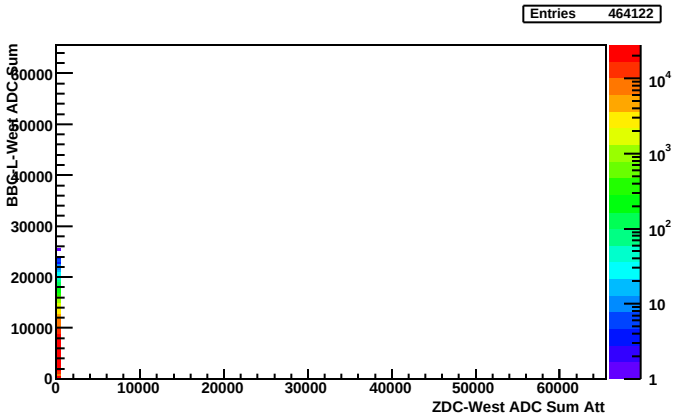

Entries 464122
BBC-L-East ADC Sum
BBC-S-East ADC Sum

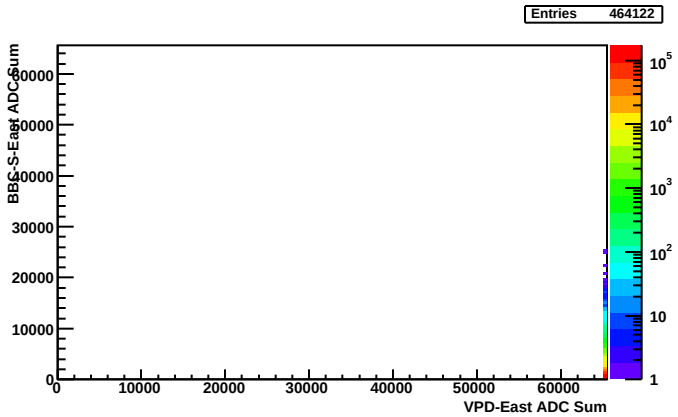

Entries 464122
BBC-L-West ADC Sum
BBC-S-West ADC Sum

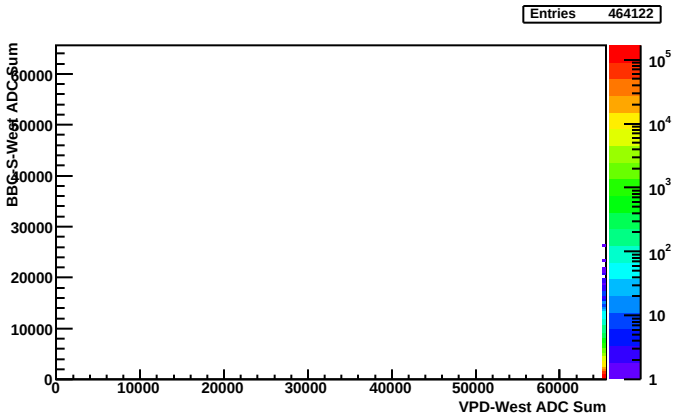

Entries 464122
BBC-L-East ADC Sum
VPD-East ADC Sum

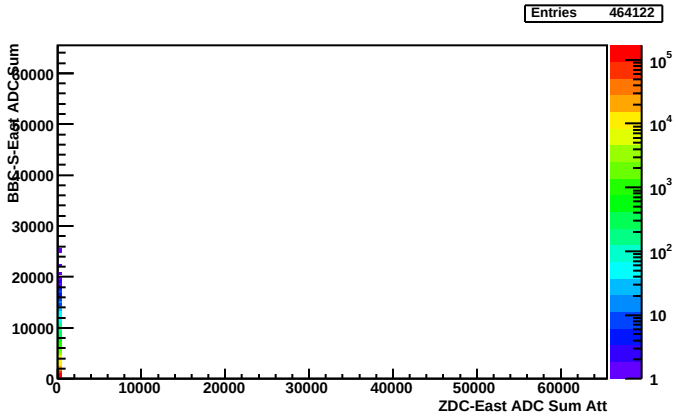

Entries 464122
BBC-L-West ADC Sum
VPD-West ADC Sum

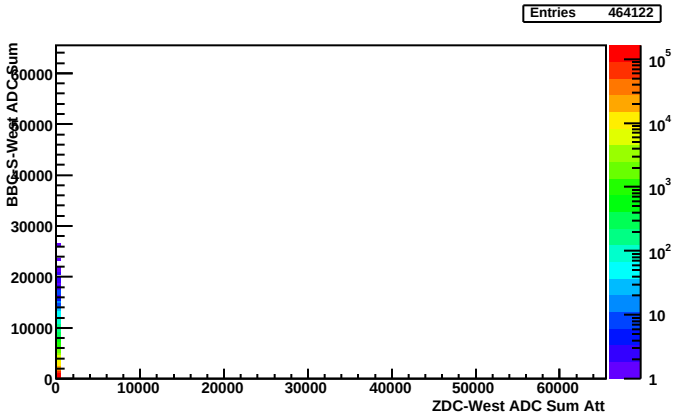

Entries 464122
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

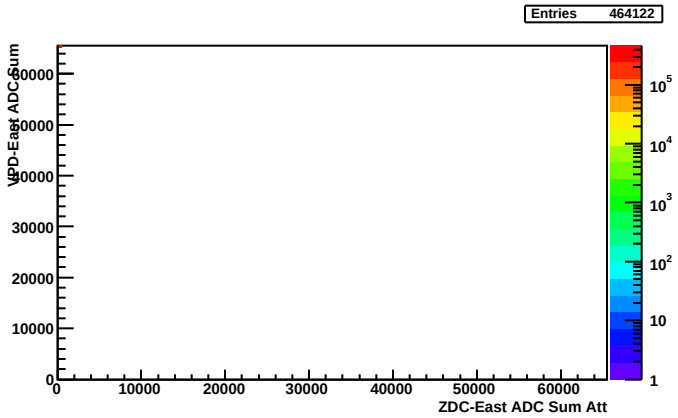

Entries 464122
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

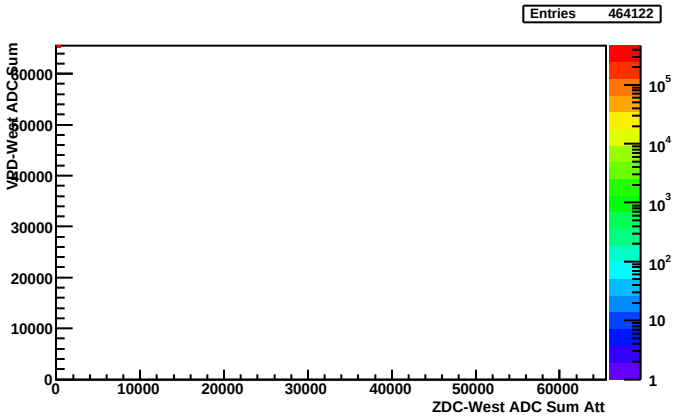

Entries 464122
BBC-S-East ADC Sum
VPD-East ADC Sum

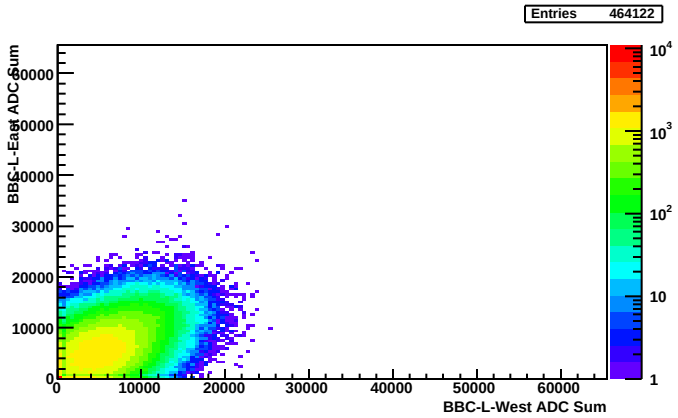

Entries 464122
BBC-S-West ADC Sum
VPD-West ADC Sum

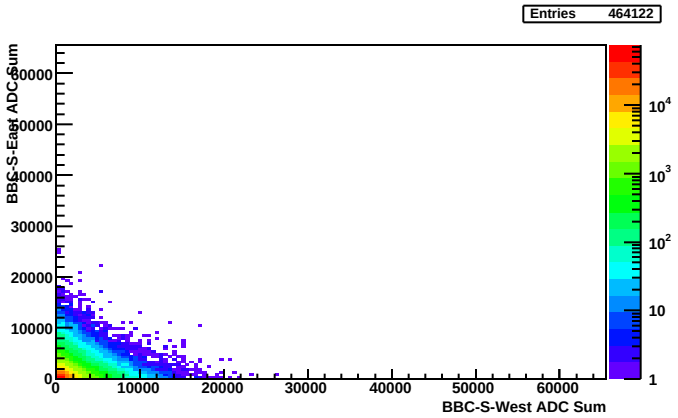

Entries 464122
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

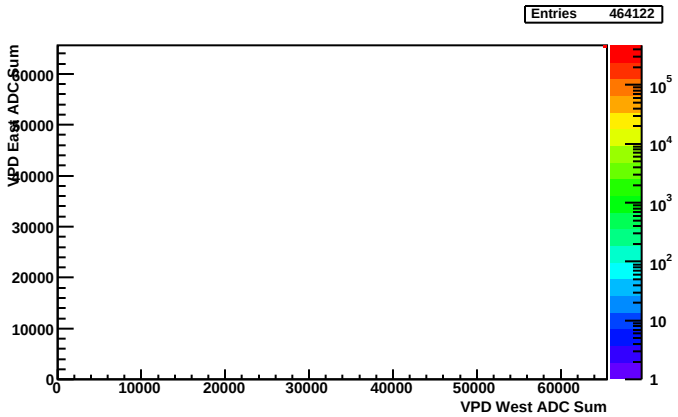

Entries 464122
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

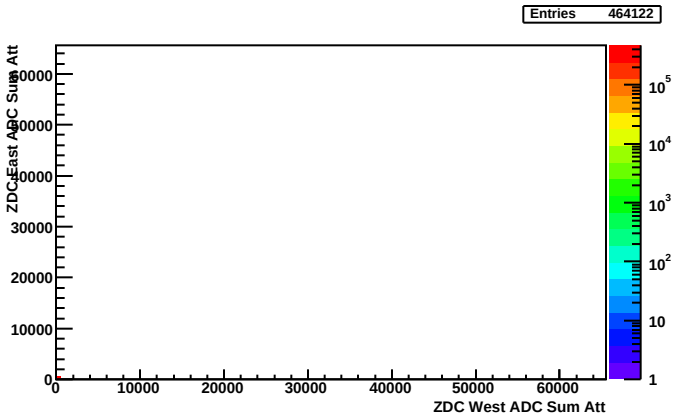

Entries 464122
VPD-East ADC Sum
ZDC-East ADC Sum Att

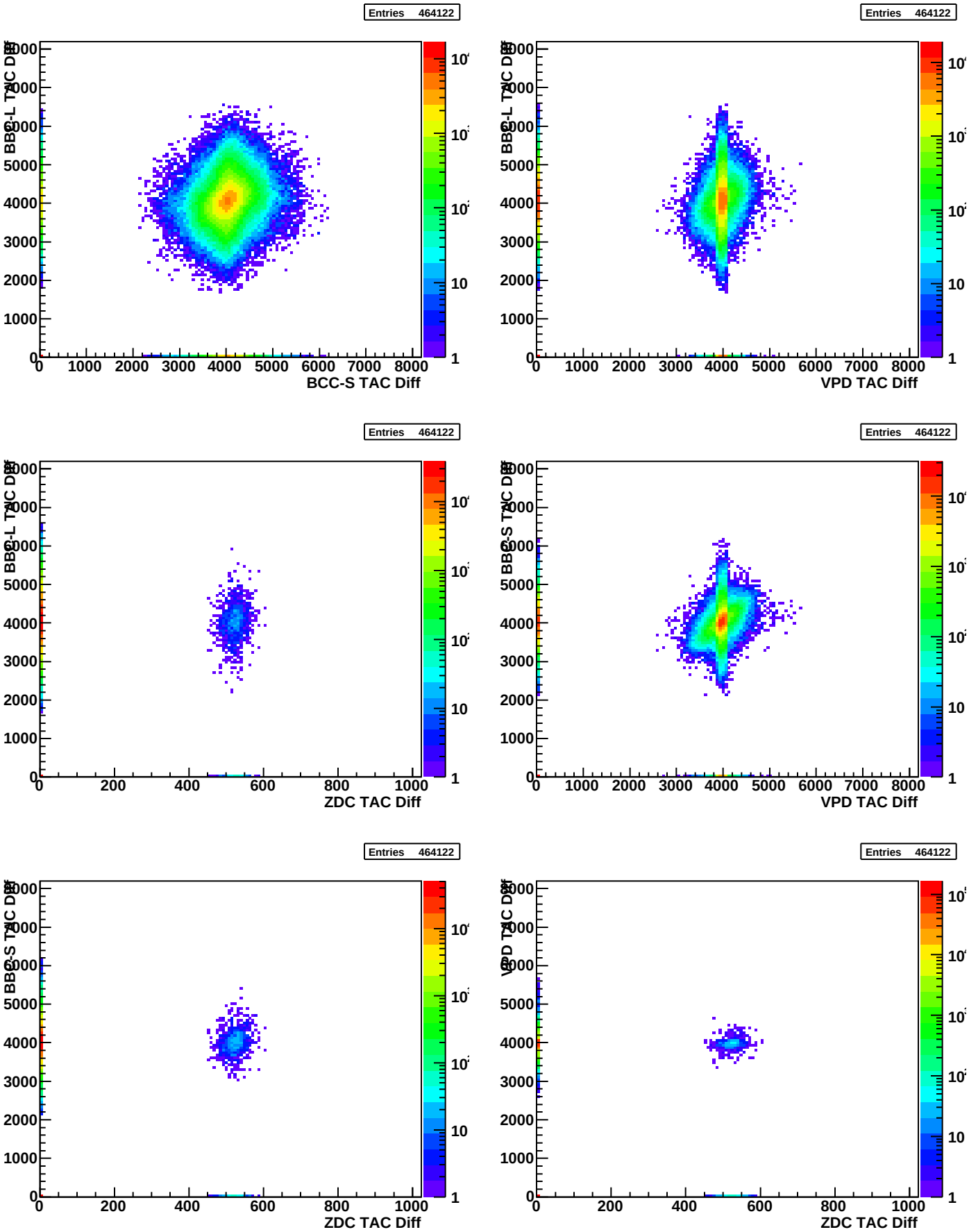

Entries 464122
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 464122
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 464122
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 464122
VPD East ADC Sum
VPD West ADC Sum


Entries 464122
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 464122
BBC-L TAC Diff
BCC-S TAC Diff
Entries 464122
BBC-L TAC Diff
VPD TAC Diff
Entries 464122
BBC-L TAC Diff
ZDC TAC Diff
Entries 464122
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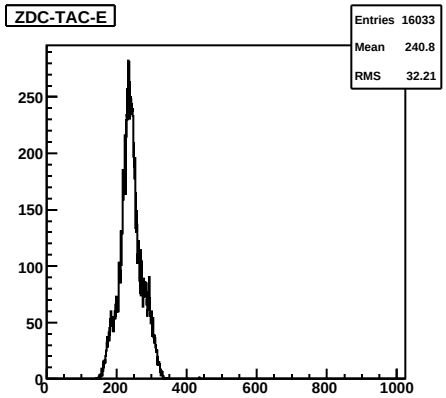
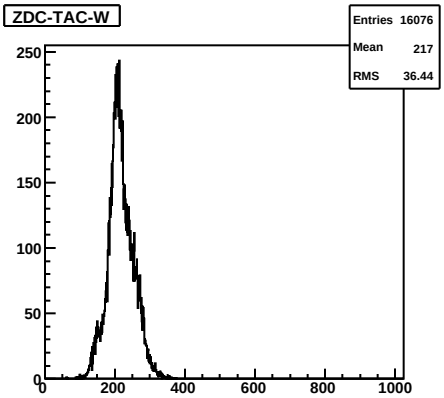
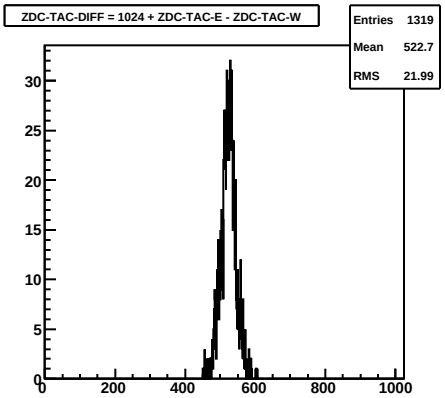
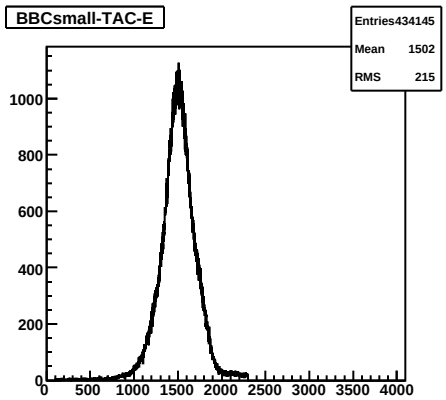
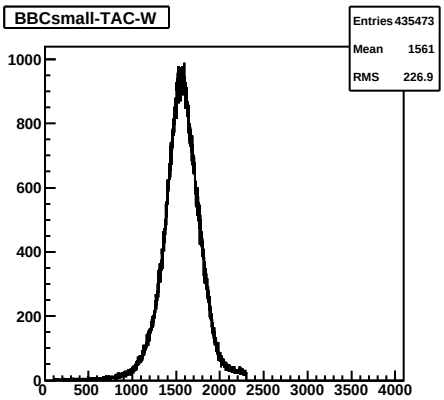
VPD TAC Diff
Entries 464122
BBC-S TAC Diff
ZDC TAC Diff
Entries 464122
VPD TAC Diff
ZDC TAC Diff

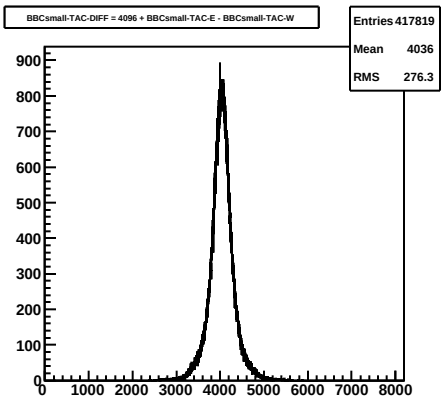

ZDC-TAC-E
Entries 16033
Mean 240.8
RMS 32.21

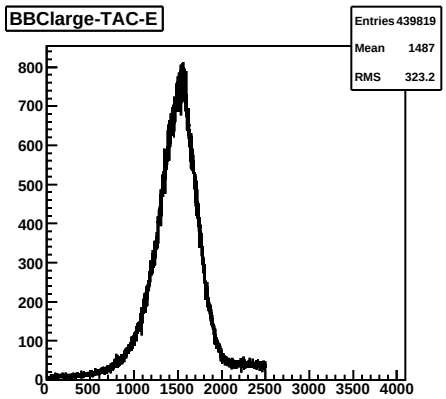

ZDC-TAC-W
Entries 16076
Mean 217
RMS 36.44

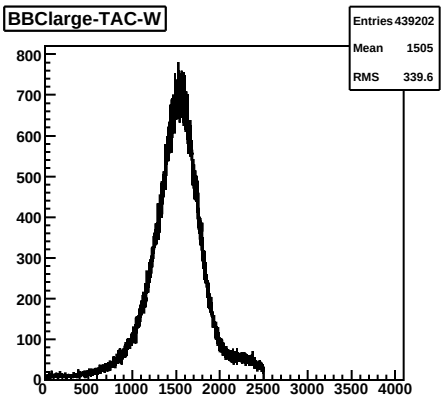

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1319
Mean 522.7
RMS 21.99

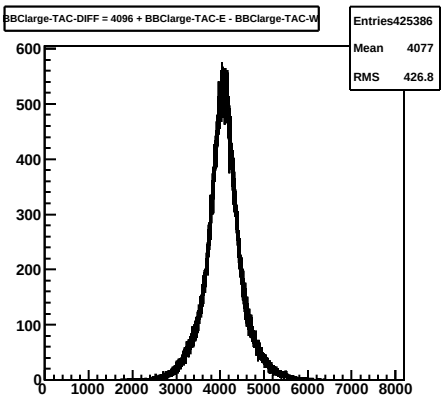

BBCsmall-TAC-E
Entries 434145
Mean 1502
RMS 215

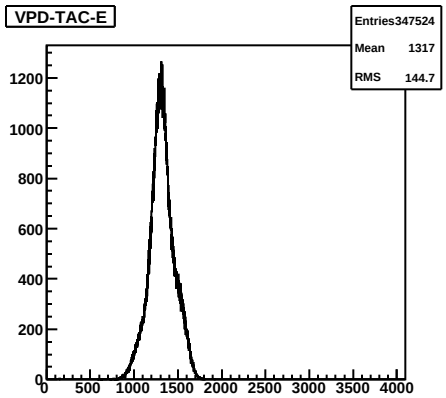

BBCsmall-TAC-W
Entries 435473
Mean 1561
RMS 226.9

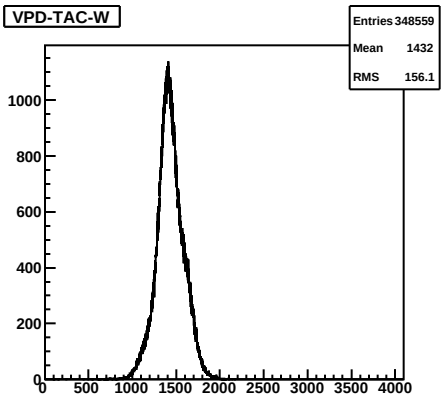

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 417819
Mean 4036
RMS 276.3

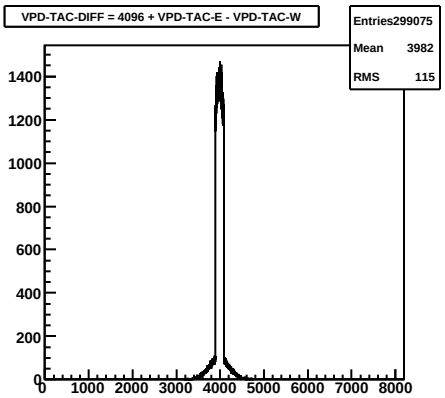

BBClarge-TAC-E
Entries 439819
Mean 1487
RMS 323.2

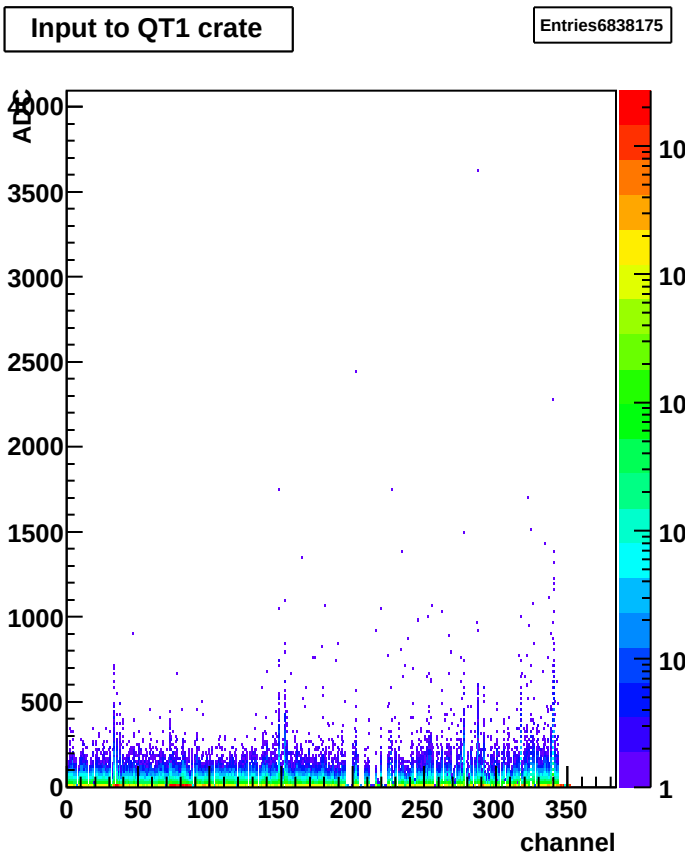

BBClarge-TAC-W
Entries 439202
Mean 1505
RMS 339.6

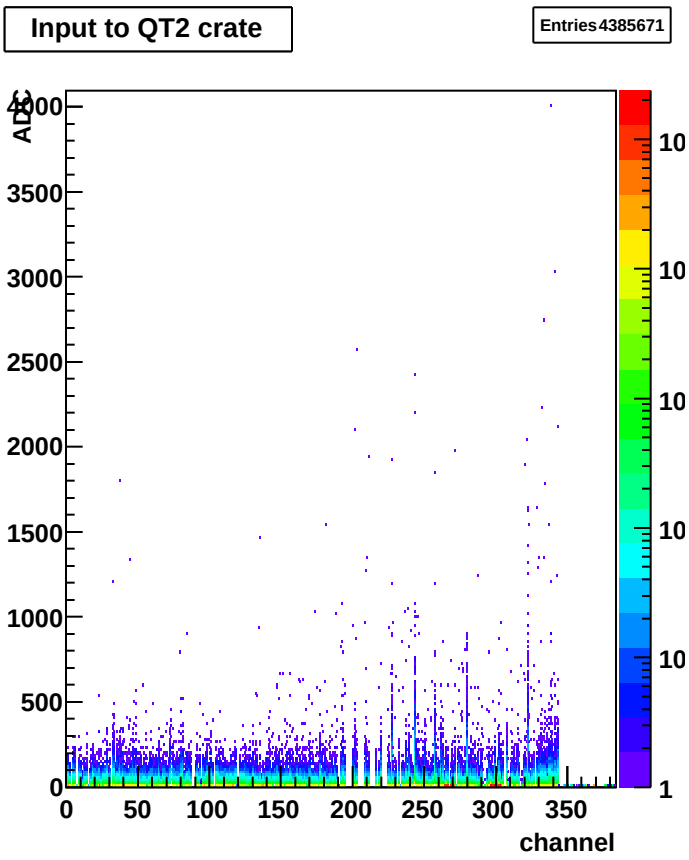

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 425386
Mean 4077
RMS 426.8

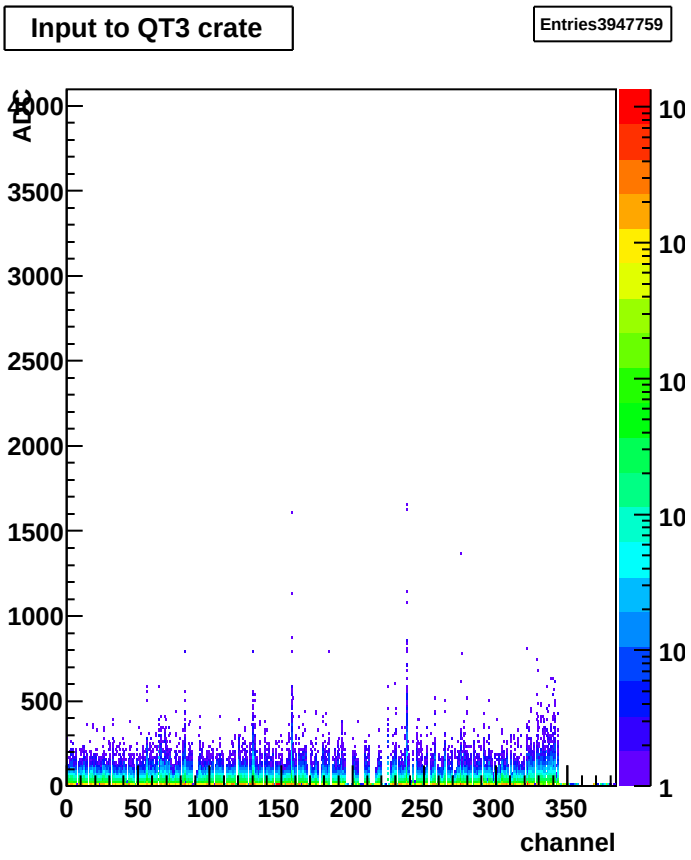

VPD-TAC-E
Entries 347524
Mean 1317
RMS 144.7

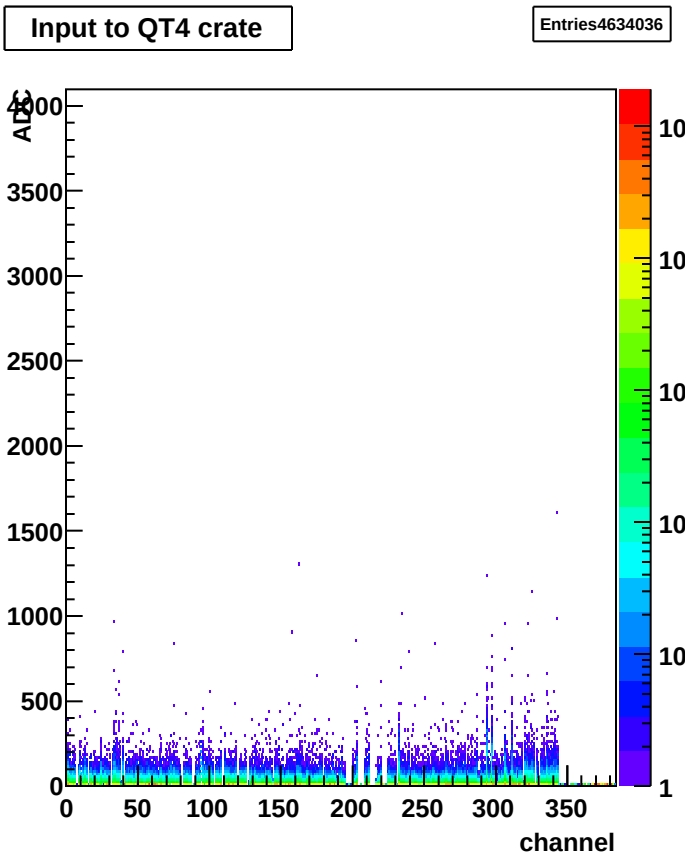

VPD-TAC-W
Entries 348559
Mean 1432
RMS 156.1

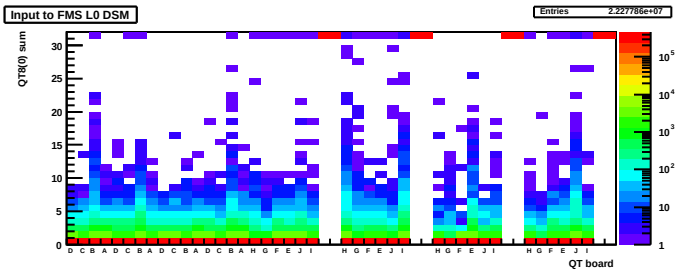

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 299075
Mean 3982
RMS 115

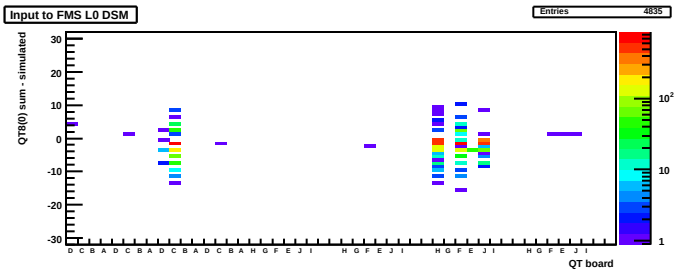

Input to QT1 crate
Entries6838175
ADC
channel

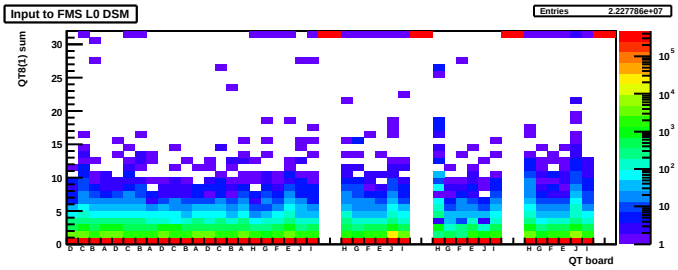

Input to QT2 crate
Entries4385671
ADC
channel

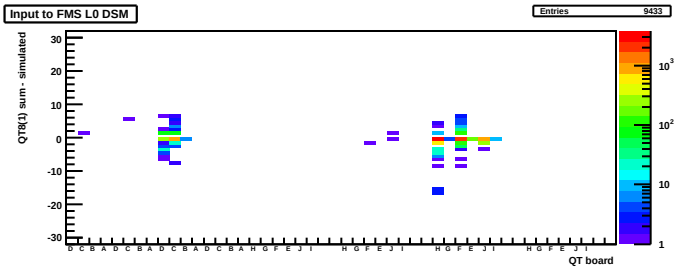

Input to QT3 crate
Entries3947759
ADC
channel

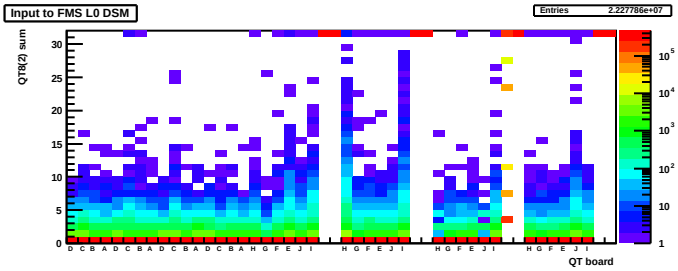

Input to QT4 crate
Entries4634036
ADC
channel

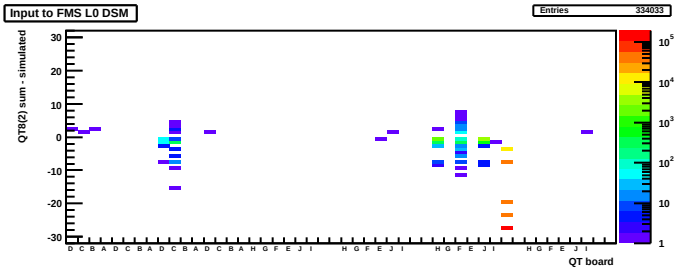

Input to FMS L0 DSM
Entries 2.227786e+07
QT8(0) sum
QT board

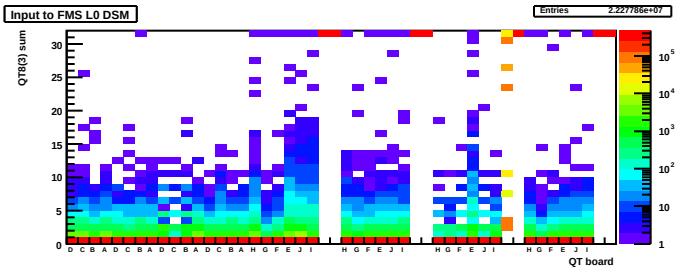

Input to FMS L0 DSM
Entries 4835
QT8(0) sum - simulated
QT board

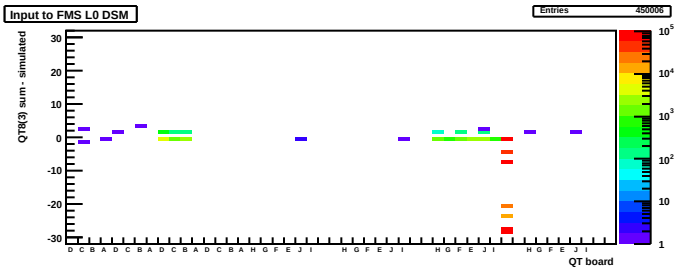

Input to FMS L0 DSM
Entries 2.227786e+07
QT8(1) sum
QT board

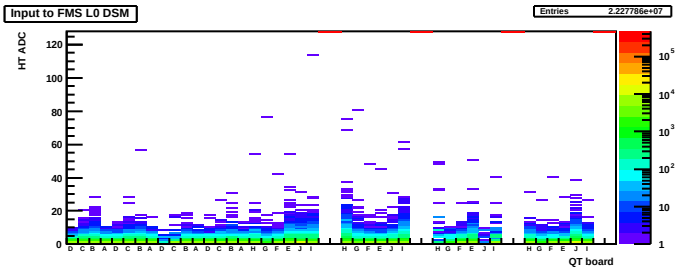

Input to FMS L0 DSM
Entries 9433
QT8(1) sum - simulated
QT board

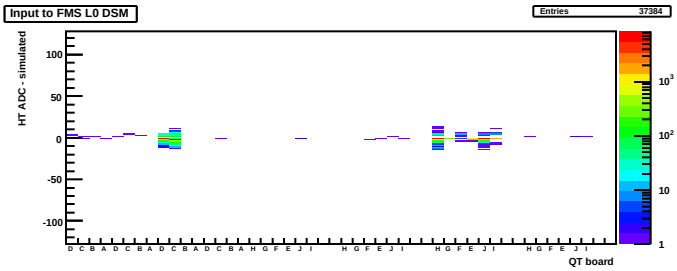

Input to FMS L0 DSM
Entries 2.227786e+07
QT8(2) sum
QT board

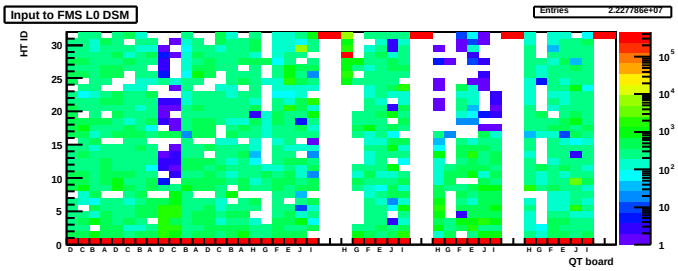

Input to FMS L0 DSM
Entries 334033
QT8(2) sum - simulated
QT board

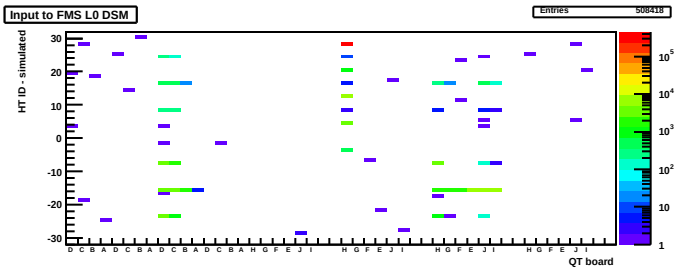

Input to FMS L0 DSM
Entries 2.227786e+07
QT8(3) sum
QT board

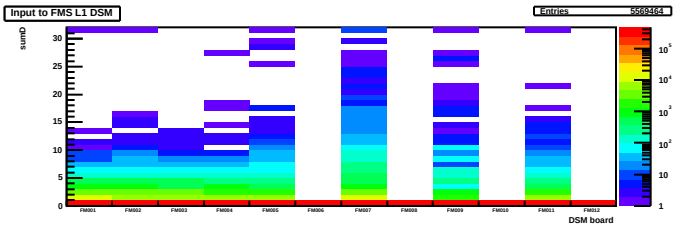

Input to FMS L0 DSM
Entries 450006
QT8(3) sum - simulated
QT board

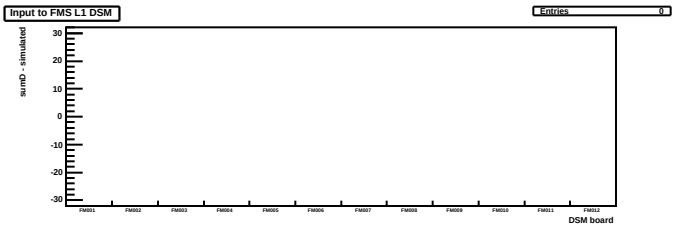

Input to FMS L0 DSM
Entries 2.227786e+07
HT ADC
QT board

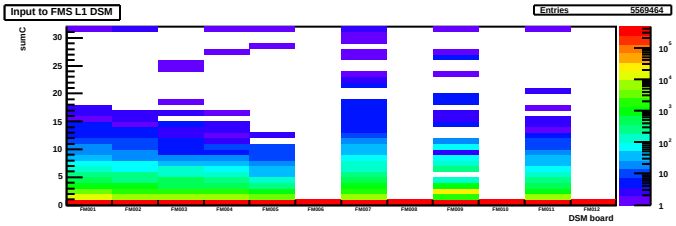

Input to FMS L0 DSM
Entries 37384
HT ADC - simulated
QT board

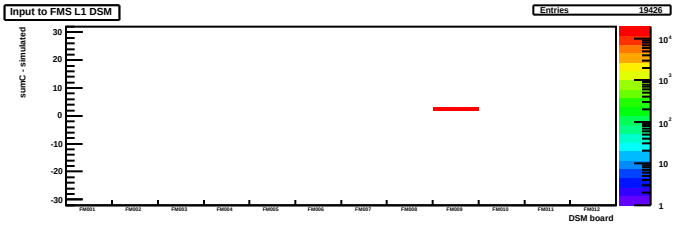

Input to FMS L0 DSM
Entries 2.227786e+07
HT ID
QT board

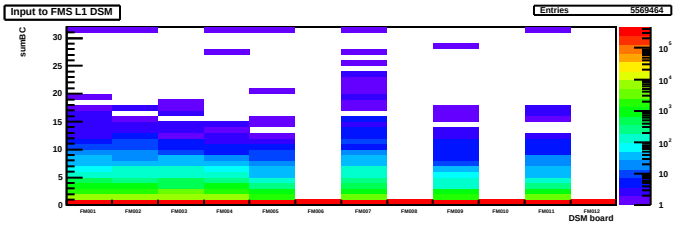

Input to FMS L0 DSM
Entries 508418
HT ID - simulated
QT board

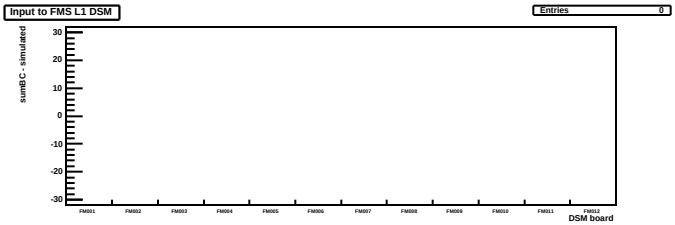

Input to FMS L1 DSM
Entries 5569464
sumD
DSM board

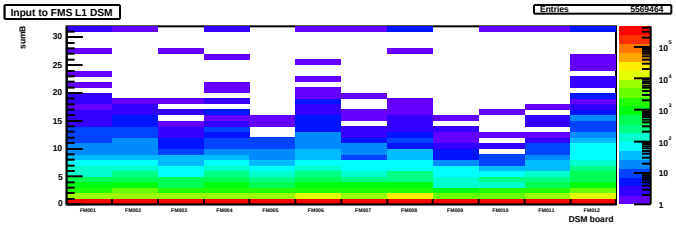

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

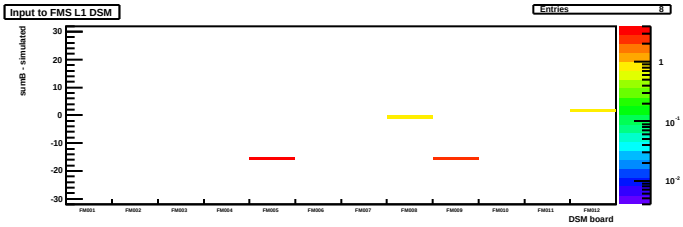

Input to FMS L1 DSM
Entries 5569464
sumC
DSM board

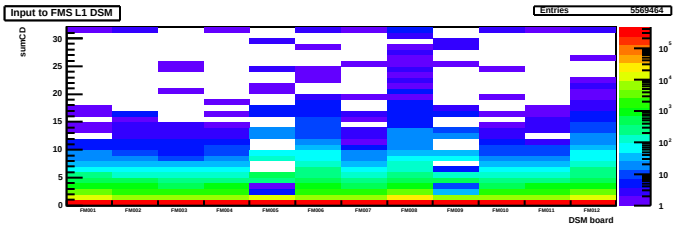

Input to FMS L1 DSM
Entries 19426
sumC - simulated
DSM board

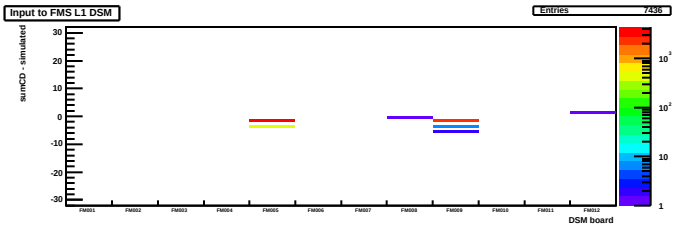

Input to FMS L1 DSM
Entries 5569464
sumBC
DSM board

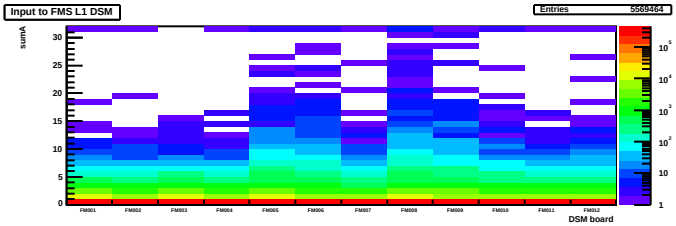

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

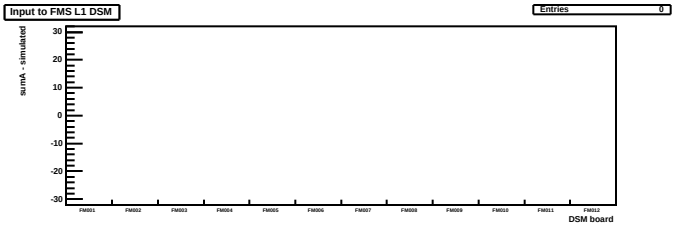

Input to FMS L1 DSM
Entries 5569464
sumB
DSM board

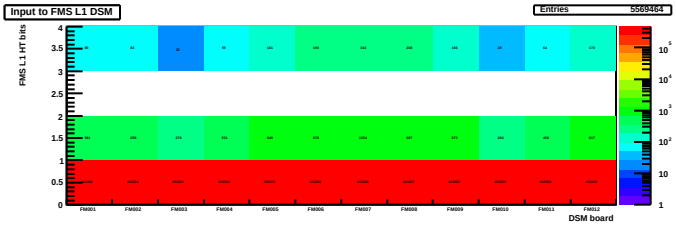

Input to FMS L1 DSM
Entries 8
sumB - simulated
DSM board

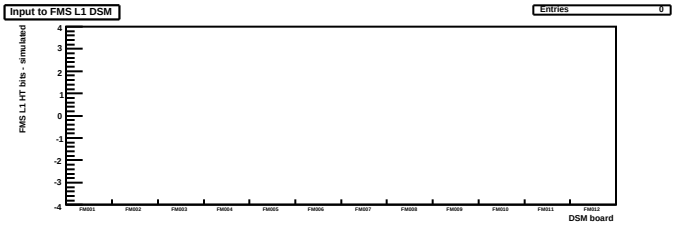

Input to FMS L1 DSM
Entries 5569464
sumCD
DSM board

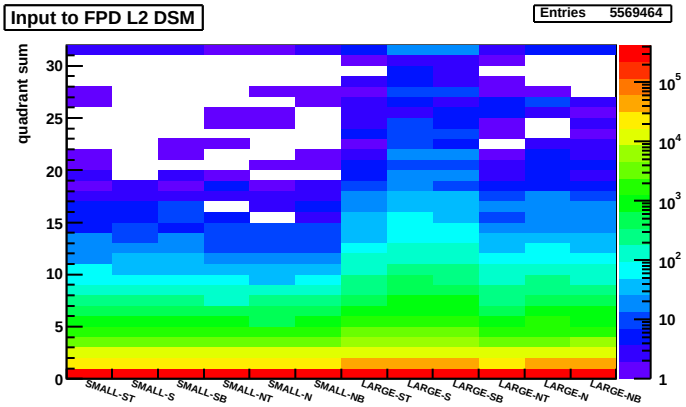

Input to FMS L1 DSM
Entries 7436
sumCD - simulated
DSM board

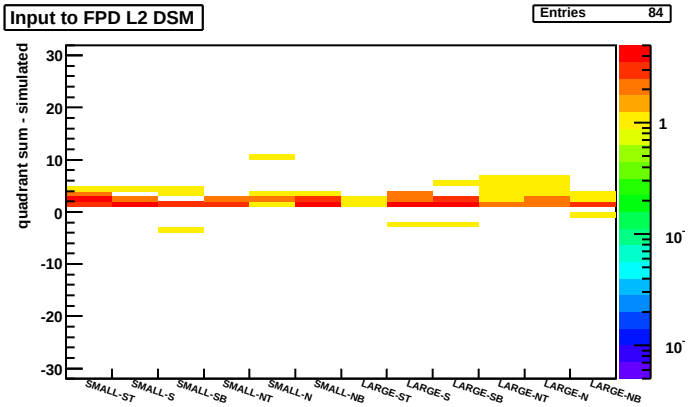

Input to FMS L1 DSM
Entries 5569464
sumA
DSM board

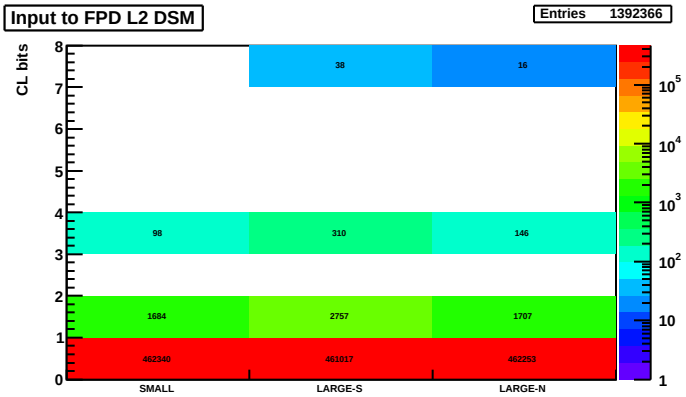

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

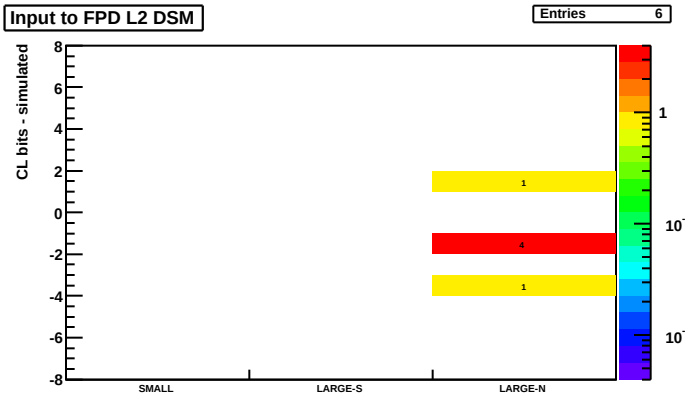

Input to FMS L1 DSM
Entries 5569464
FMS L1 HT bits
DSM board

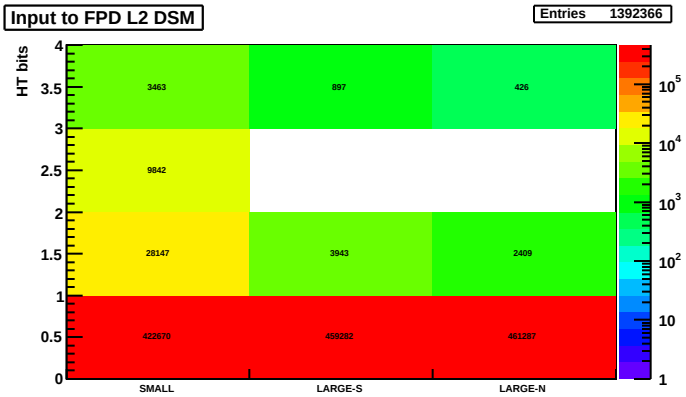

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

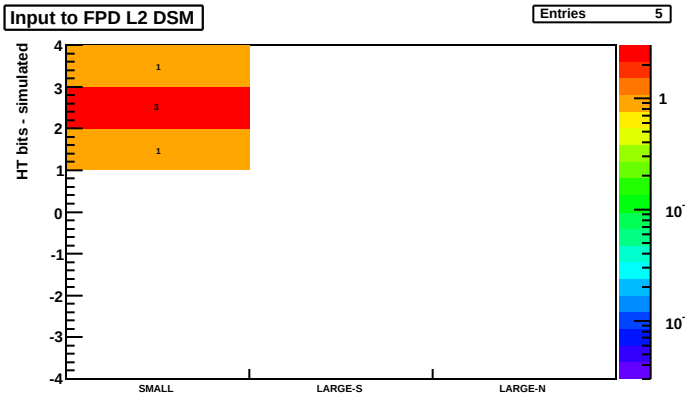

Input to FPD L2 DSM
Entries 5569464
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

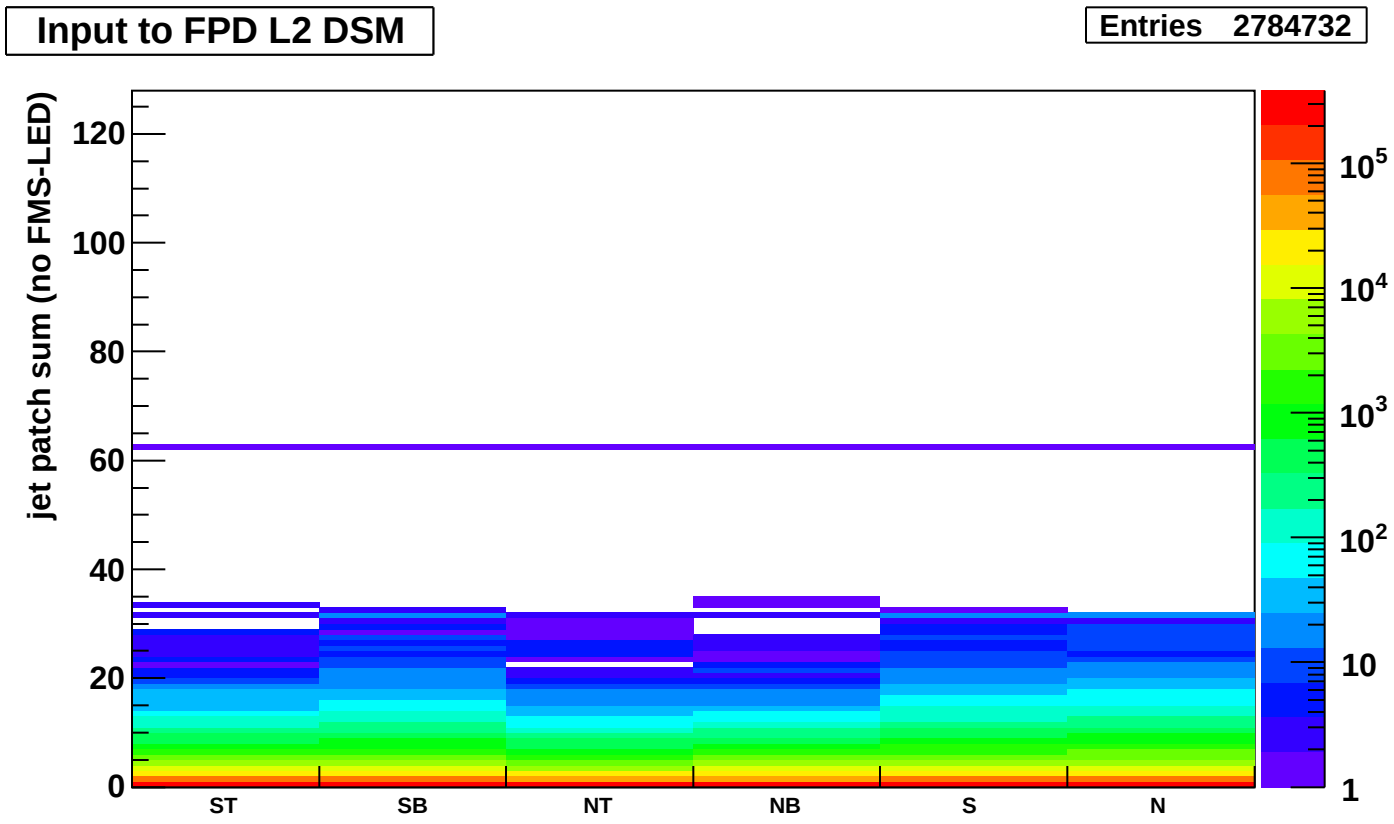

Input to FPD L2 DSM
Entries 84
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

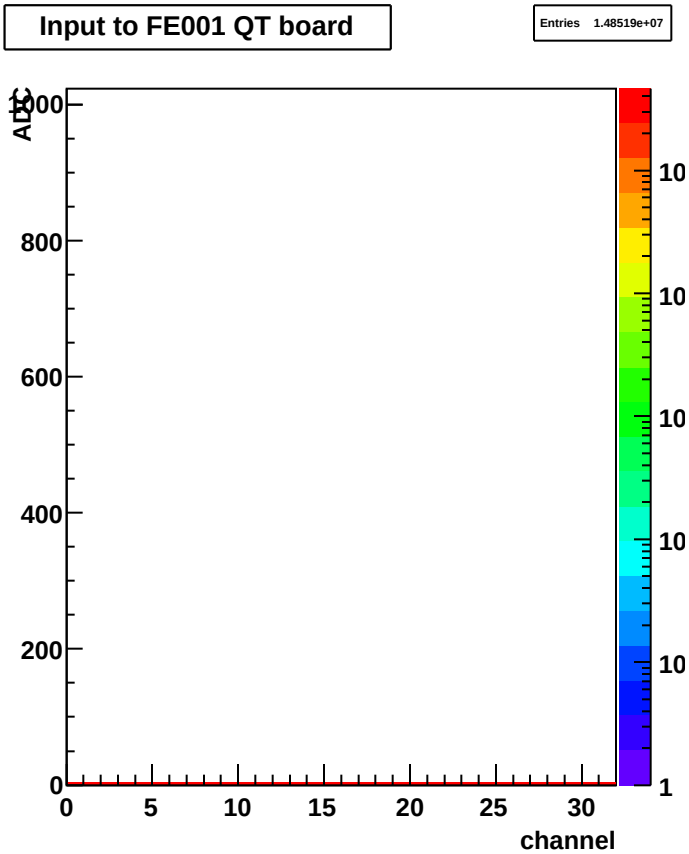

Input to FPD L2 DSM
Entries 1392366
CL bits
38
16
98
310
146
1684
2757
1707
462340
461017
462253
SMALL
LARGE-S
LARGE-N

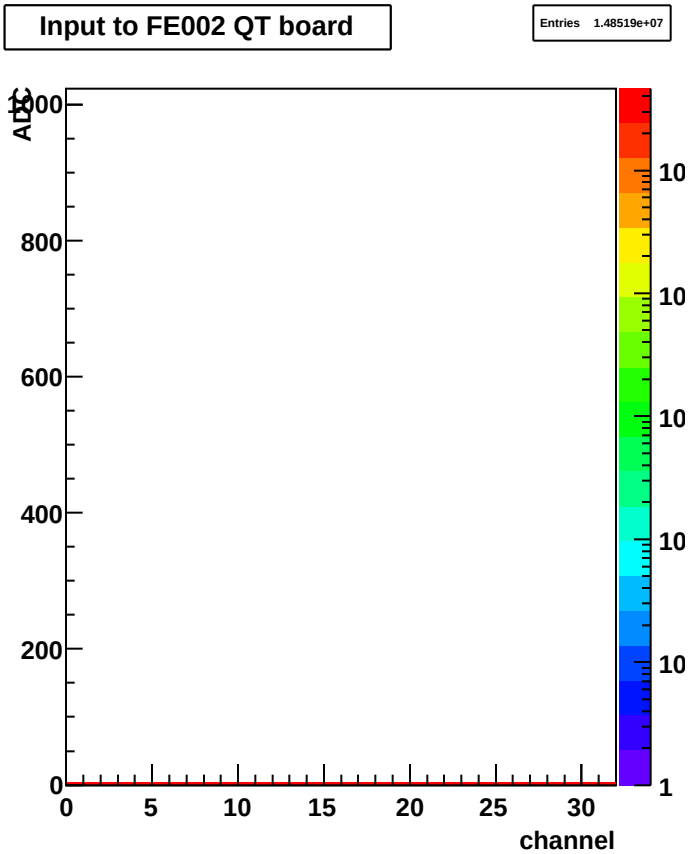

Input to FPD L2 DSM
Entries 6
CL bits - simulated
1
4
1
SMALL
LARGE-S
LARGE-N

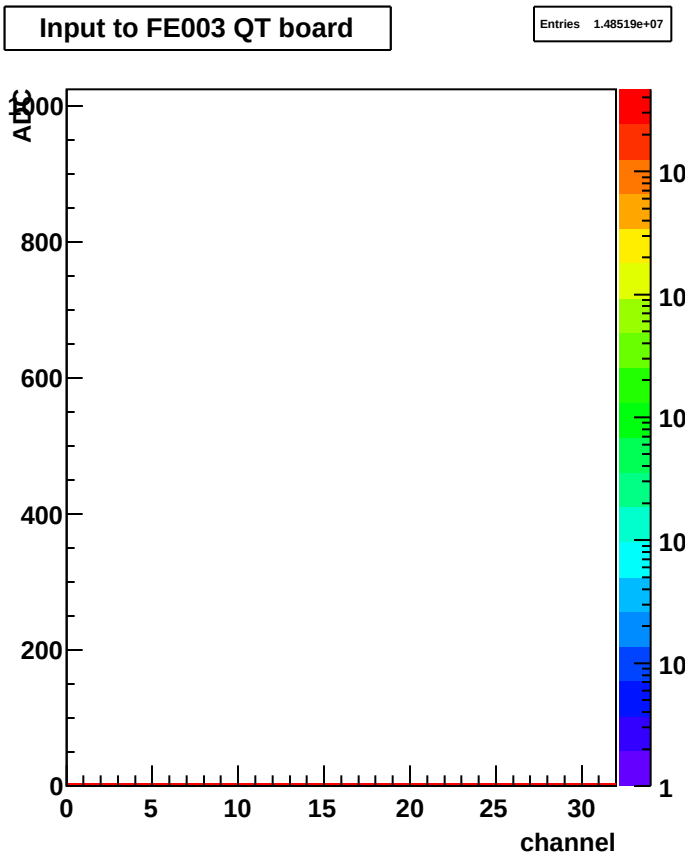

Input to FPD L2 DSM
Entries 1392366
HT bits
3463
897
426
9842
28147
3943
2409
422670
459282
461287
SMALL
LARGE-S
LARGE-N

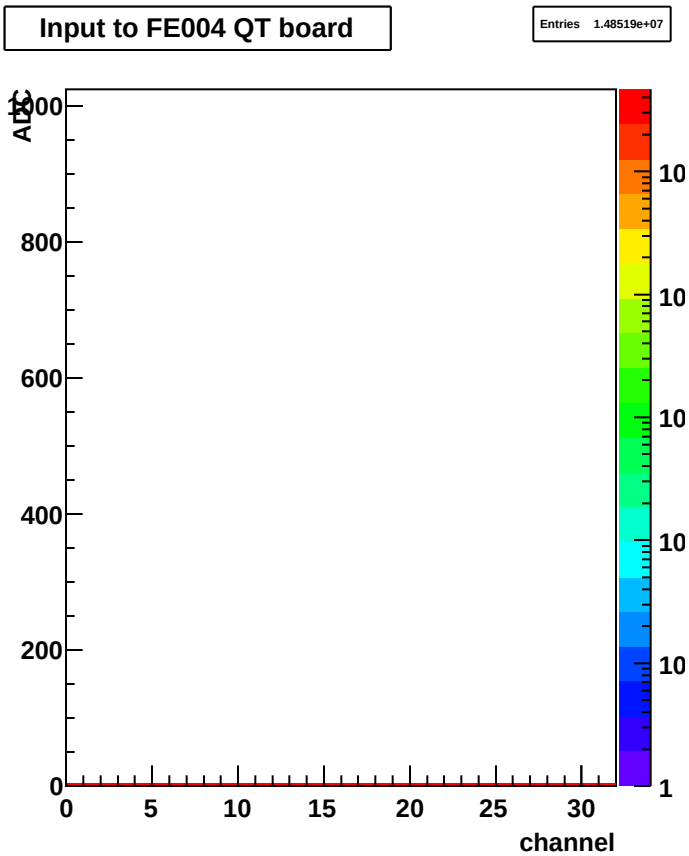

Input to FPD L2 DSM
Entries 5
HT bits - simulated
1
3
1
SMALL
LARGE-S
LARGE-N

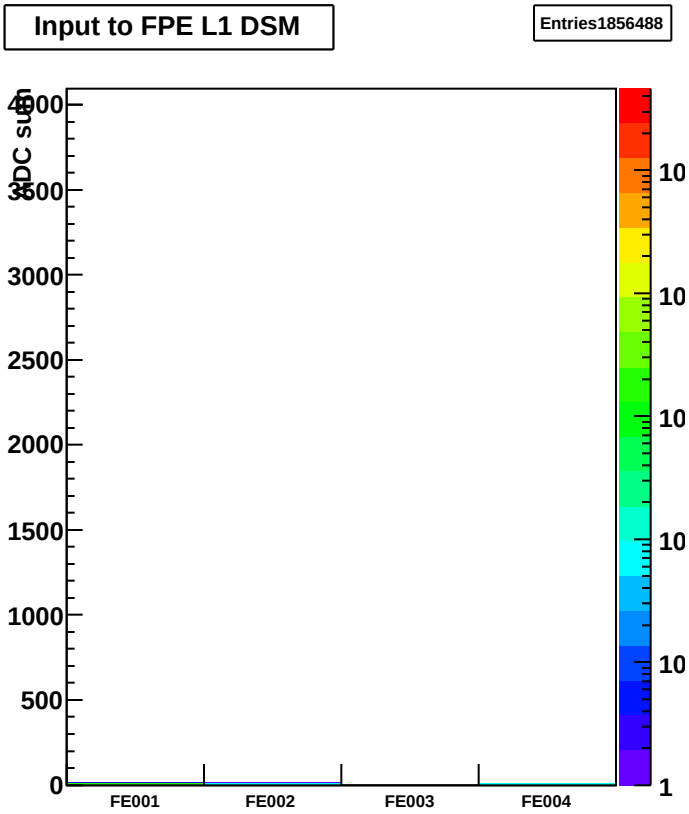

Input to FPD L2 DSM
Entries 2784732
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

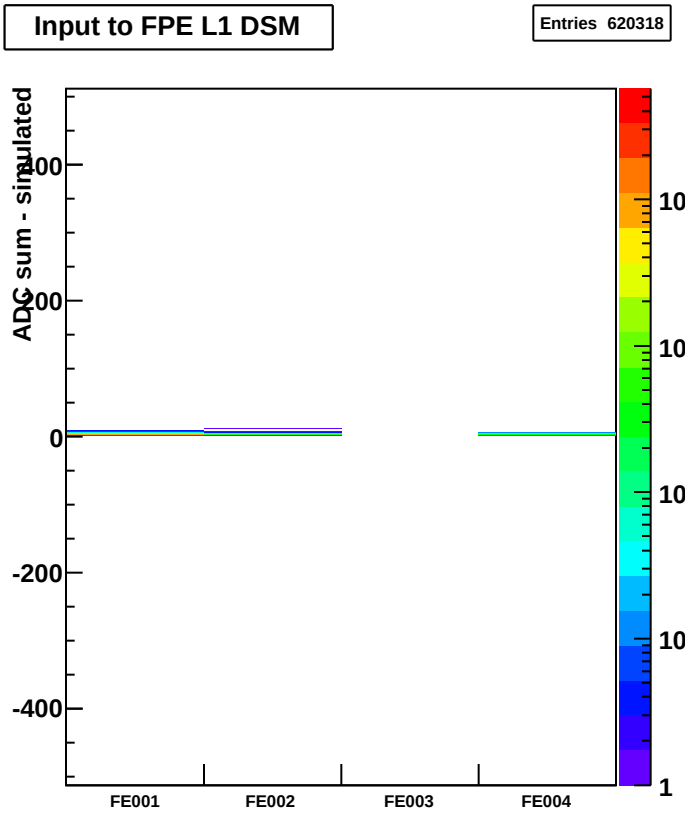

Input to FE001 QT board
Entries 1.48519e+07
ADC
channel


Input to FE002 QT board
Entries 1.48519e+07
ADC
channel

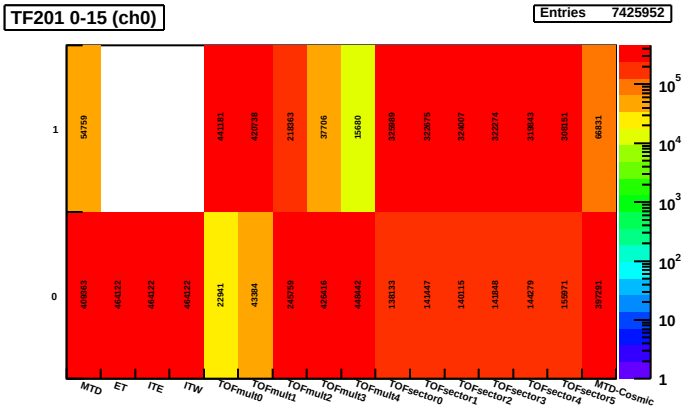

Input to FE003 QT board
Entries 1.48519e+07
ADC
channel


Input to FE004 QT board
Entries 1.48519e+07
ADC
channel

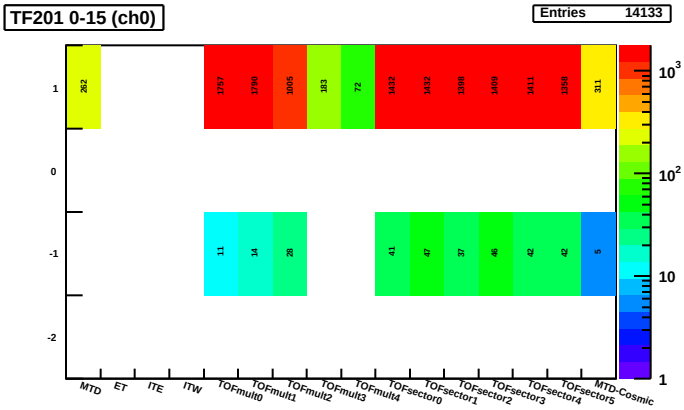

Input to FPE L1 DSM
Entries1856488
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 620318
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

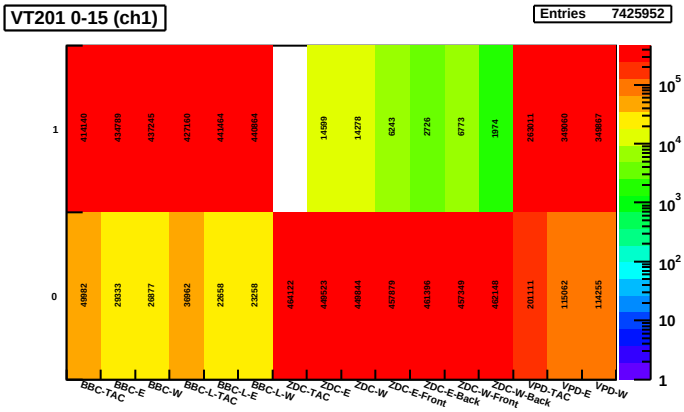
TF201 0-15 (ch0)



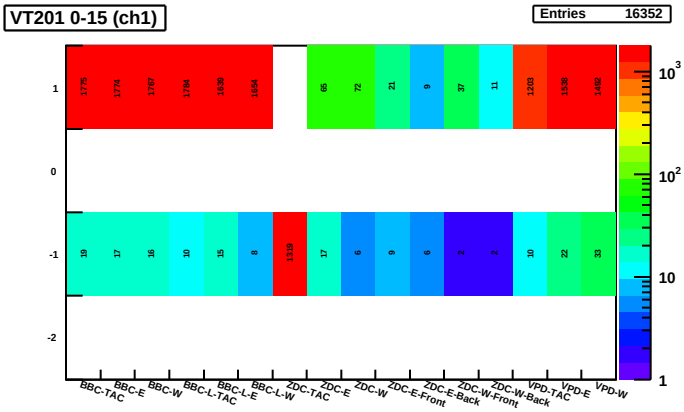
TF201 0-15 (ch0)



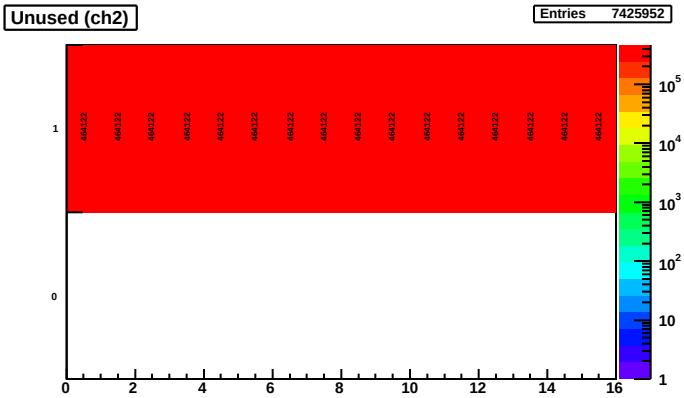
VT201 0-15 (ch1)

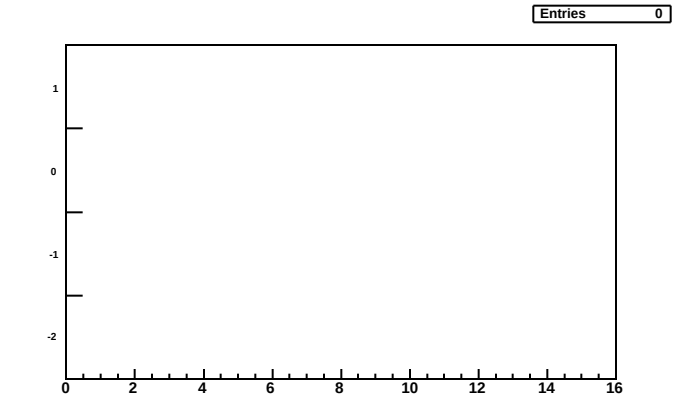


VT201 0-15 (ch1)

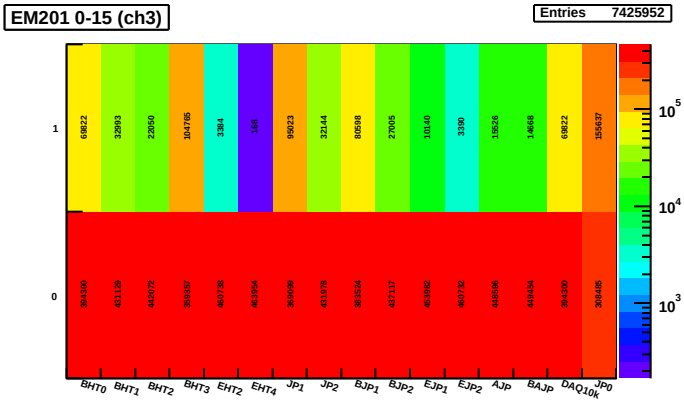


Unused (ch2)





EM201 0-15 (ch3)



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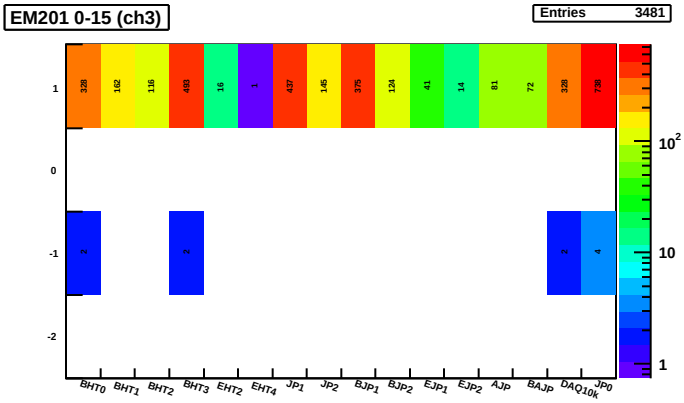
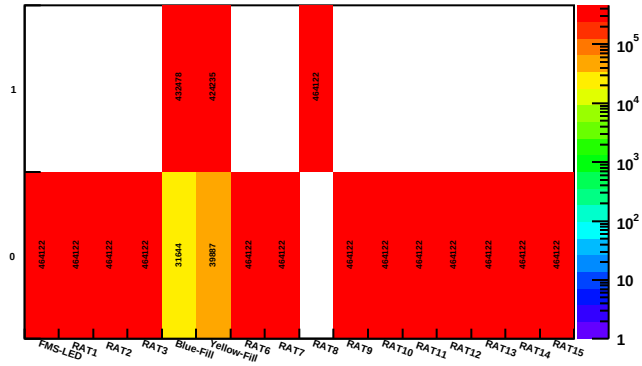
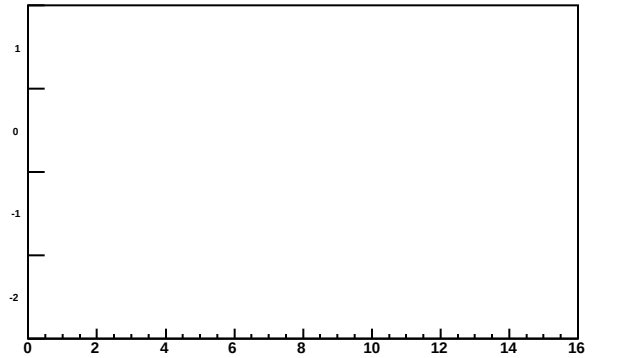
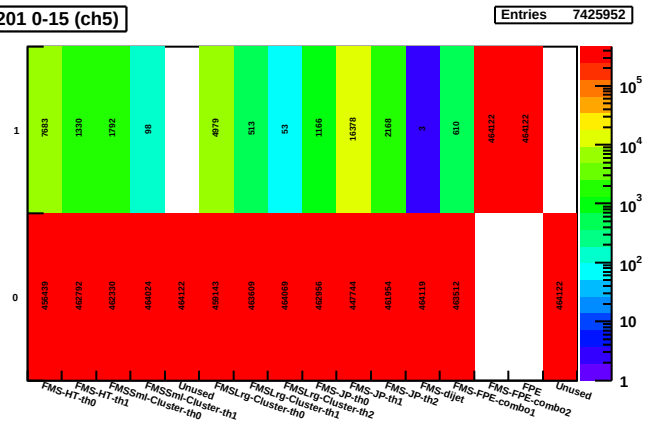


RAT board (ch4)





FP201 0-15 (ch5)



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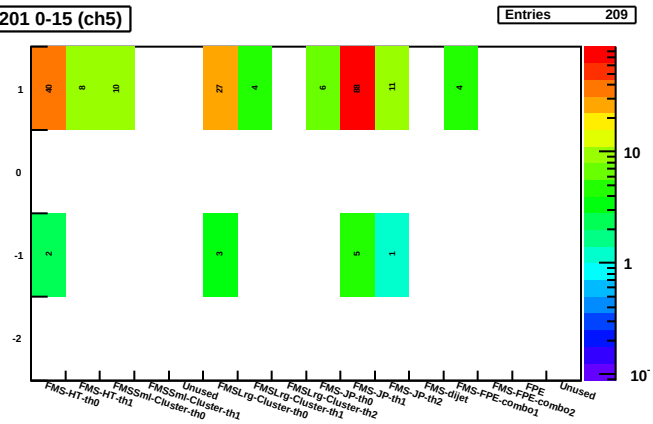

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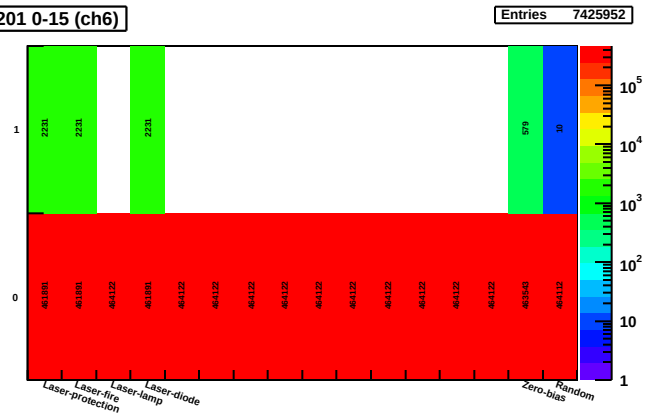

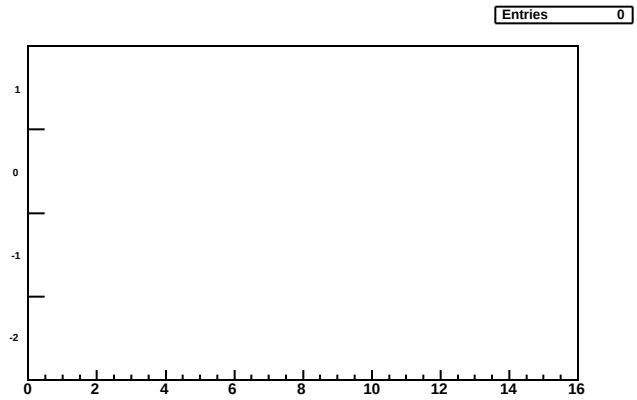
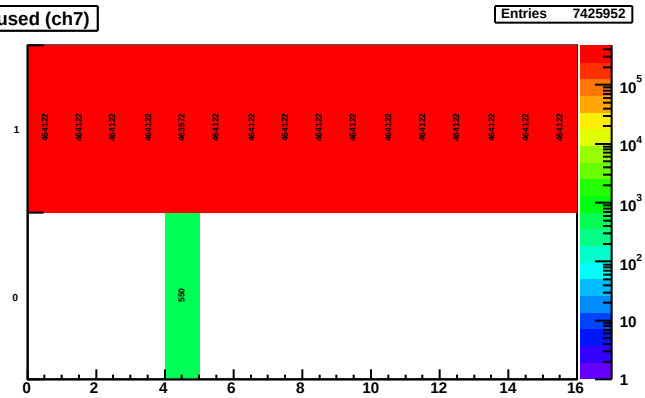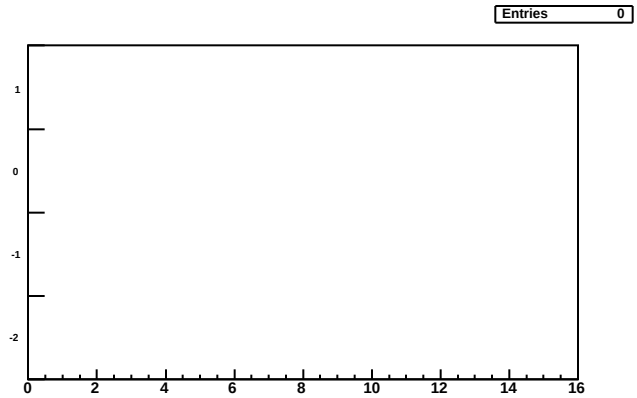
Unused (ch7)