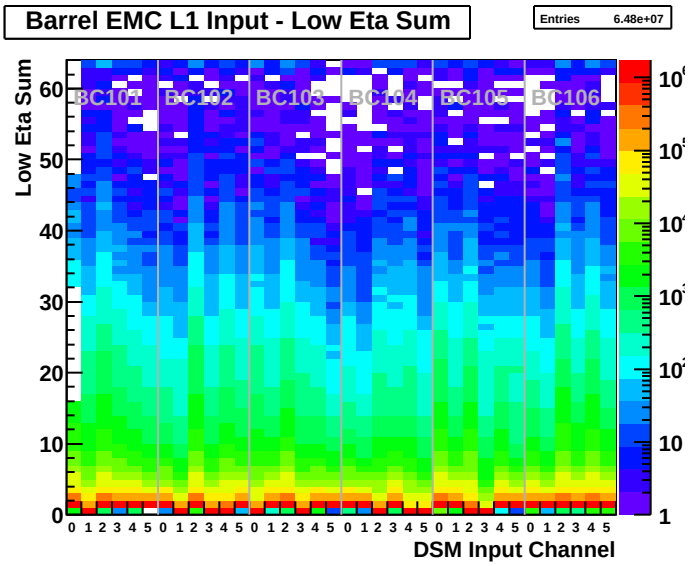
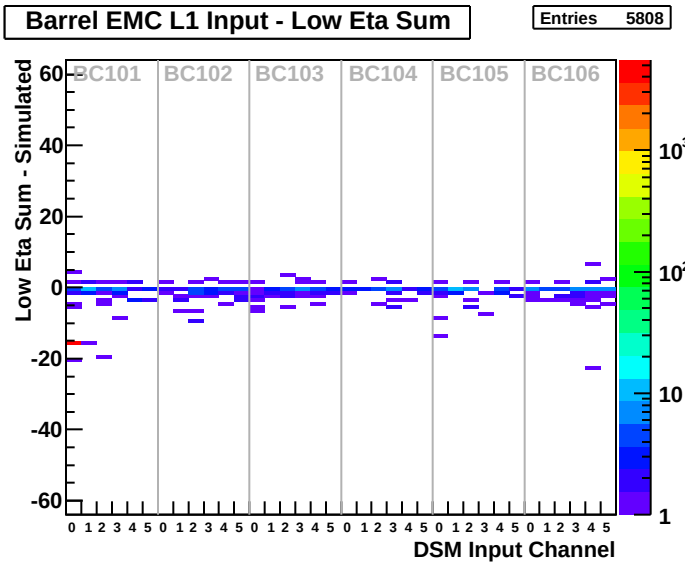
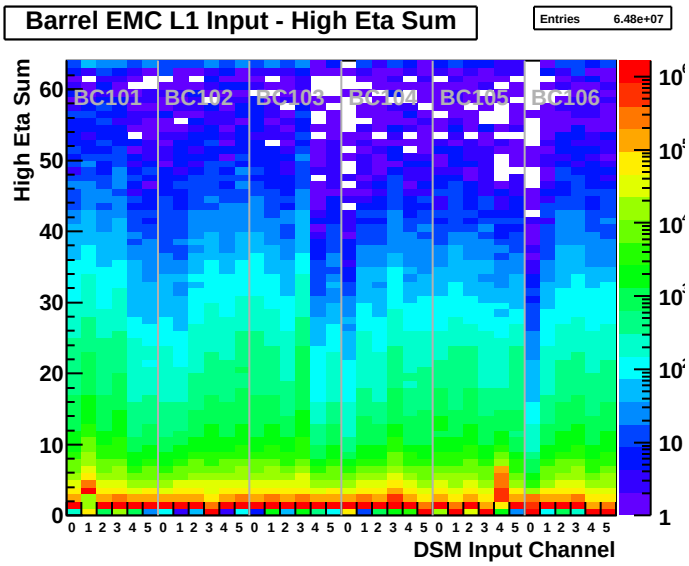
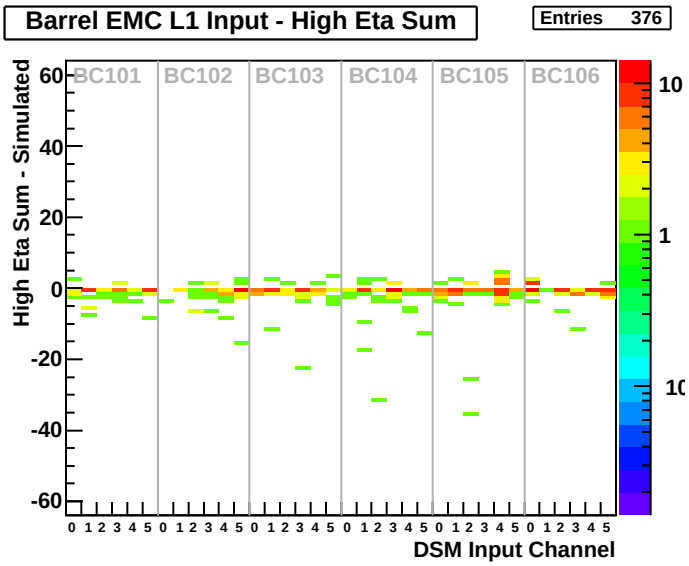
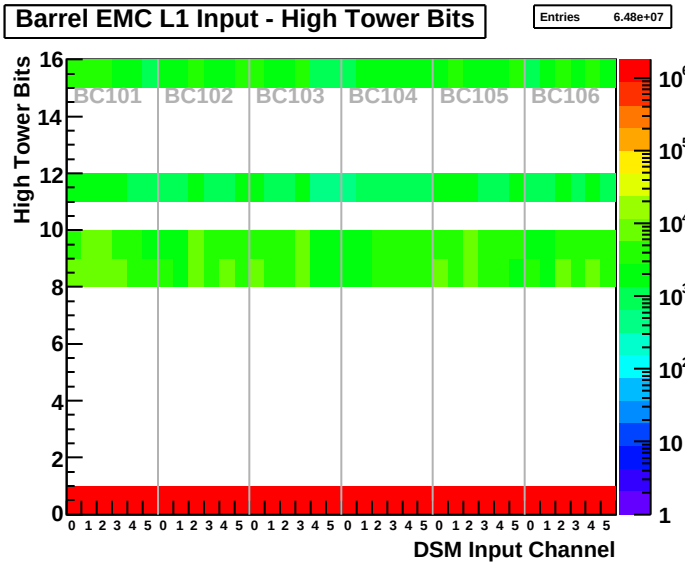
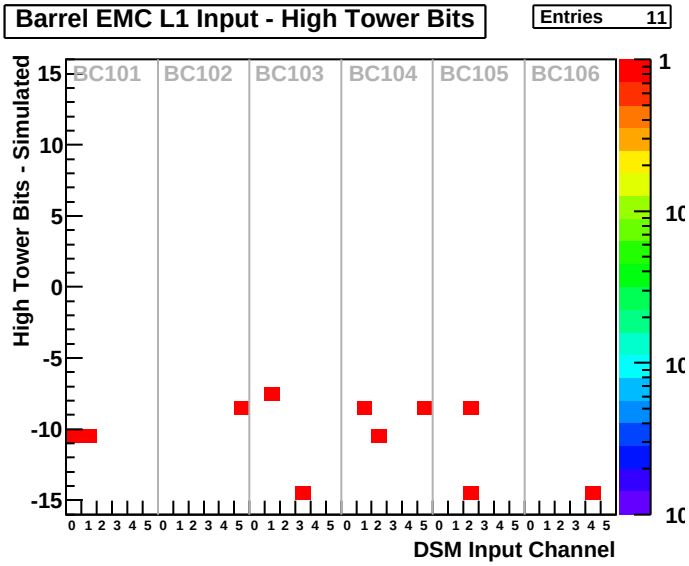

Barrel EMC L1 Input - Low Eta Sum
Entries 6.48e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

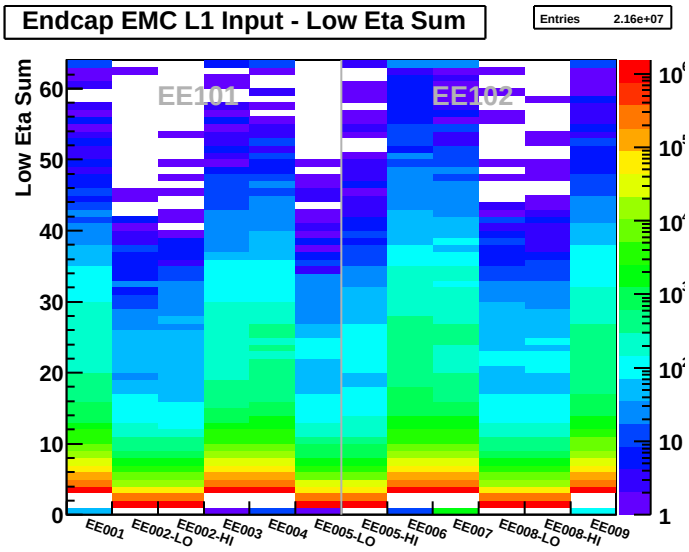

Barrel EMC L1 Input - Low Eta Sum
Entries 5808
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

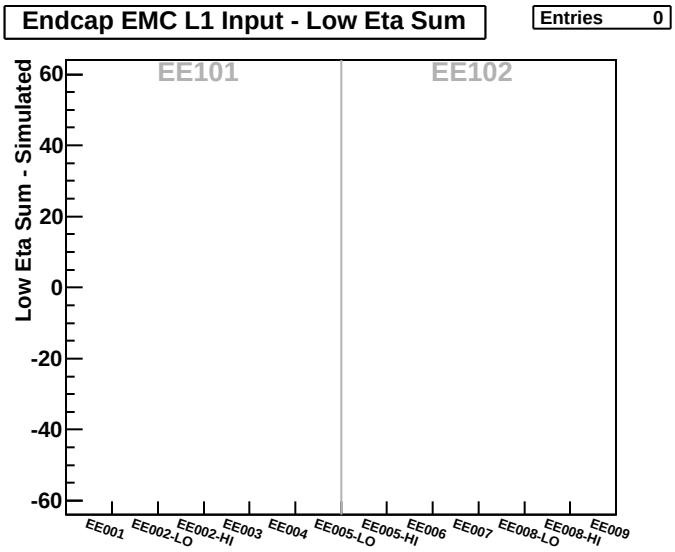

Barrel EMC L1 Input - High Eta Sum
Entries 6.48e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

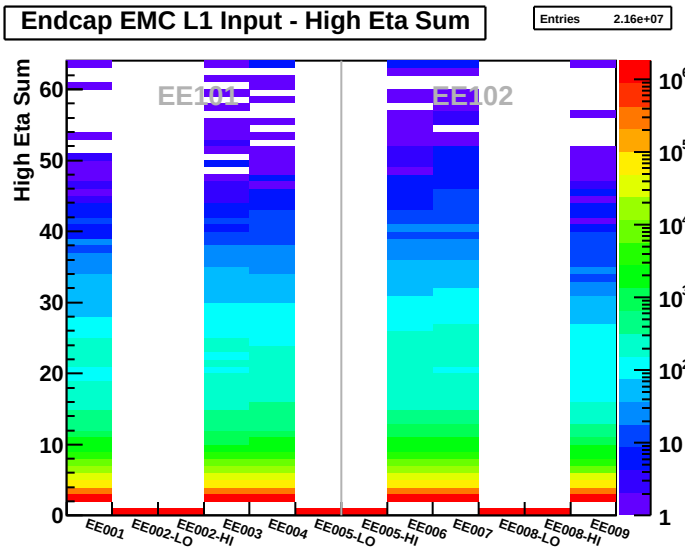

Barrel EMC L1 Input - High Eta Sum
Entries 376
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

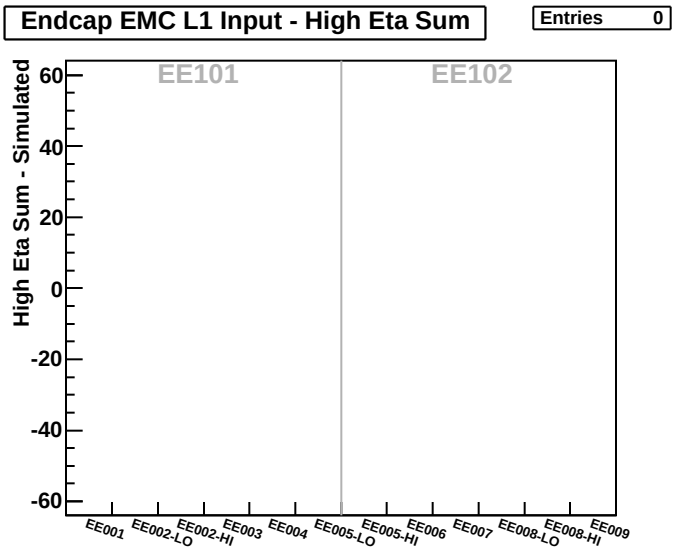

Barrel EMC L1 Input - High Tower Bits
Entries 6.48e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

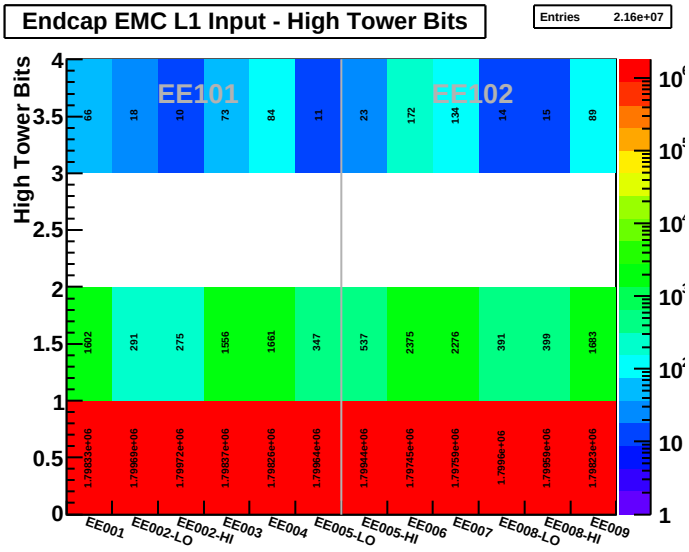

Barrel EMC L1 Input - High Tower Bits
Entries 11
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

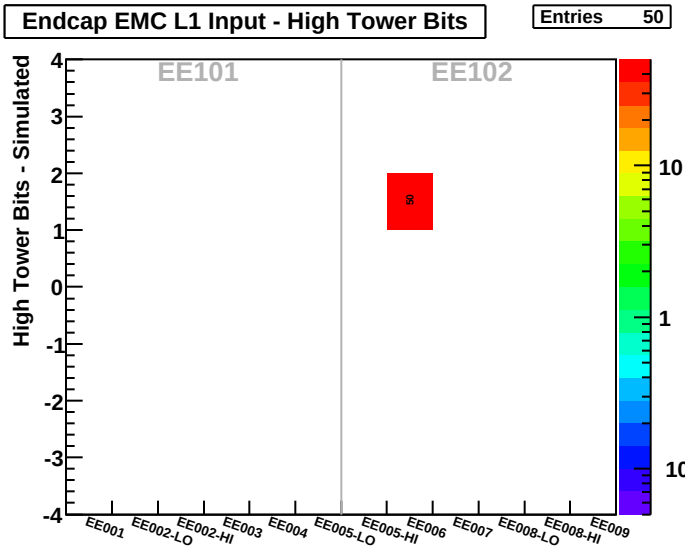

Endcap EMC L1 Input - Low Eta Sum
Entries 2.16e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

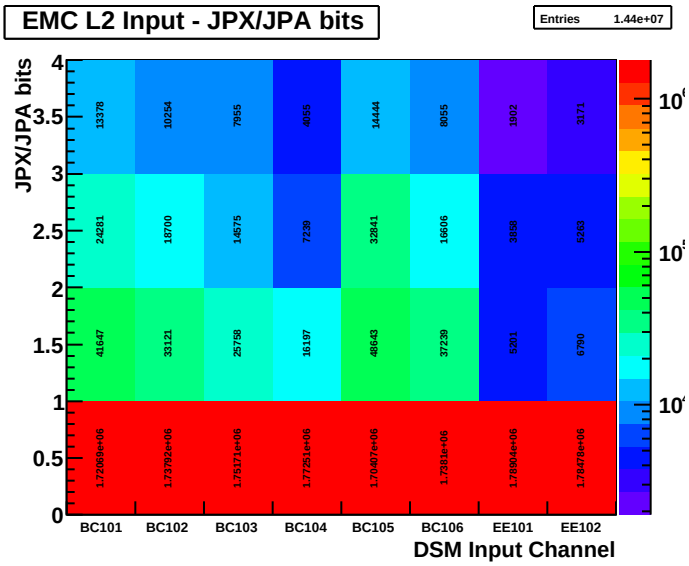

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

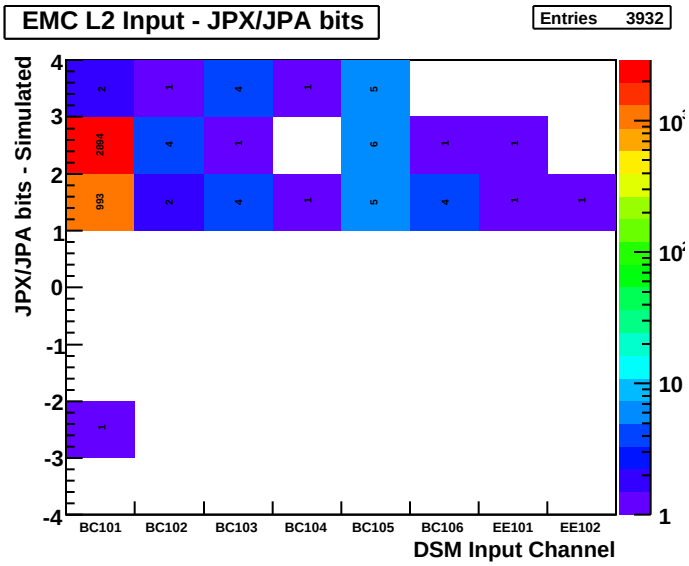

Endcap EMC L1 Input - High Eta Sum
Entries 2.16e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

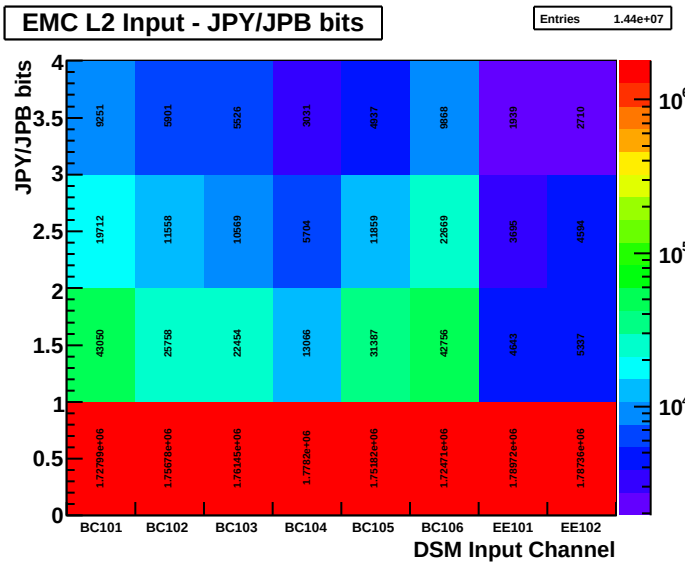

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

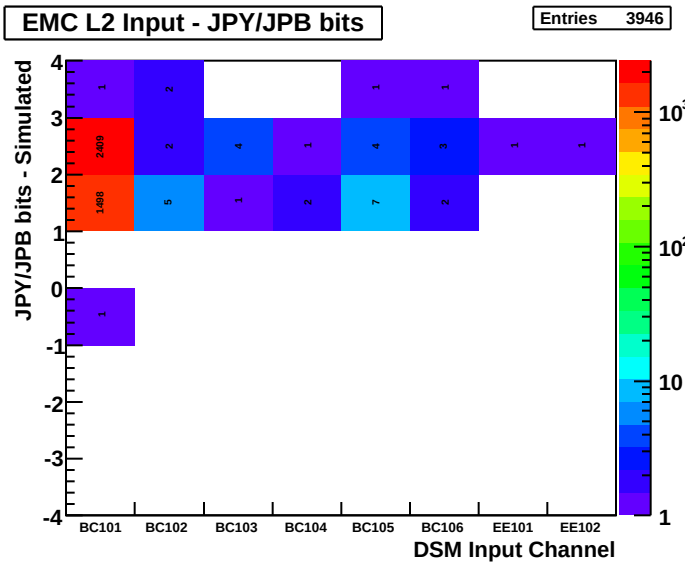

Endcap EMC L1 Input - High Tower Bits
Entries 2.16e+07
High Tower Bits
EE101
EE102
66 18 10 73 84 11 23 172 134 14 15 89
1602 291 275 1566 1661 347 537 2375 2276 391 399 1683
1.79833e+06 1.79969e+06 1.79972e+06 1.79837e+06 1.79826e+06 1.79964e+06 1.79944e+06 1.79745e+06 1.79759e+06 1.7996e+06 1.79959e+06 1.79823e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

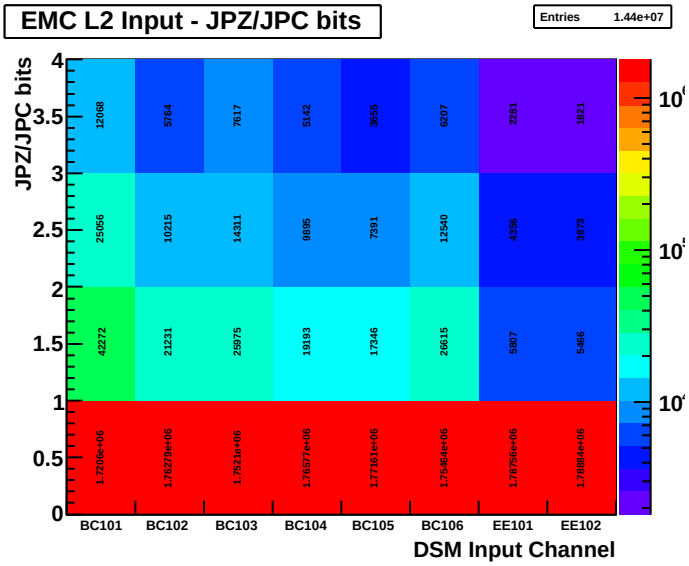

Endcap EMC L1 Input - High Tower Bits
Entries 50
High Tower Bits - Simulated
EE101
EE102
50
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

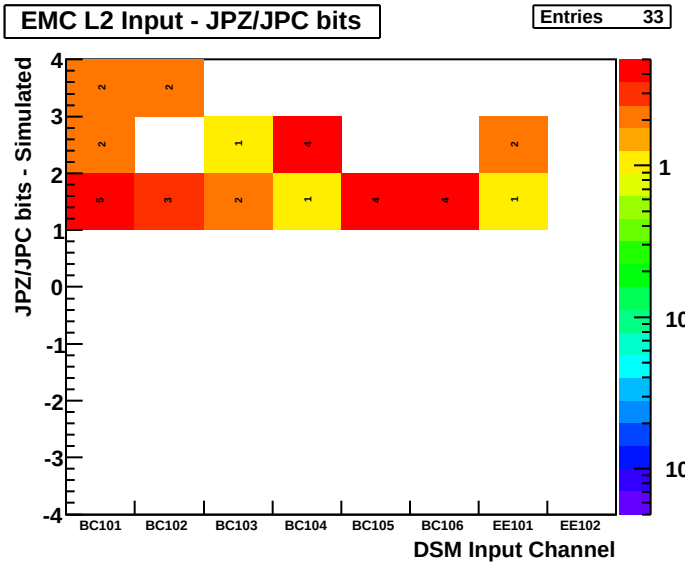

EMC L2 Input - JPX/JPA bits
Entries 1.44e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

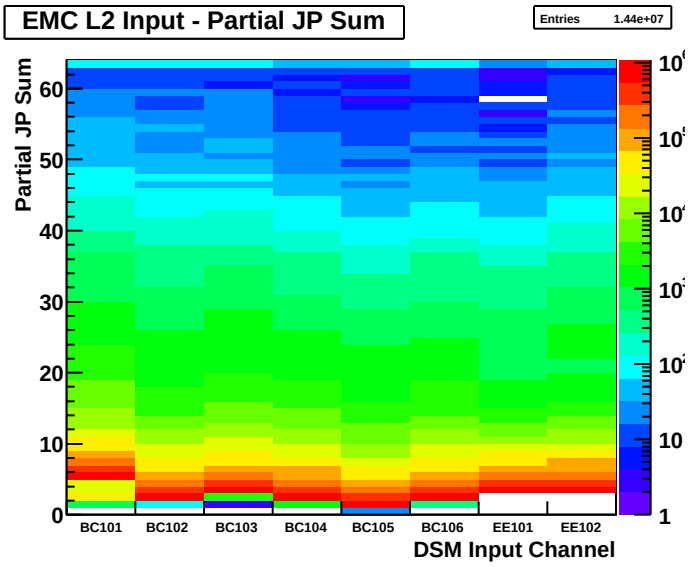

EMC L2 Input - JPX/JPA bits
Entries 3932
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

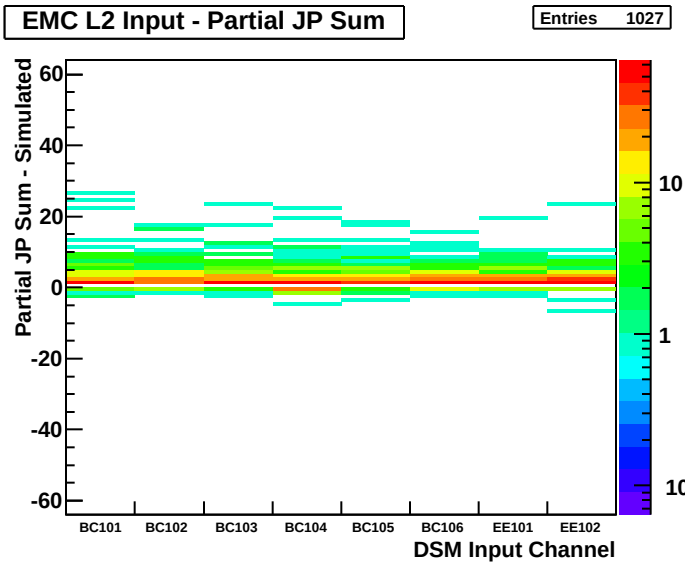

EMC L2 Input - JPY/JPB bits
Entries 1.44e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

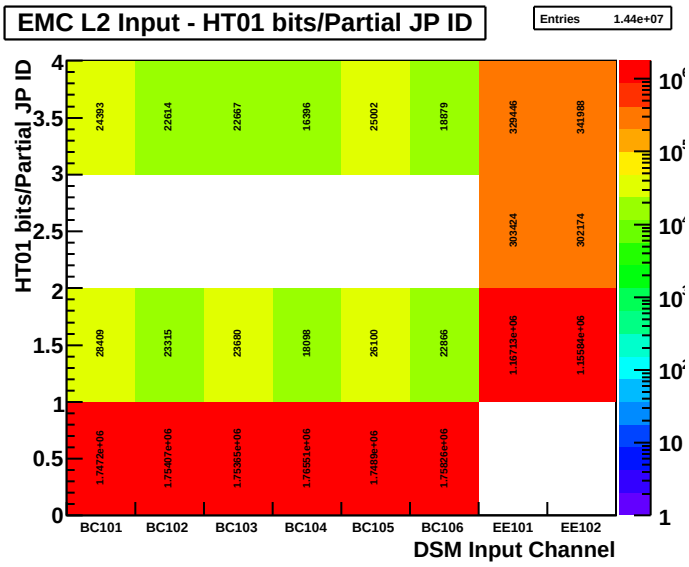

EMC L2 Input - JPY/JPB bits
Entries 3946
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

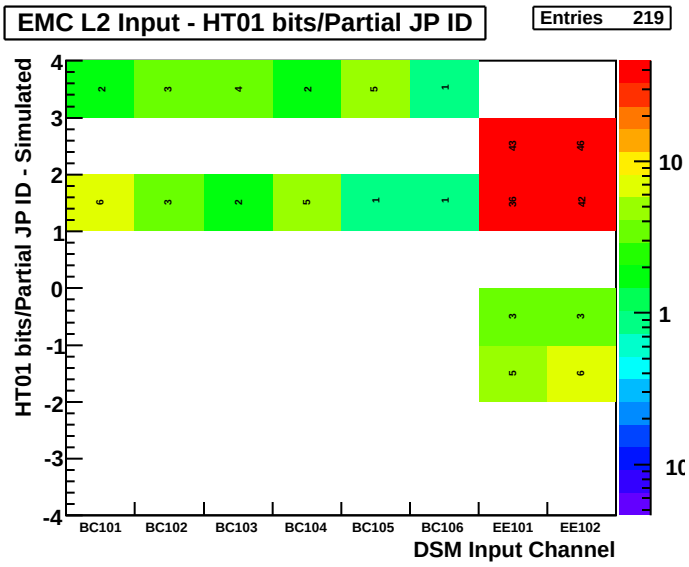

EMC L2 Input - JPZ/JPC bits
Entries 1.44e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

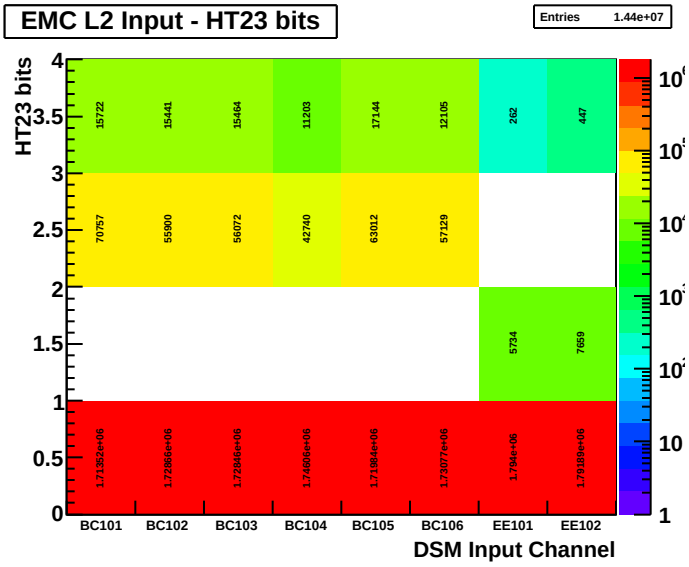

EMC L2 Input - JPZ/JPC bits
Entries 33
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

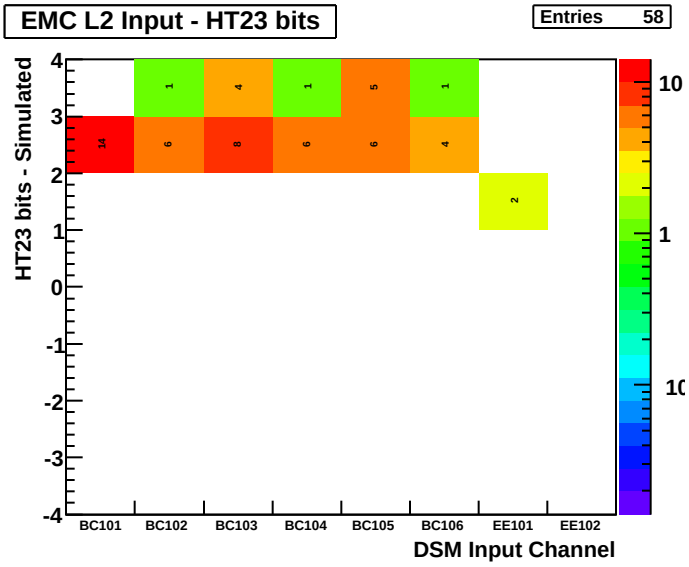

EMC L2 Input - Partial JP Sum
Entries 1.44e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

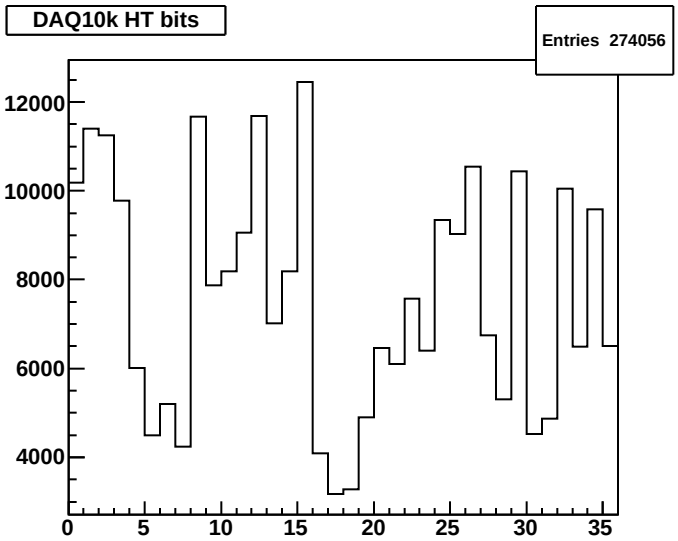

EMC L2 Input - Partial JP Sum
Entries 1027
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

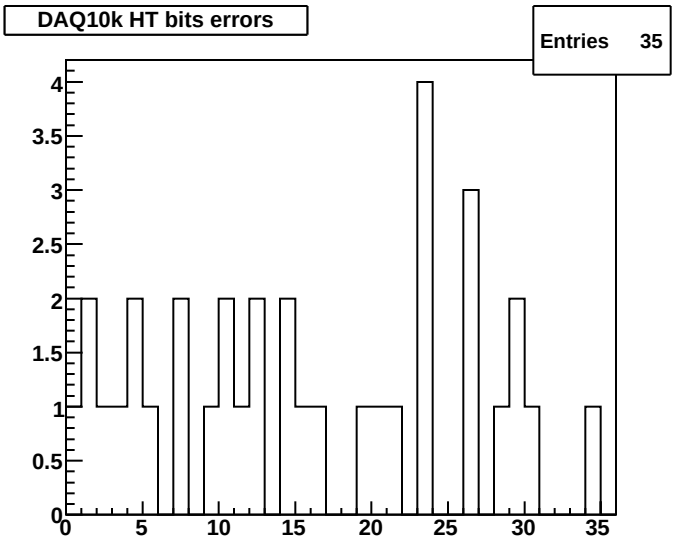

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.44e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

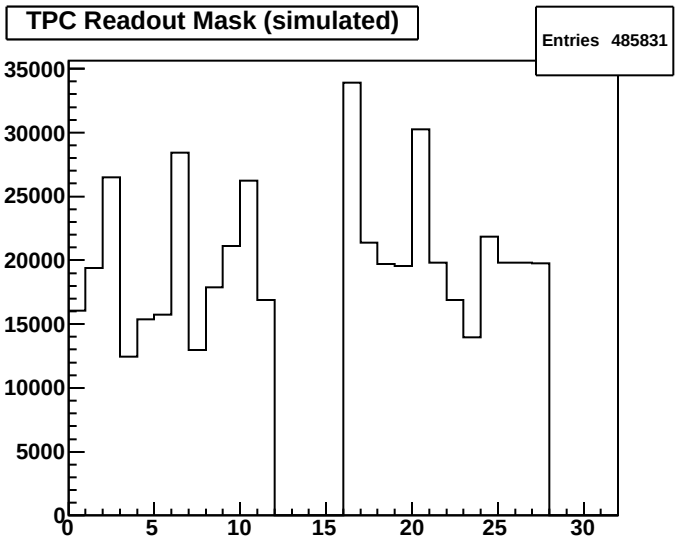

EMC L2 Input - HT01 bits/Partial JP ID
Entries 219
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

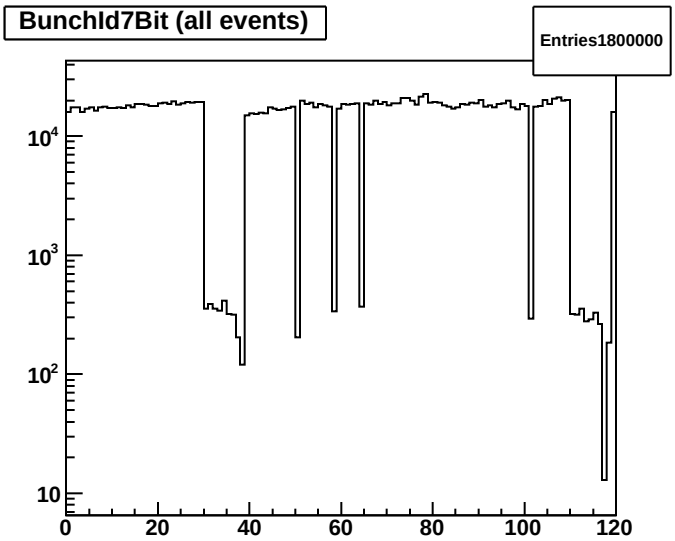

EMC L2 Input - HT23 bits
Entries 1.44e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

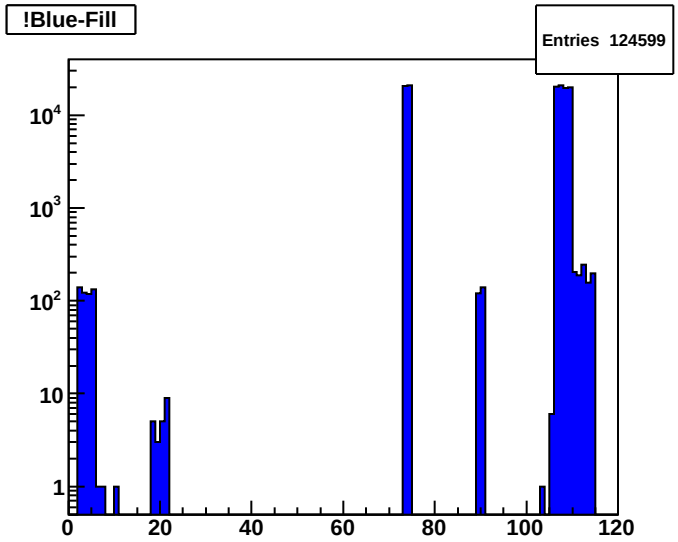

EMC L2 Input - HT23 bits
Entries 58
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

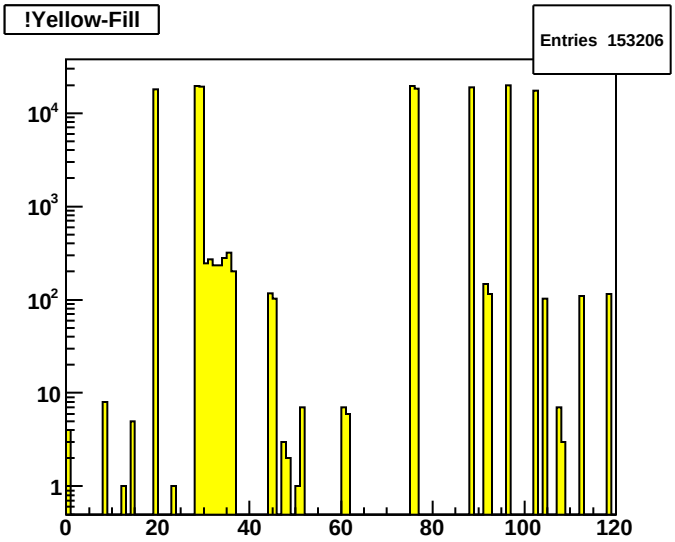

DAQ10k HT bits
Entries 274056

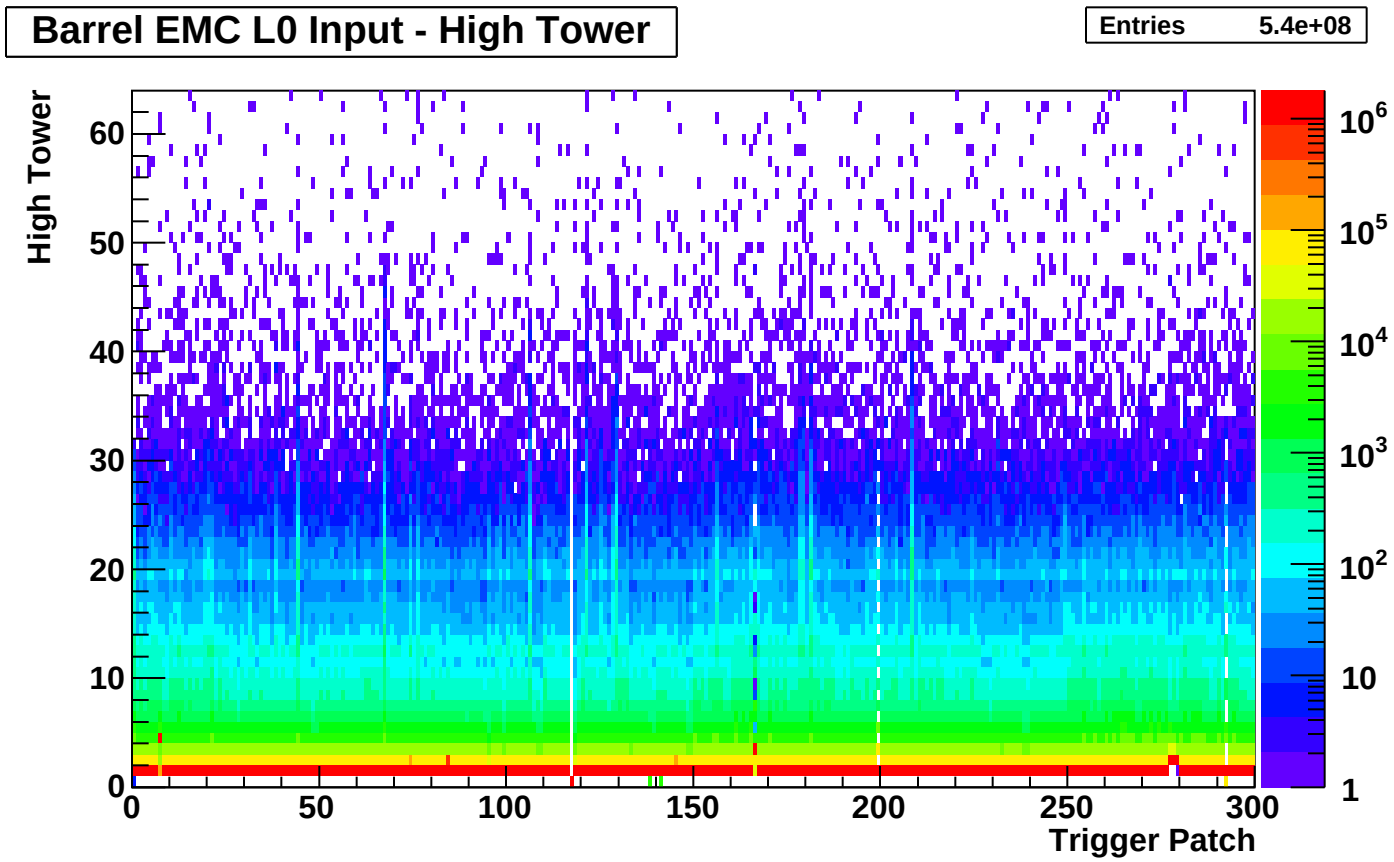

DAQ10k HT bits errors
Entries 35

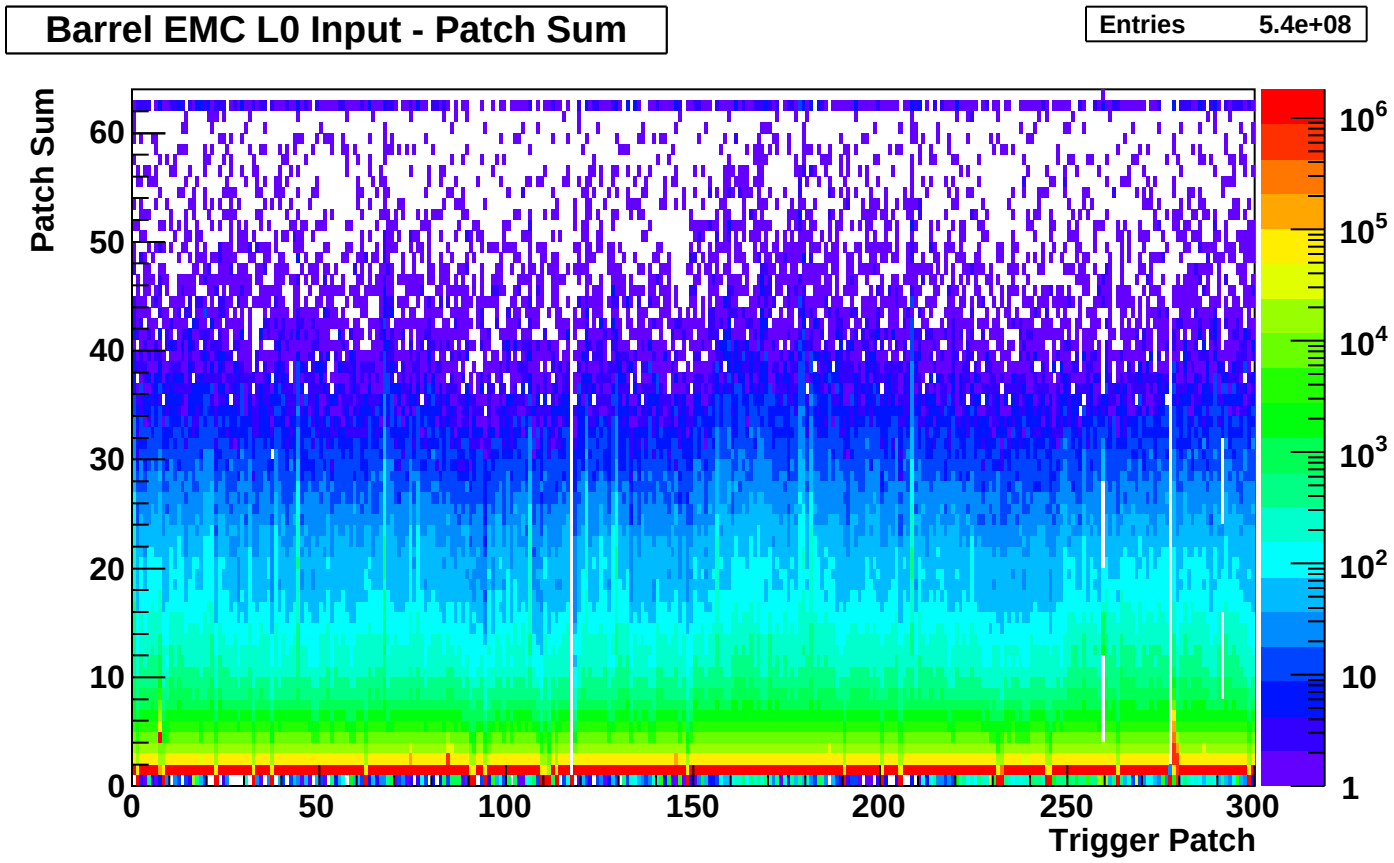

TPC Readout Mask (simulated)
Entries 485831

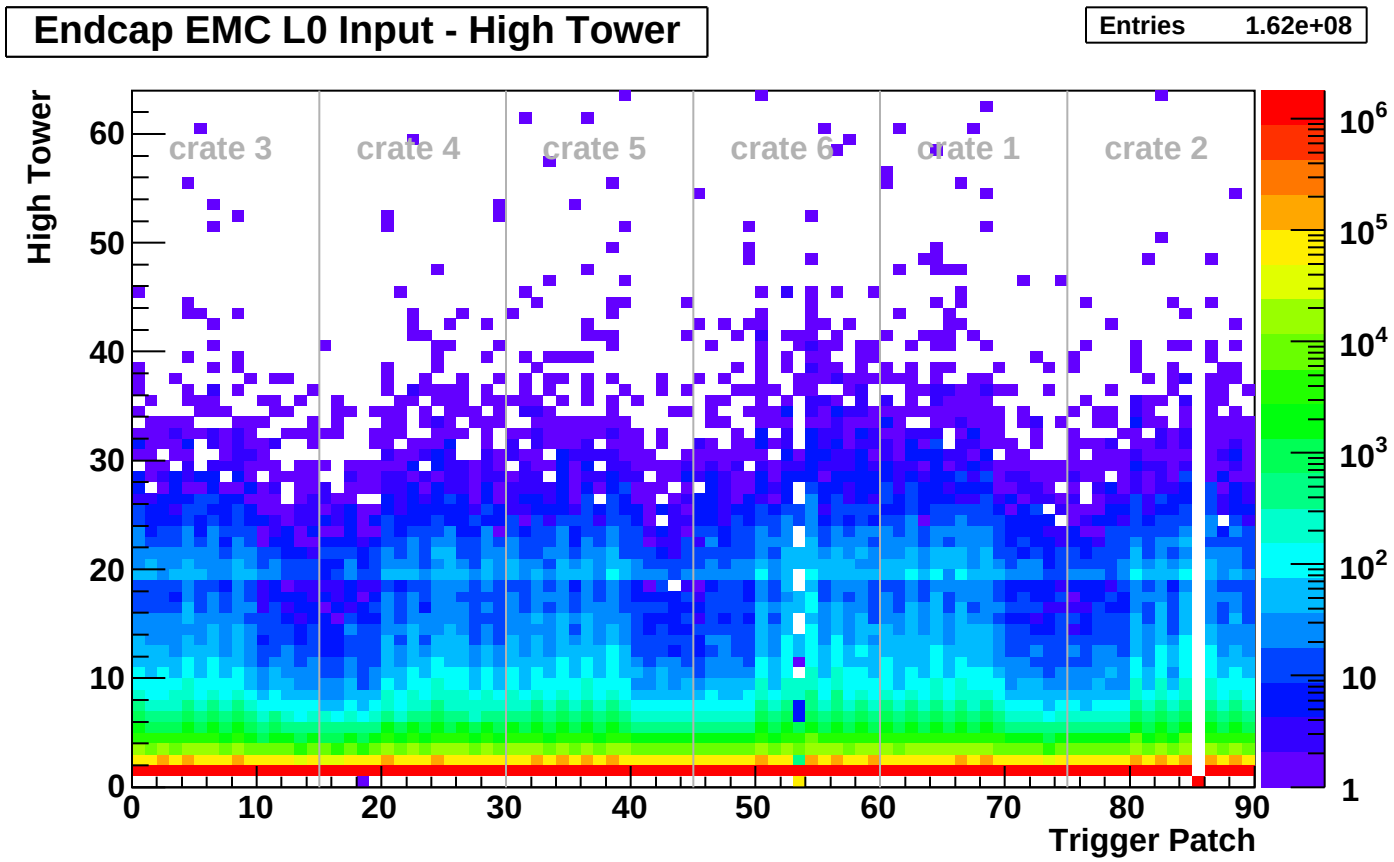

BunchId7Bit (all events)
Entries1800000

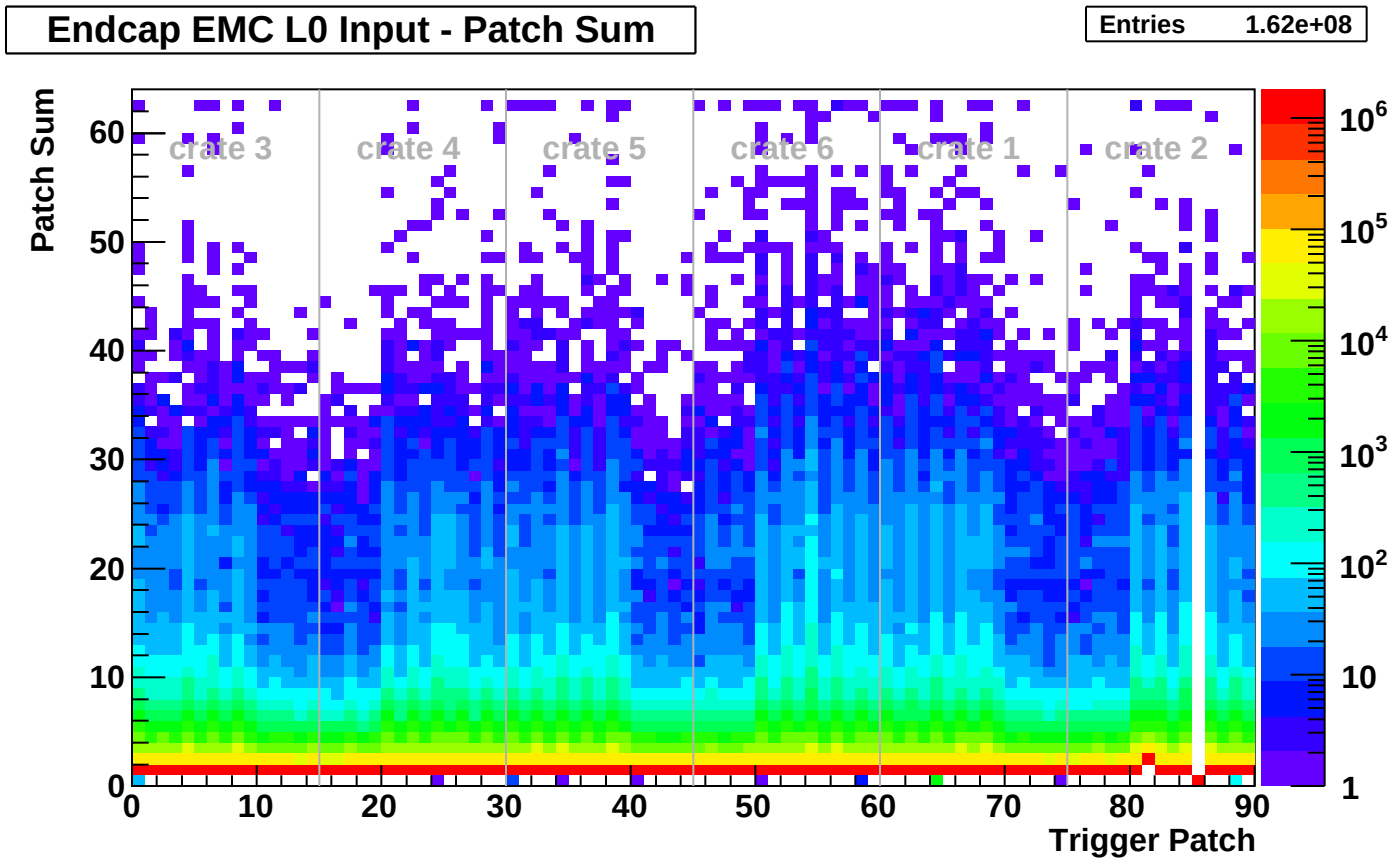

!Blue-Fill
Entries 124599

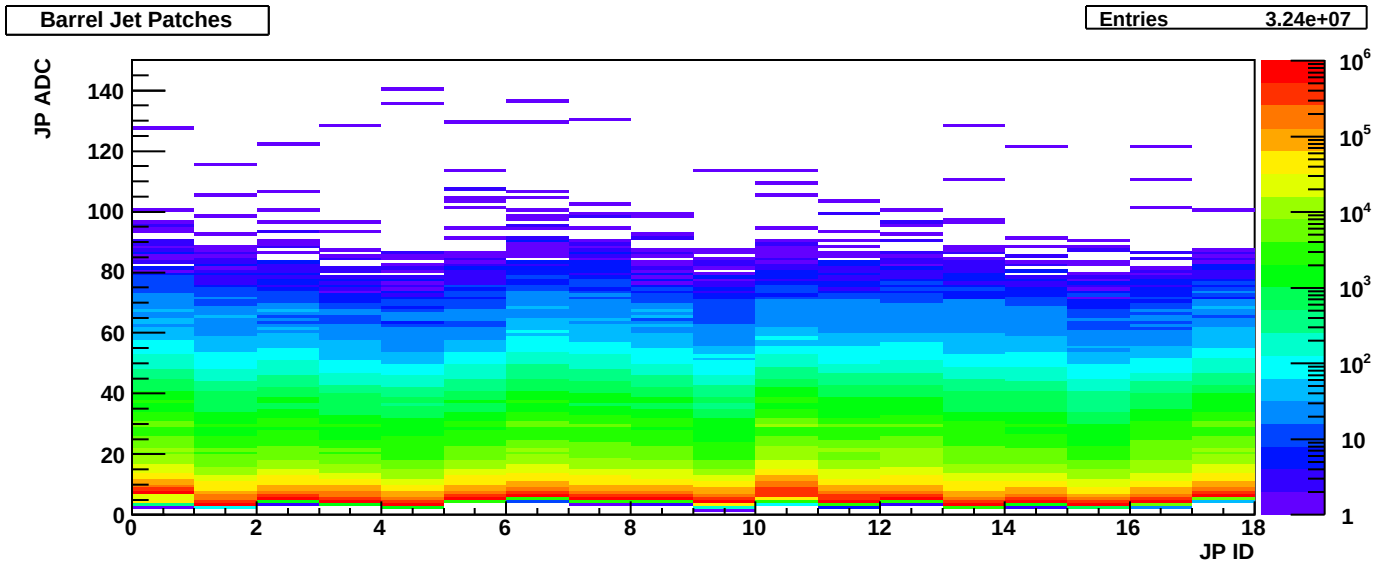

!Yellow-Fill
Entries 153206

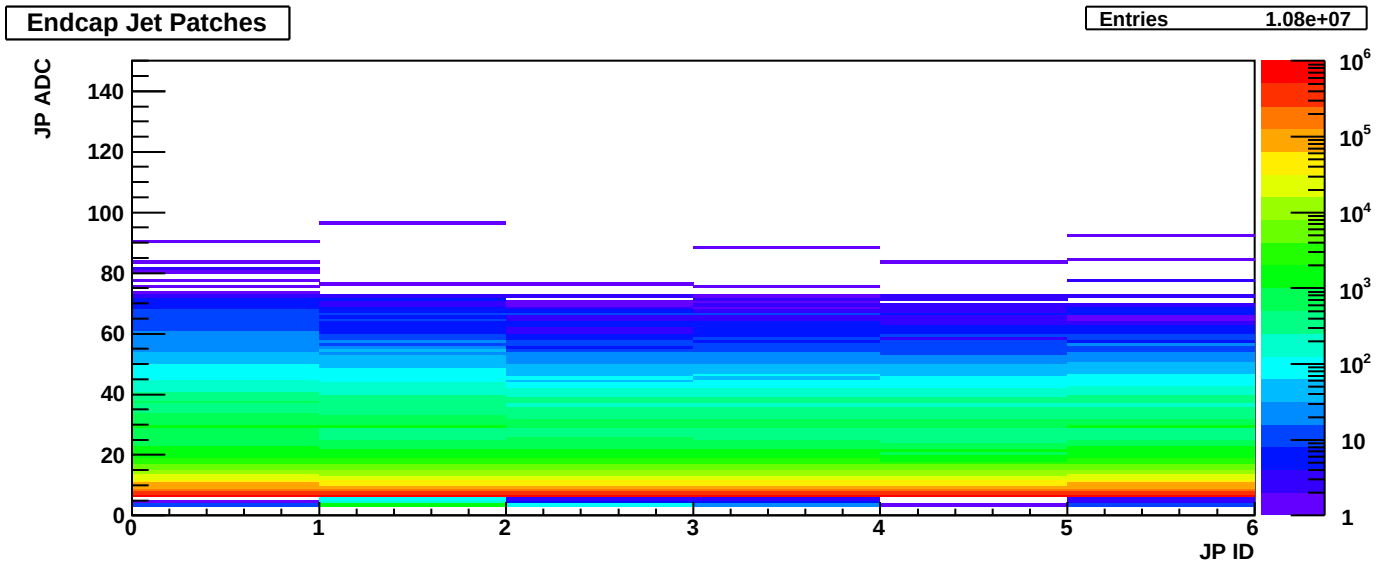

Barrel EMC L0 Input - High Tower
Entries 5.4e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

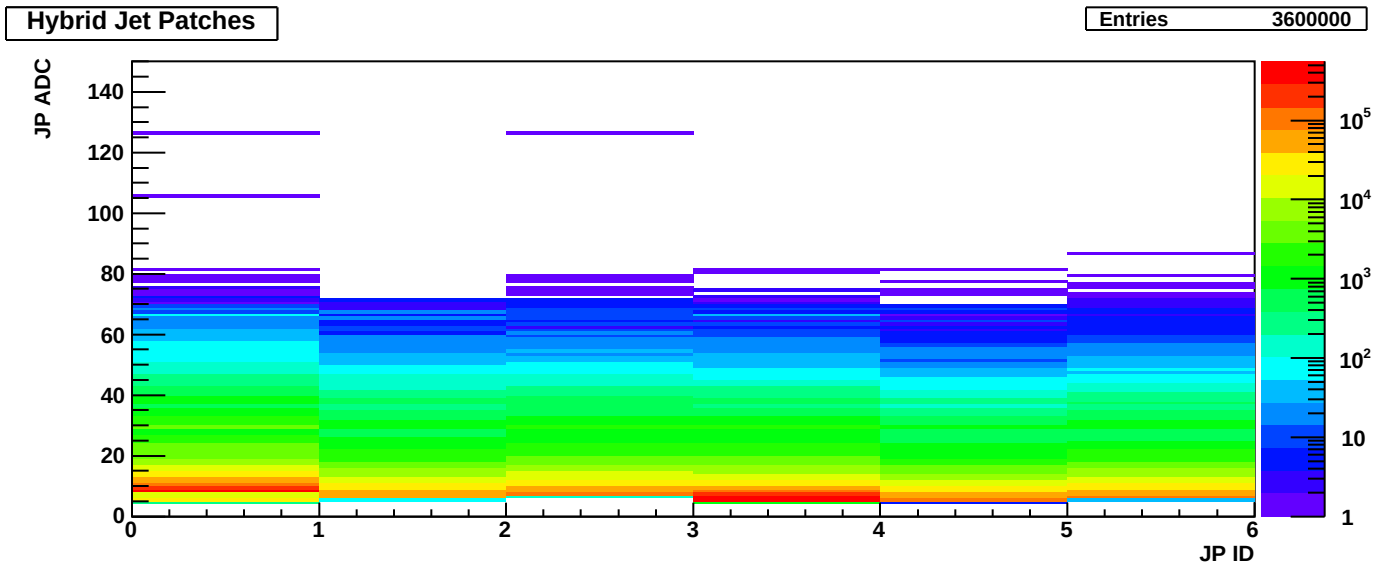

Barrel EMC L0 Input - Patch Sum
Entries 5.4e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

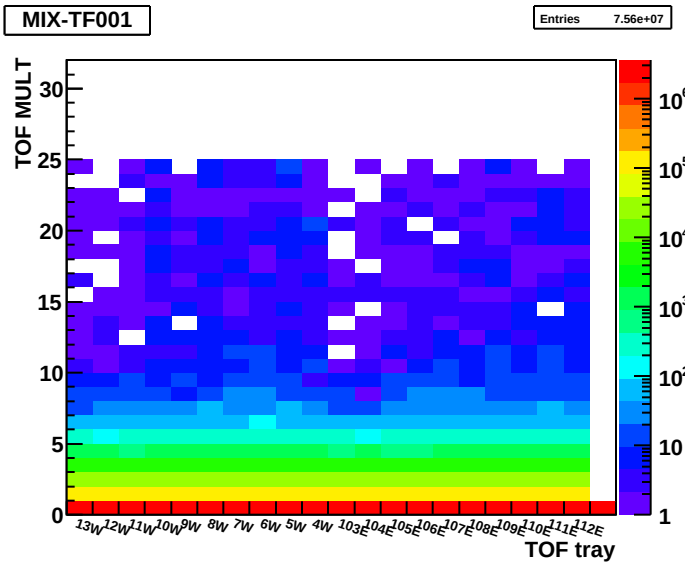

Endcap EMC L0 Input - High Tower
Entries 1.62e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

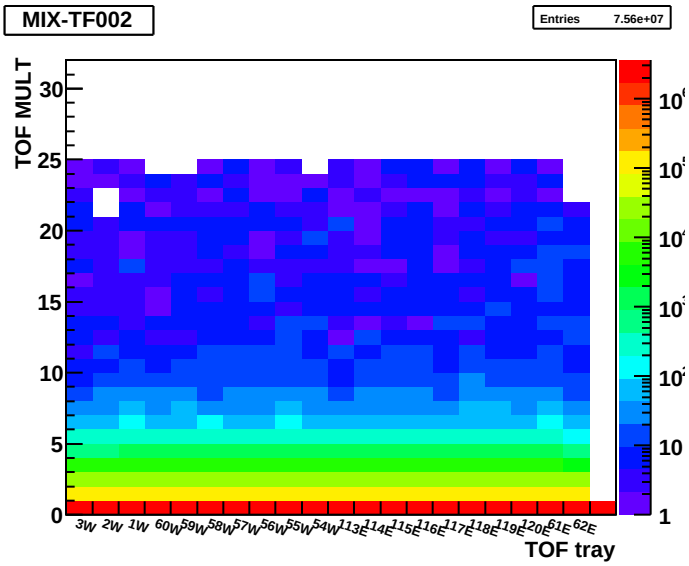

Endcap EMC L0 Input - Patch Sum
Entries 1.62e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

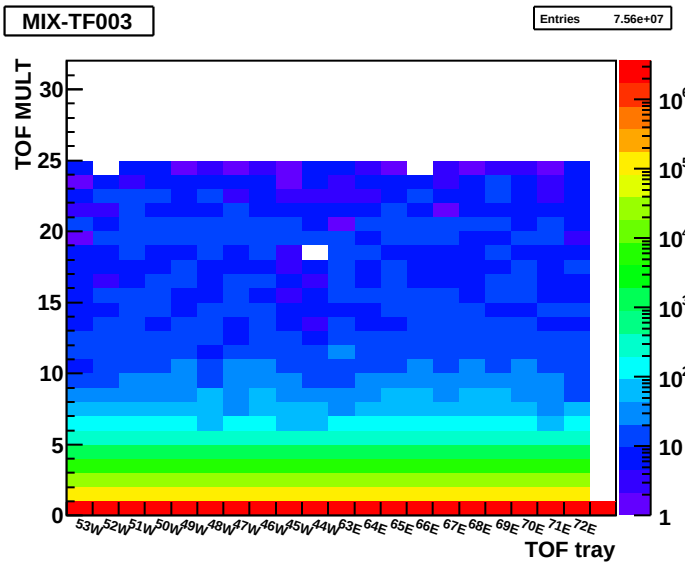

Barrel Jet Patches
Entries 3.24e+07
JP ADC
JP ID

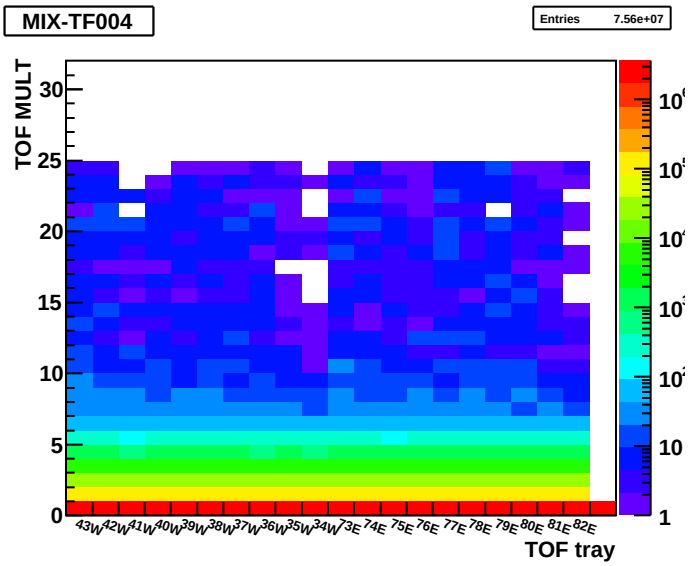

Endcap Jet Patches
Entries 1.08e+07
JP ADC
JP ID

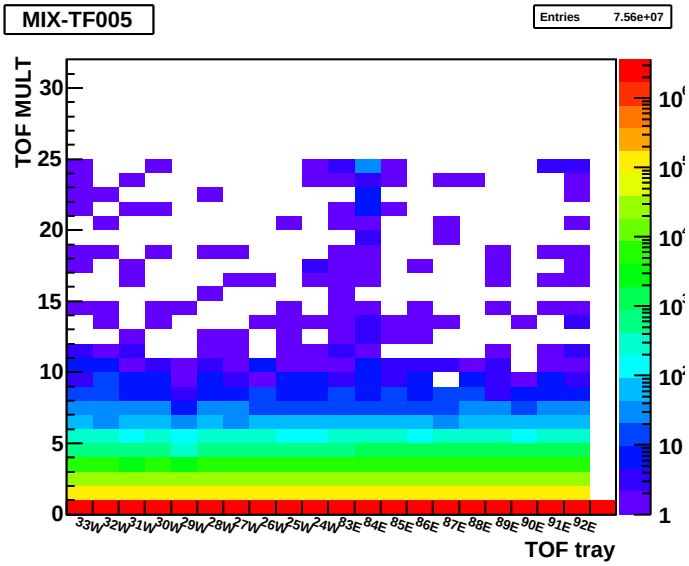

Hybrid Jet Patches
Entries 3600000
JP ADC
JP ID

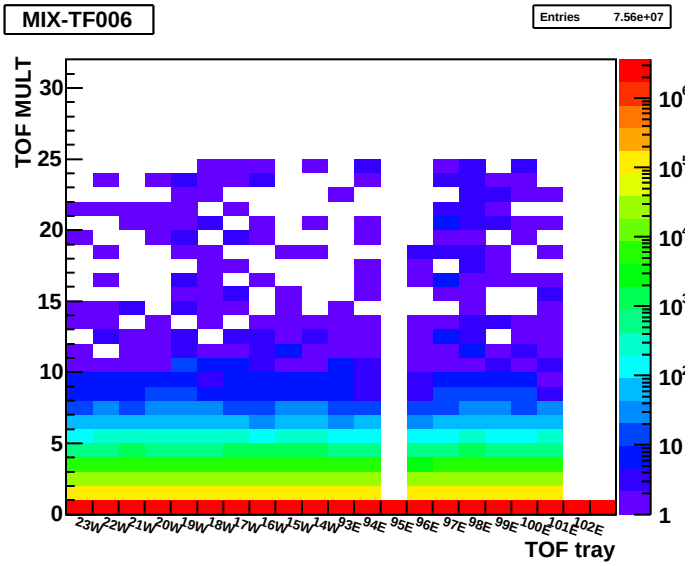

MIX-TF001
Entries 7.56e+07
TOF MULT
TOF tray

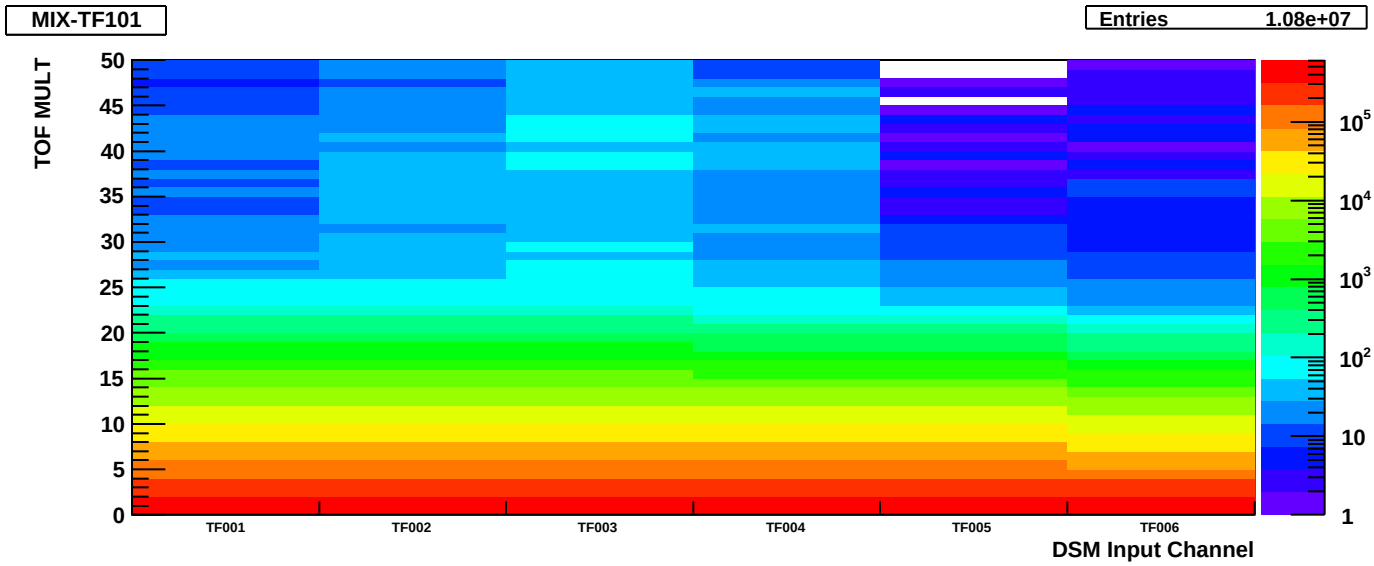

MIX-TF002
Entries 7.56e+07
TOF MULT
TOF tray

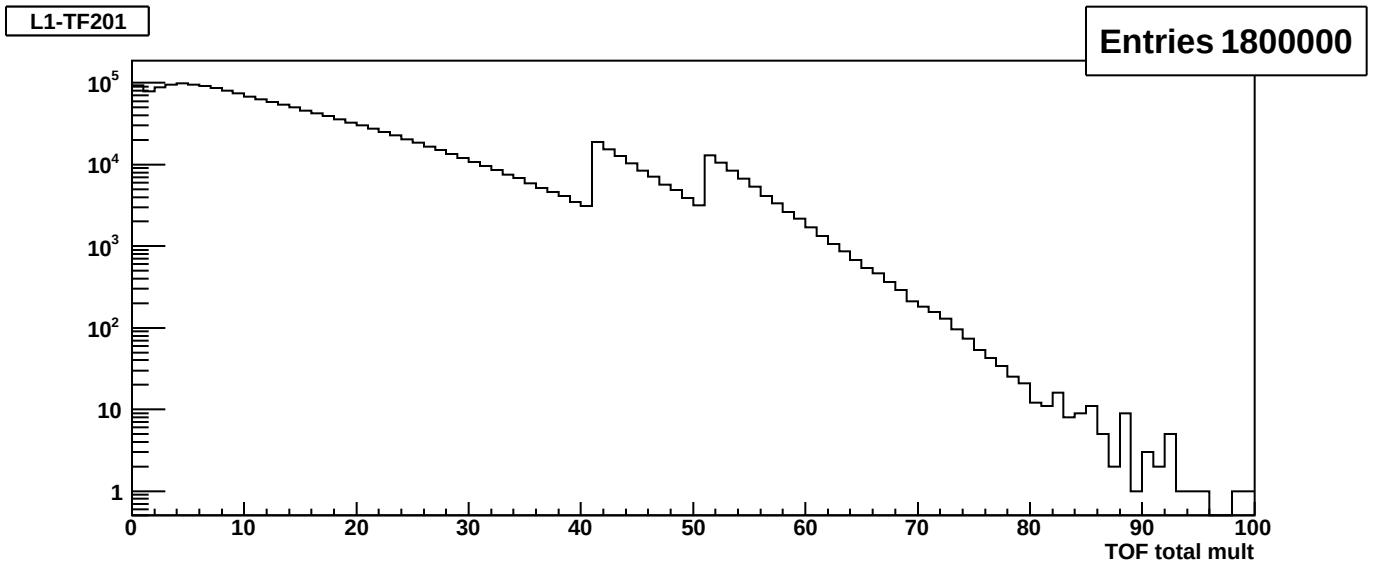

MIX-TF003
Entries 7.56e+07
TOF MULT
TOF tray

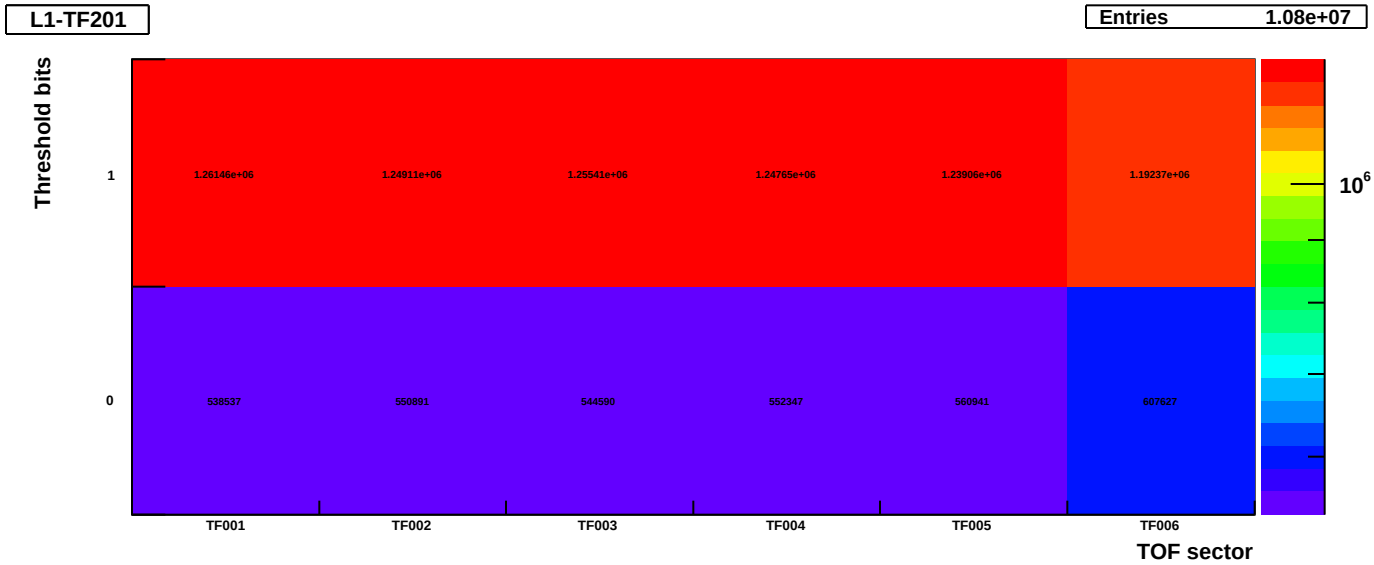

MIX-TF004
Entries 7.56e+07
TOF MULT
TOF tray

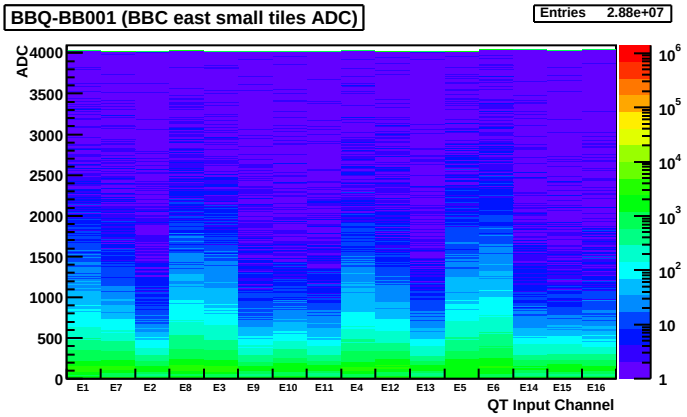

MIX-TF005
Entries 7.56e+07
TOF MULT
TOF tray

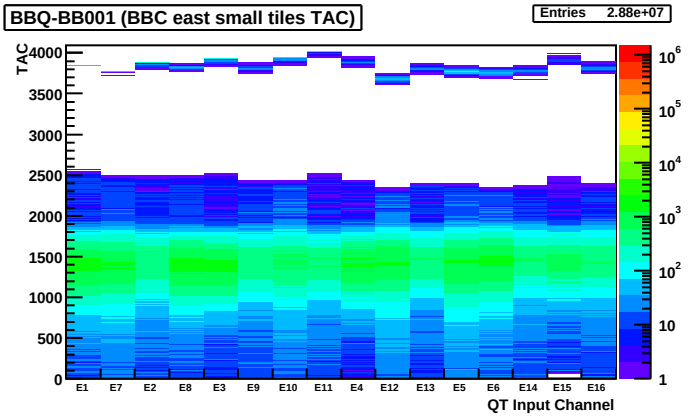

MIX-TF006
Entries 7.56e+07
TOF MULT
TOF tray

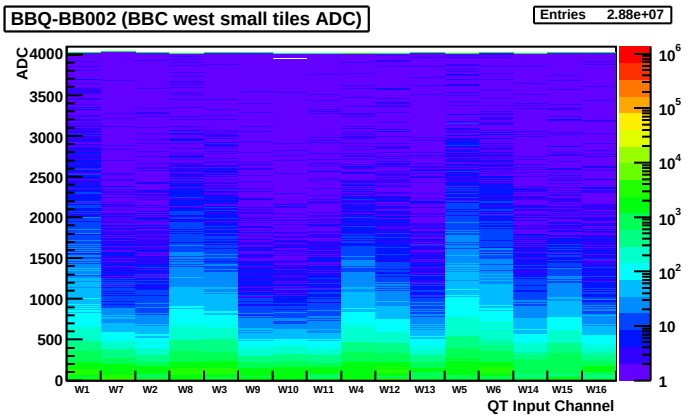

MIX-TF101
Entries 1.08e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

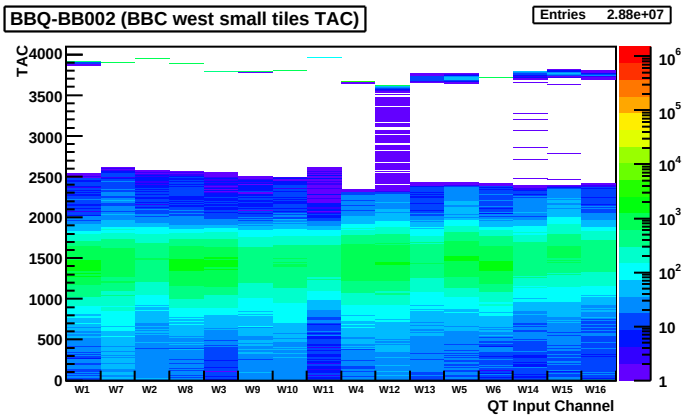

L1-TF201
Entries 1800000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

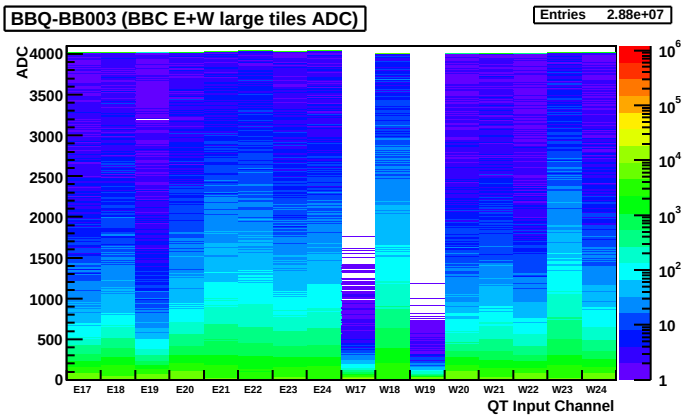

L1-TF201
Entries 1.08e+07
Threshold bits
1
0
1.26146e+06
1.24911e+06
1.25541e+06
1.24765e+06
1.23906e+06
1.19237e+06
538537
550891
544590
552347
560941
607627
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

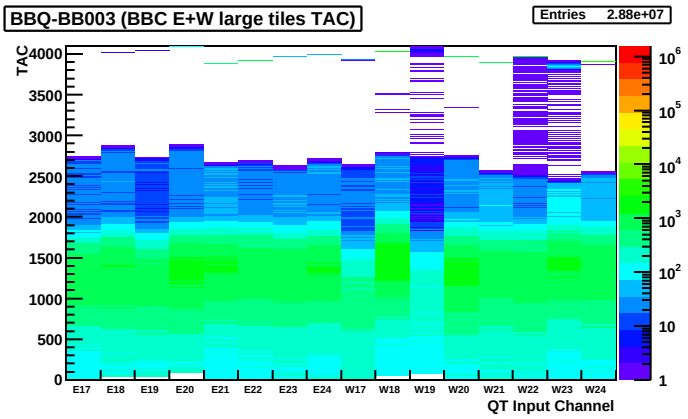

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

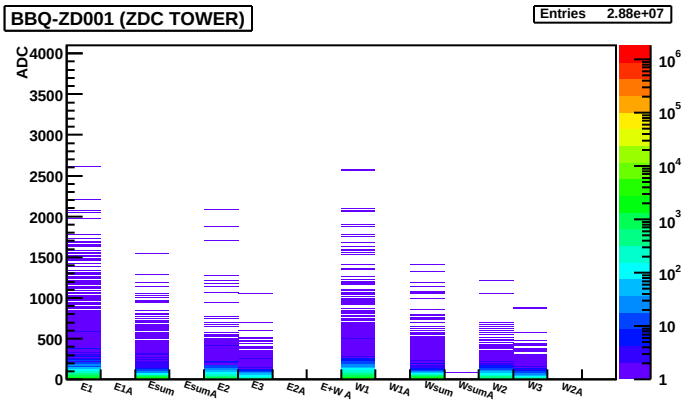

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

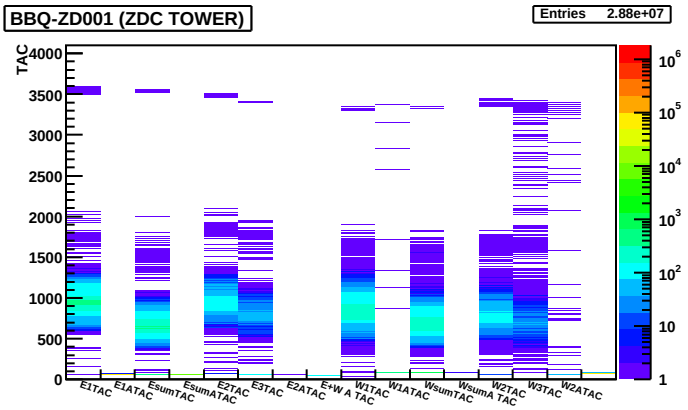

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

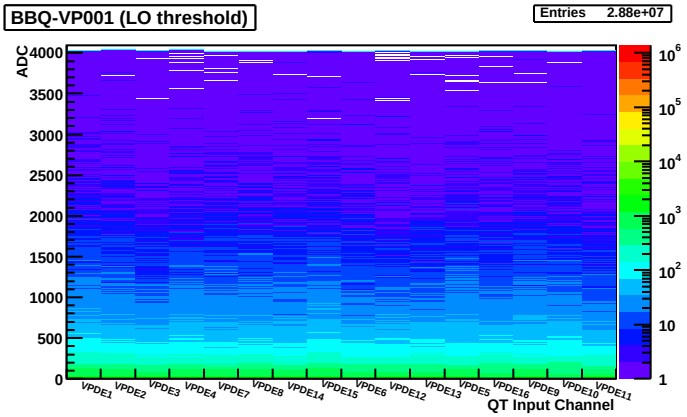

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

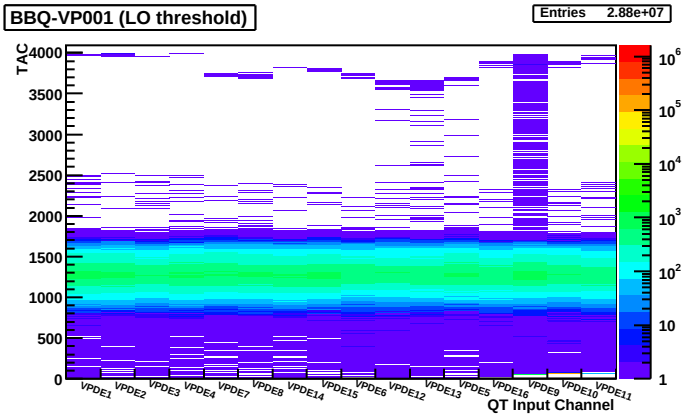

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.88e+07
ADC
QT Input Channel

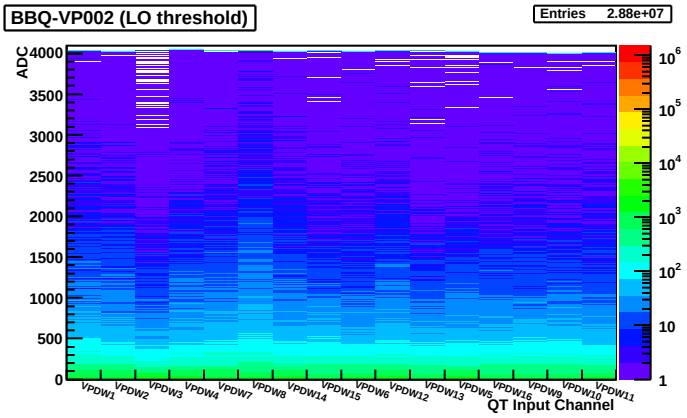

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.88e+07
TAC
QT Input Channel

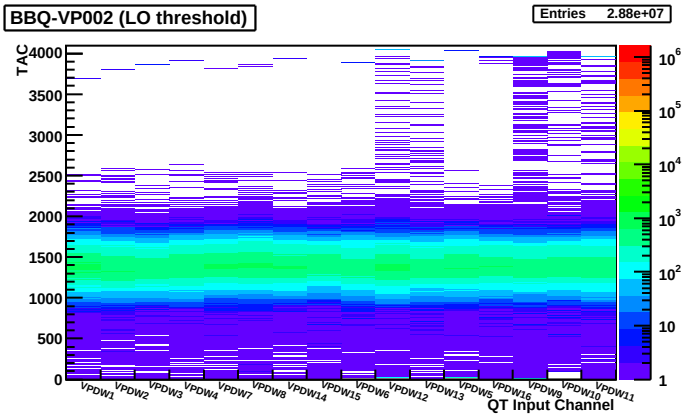

BBQ-ZD001 (ZDC TOWER)
Entries 2.88e+07
ADC

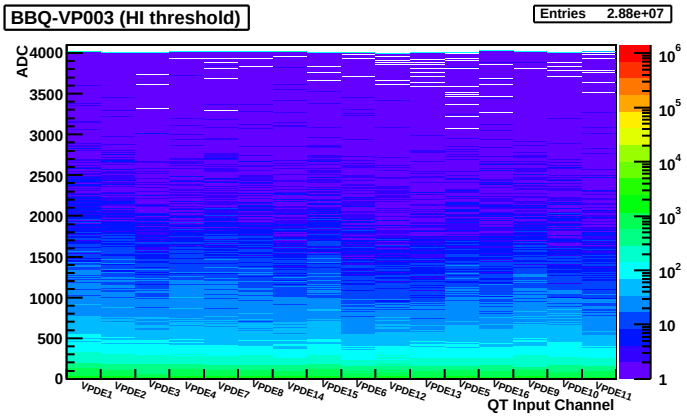

BBQ-ZD001 (ZDC TOWER)
Entries 2.88e+07
TAC

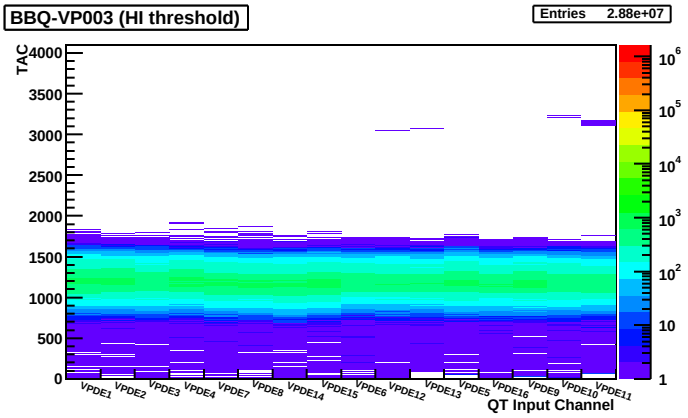

BBQ-VP001 (LO threshold)
Entries 2.88e+07
ADC
QT Input Channel

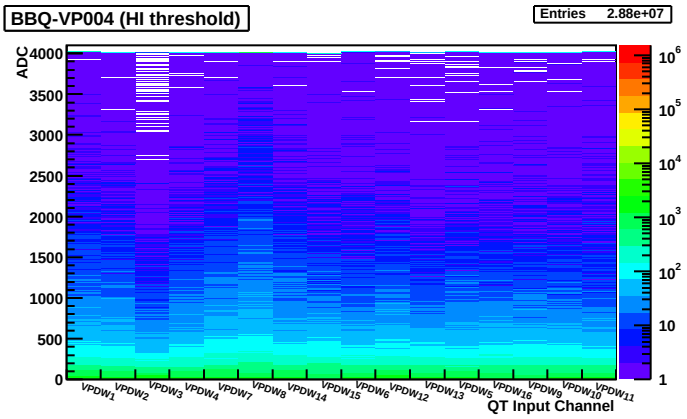

BBQ-VP001 (LO threshold)
Entries 2.88e+07
TAC
QT Input Channel

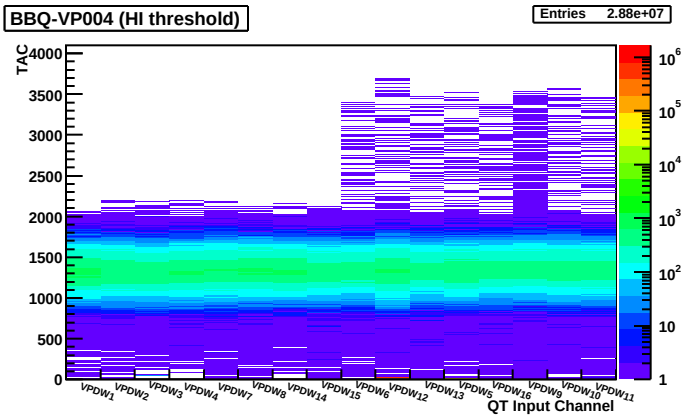

BBQ-VP002 (LO threshold)
Entries 2.88e+07
ADC
QT Input Channel

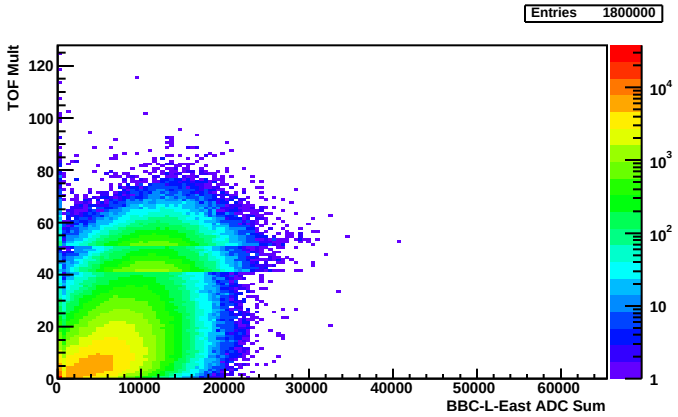

BBQ-VP002 (LO threshold)
Entries 2.88e+07
TAC
QT Input Channel

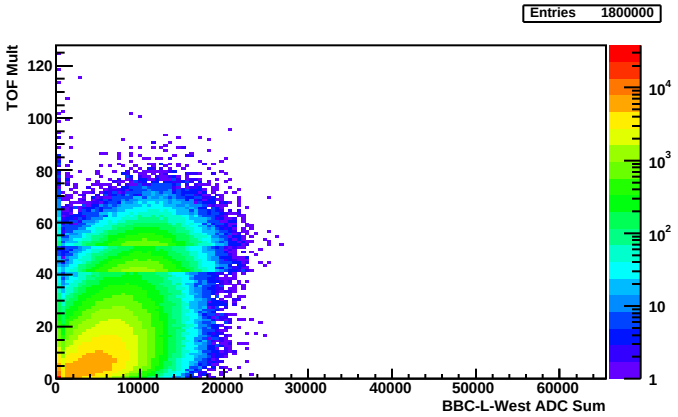

BBQ-VP003 (HI threshold)
Entries 2.88e+07
ADC
QT Input Channel

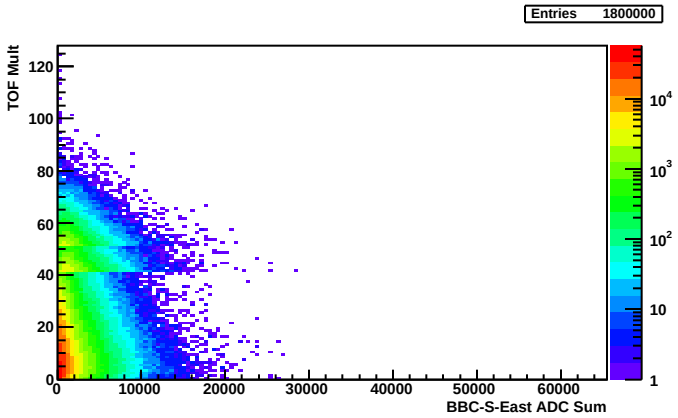

BBQ-VP003 (HI threshold)
Entries 2.88e+07
TAC
QT Input Channel

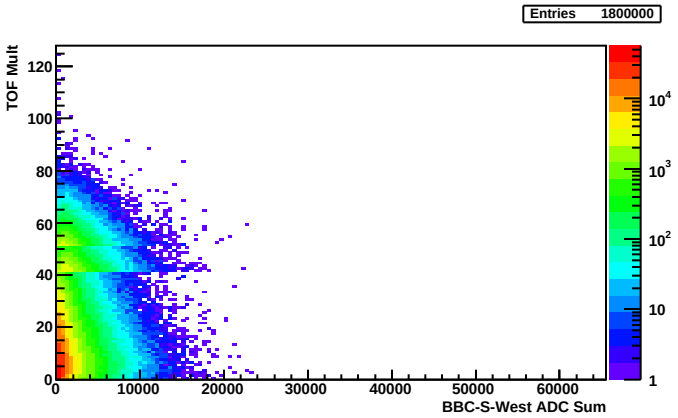

BBQ-VP004 (HI threshold)
Entries 2.88e+07
ADC
QT Input Channel

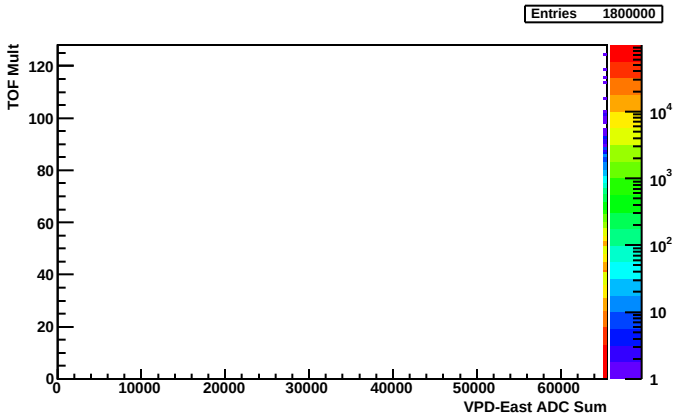

BBQ-VP004 (HI threshold)
Entries 2.88e+07
TAC
QT Input Channel

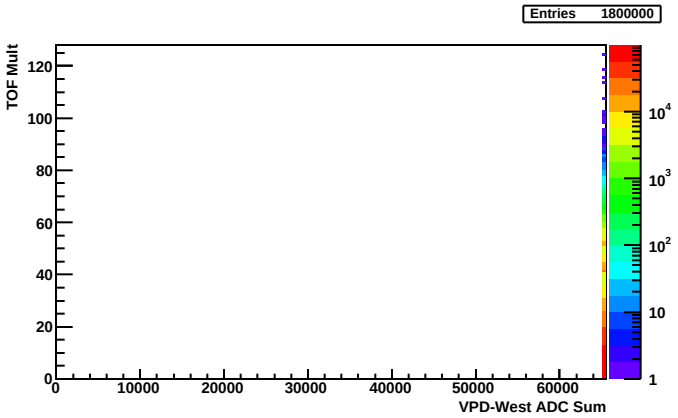

Entries 1800000
TOF Mult
BBC-L-East ADC Sum

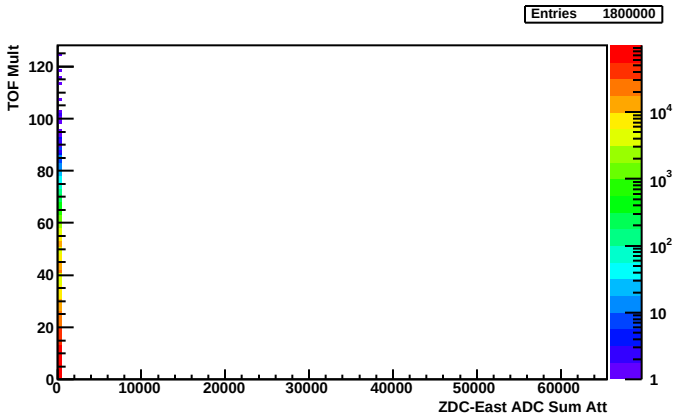

Entries 1800000
TOF Mult
BBC-L-West ADC Sum

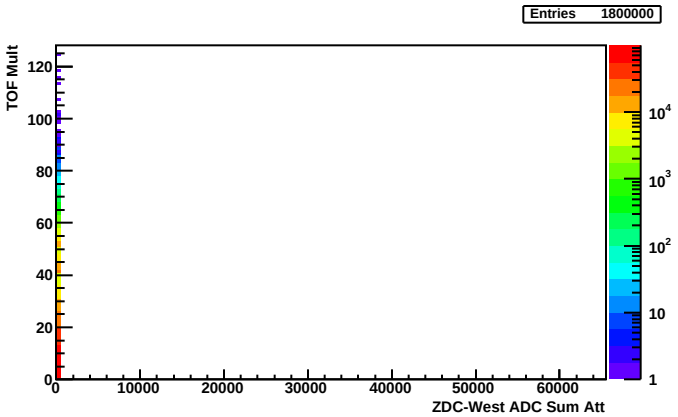

Entries 1800000
TOF Mult
BBC-S-East ADC Sum

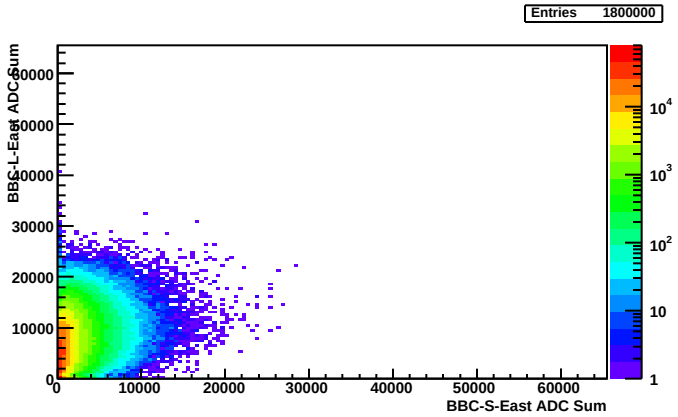

Entries 1800000
TOF Mult
BBC-S-West ADC Sum

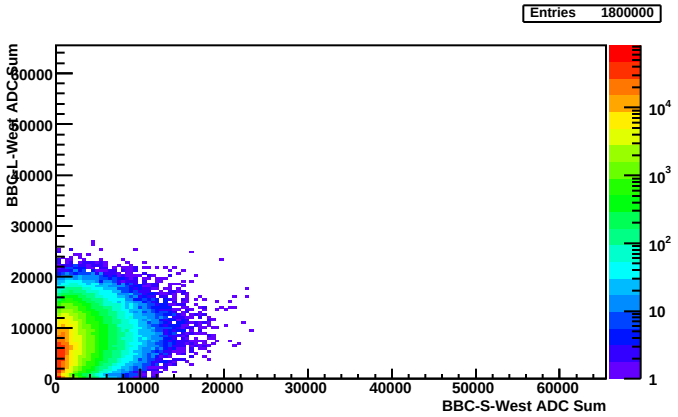

Entries 1800000
TOF Mult
VPD-East ADC Sum

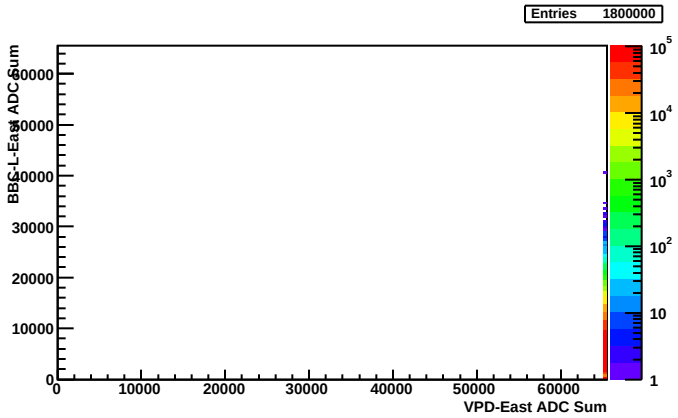

Entries 1800000
TOF Mult
VPD-West ADC Sum

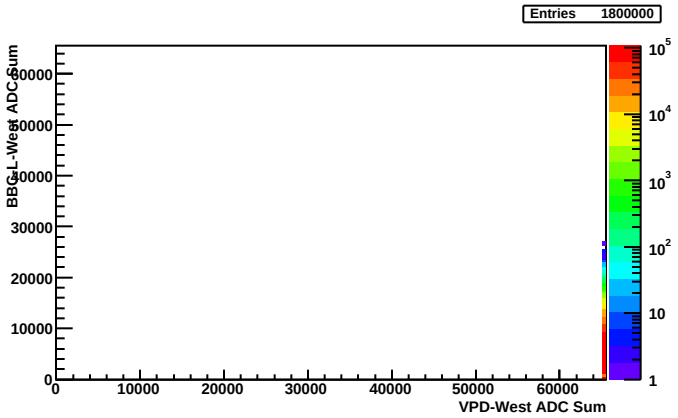

Entries 1800000
TOF Mult
ZDC-East ADC Sum Att

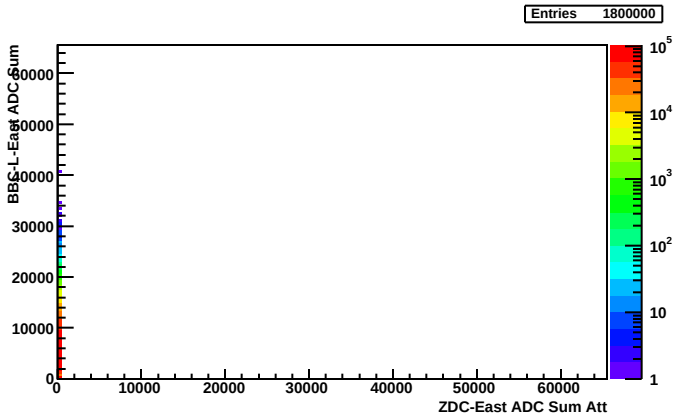

Entries 1800000
TOF Mult
ZDC-West ADC Sum Att

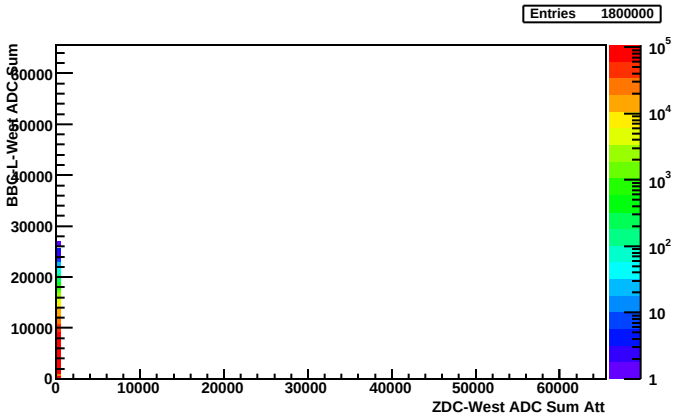

Entries 1800000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

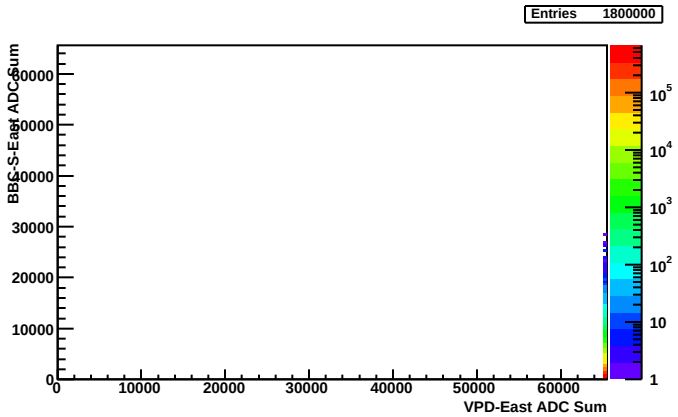

Entries 1800000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

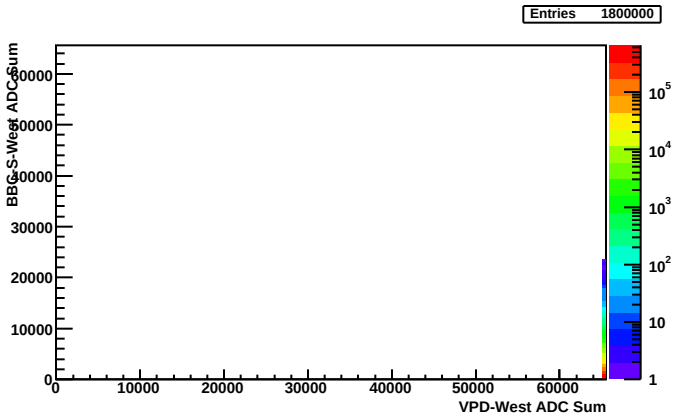

Entries 1800000
BBC-L-East ADC Sum
VPD-East ADC Sum

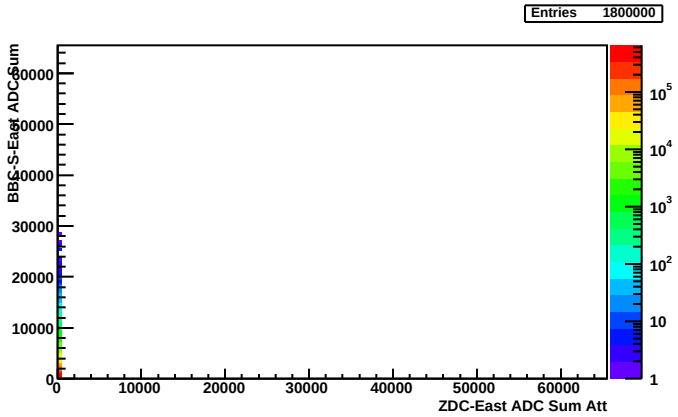

Entries 1800000
BBC-L-West ADC Sum
VPD-West ADC Sum

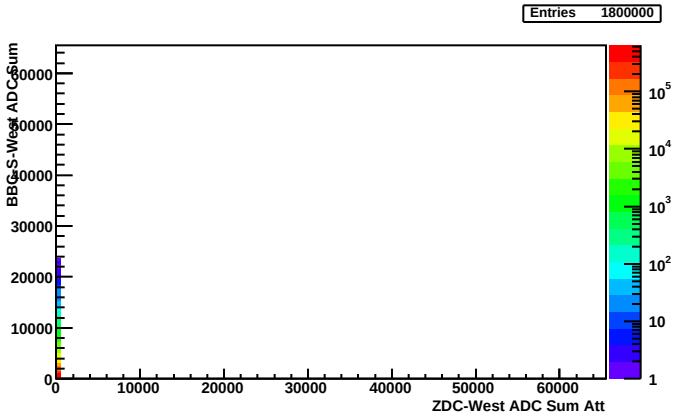

Entries 1800000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

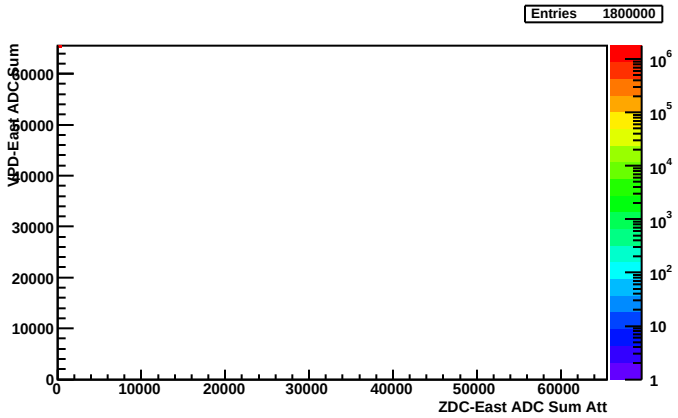

Entries 1800000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

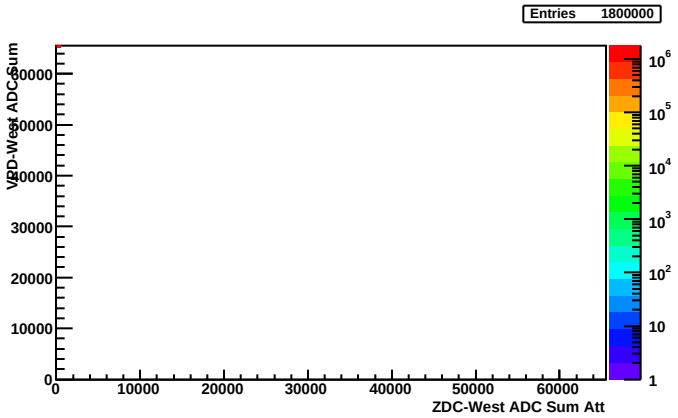

Entries 1800000
BBC-S-East ADC Sum
VPD-East ADC Sum

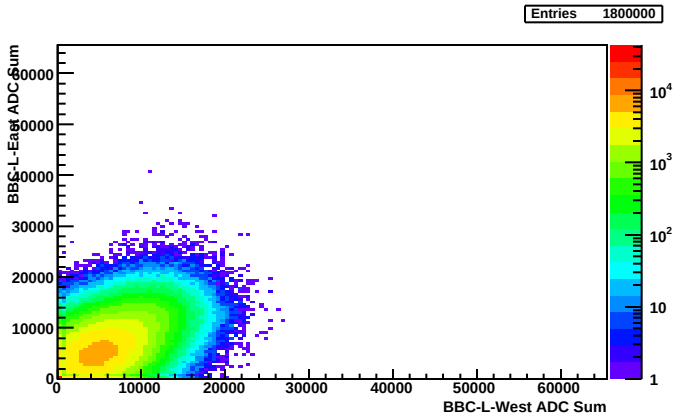

Entries 1800000
BBC-S-West ADC Sum
VPD-West ADC Sum

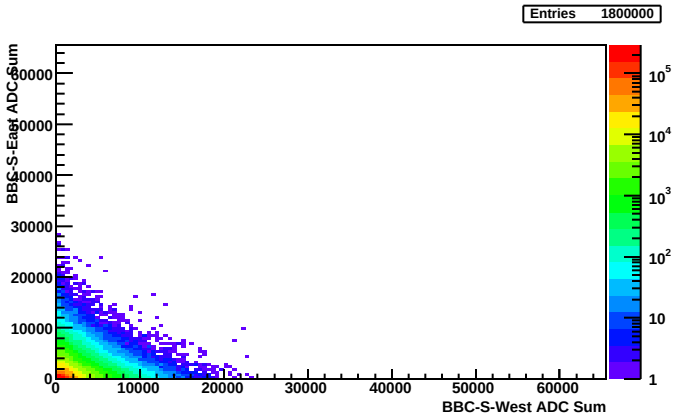

Entries 1800000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

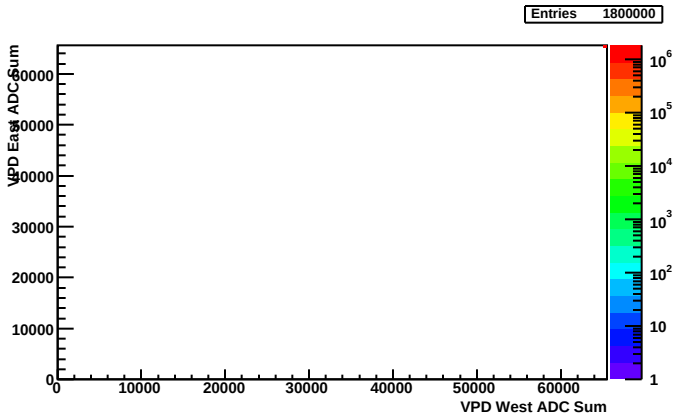

Entries 1800000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

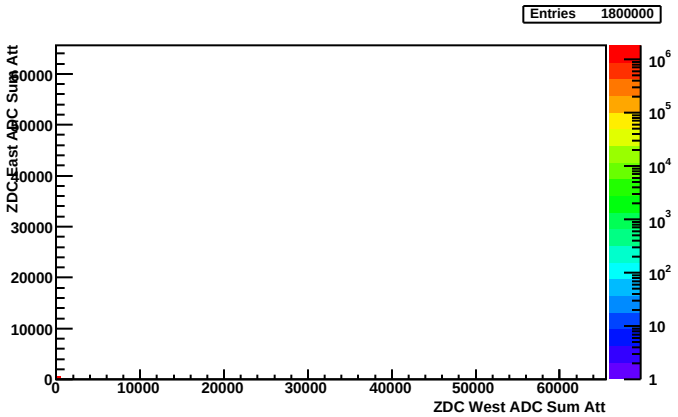

Entries 1800000
VPD-East ADC Sum
ZDC-East ADC Sum Att

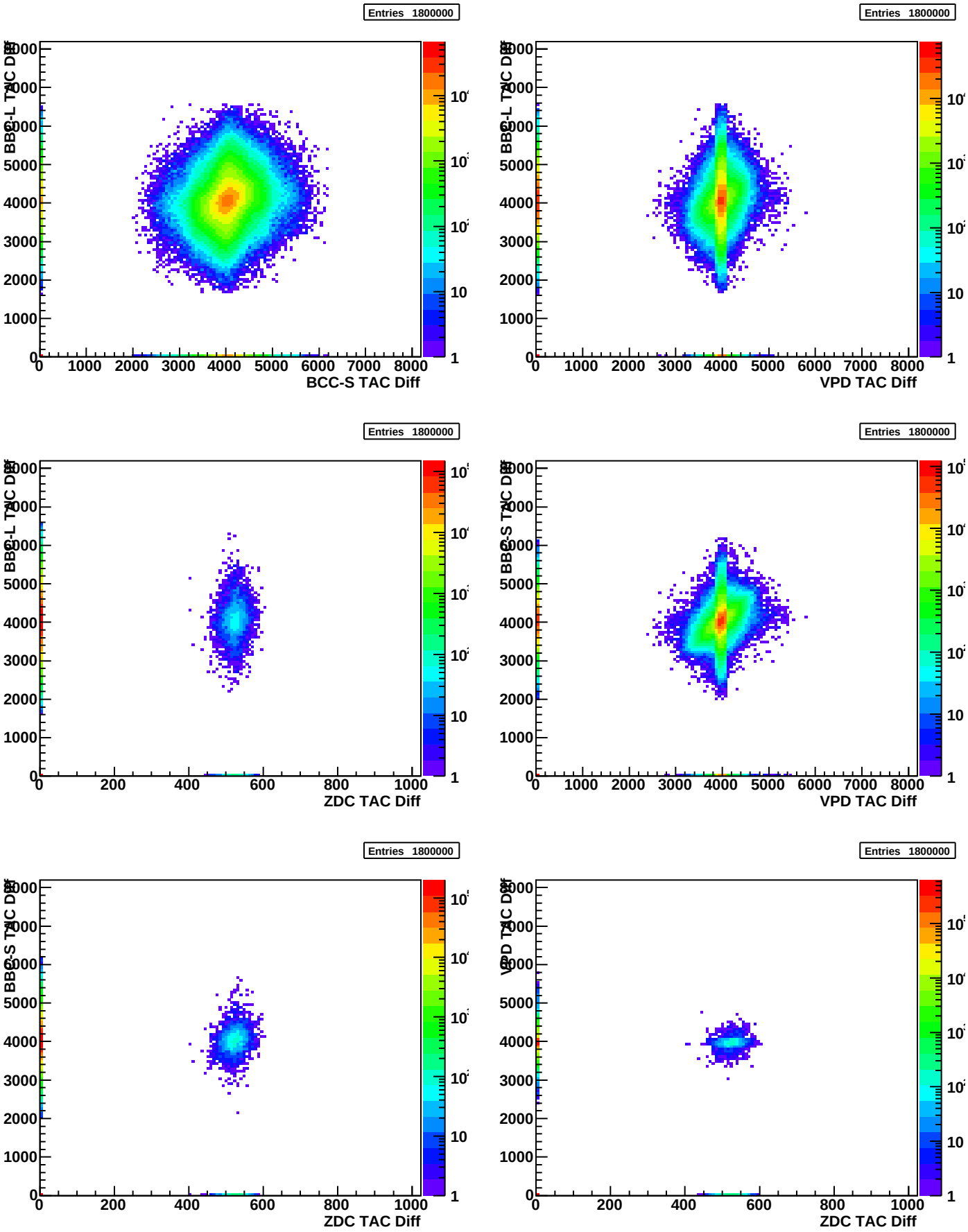

Entries 1800000
VPD-West ADC Sum
ZDC-West ADC Sum Att

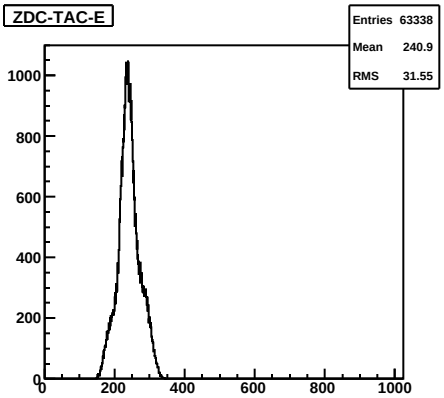

Entries 1800000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

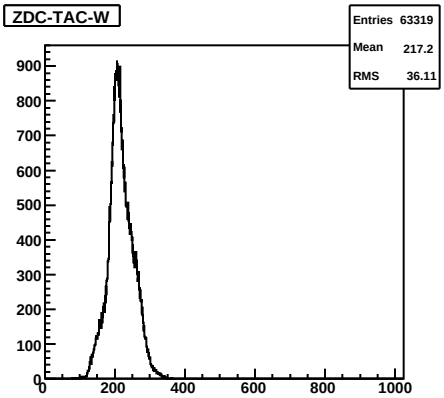

Entries 1800000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

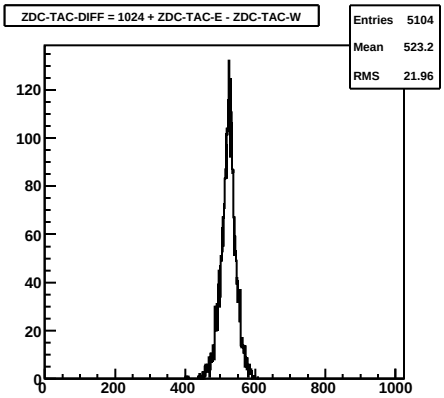

Entries 1800000
VPD East ADC Sum
VPD West ADC Sum

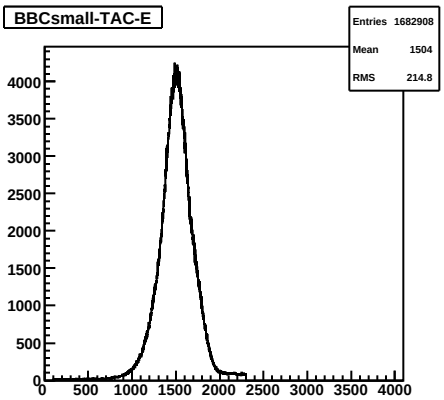

Entries 1800000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

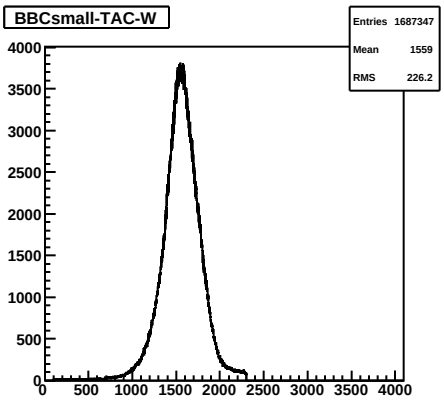

Entries 1800000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1800000
BBC-L TAC Diff
VPD TAC Diff
Entries 1800000
BBC-L TAC Diff
ZDC TAC Diff
Entries 1800000
BBC-S TAC Diff
VPD TAC Diff
Entries 1800000
BBC-S TAC Diff
ZDC TAC Diff
Entries 1800000
VPD TAC Diff
ZDC TAC Diff

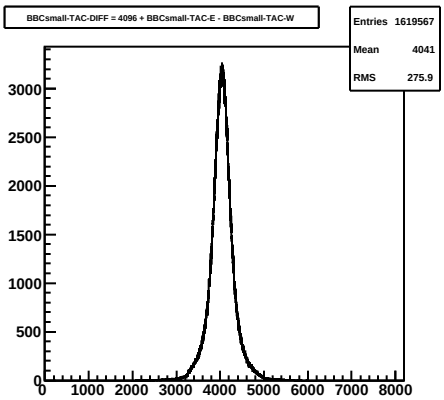

ZDC-TAC-E
Entries 63338
Mean 240.9
RMS 31.55

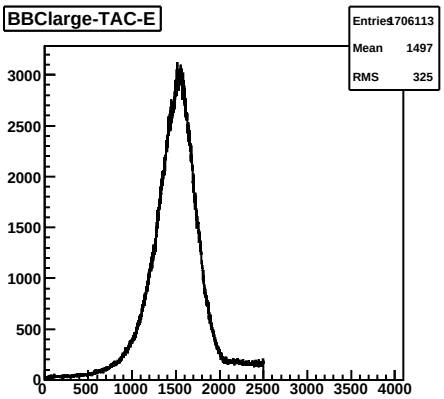

ZDC-TAC-W
Entries 63319
Mean 217.2
RMS 36.11

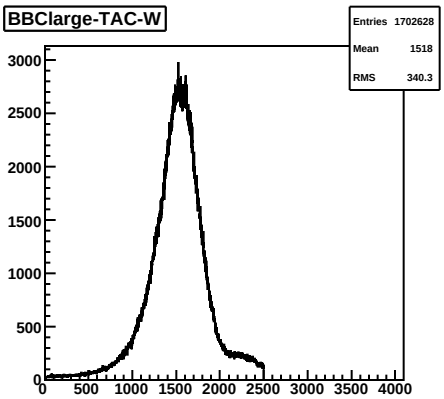

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5104
Mean 523.2
RMS 21.96

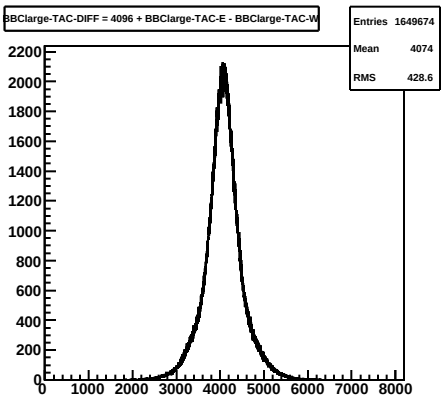

BBCsmall-TAC-E
Entries 1682908
Mean 1504
RMS 214.8

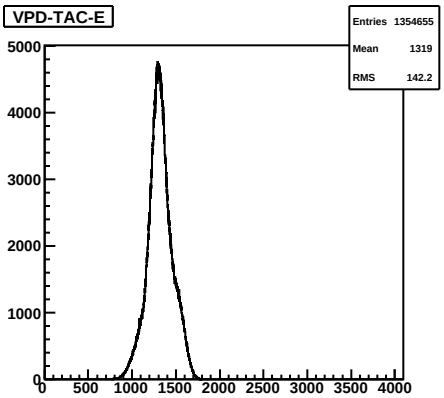

BBCsmall-TAC-W
Entries 1687347
Mean 1559
RMS 226.2

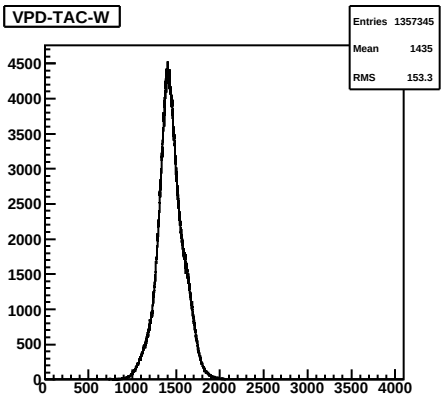

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1619567
Mean 4041
RMS 275.9

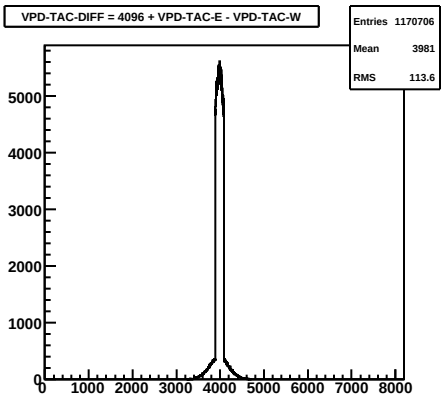

BBClarge-TAC-E
Entries 1706113
Mean 1497
RMS 325

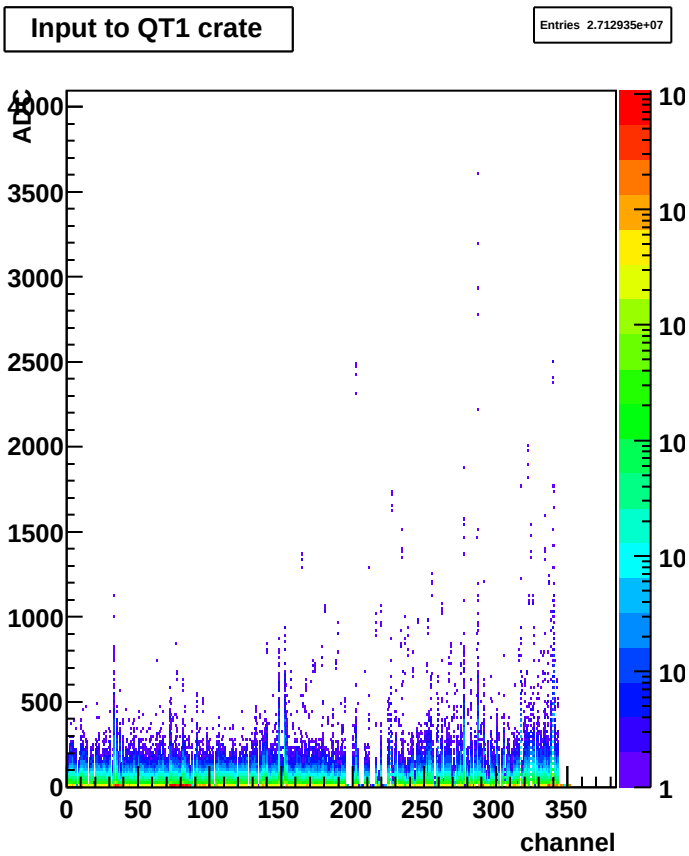

BBClarge-TAC-W
Entries 1702628
Mean 1518
RMS 340.3

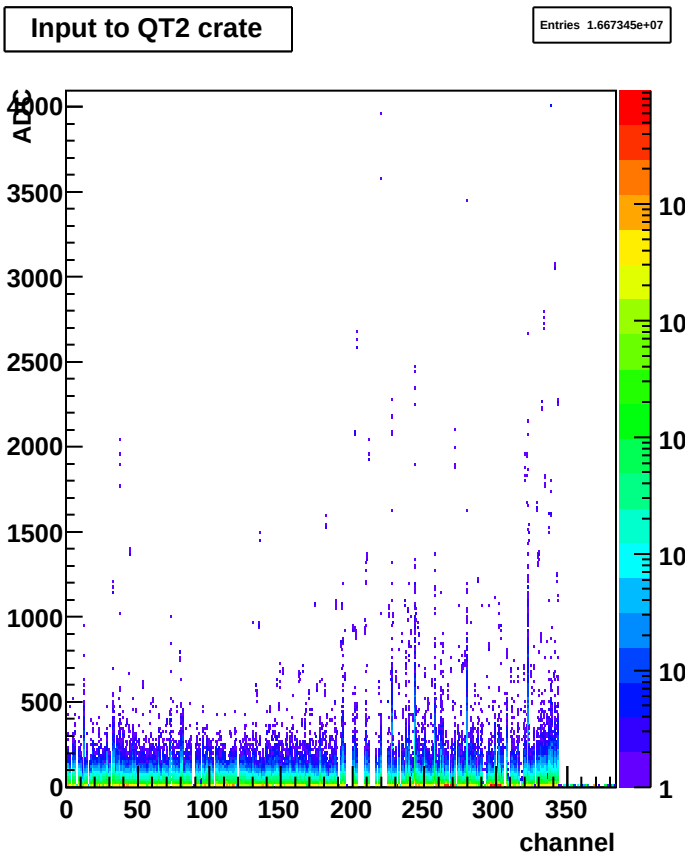

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1649674
Mean 4074
RMS 428.6

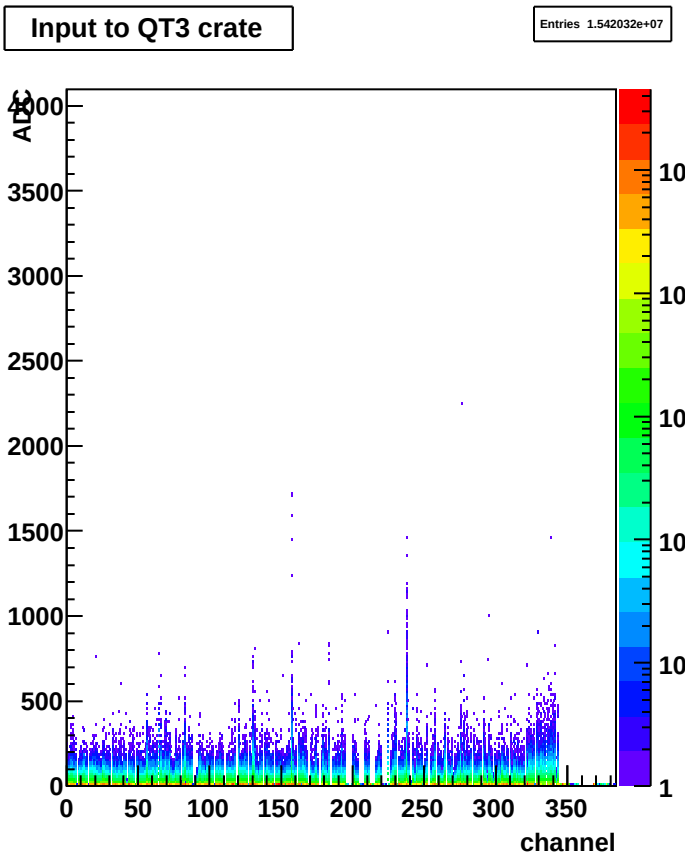

VPD-TAC-E
Entries 1354655
Mean 1319
RMS 142.2

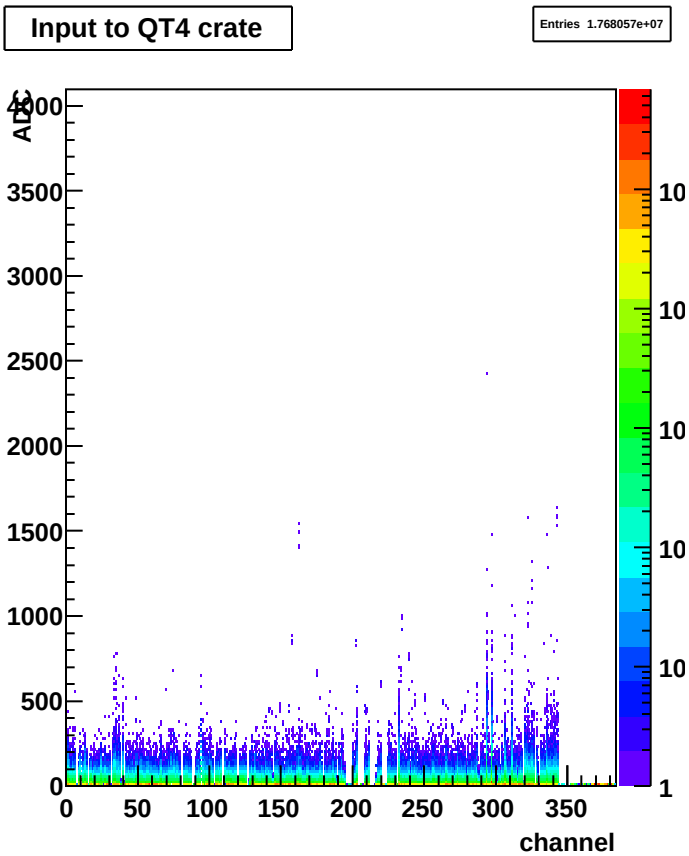

VPD-TAC-W
Entries 1357345
Mean 1435
RMS 153.3

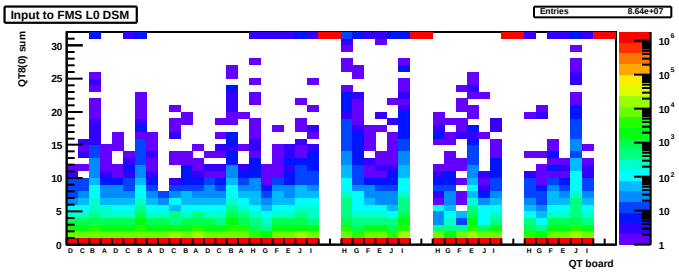

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1170706
Mean 3981
RMS 113.6

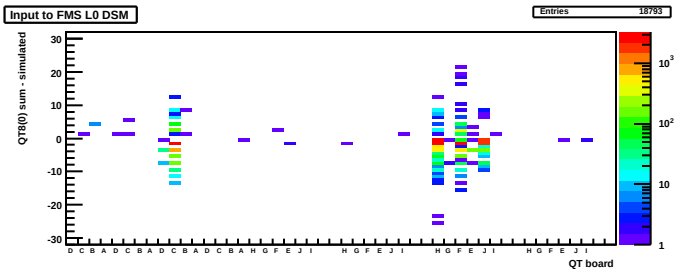

Input to QT1 crate
Entries 2.712935e+07
ADC
channel

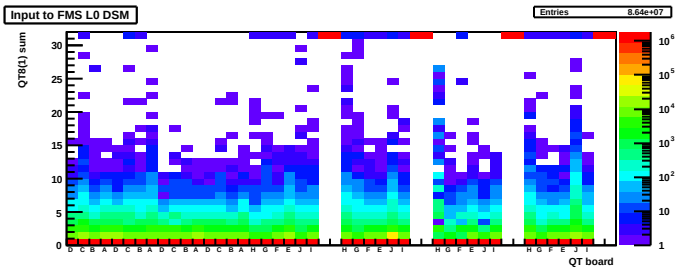

Input to QT2 crate
Entries 1.667345e+07
ADC
channel

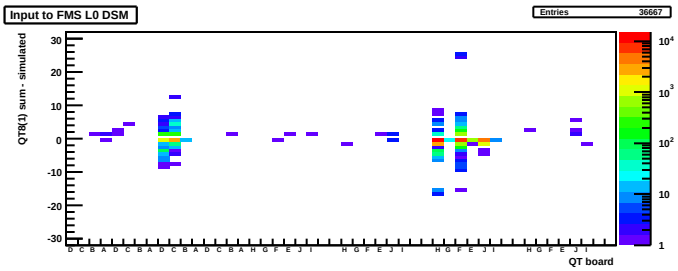

Input to QT3 crate
Entries 1.542032e+07
ADC
channel

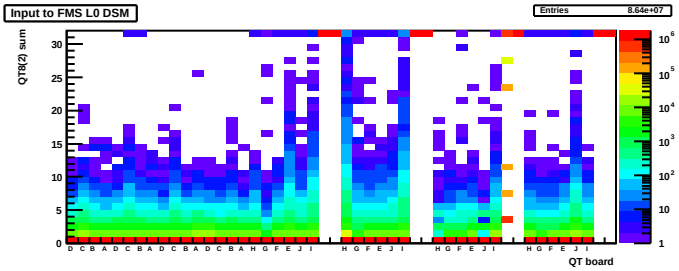

Input to QT4 crate
Entries 1.768057e+07
ADC
channel

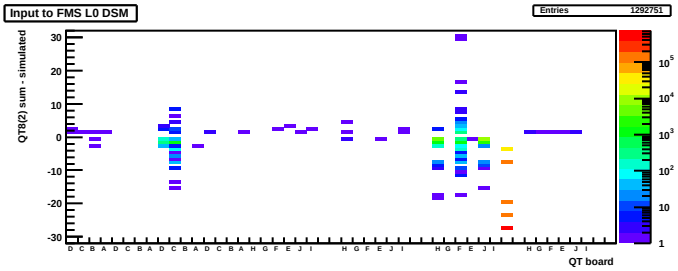

Input to FMS L0 DSM
Entries 8.64e+07
QT8(0) sum
QT board

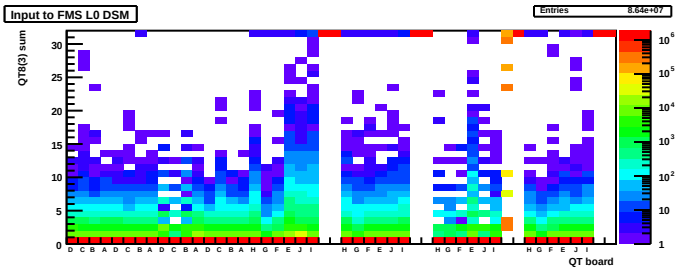

Input to FMS L0 DSM
Entries 18793
QT8(0) sum - simulated
QT board

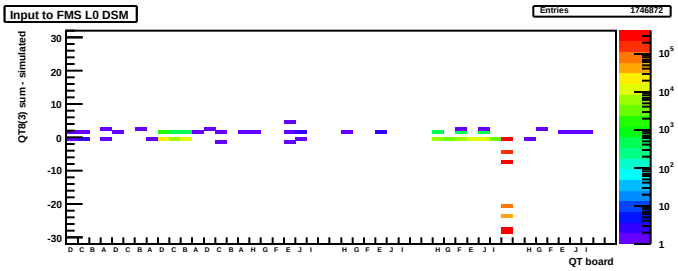

Input to FMS L0 DSM
Entries 8.64e+07
QT8(1) sum
QT board

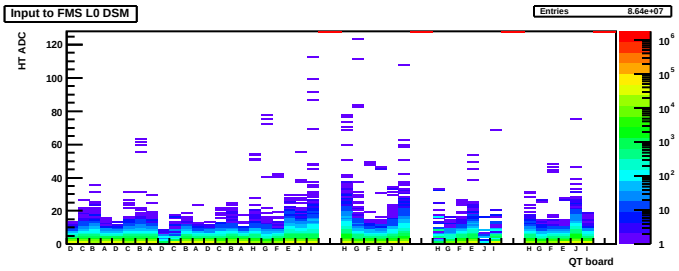

Input to FMS L0 DSM
Entries 36667
QT8(1) sum - simulated
QT board

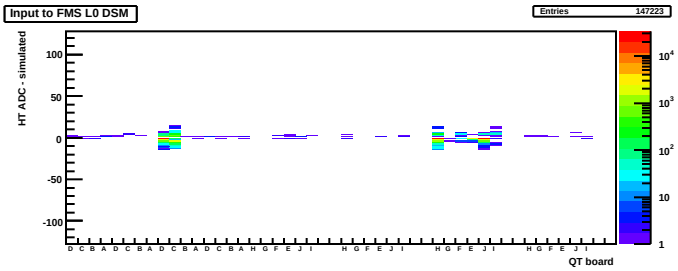

Input to FMS L0 DSM
Entries 8.64e+07
QT8(2) sum
QT board

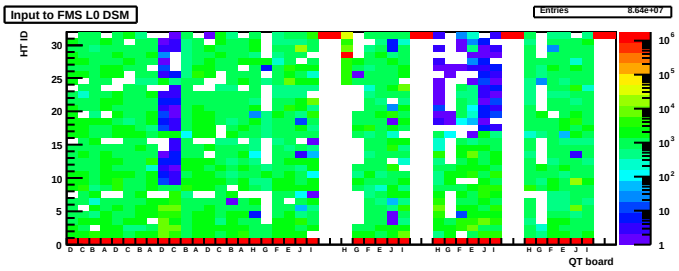

Input to FMS L0 DSM
Entries 1292751
QT8(2) sum - simulated
QT board

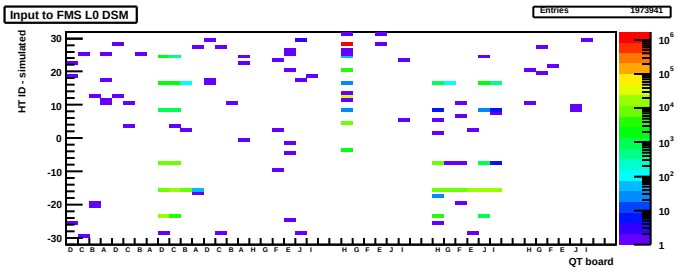

Input to FMS L0 DSM
Entries 8.64e+07
QT8(3) sum
QT board

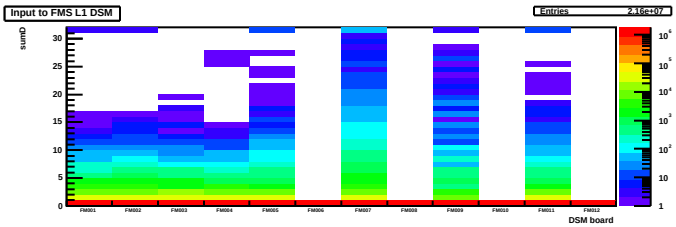

Input to FMS L0 DSM
Entries 1746872
QT8(3) sum - simulated
QT board

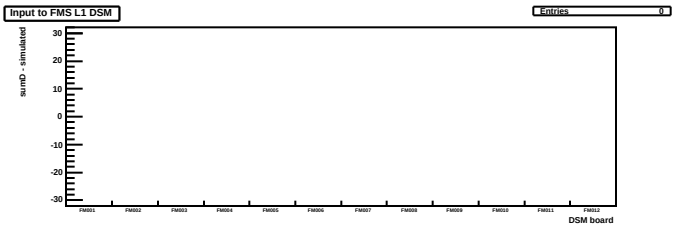

Input to FMS L0 DSM
Entries 8.64e+07
HT ADC
QT board

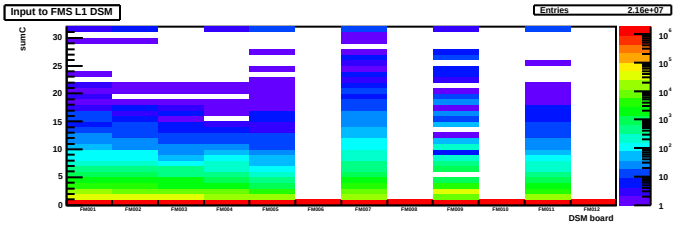

Input to FMS L0 DSM
Entries 147223
HT ADC - simulated
QT board

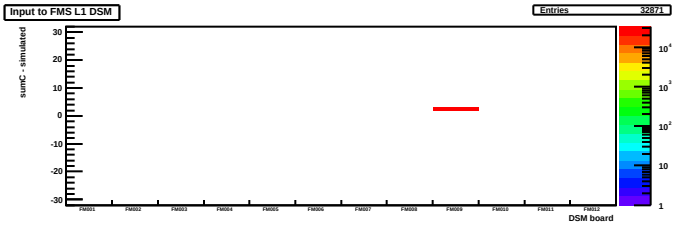

Input to FMS L0 DSM
Entries 8.64e+07
HT ID
QT board

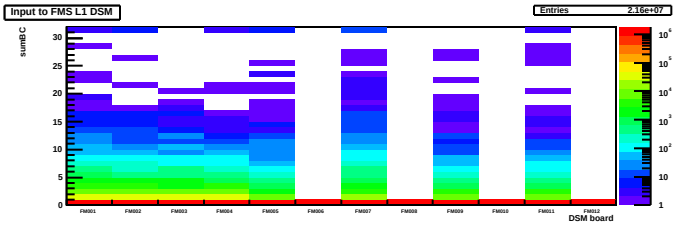

Input to FMS L0 DSM
Entries 1973941
HT ID - simulated
QT board

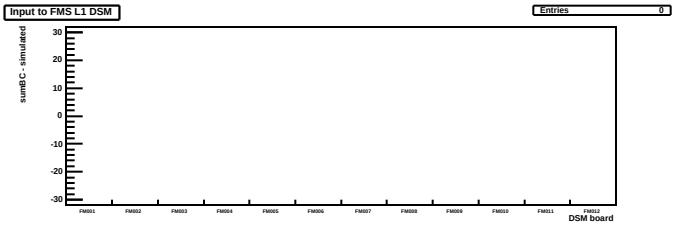

Input to FMS L1 DSM
Entries 2.16e+07
sumD
DSM board

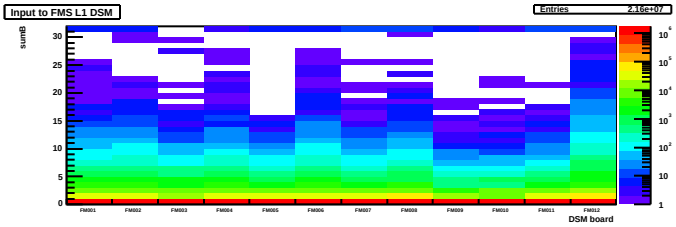

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

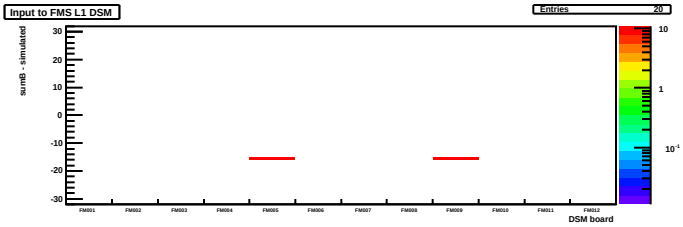

Input to FMS L1 DSM
Entries 2.16e+07
sumC
DSM board

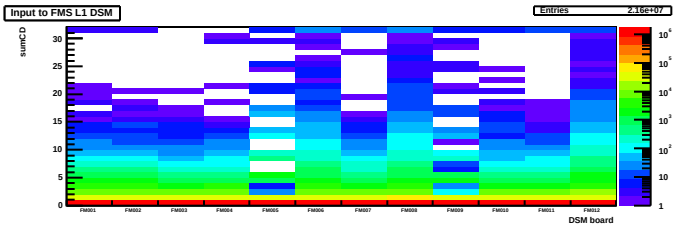

Input to FMS L1 DSM
Entries 32871
sumC - simulated
DSM board

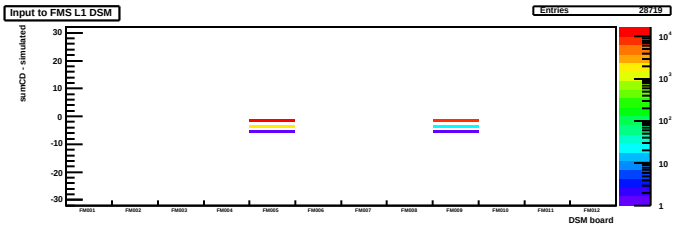

Input to FMS L1 DSM
Entries 2.16e+07
sumBC
DSM board

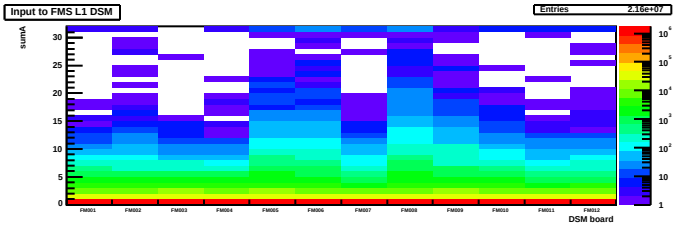

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

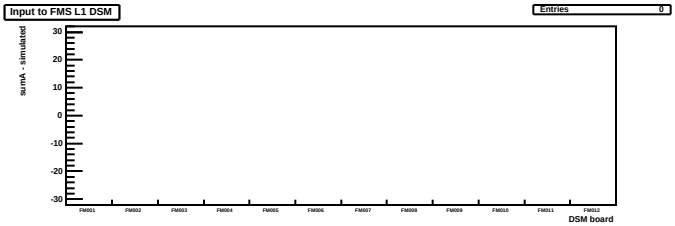

Input to FMS L1 DSM
Entries 2.16e+07
sumB
DSM board

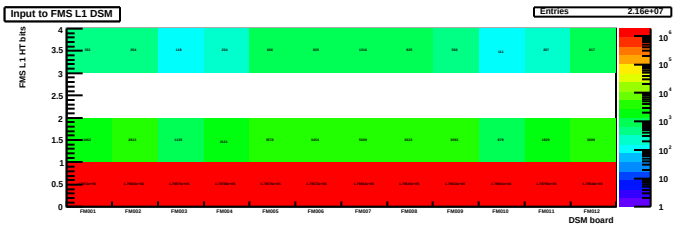

Input to FMS L1 DSM
Entries 20
sumB - simulated
DSM board

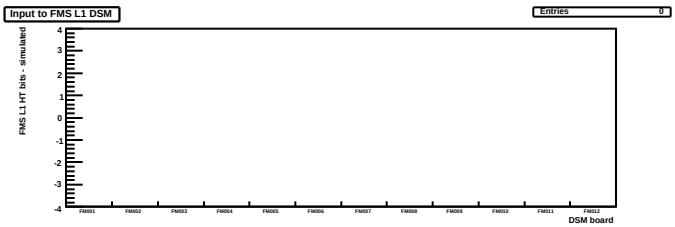

Input to FMS L1 DSM
Entries 2.16e+07
sumCD
DSM board

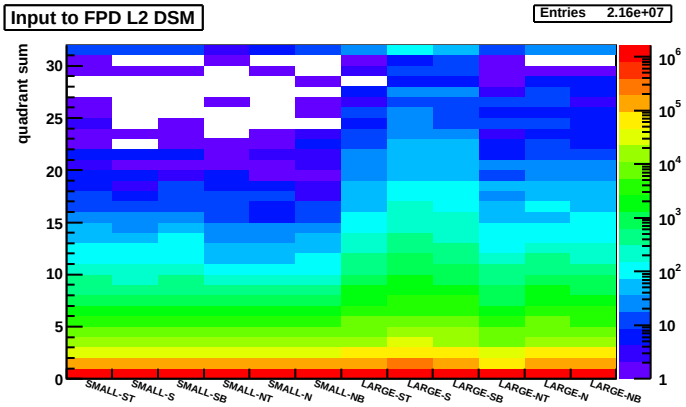

Input to FMS L1 DSM
Entries 28719
sumCD - simulated
DSM board

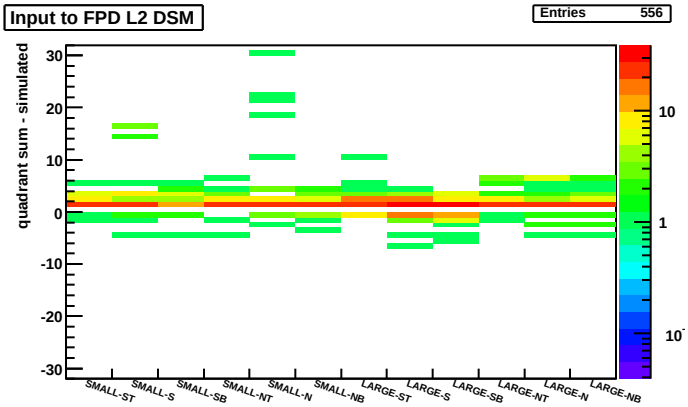

Input to FMS L1 DSM
Entries 2.16e+07
sumA
DSM board

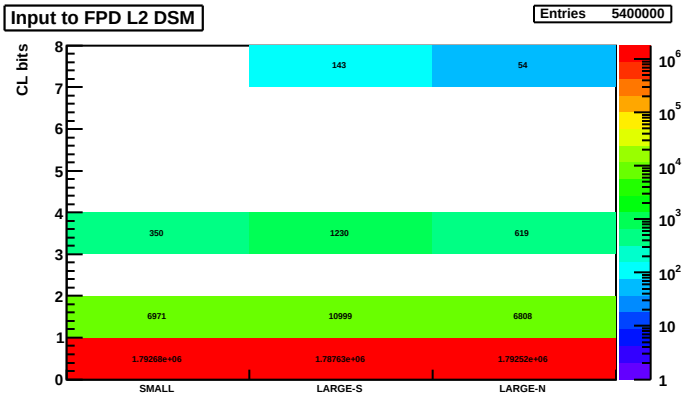

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

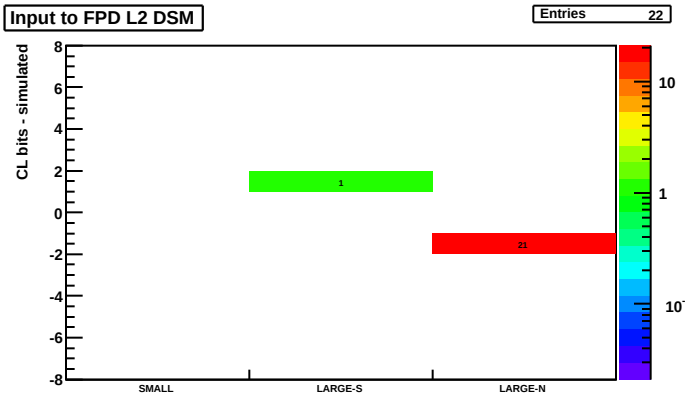

Input to FMS L1 DSM
Entries 2.16e+07
FMS L1 HT bits
DSM board

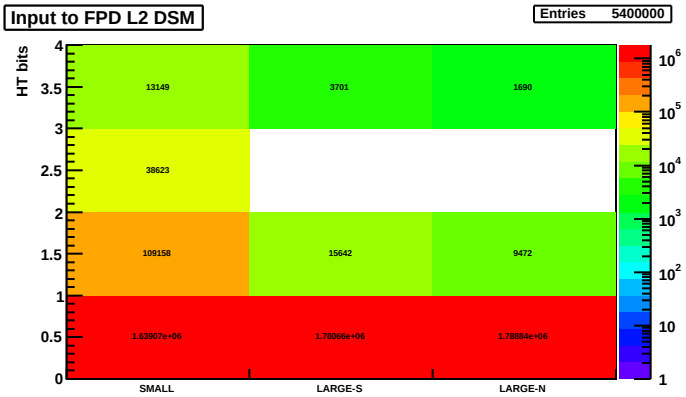

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

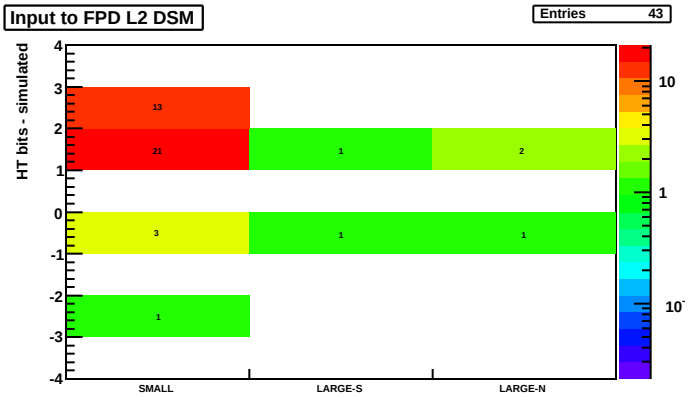

Input to FPD L2 DSM
Entries 2.16e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

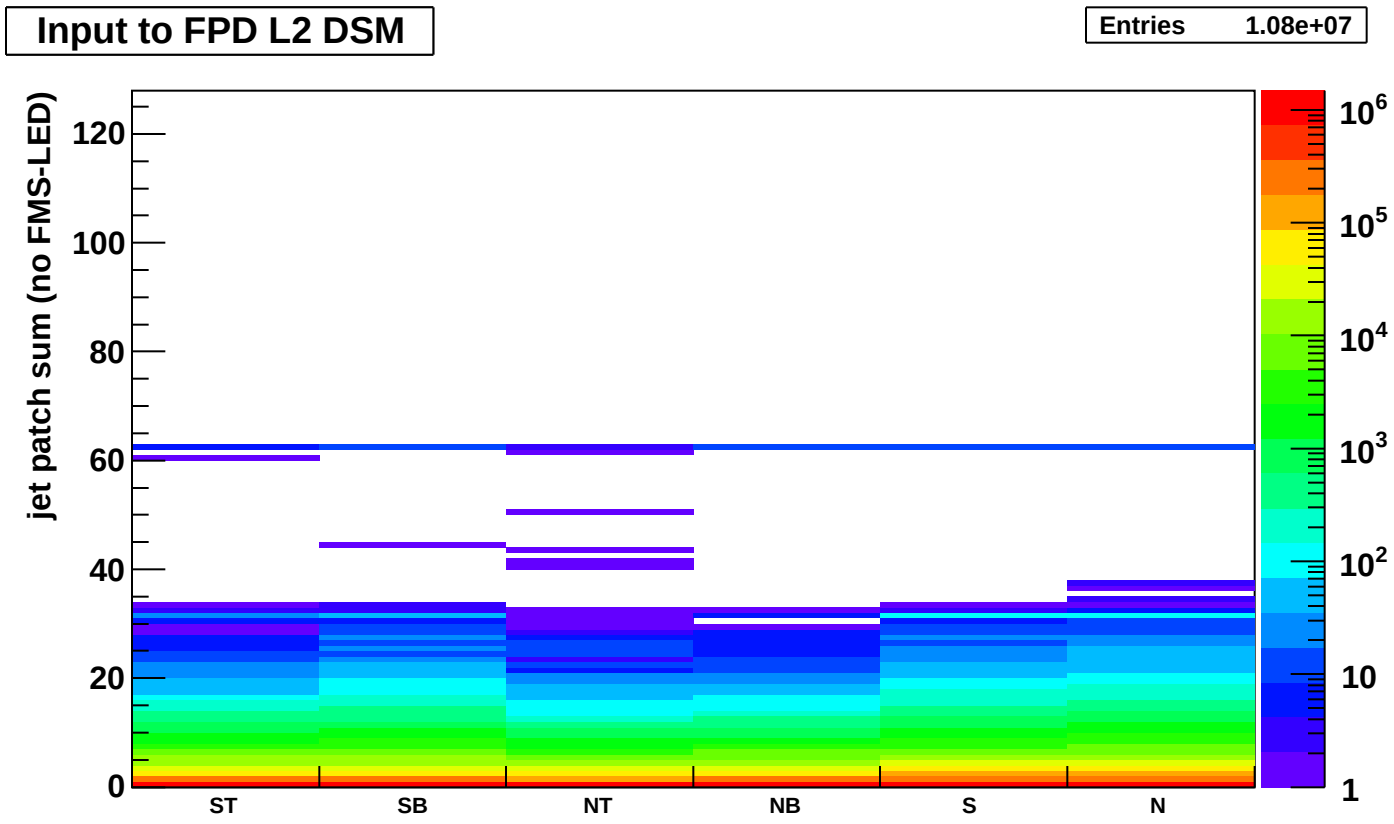

Input to FPD L2 DSM
Entries 556
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

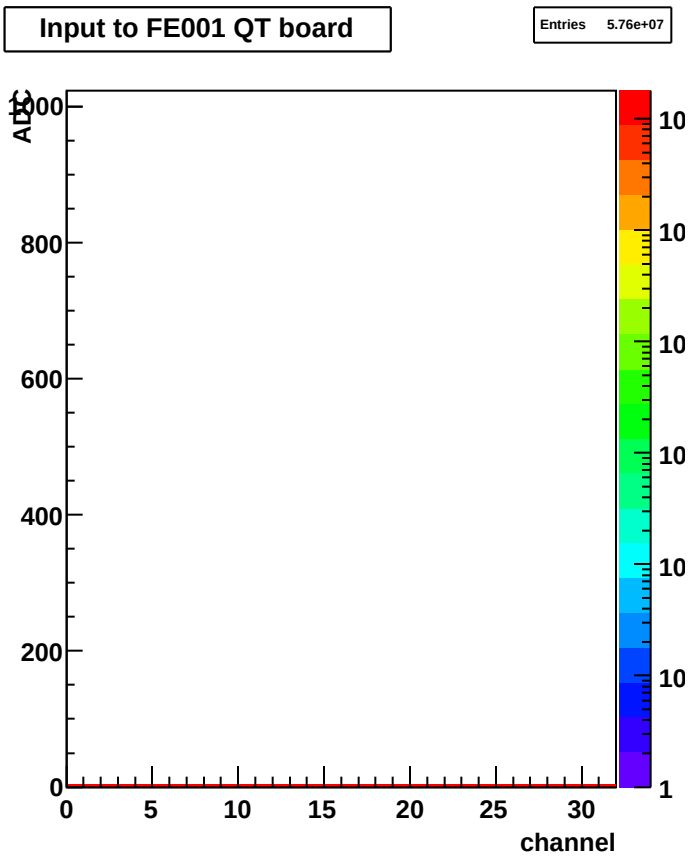

Input to FPD L2 DSM
Entries 5400000
CL bits
143
54
350
1230
619
6971
10999
6808
1.79268e+06
1.78763e+06
1.79252e+06
SMALL
LARGE-S
LARGE-N

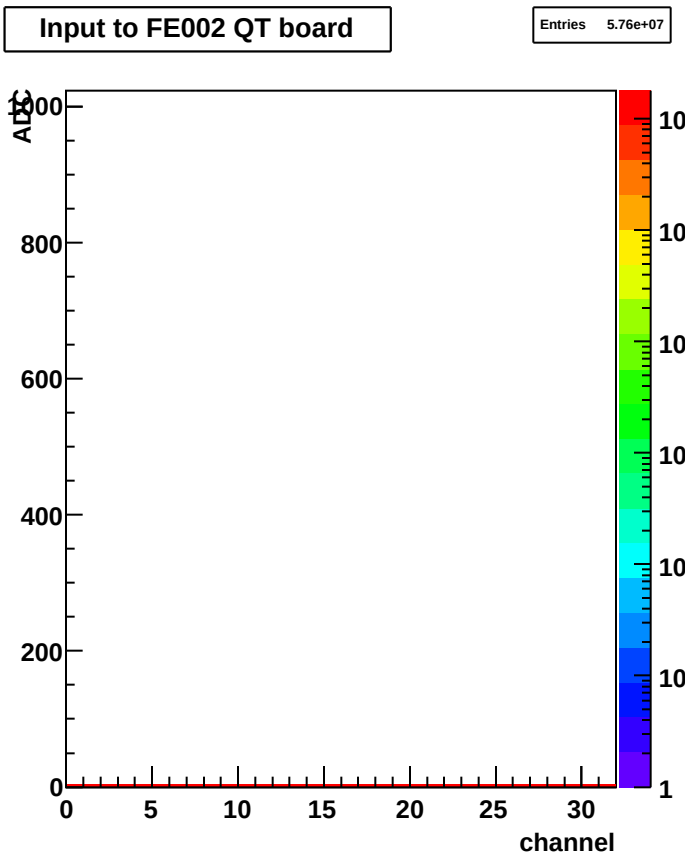

Input to FPD L2 DSM
Entries 22
CL bits - simulated
1
21
SMALL
LARGE-S
LARGE-N

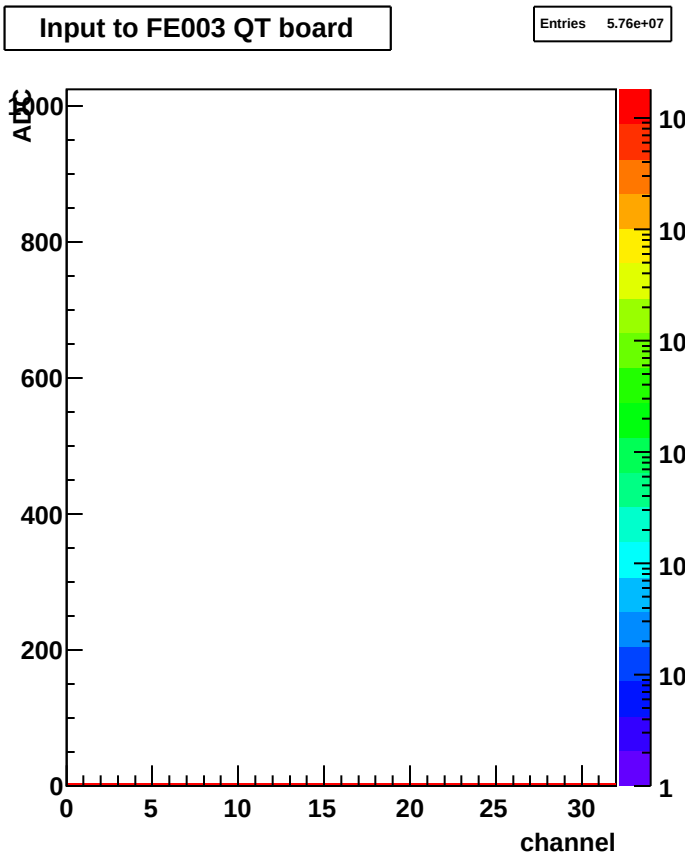

Input to FPD L2 DSM
Entries 5400000
HT bits
13149
3701
1690
38623
109158
15642
9472
1.63907e+06
1.78066e+06
1.78884e+06
SMALL
LARGE-S
LARGE-N

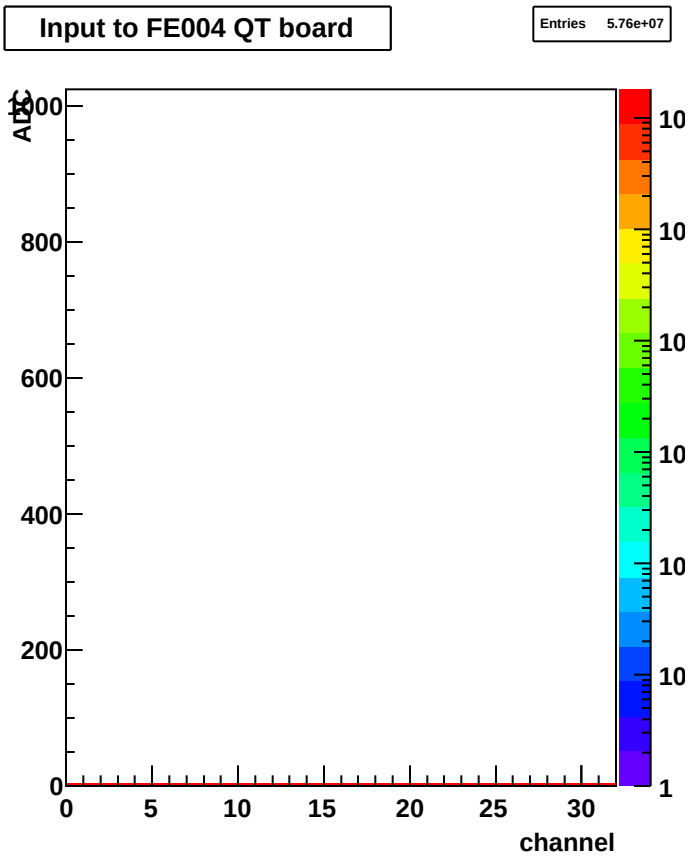

Input to FPD L2 DSM
Entries 43
HT bits - simulated
13
21
1
2
3
1
1
1
SMALL
LARGE-S
LARGE-N

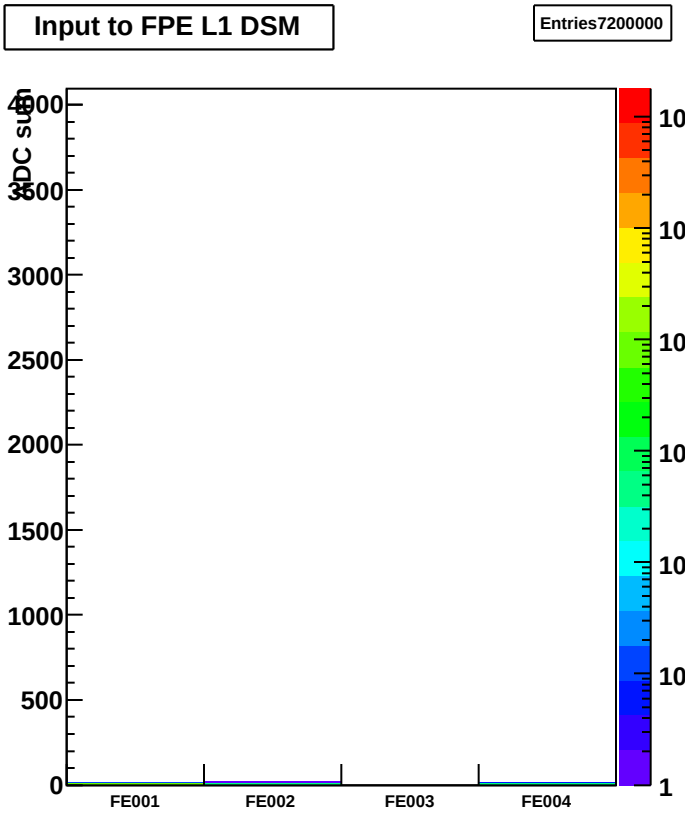

Input to FPD L2 DSM
Entries 1.08e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

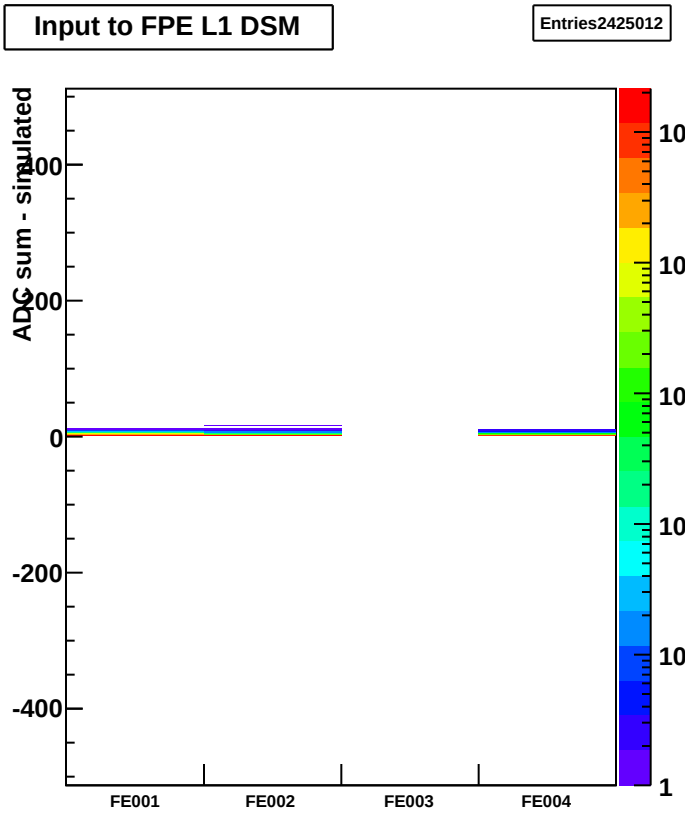

Input to FE001 QT board
Entries 5.76e+07
ADC
channel

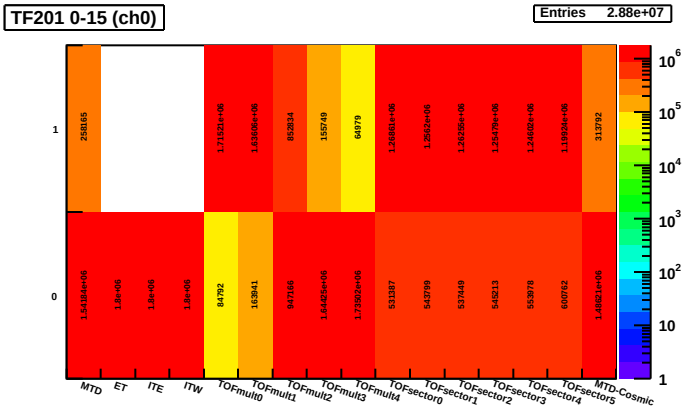

Input to FE002 QT board
Entries 5.76e+07
ADC
channel

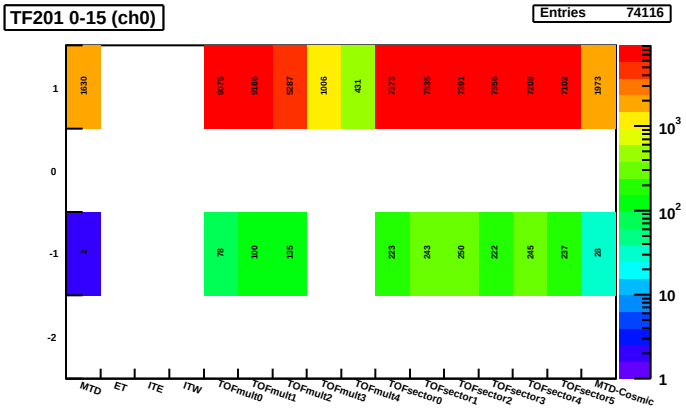

Input to FE003 QT board
Entries 5.76e+07
ADC
channel

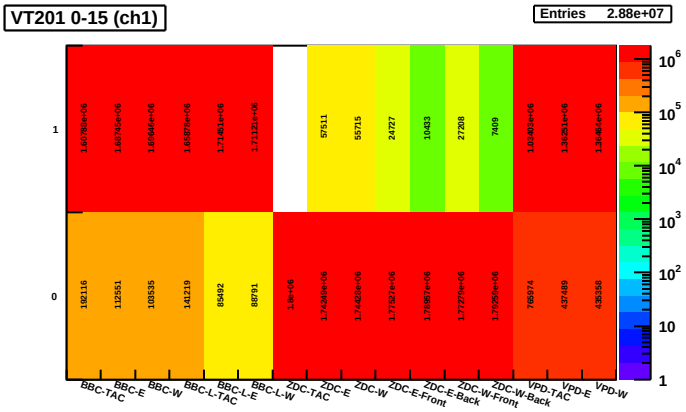

Input to FE004 QT board
Entries 5.76e+07
ADC
channel


Input to FPE L1 DSM
Entries7200000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries2425012
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.88e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 74116
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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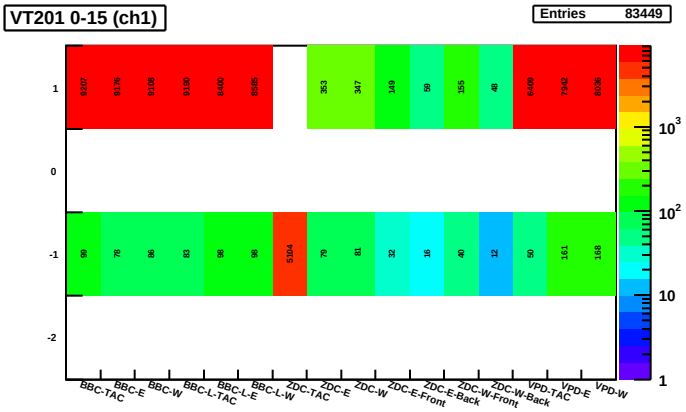
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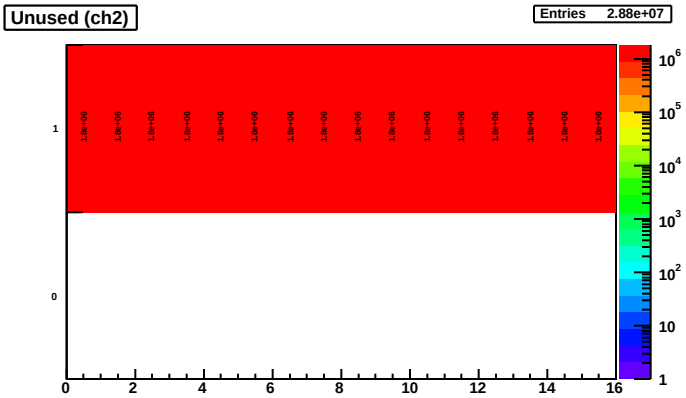
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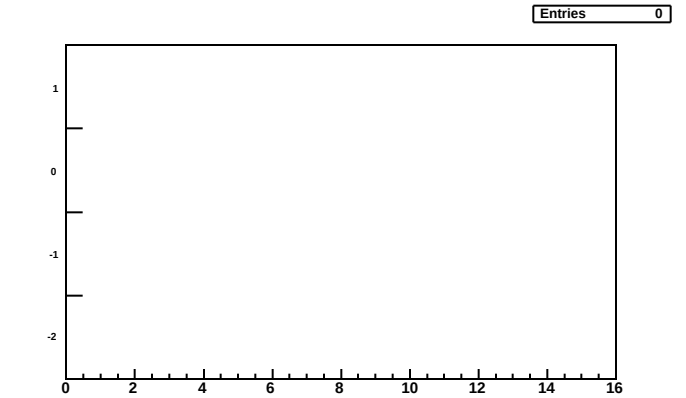
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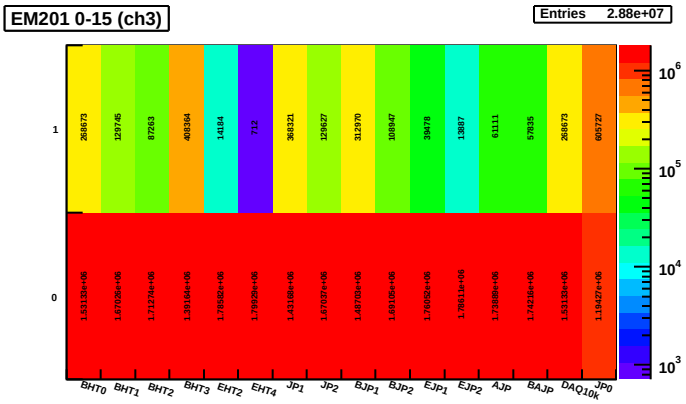
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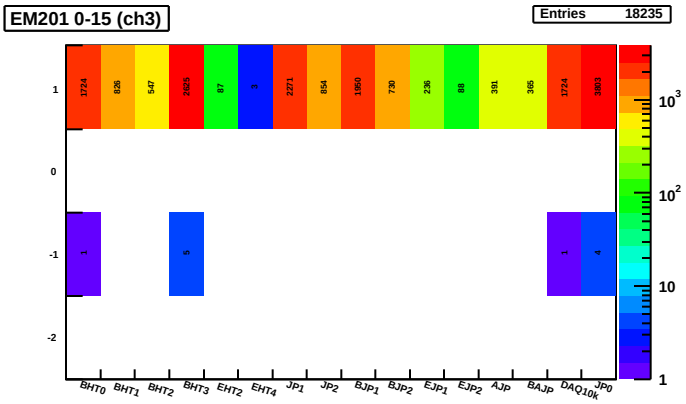
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Entries 18235
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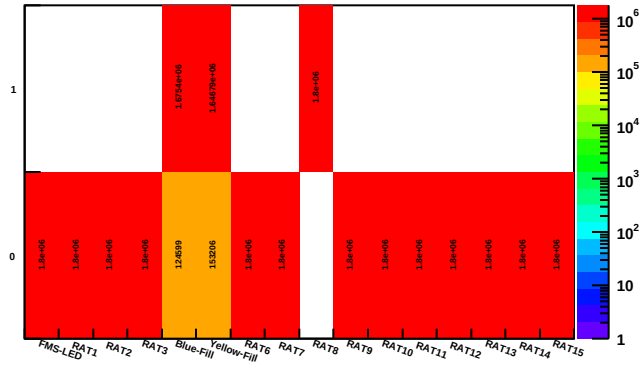
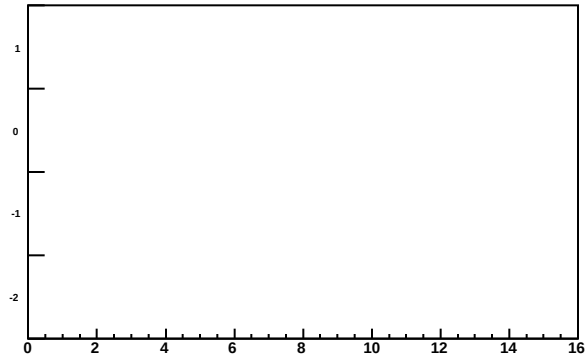
RAT board (ch4)





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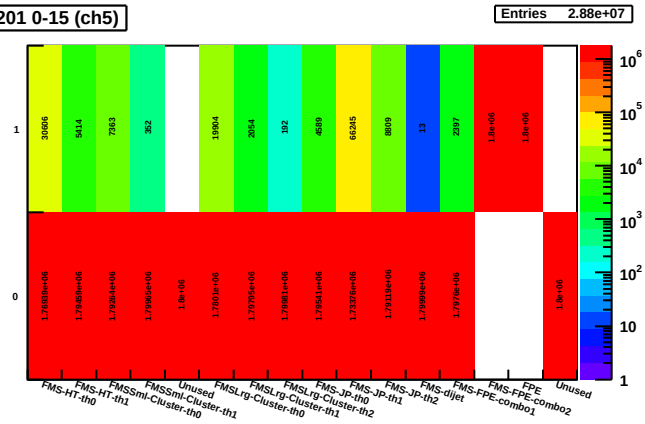

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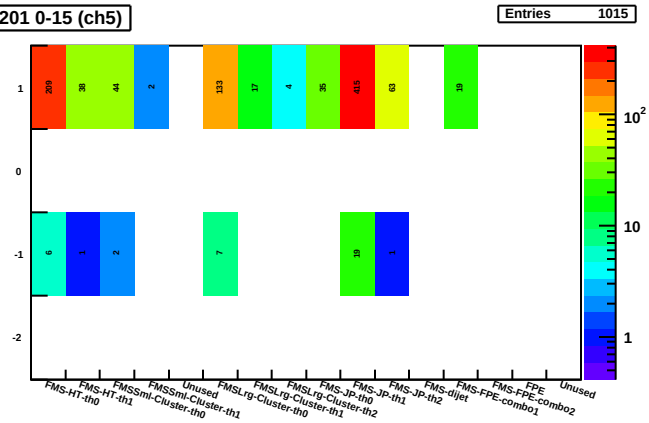
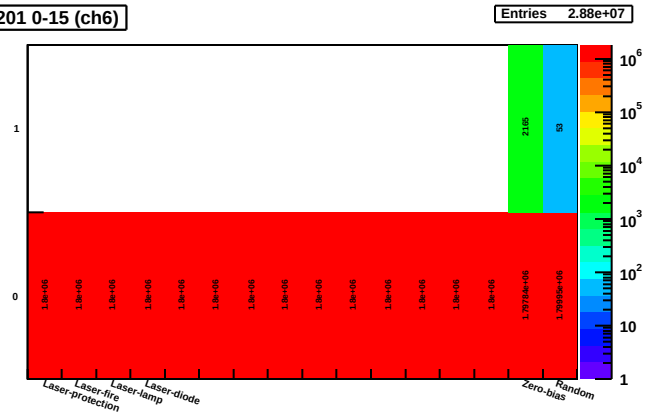
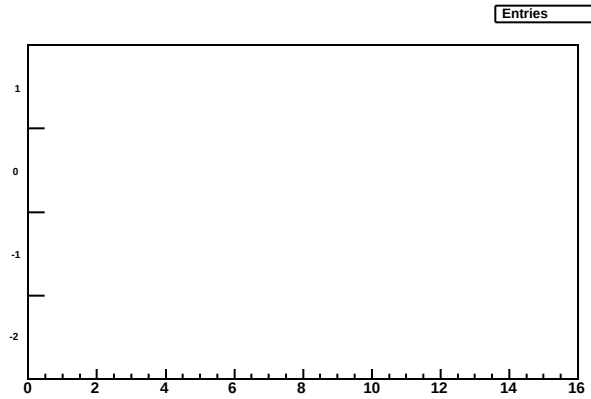
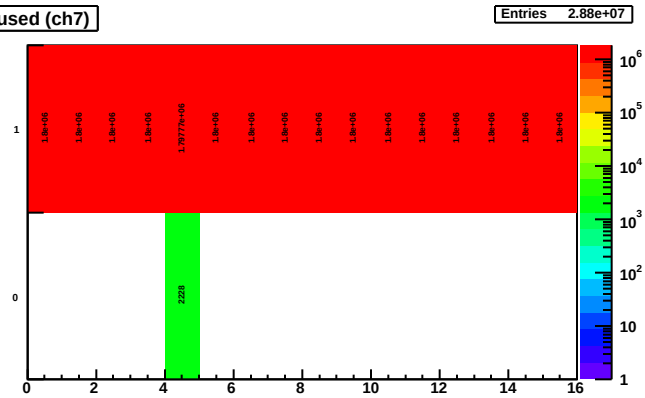
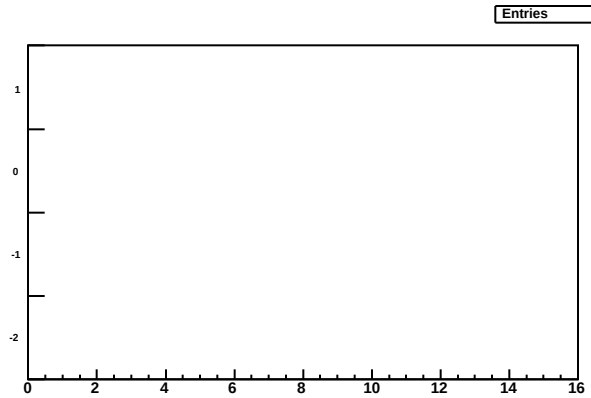


ST201 0-15 (ch6)





Unused (ch7)