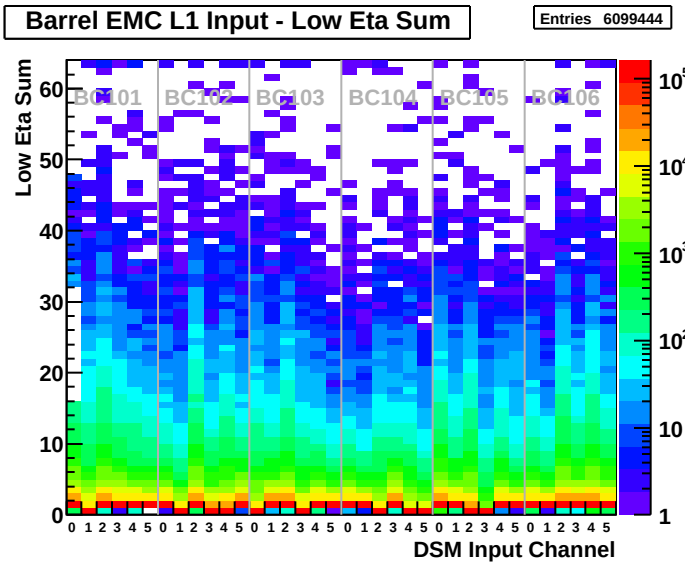
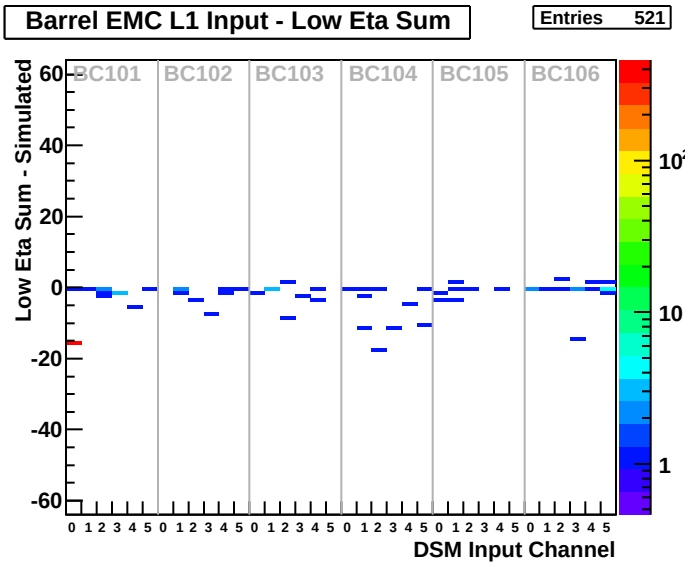
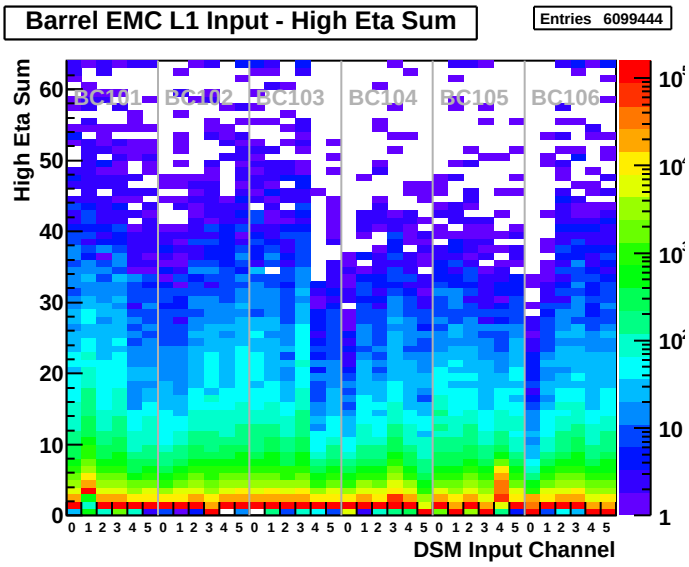
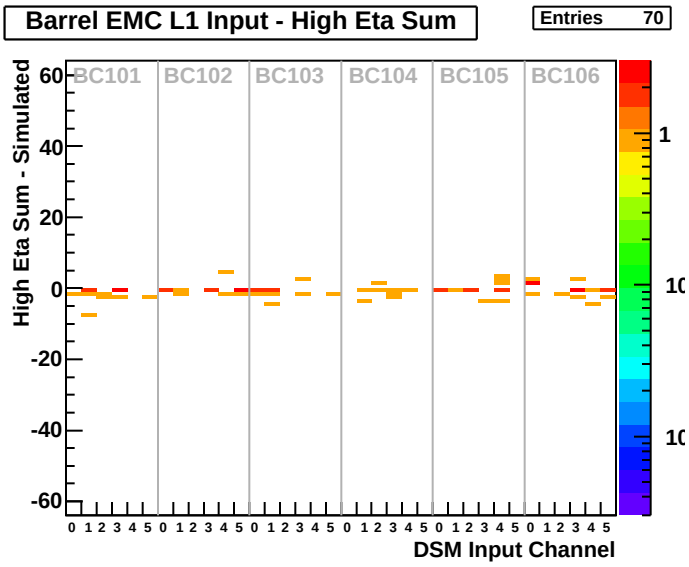
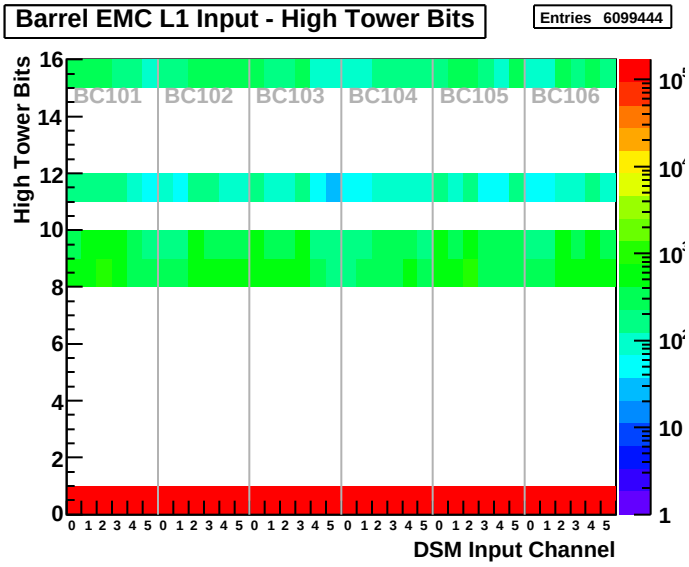
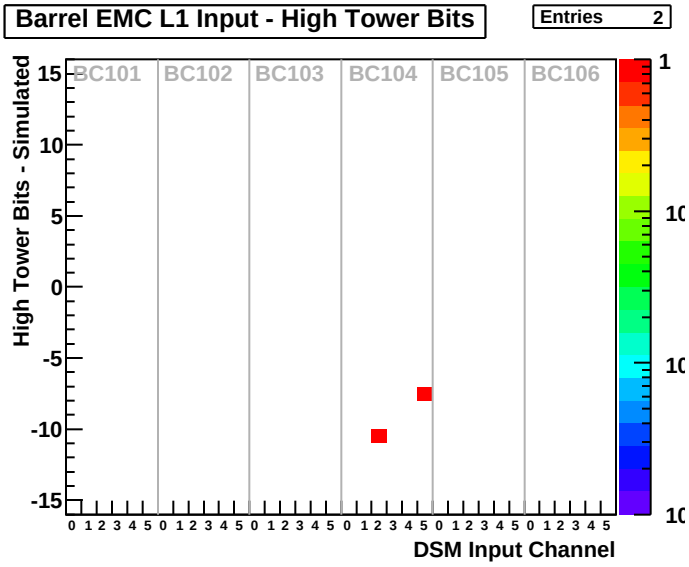

Barrel EMC L1 Input - Low Eta Sum
Entries 6099444
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

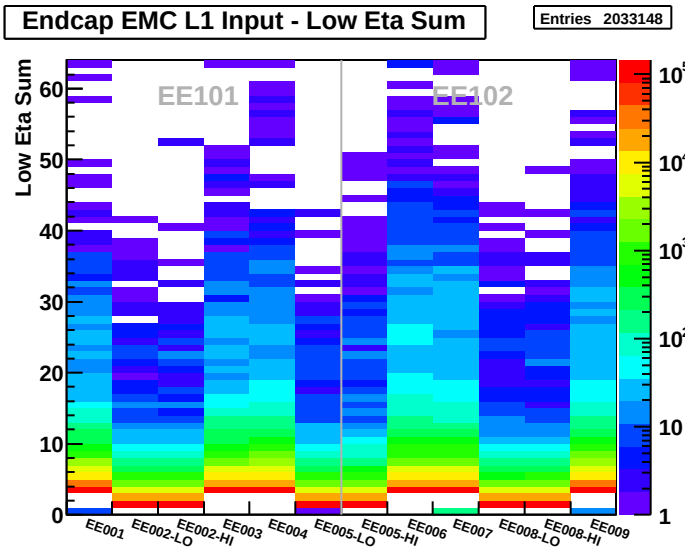

Barrel EMC L1 Input - Low Eta Sum
Entries 521
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

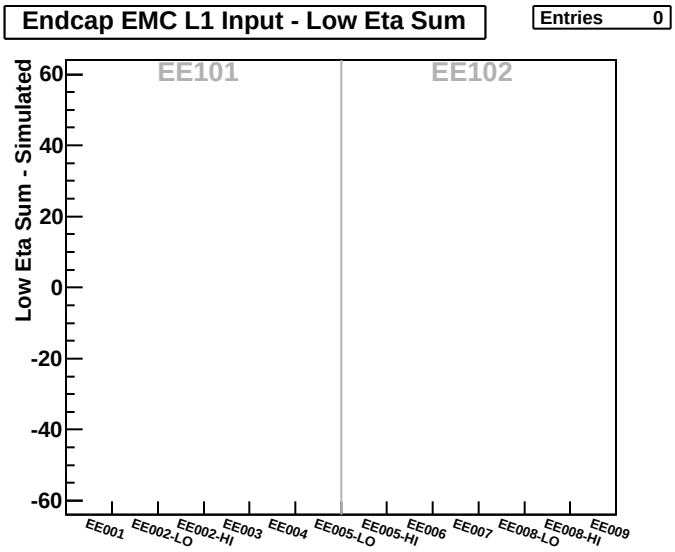

Barrel EMC L1 Input - High Eta Sum
Entries 6099444
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

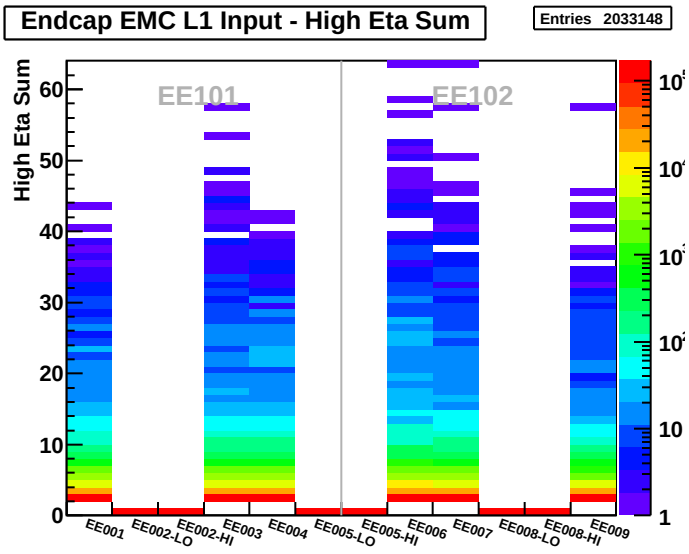

Barrel EMC L1 Input - High Eta Sum
Entries 70
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

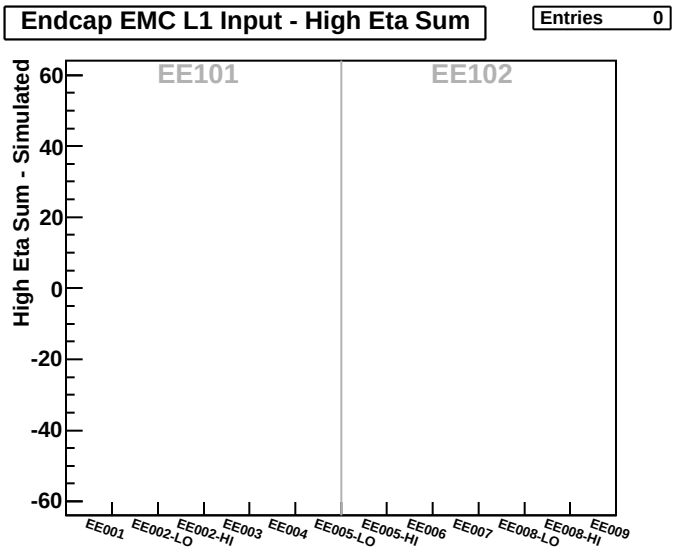

Barrel EMC L1 Input - High Tower Bits
Entries 6099444
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

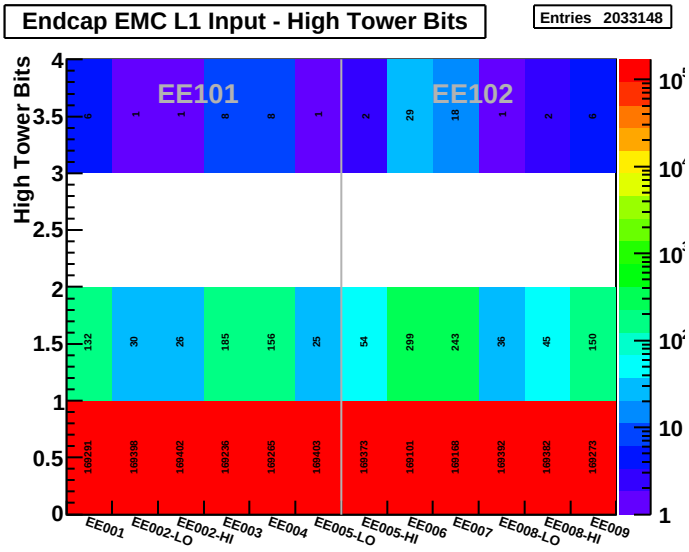

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

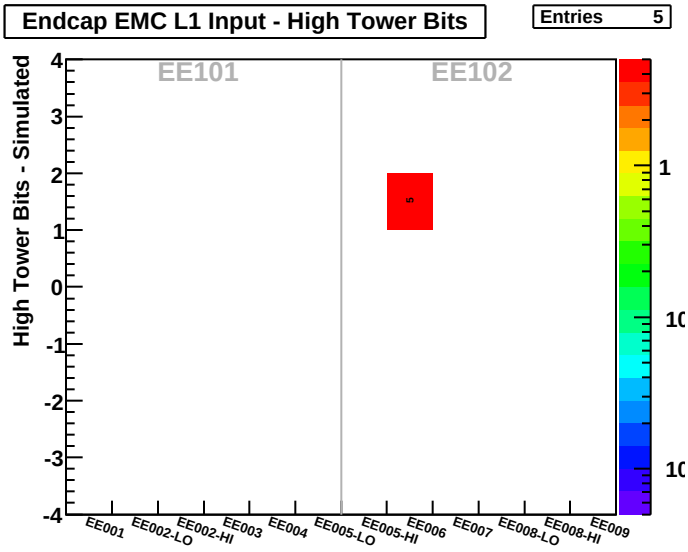

Endcap EMC L1 Input - Low Eta Sum
Entries 2033148
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

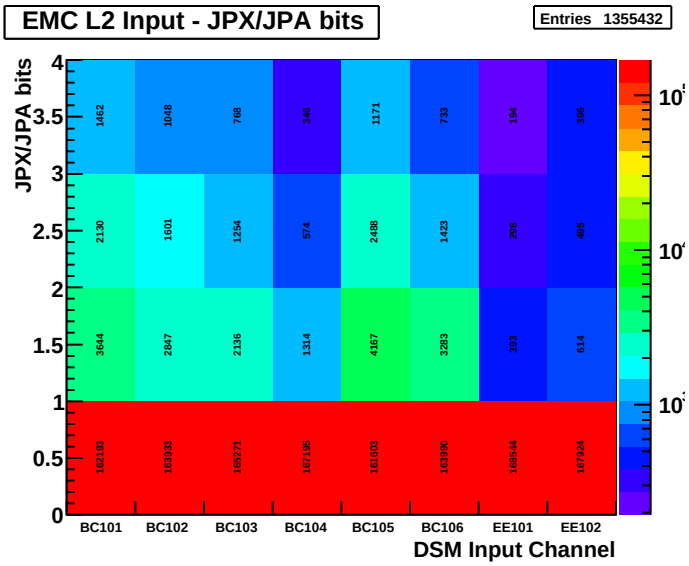

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

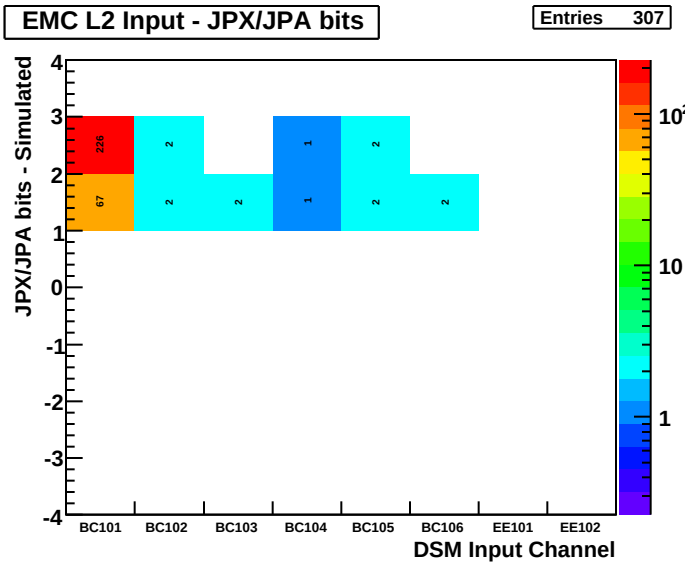

Endcap EMC L1 Input - High Eta Sum
Entries 2033148
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

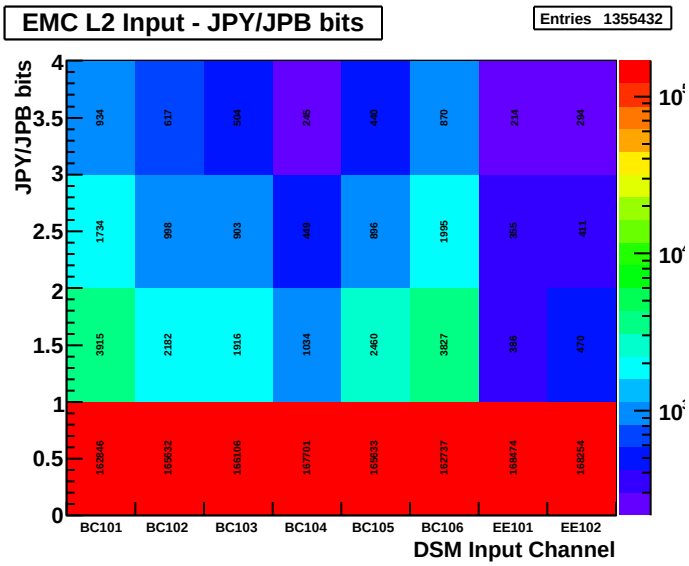

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

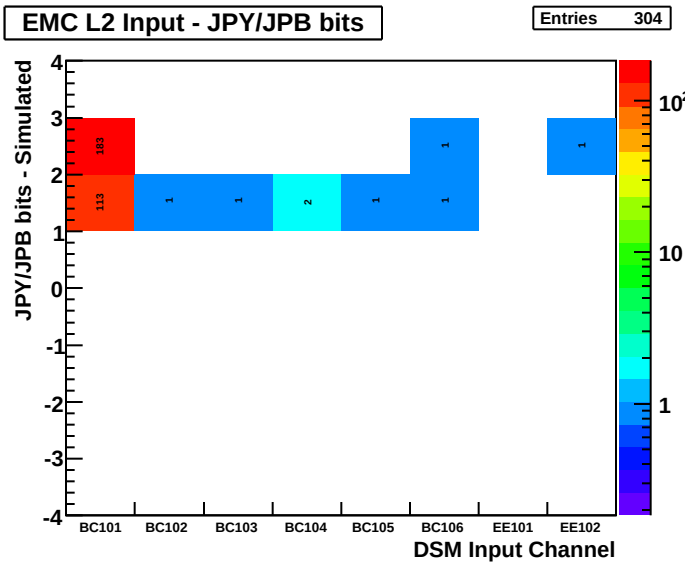

Endcap EMC L1 Input - High Tower Bits
Entries 2033148
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

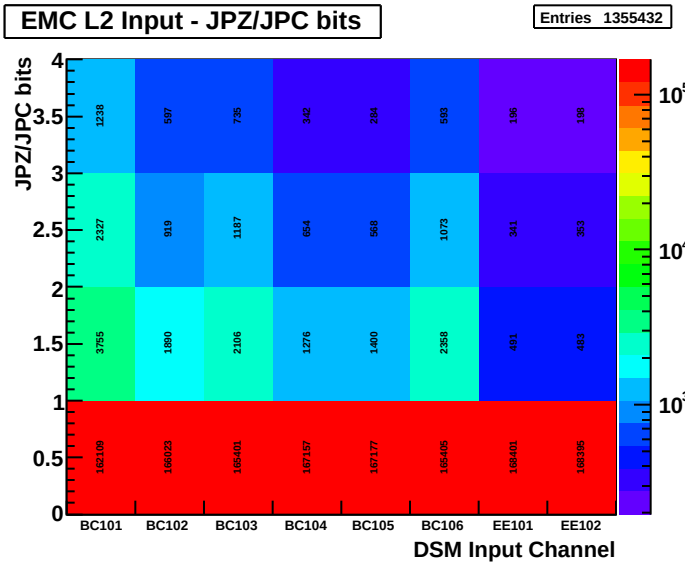

Endcap EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

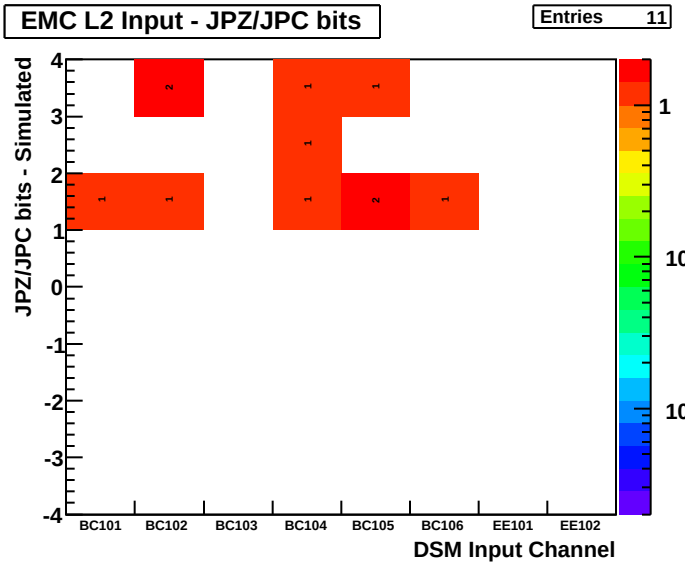

EMC L2 Input - JPX/JPA bits
Entries 1355432
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

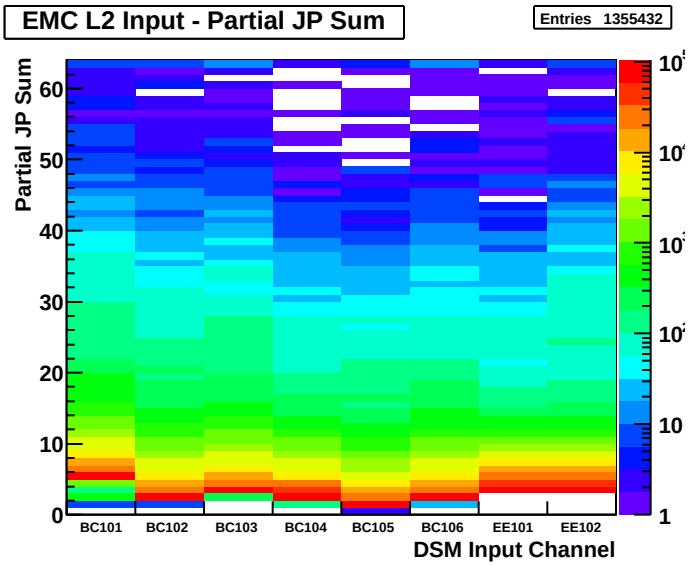

EMC L2 Input - JPX/JPA bits
Entries 307
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

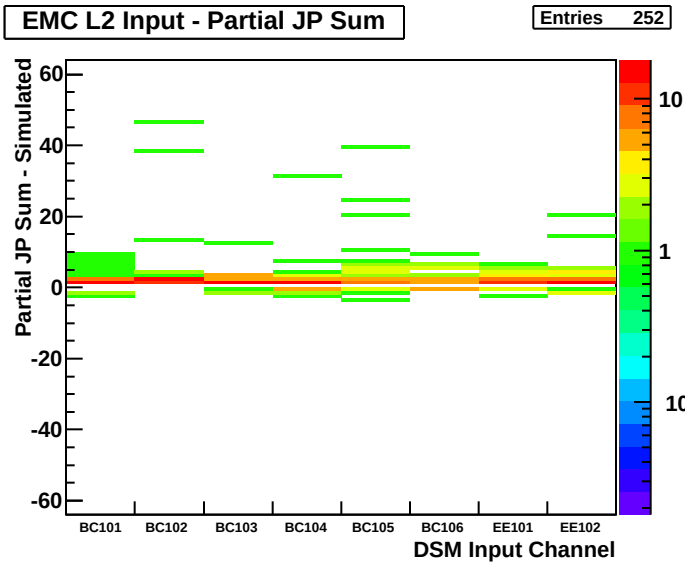

EMC L2 Input - JPY/JPB bits
Entries 1355432
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

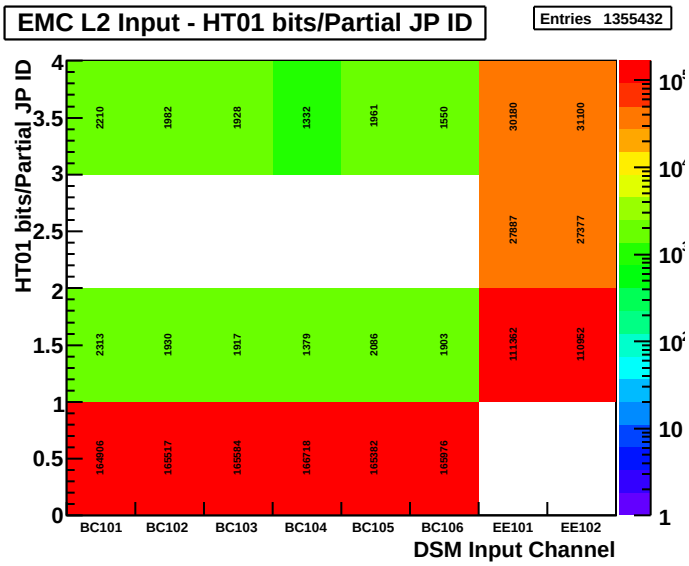

EMC L2 Input - JPY/JPB bits
Entries 304
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

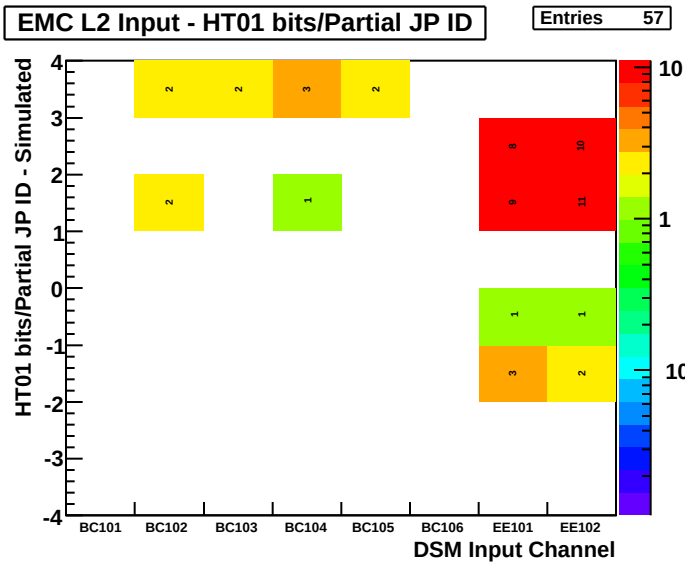

EMC L2 Input - JPZ/JPC bits
Entries 1355432
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

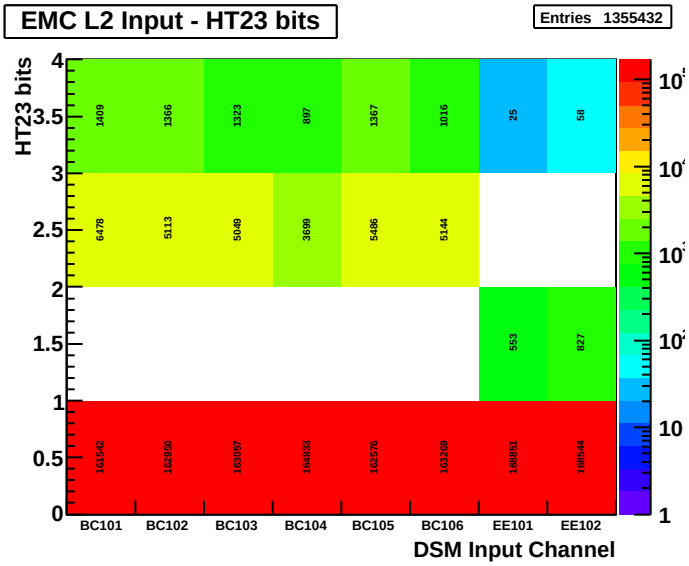

EMC L2 Input - JPZ/JPC bits
Entries 11
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

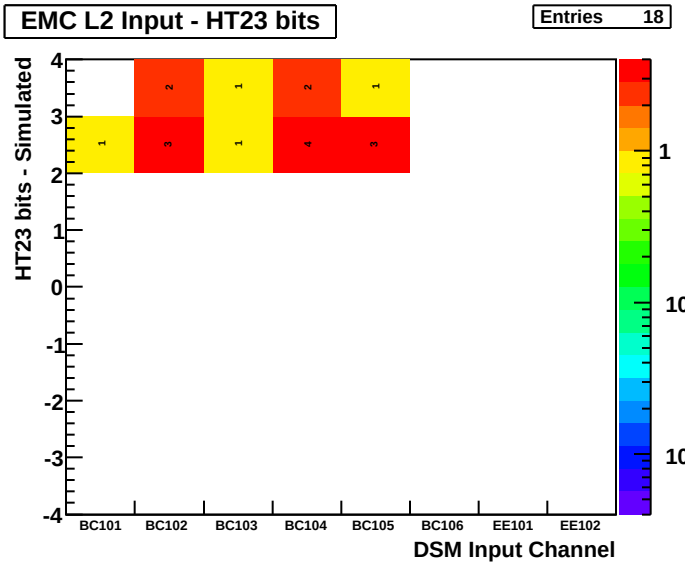

EMC L2 Input - Partial JP Sum
Entries 1355432
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

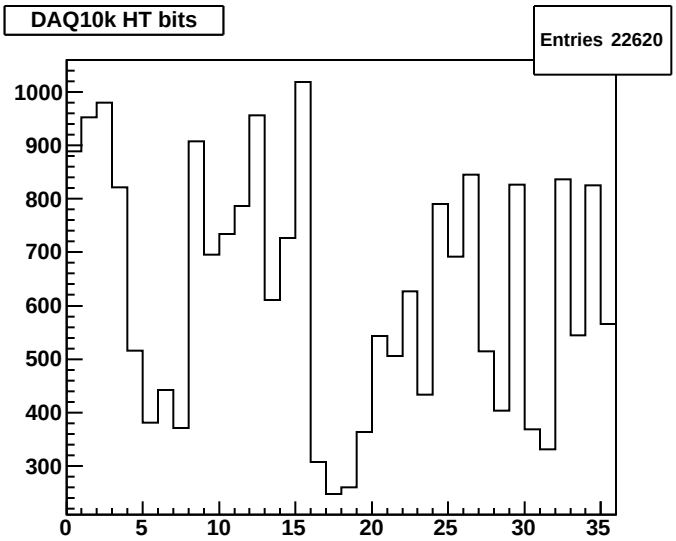

EMC L2 Input - Partial JP Sum
Entries 252
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

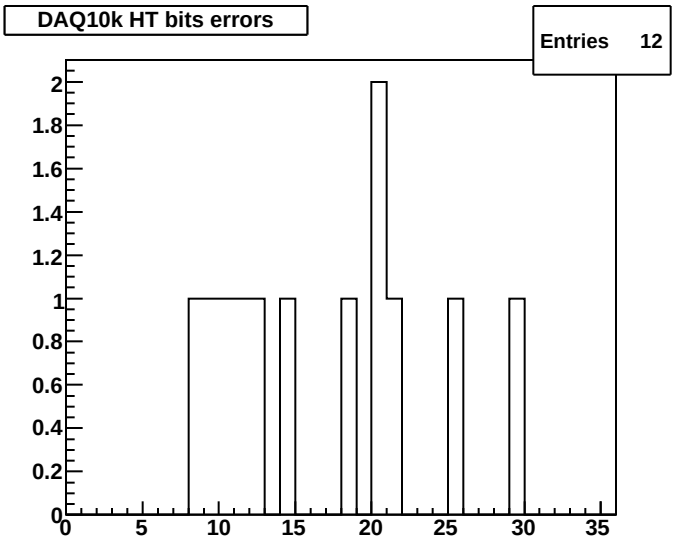

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1355432
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

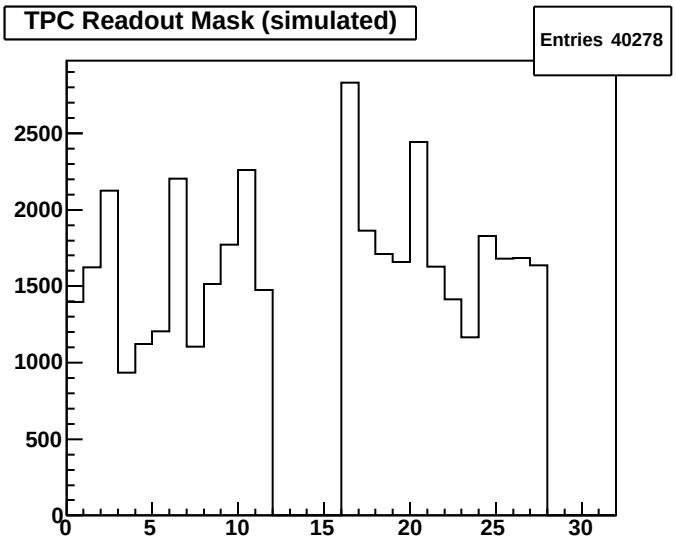

EMC L2 Input - HT01 bits/Partial JP ID
Entries 57
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

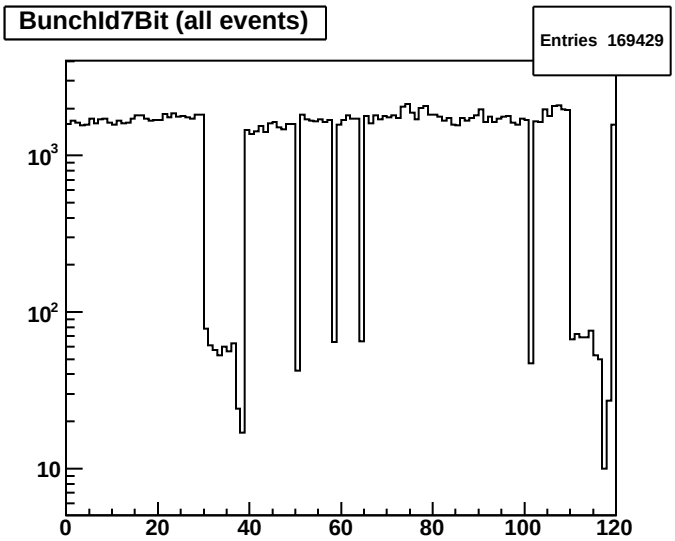

EMC L2 Input - HT23 bits
Entries 1355432
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

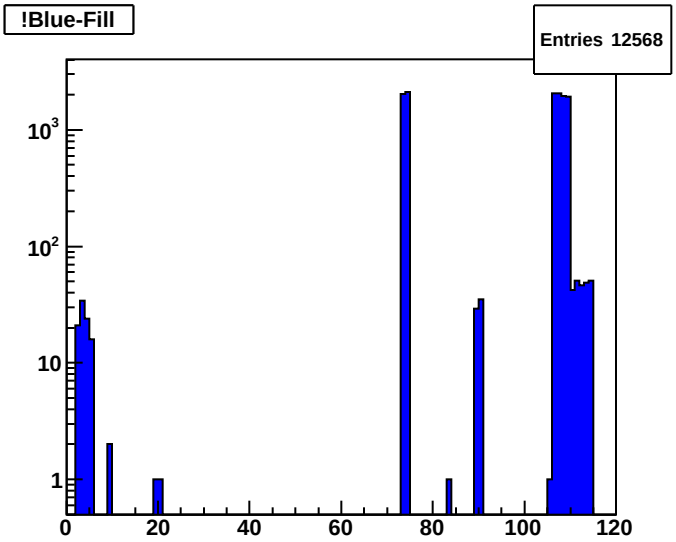

EMC L2 Input - HT23 bits
Entries 18
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

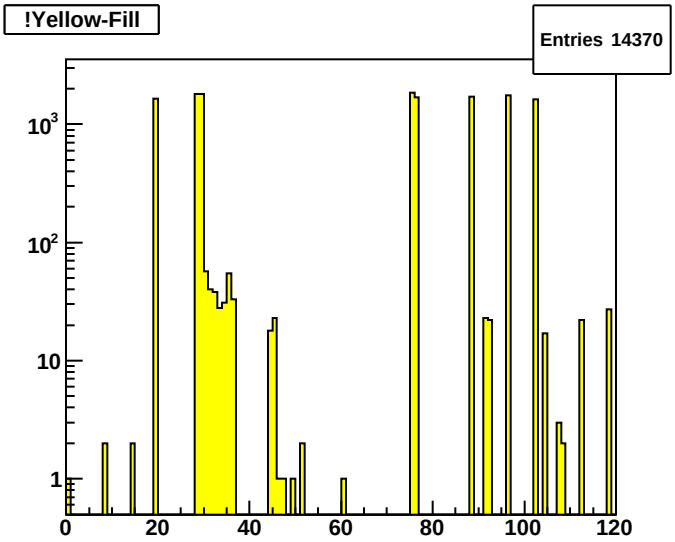

DAQ10k HT bits
Entries 22620

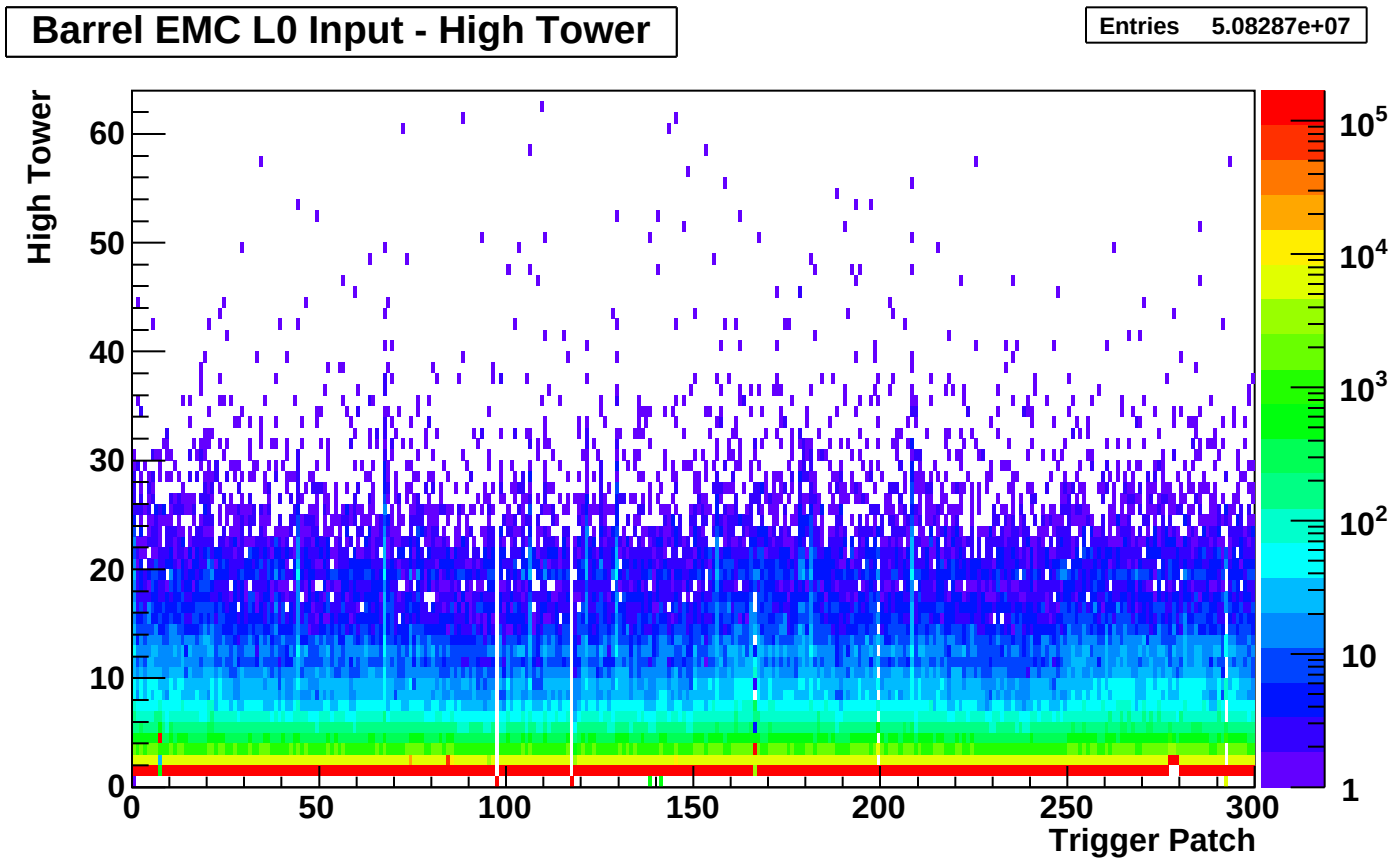

DAQ10k HT bits errors
Entries 12

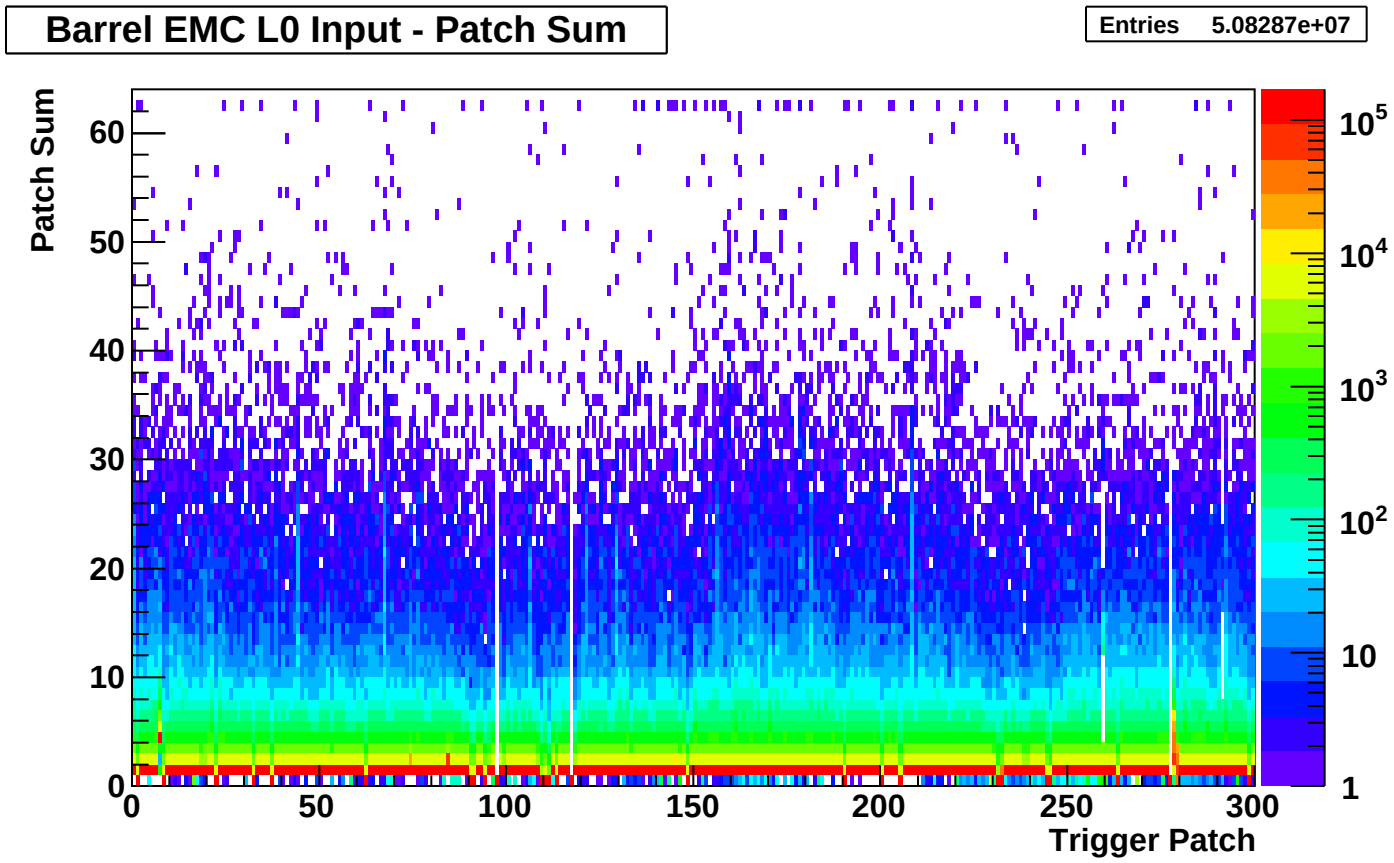

TPC Readout Mask (simulated)
Entries 40278

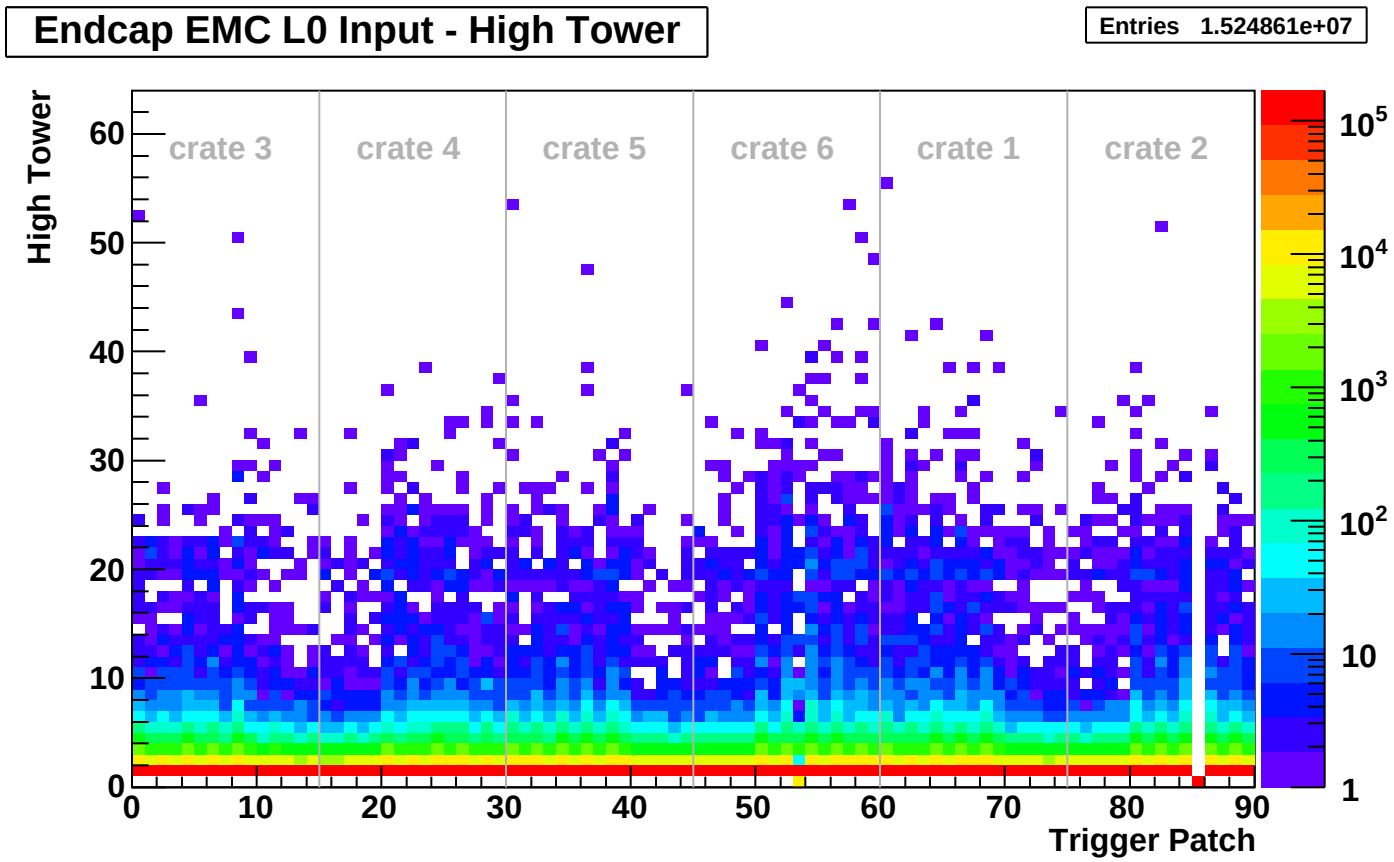

BunchId7Bit (all events)
Entries 169429

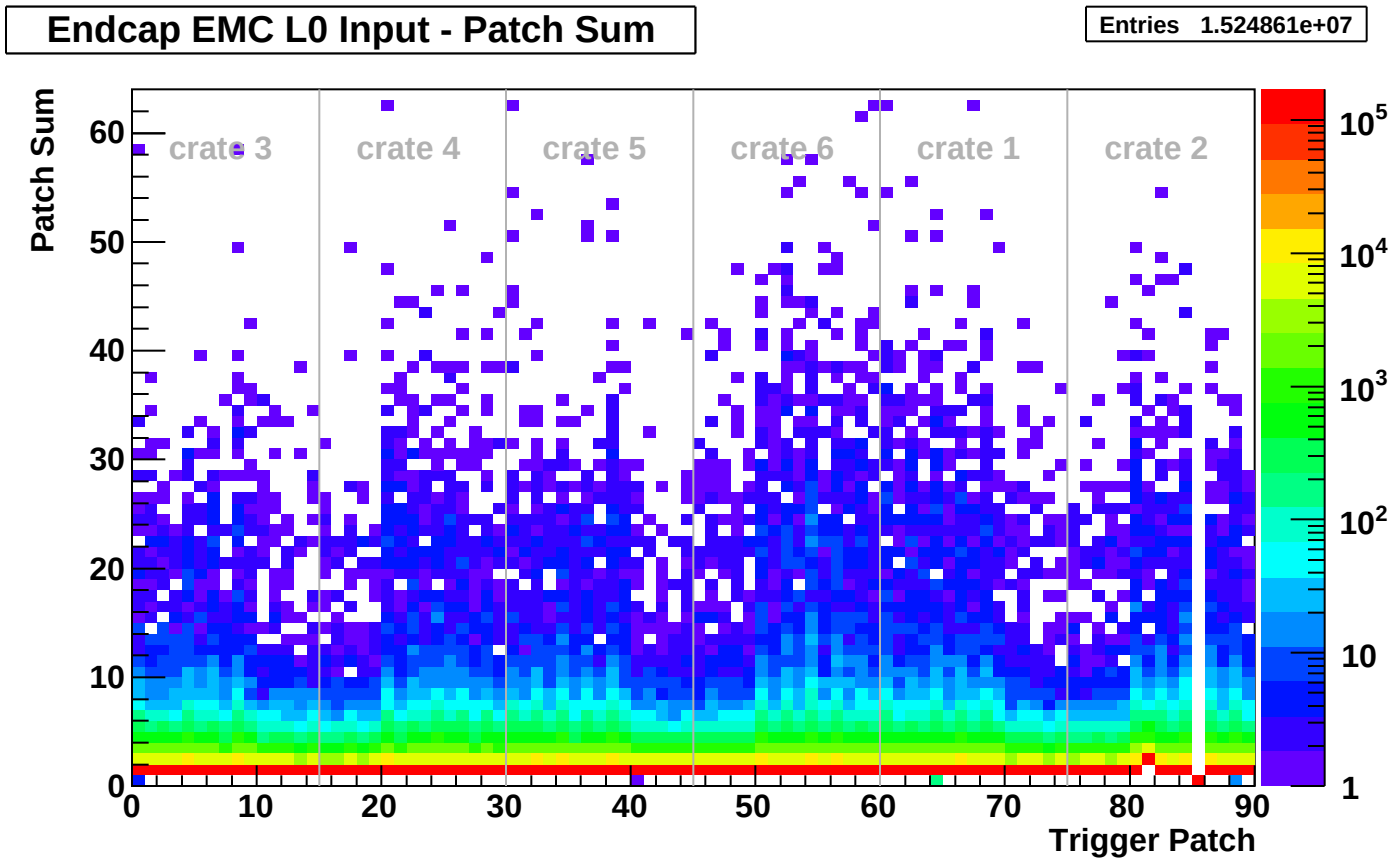

!Blue-Fill
Entries 12568

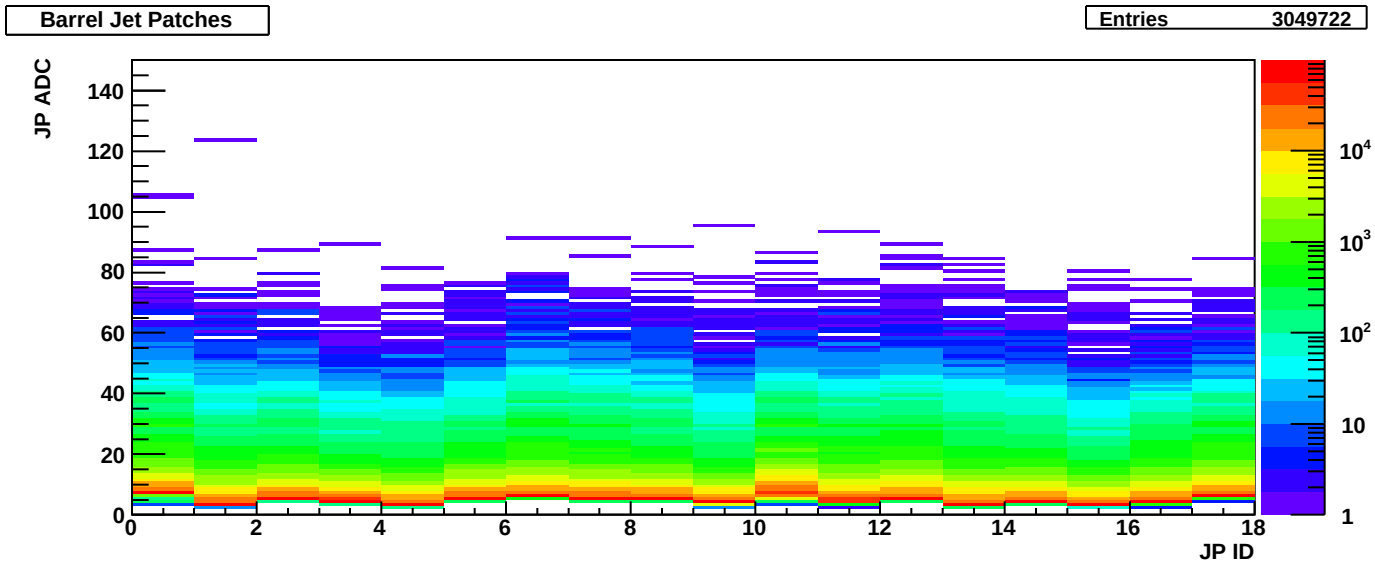

!Yellow-Fill
Entries 14370

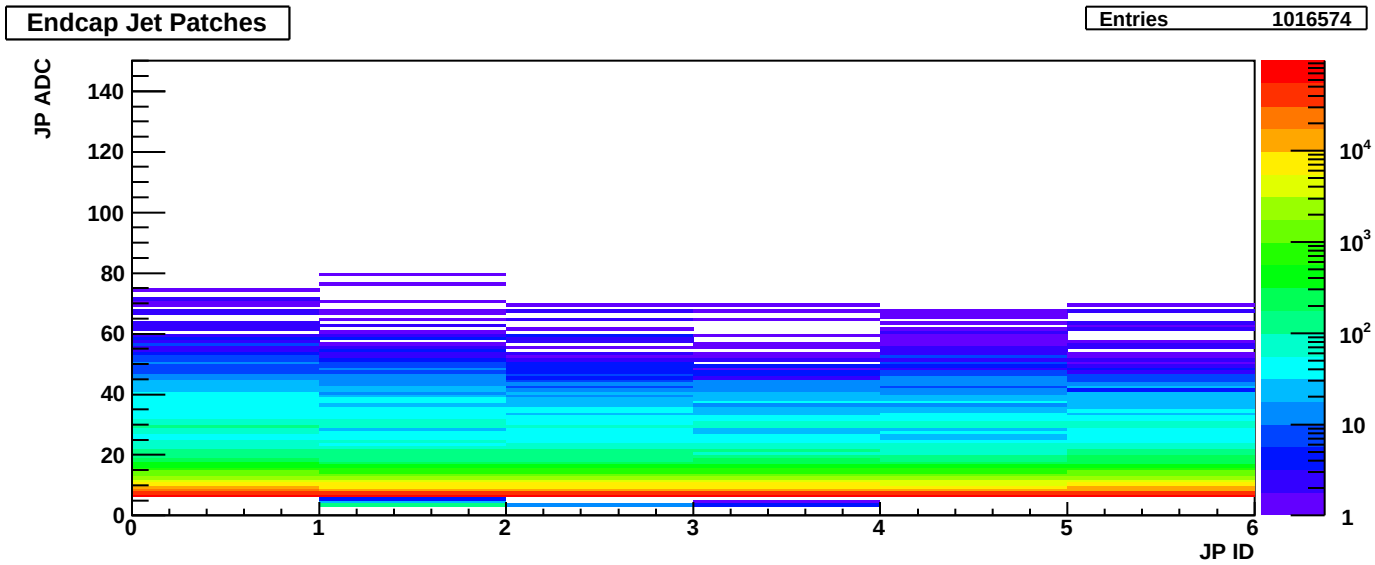

Barrel EMC L0 Input - High Tower
Entries 5.08287e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

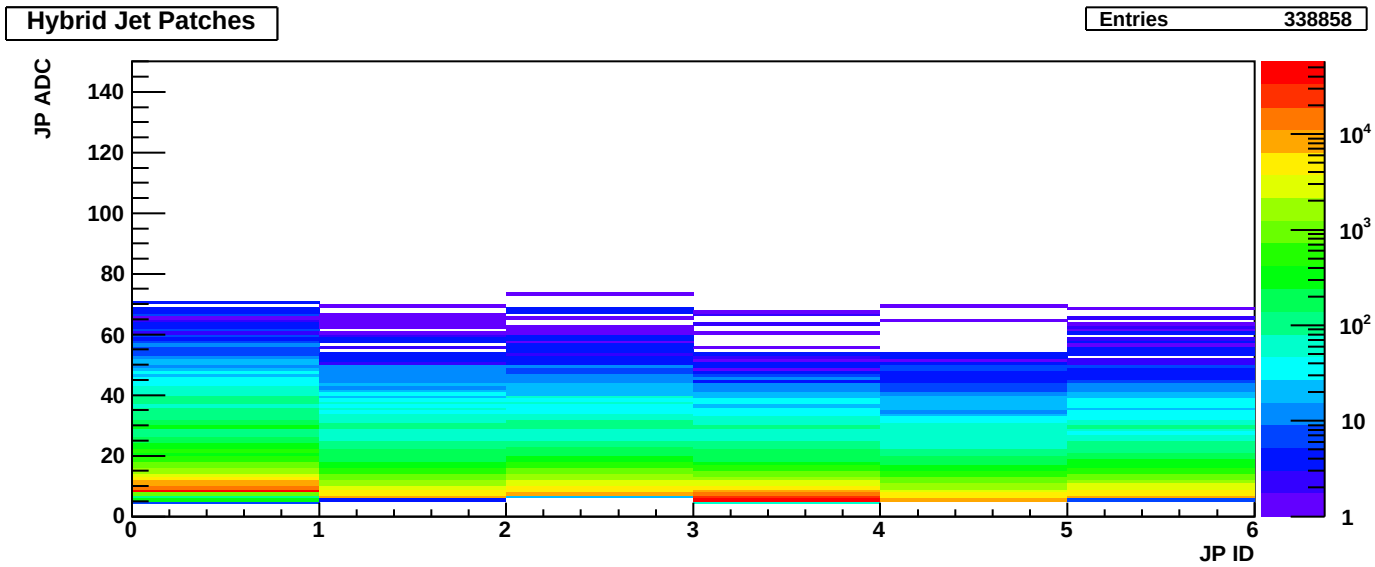

Barrel EMC L0 Input - Patch Sum
Entries 5.08287e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

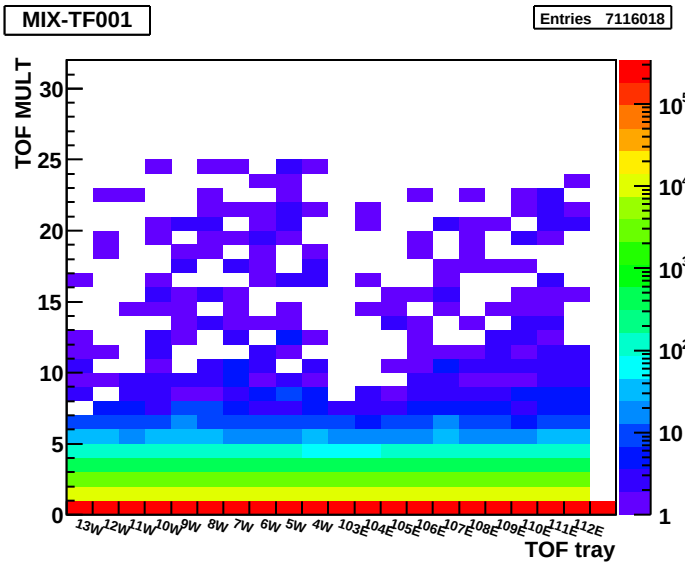

Endcap EMC L0 Input - High Tower
Entries 1.524861e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

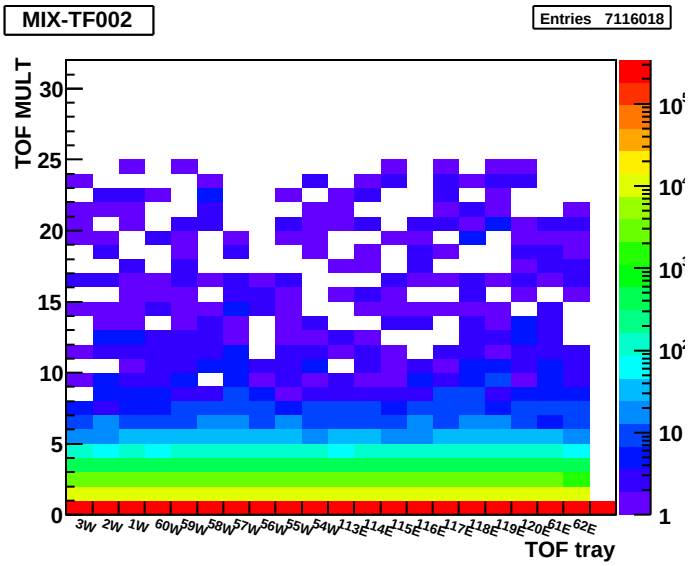

Endcap EMC L0 Input - Patch Sum
Entries 1.524861e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

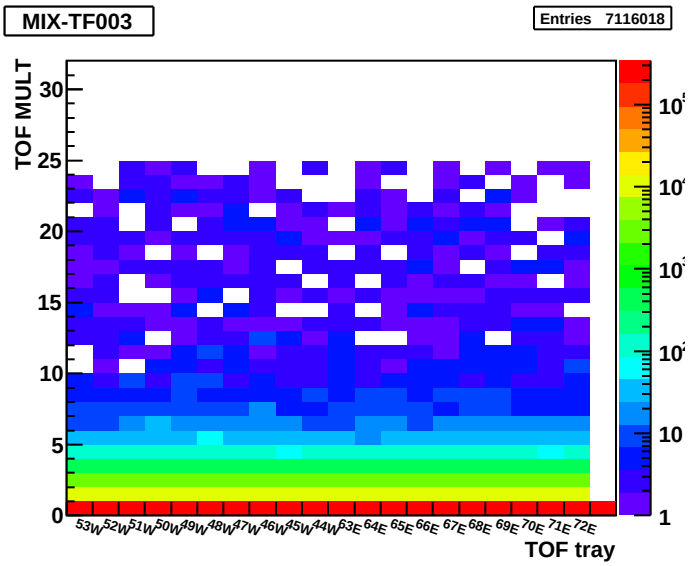

Barrel Jet Patches
Entries 3049722
JP ADC
JP ID

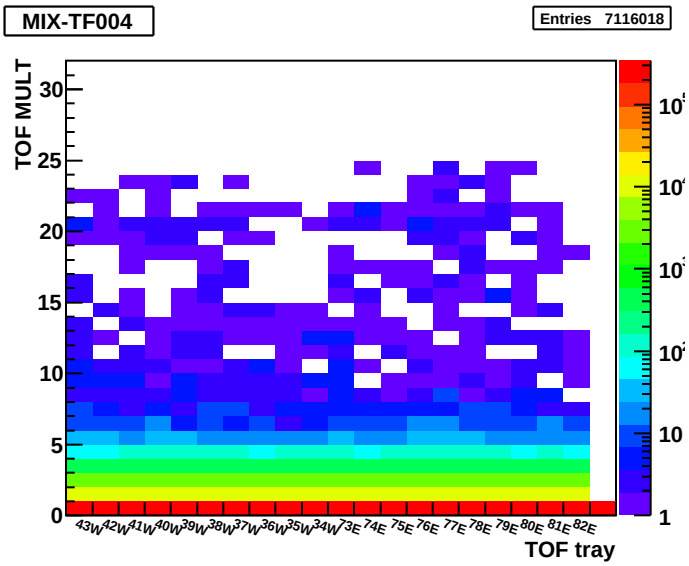

Endcap Jet Patches
Entries 1016574
JP ADC
JP ID

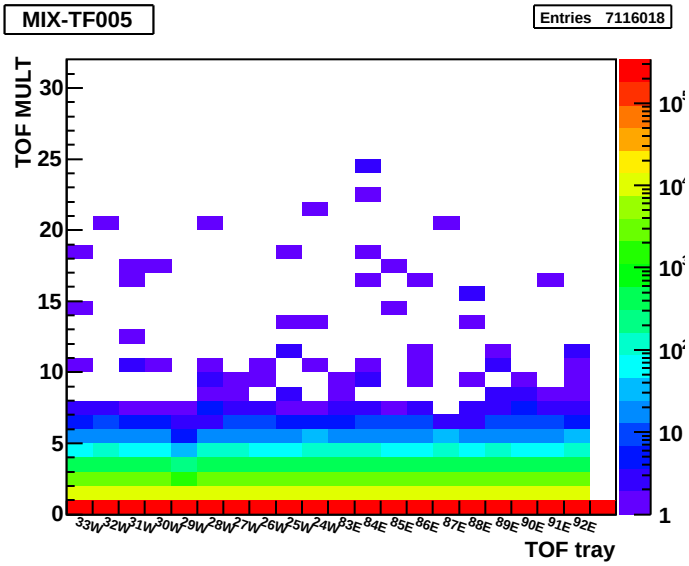

Hybrid Jet Patches
Entries 338858
JP ADC
JP ID

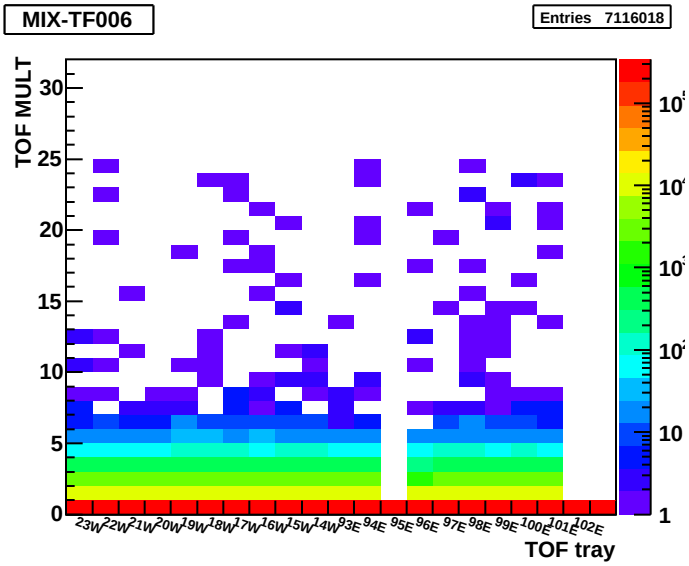

MIX-TF001
Entries 7116018
TOF MULT
TOF tray

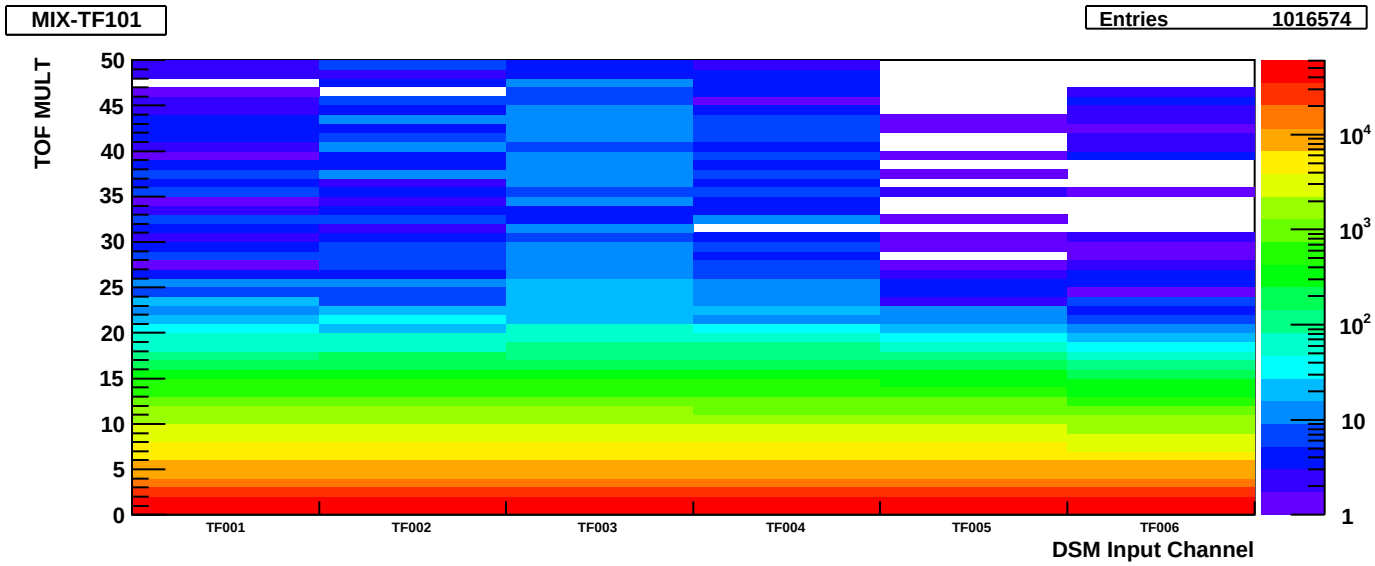

MIX-TF002
Entries 7116018
TOF MULT
TOF tray

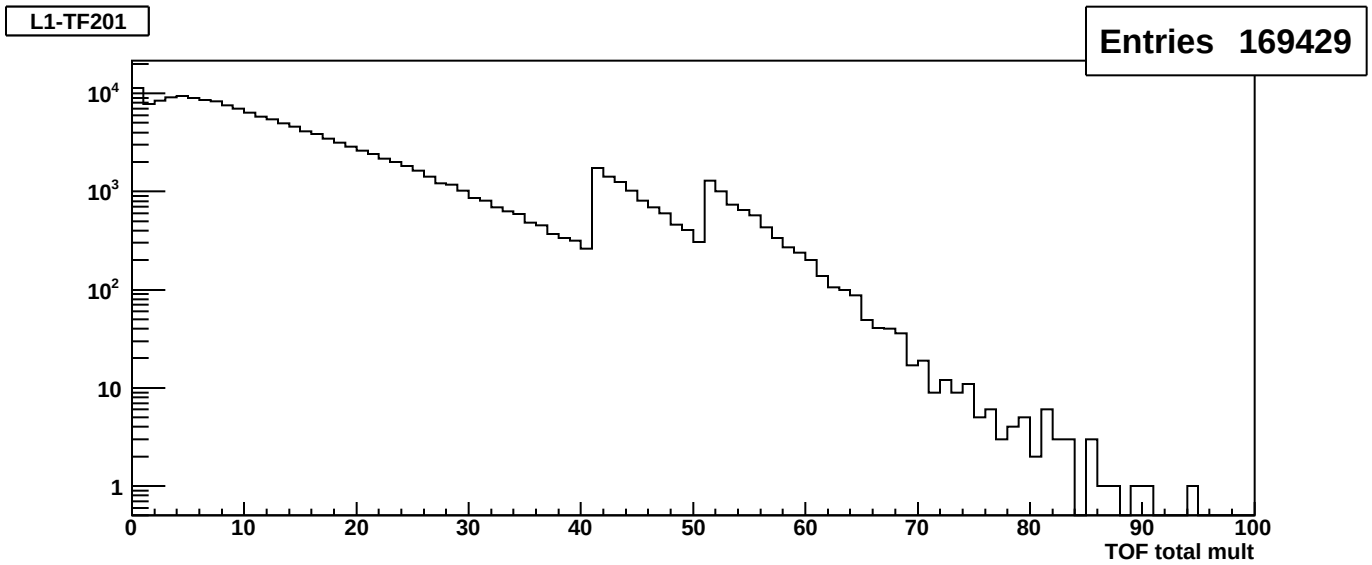

MIX-TF003
Entries 7116018
TOF MULT
TOF tray

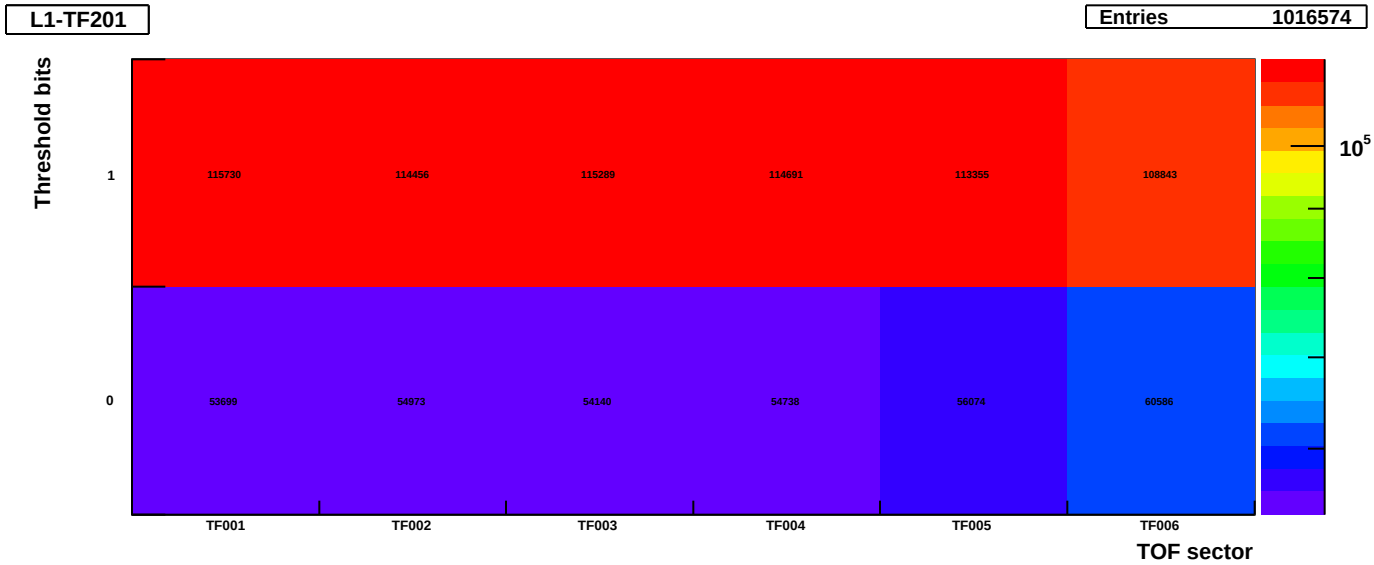

MIX-TF004
Entries 7116018
TOF MULT
TOF tray

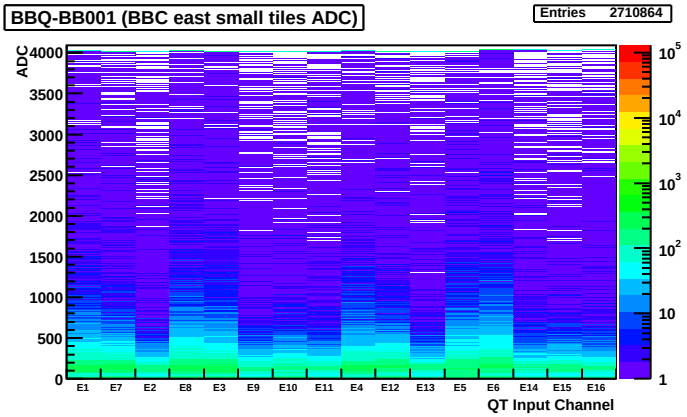

MIX-TF005
Entries 7116018
TOF MULT
TOF tray

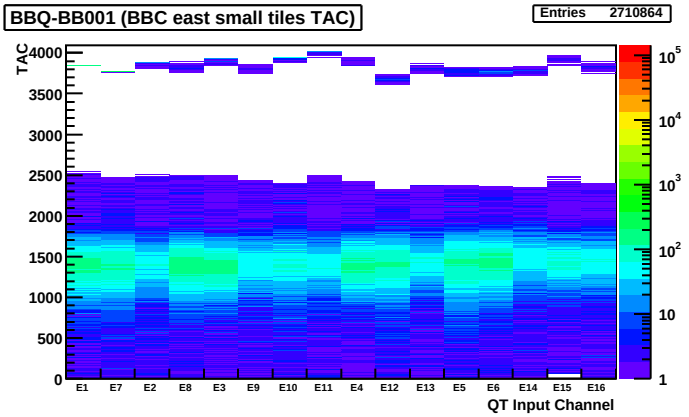

MIX-TF006
Entries 7116018
TOF MULT
TOF tray

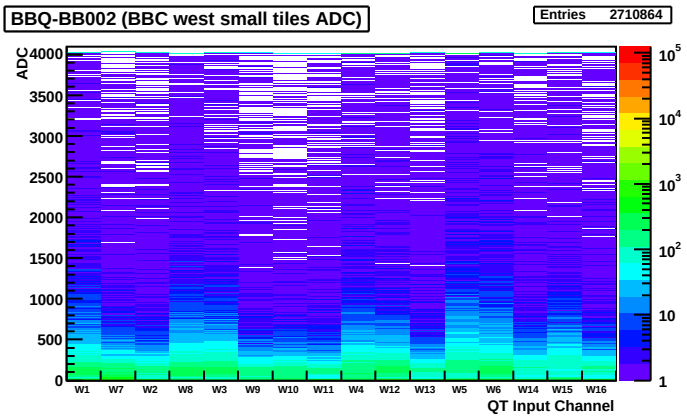

MIX-TF101
Entries 1016574
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

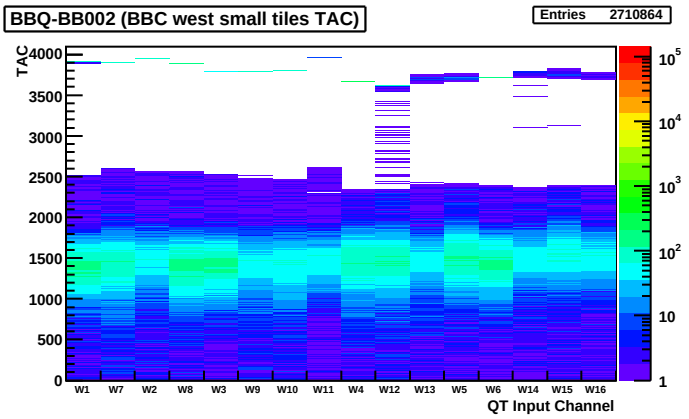

L1-TF201
Entries 169429
TOF total mult

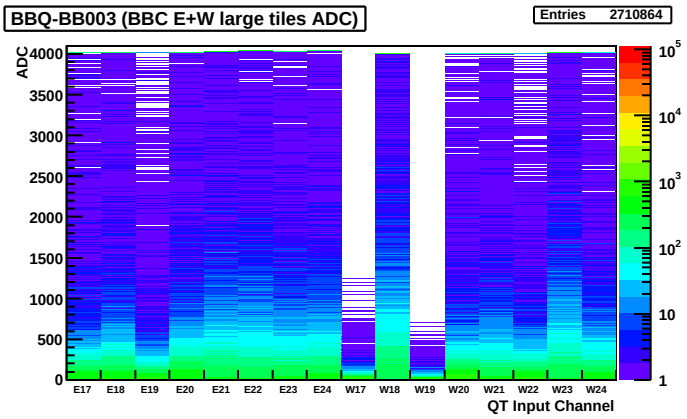

L1-TF201
Entries 1016574
Threshold bits
115730
114456
115289
114691
113355
108843
53699
54973
54140
54738
56074
60586
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

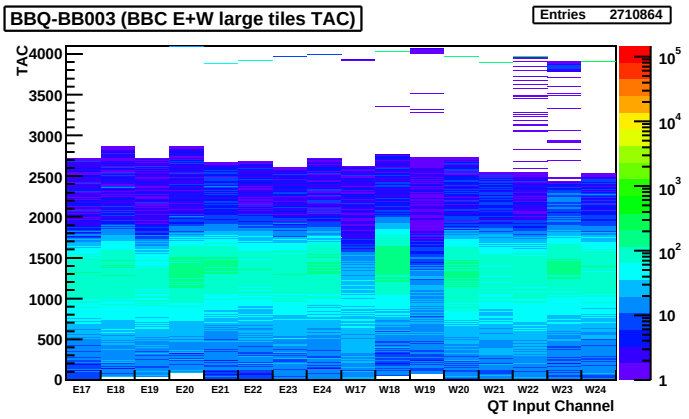

BBQ-BB001 (BBC east small tiles ADC)
Entries 2710864
ADC
QT Input Channel

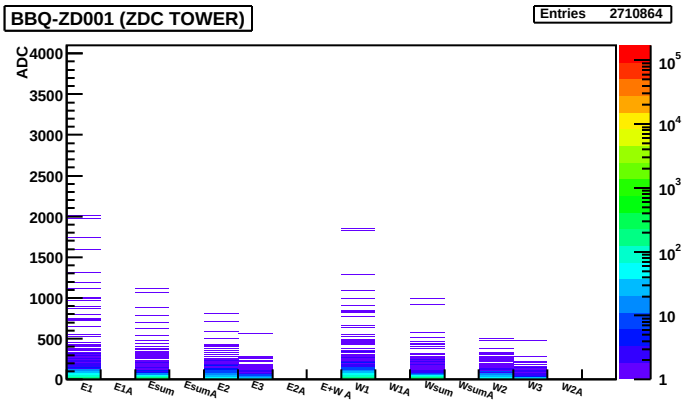

BBQ-BB001 (BBC east small tiles TAC)
Entries 2710864
TAC
QT Input Channel

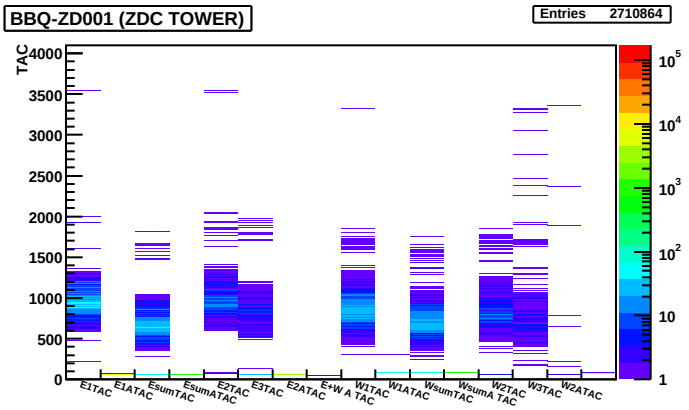

BBQ-BB002 (BBC west small tiles ADC)
Entries 2710864
ADC
QT Input Channel

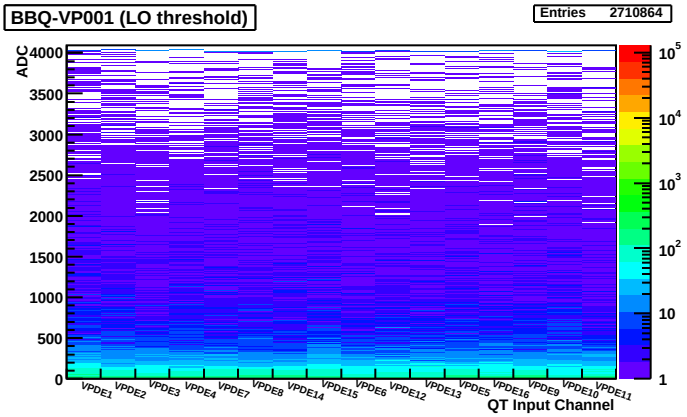

BBQ-BB002 (BBC west small tiles TAC)
Entries 2710864
TAC
QT Input Channel

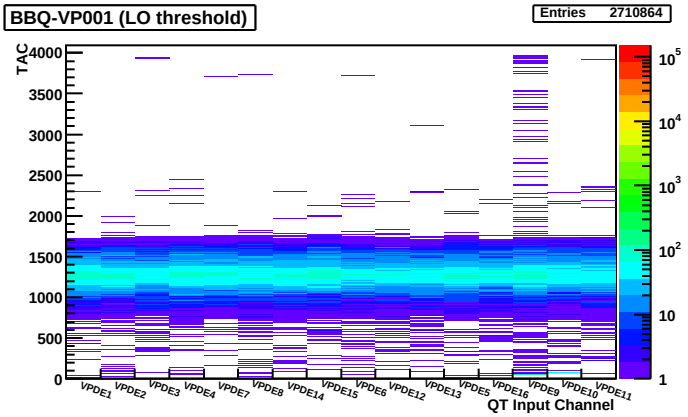

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2710864
ADC
QT Input Channel

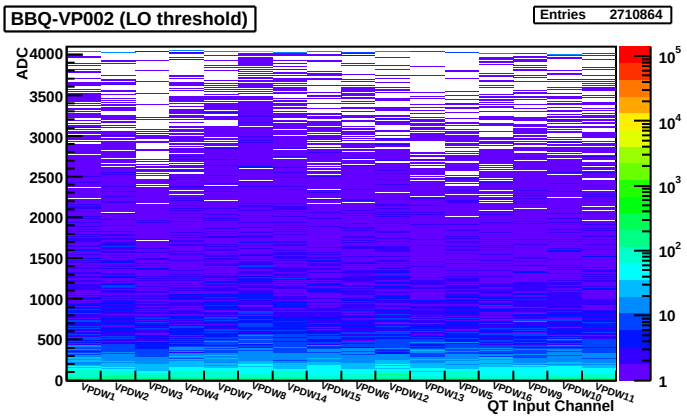

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2710864
TAC
QT Input Channel

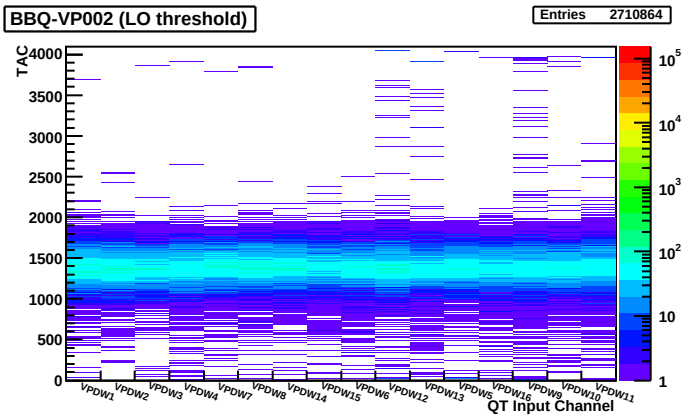

BBQ-ZD001 (ZDC TOWER)
Entries 2710864
ADC

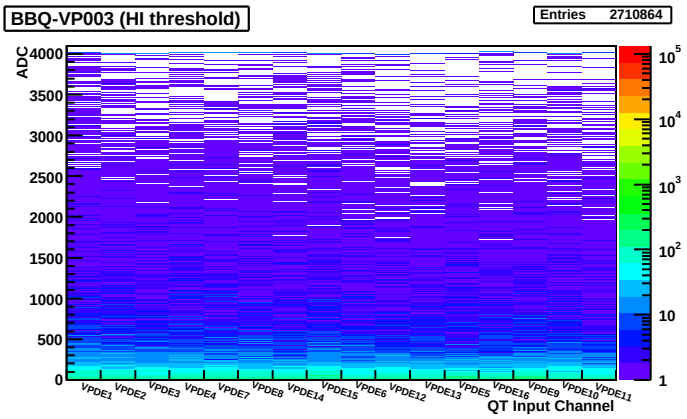

BBQ-ZD001 (ZDC TOWER)
Entries 2710864
TAC

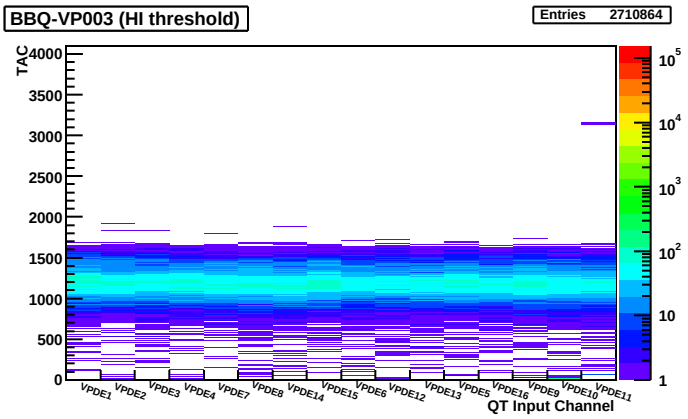

BBQ-VP001 (LO threshold)
Entries 2710864
ADC
QT Input Channel

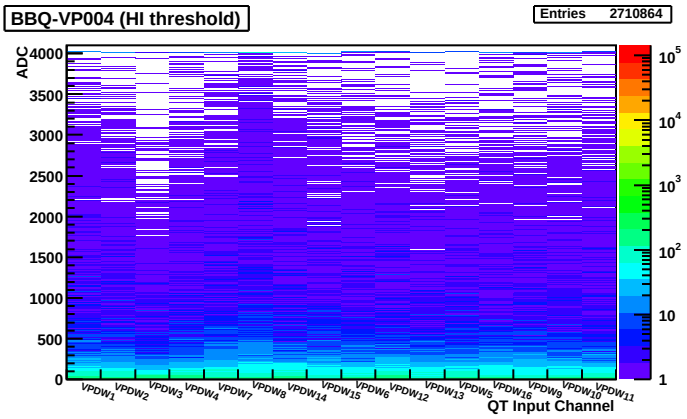

BBQ-VP001 (LO threshold)
Entries 2710864
TAC
QT Input Channel

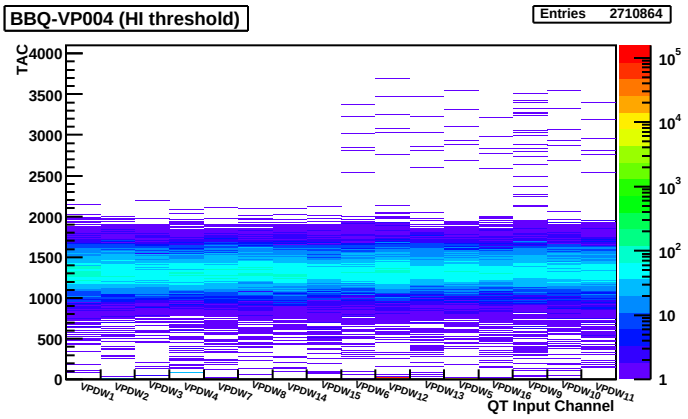

BBQ-VP002 (LO threshold)
Entries 2710864
ADC
QT Input Channel

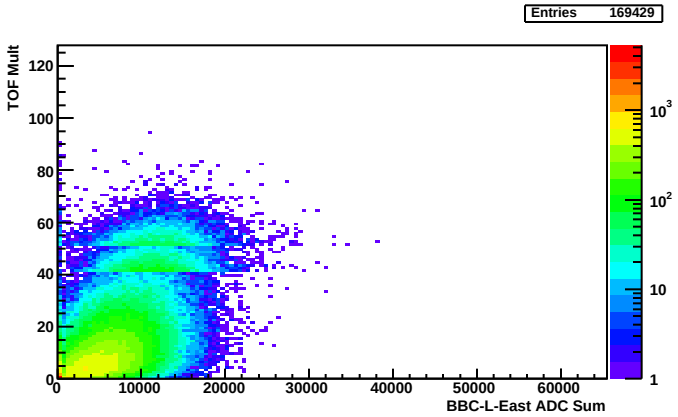

BBQ-VP002 (LO threshold)
Entries 2710864
TAC
QT Input Channel

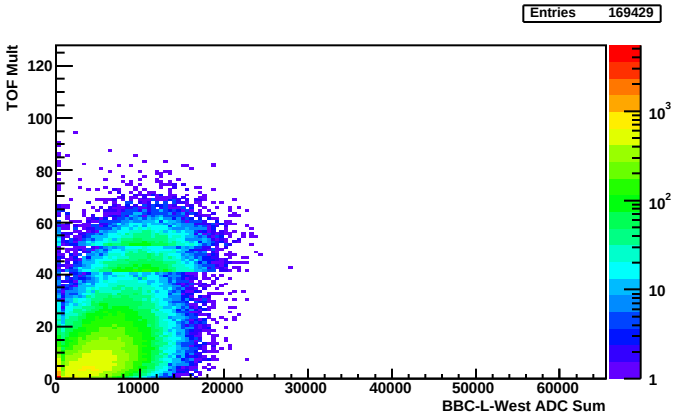

BBQ-VP003 (HI threshold)
Entries 2710864
ADC
QT Input Channel

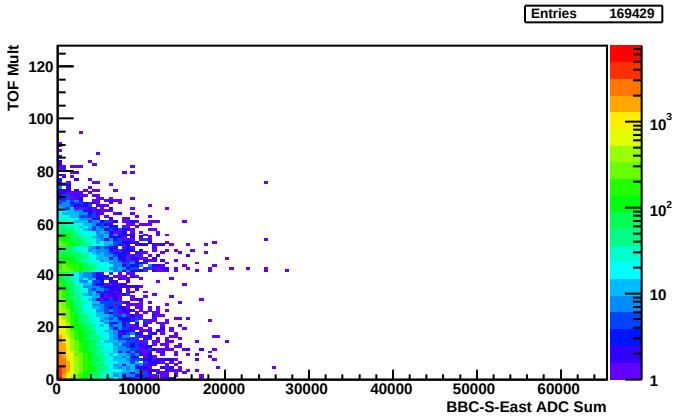

BBQ-VP003 (HI threshold)
Entries 2710864
TAC
QT Input Channel

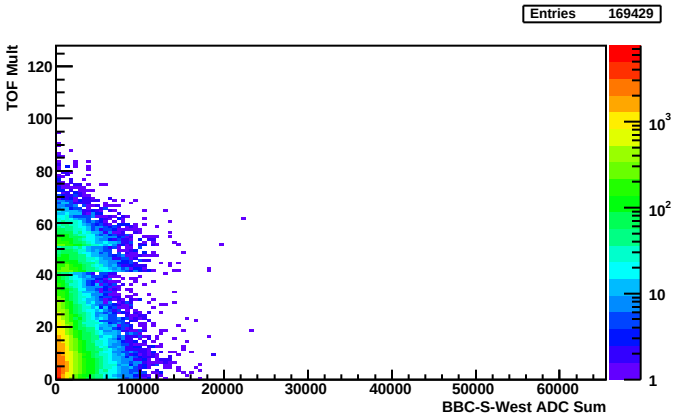

BBQ-VP004 (HI threshold)
Entries 2710864
ADC
QT Input Channel

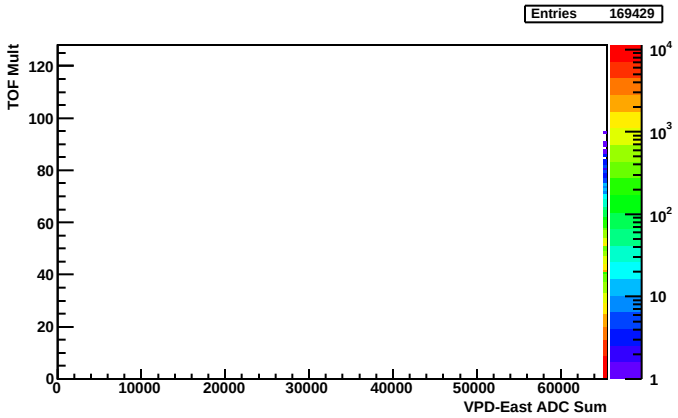

BBQ-VP004 (HI threshold)
Entries 2710864
TAC
QT Input Channel

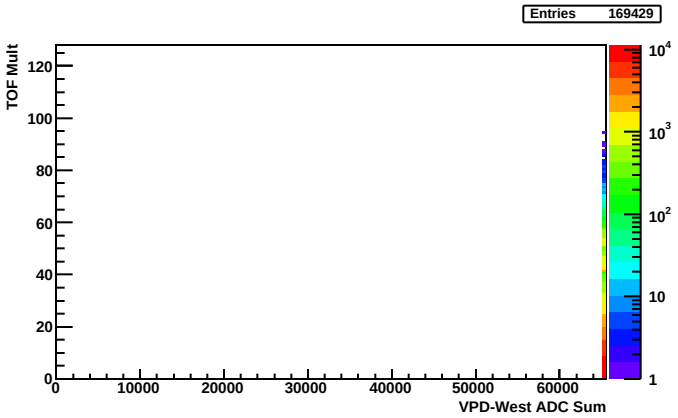

Entries 169429
TOF Mult
BBC-L-East ADC Sum

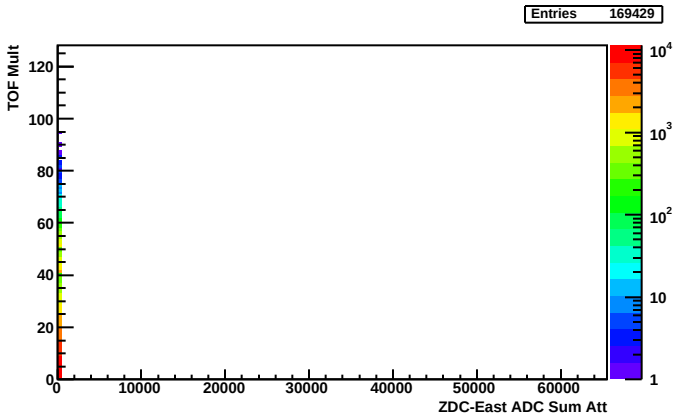

Entries 169429
TOF Mult
BBC-L-West ADC Sum

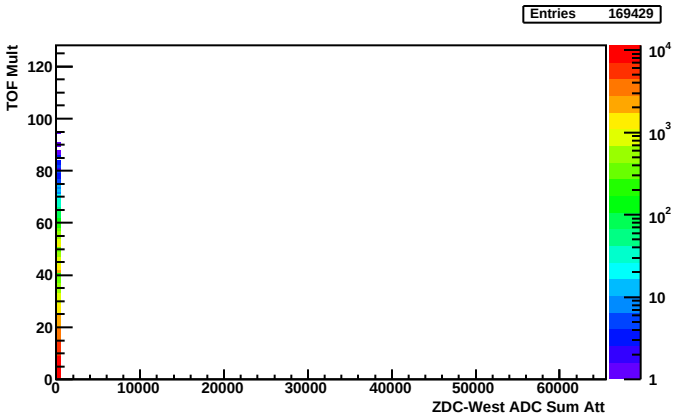

Entries 169429
TOF Mult
BBC-S-East ADC Sum

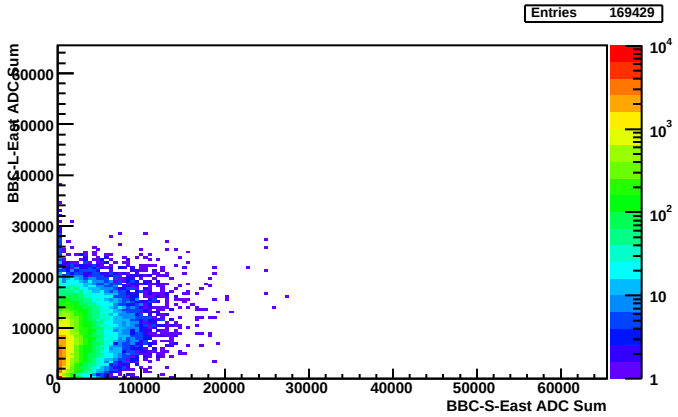

Entries 169429
TOF Mult
BBC-S-West ADC Sum

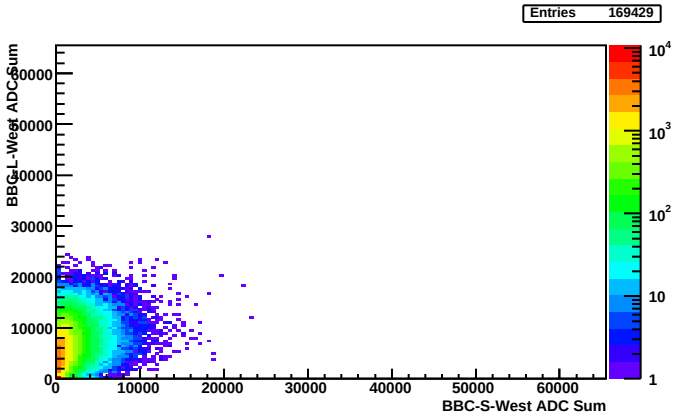

Entries 169429
TOF Mult
VPD-East ADC Sum

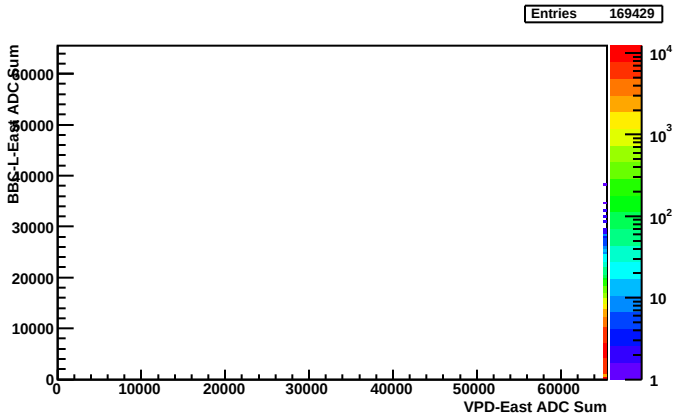

Entries 169429
TOF Mult
VPD-West ADC Sum

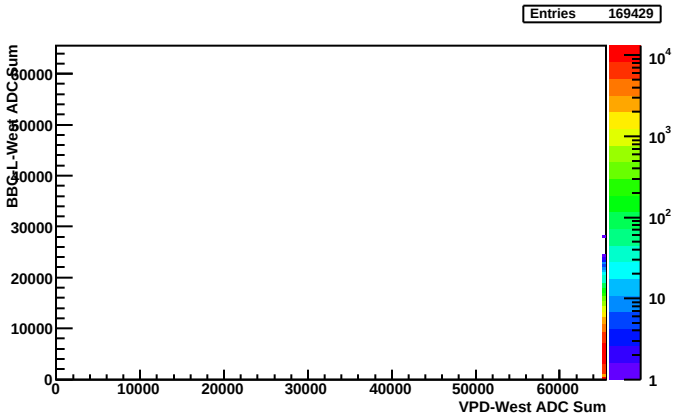

Entries 169429
TOF Mult
ZDC-East ADC Sum Att

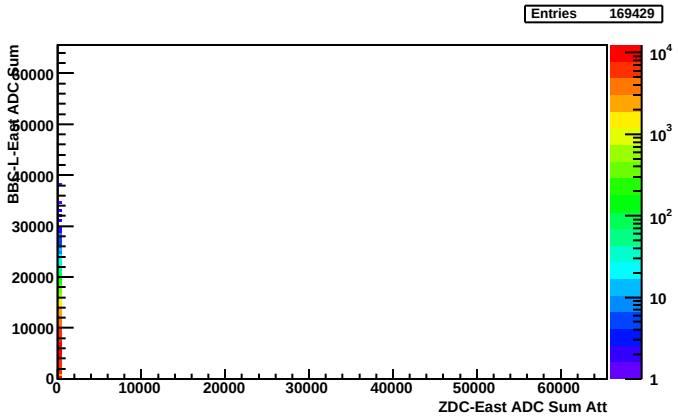

Entries 169429
TOF Mult
ZDC-West ADC Sum Att

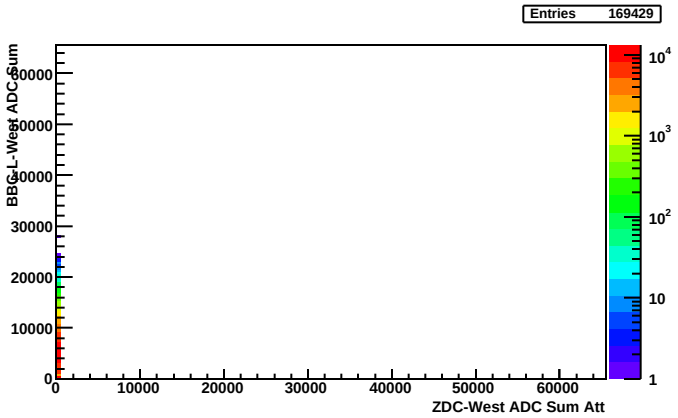

Entries 169429
BBC-L-East ADC Sum
BBC-S-East ADC Sum

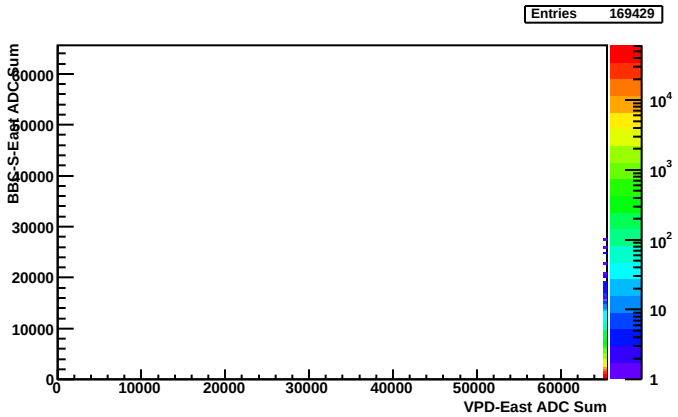

Entries 169429
BBC-L-West ADC Sum
BBC-S-West ADC Sum

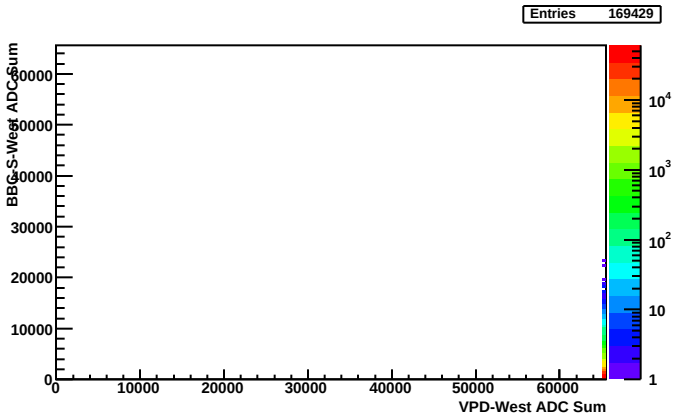

Entries 169429
BBC-L-East ADC Sum
VPD-East ADC Sum

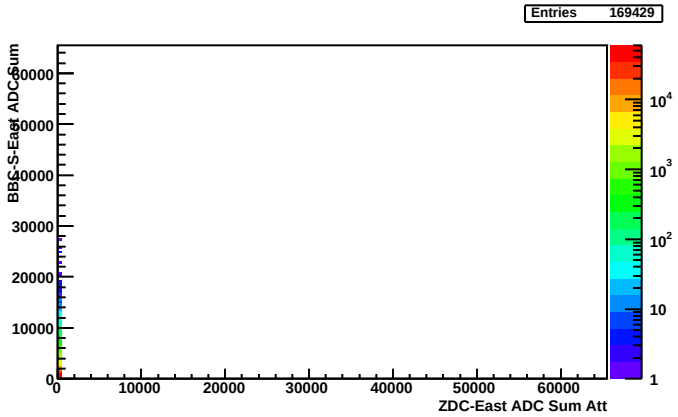

Entries 169429
BBC-L-West ADC Sum
VPD-West ADC Sum

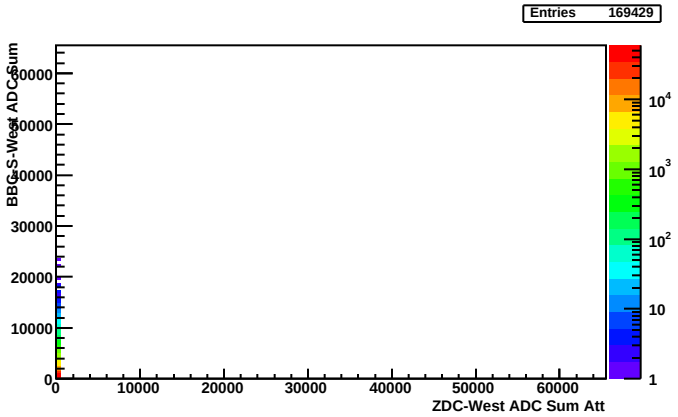

Entries 169429
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

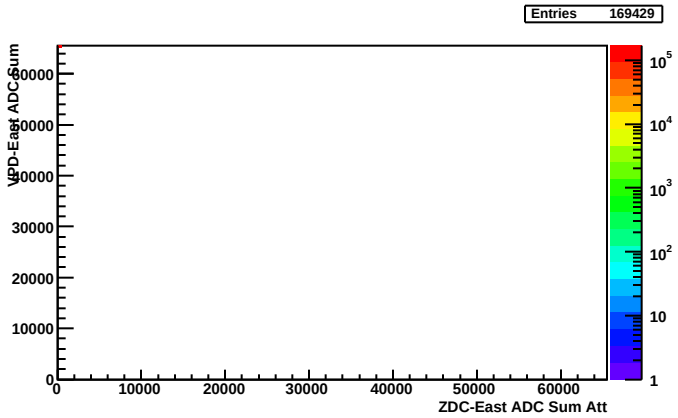

Entries 169429
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

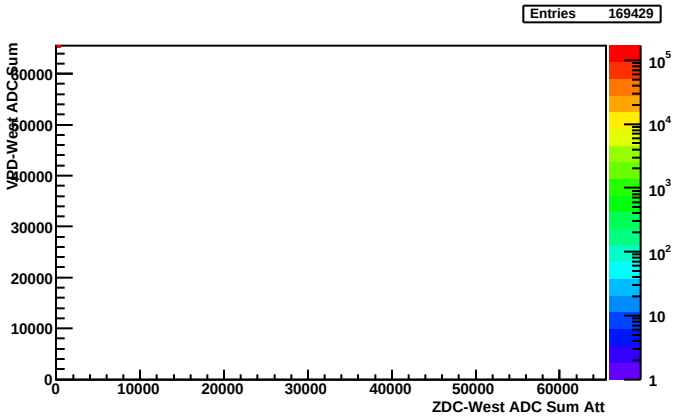

Entries 169429
BBC-S-East ADC Sum
VPD-East ADC Sum

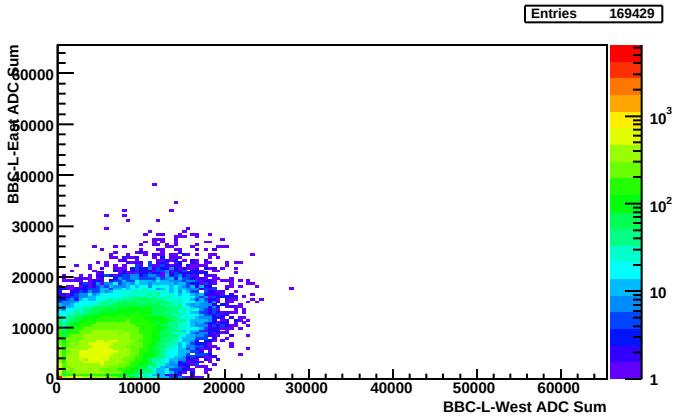

Entries 169429
BBC-S-West ADC Sum
VPD-West ADC Sum

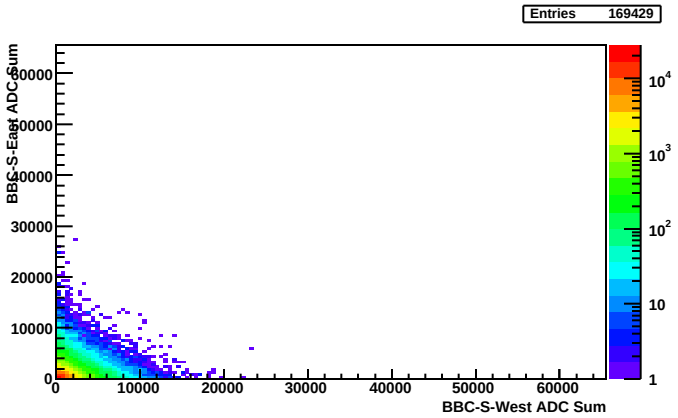

Entries 169429
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

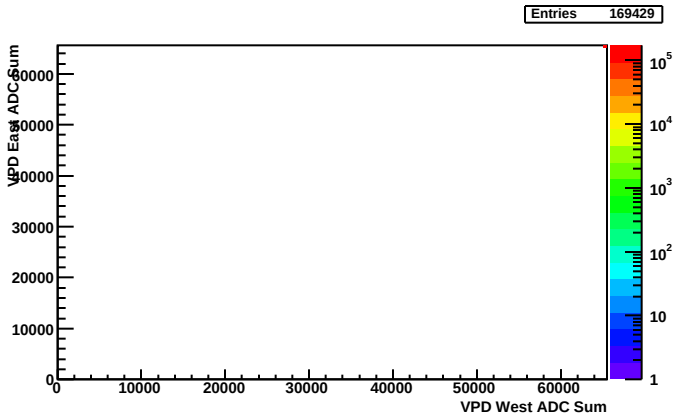

Entries 169429
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

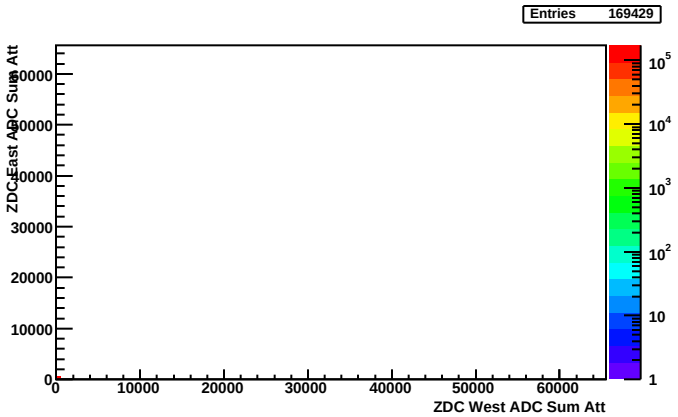

Entries 169429
VPD-East ADC Sum
ZDC-East ADC Sum Att

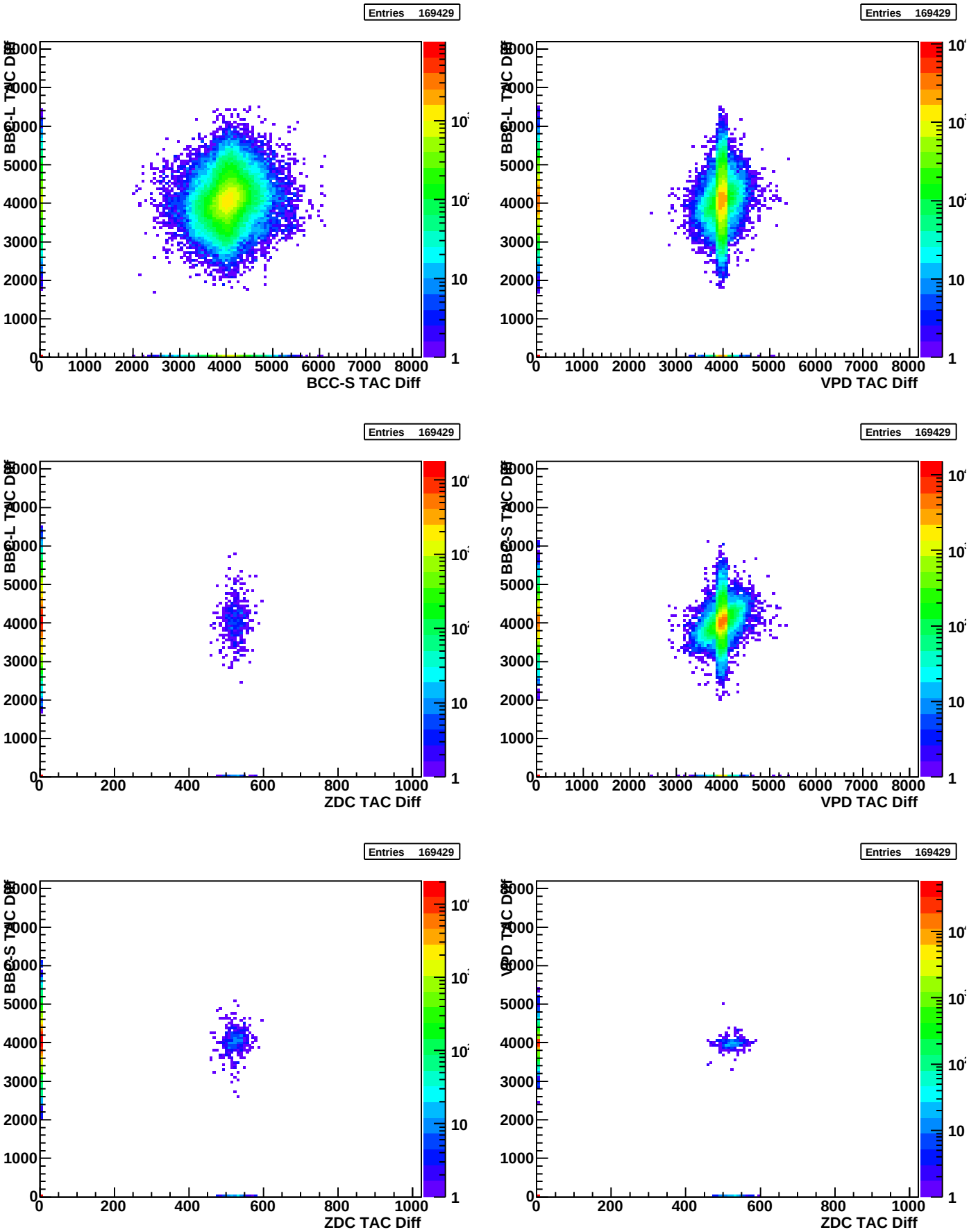

Entries 169429
VPD-West ADC Sum
ZDC-West ADC Sum Att

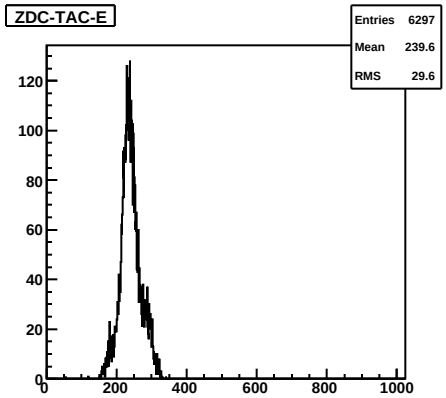

Entries 169429
BBC-L-East ADC Sum
BBC-L-West ADC Sum

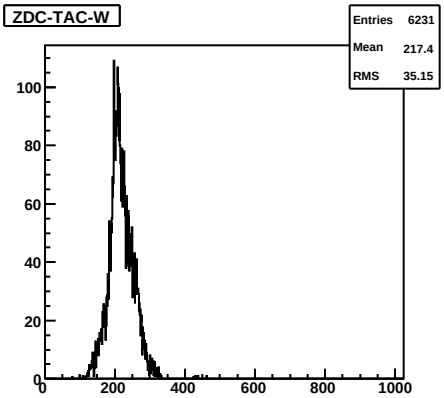

Entries 169429
BBC-S-East ADC Sum
BBC-S-West ADC Sum

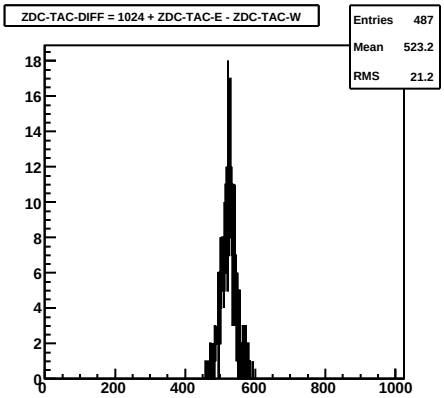

Entries 169429
VPD East ADC Sum
VPD West ADC Sum

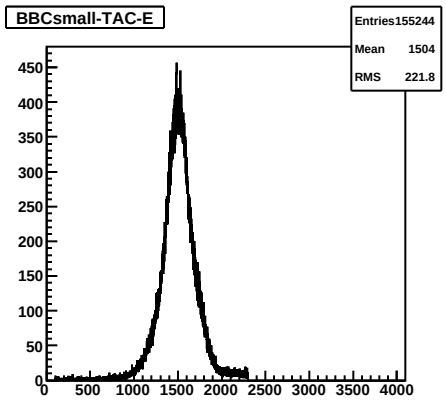

Entries 169429
ZDC East ADC Sum Att
ZDC West ADC Sum Att

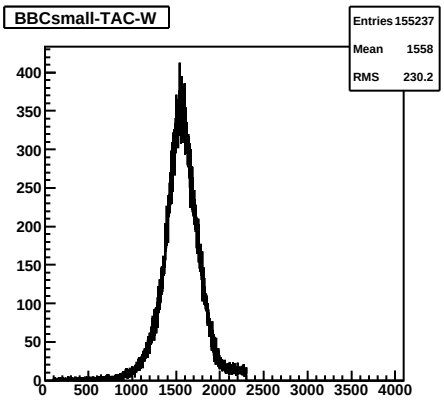

Entries 169429
BBC-L TAC Diff
BCC-S TAC Diff
Entries 169429
BBC-L TAC Diff
VPD TAC Diff
Entries 169429
BBC-L TAC Diff
ZDC TAC Diff
Entries 169429
BBC-S TAC Diff
VPD TAC Diff
Entries 169429
BBC-S TAC Diff
ZDC TAC Diff
Entries 169429
VPD TAC Diff
ZDC TAC Diff

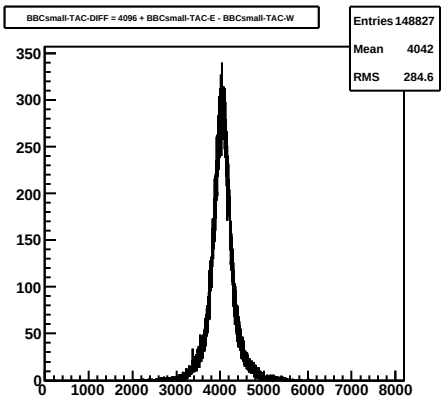

ZDC-TAC-E
Entries 6297
Mean 239.6
RMS 29.6

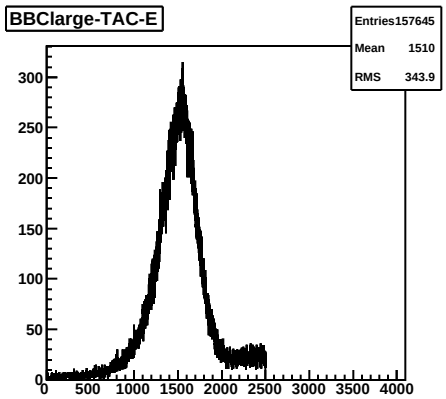

ZDC-TAC-W
Entries 6231
Mean 217.4
RMS 35.15

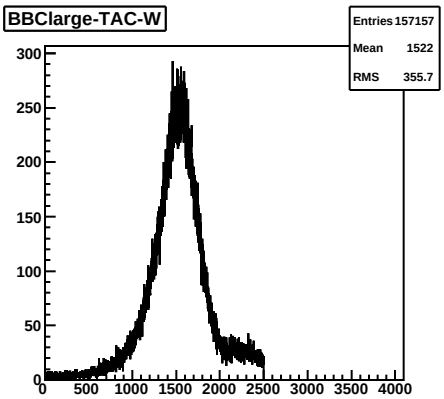

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 487
Mean 523.2
RMS 21.2

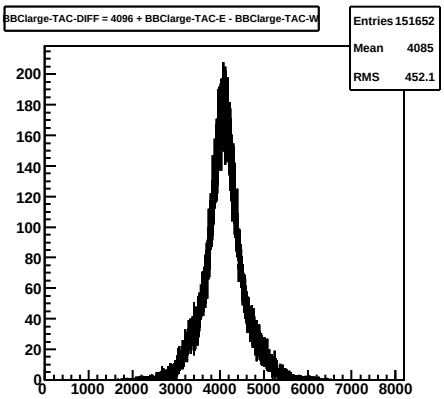

BBCsmall-TAC-E
Entries 155244
Mean 1504
RMS 221.8

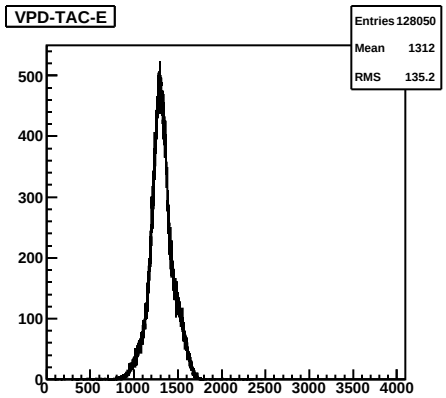

BBCsmall-TAC-W
Entries 155237
Mean 1558
RMS 230.2

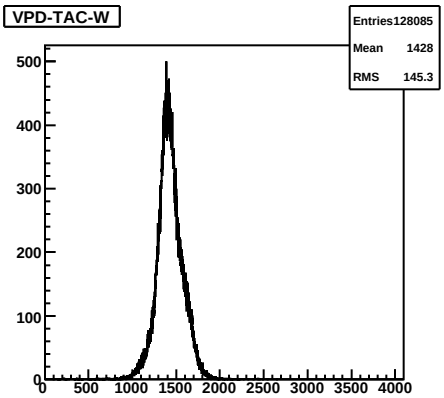

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 148827
Mean 4042
RMS 284.6

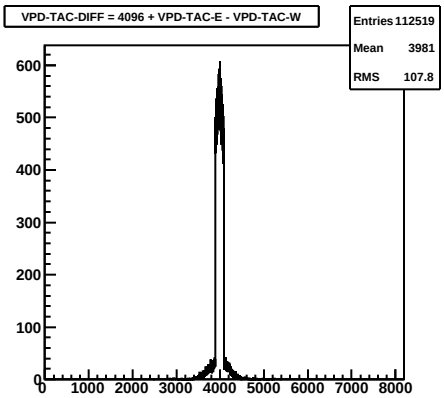

BBClarge-TAC-E
Entries 157645
Mean 1510
RMS 343.9

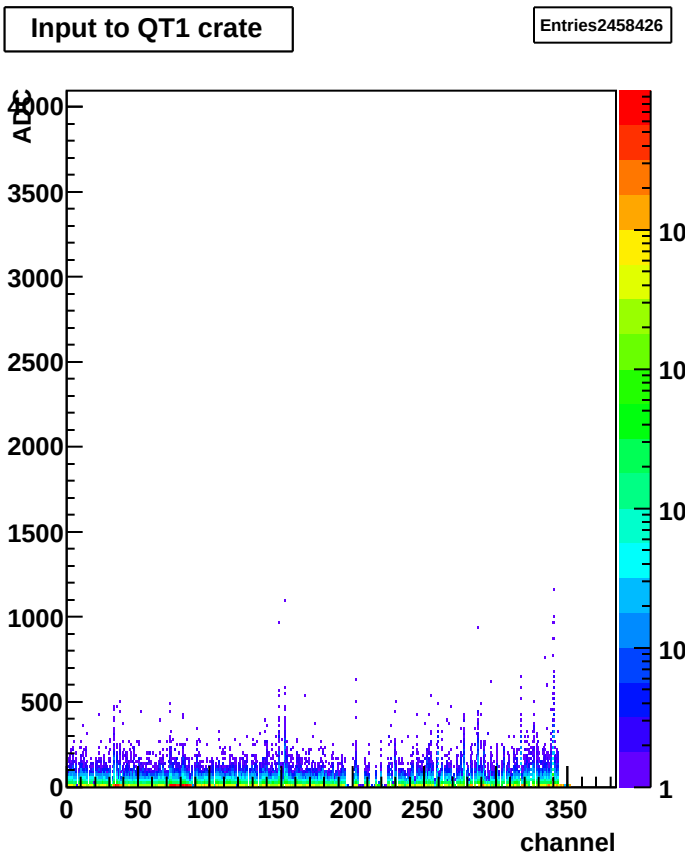

BBClarge-TAC-W
Entries 157157
Mean 1522
RMS 355.7

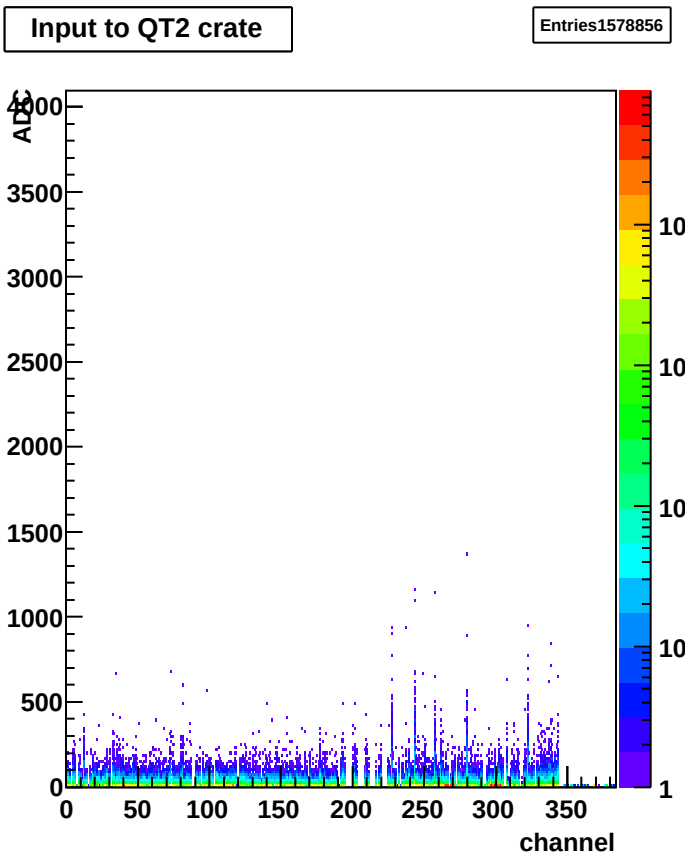

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 151652
Mean 4085
RMS 452.1

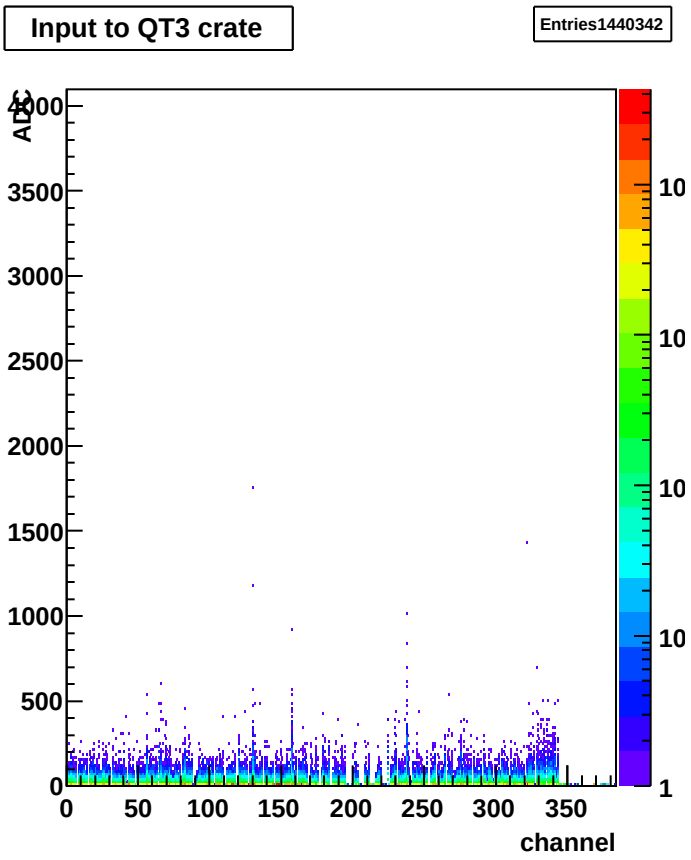

VPD-TAC-E
Entries 128050
Mean 1312
RMS 135.2

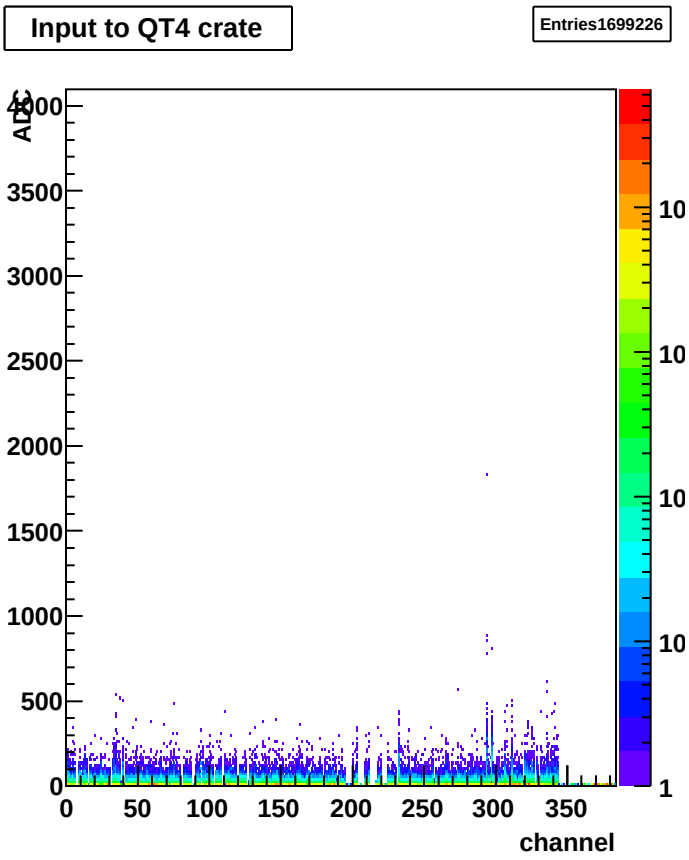

VPD-TAC-W
Entries 128085
Mean 1428
RMS 145.3

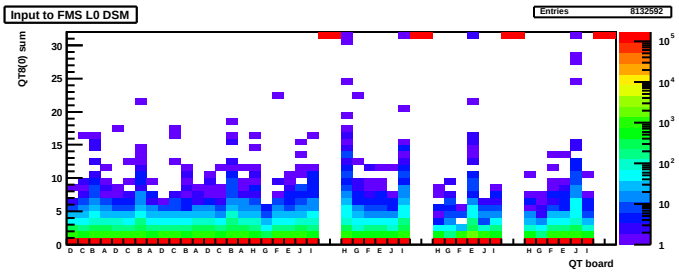

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 112519
Mean 3981
RMS 107.8

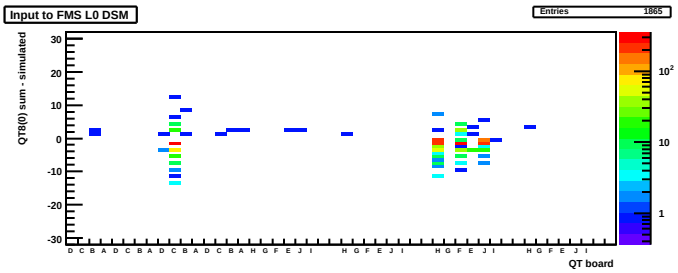

Input to QT1 crate
Entries2458426
ADC
channel

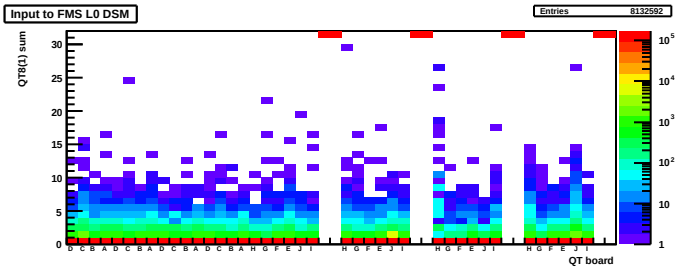

Input to QT2 crate
Entries1578856
ADC
channel

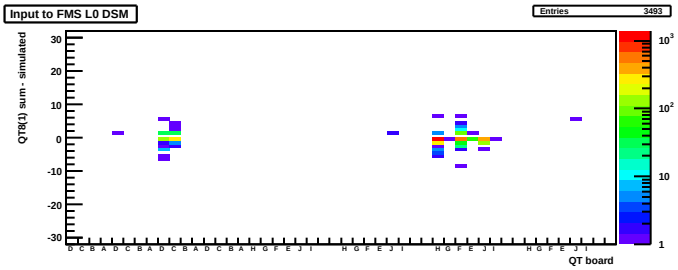

Input to QT3 crate
Entries1440342
ADC
channel

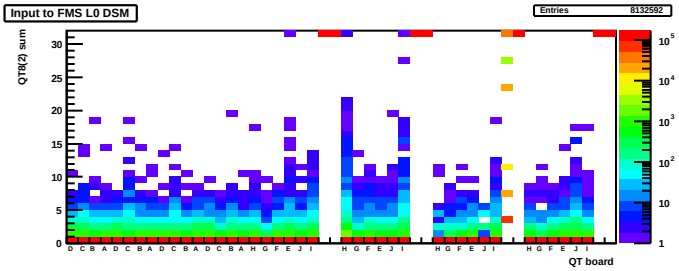

Input to QT4 crate
Entries1699226
ADC
channel

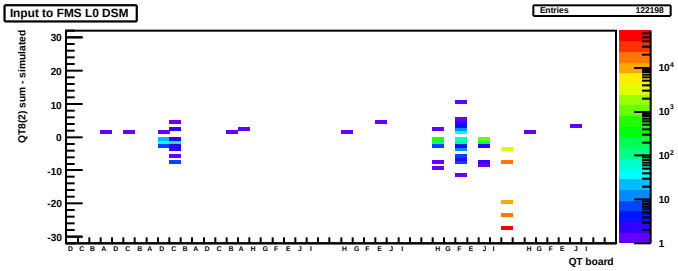

Input to FMS L0 DSM
Entries 8132592
QT8(0) sum
QT board

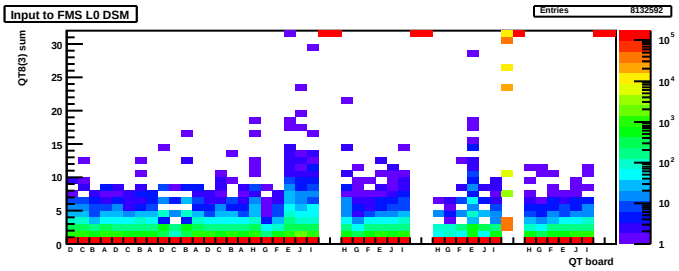

Input to FMS L0 DSM
Entries 1865
QT8(0) sum - simulated
QT board

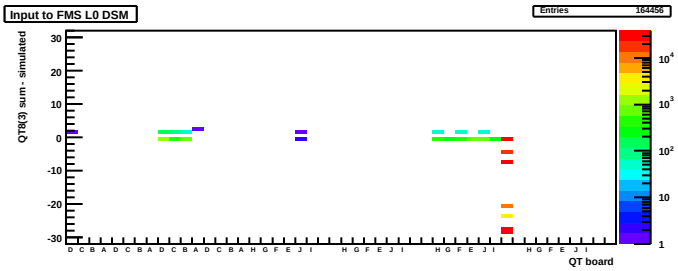

Input to FMS L0 DSM
Entries 8132592
QT8(1) sum
QT board

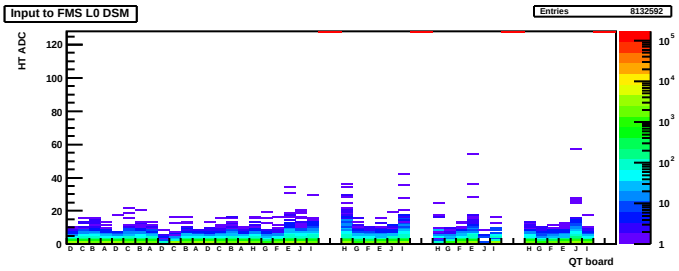

Input to FMS L0 DSM
Entries 3493
QT8(1) sum - simulated
QT board

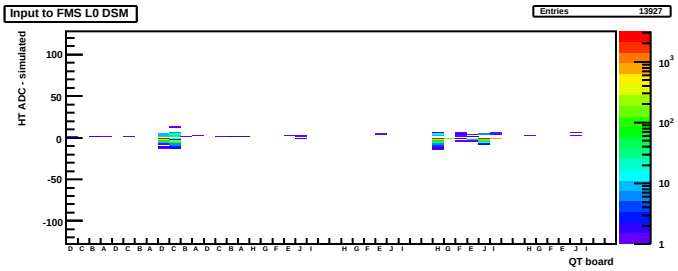

Input to FMS L0 DSM
Entries 8132592
QT8(2) sum
QT board

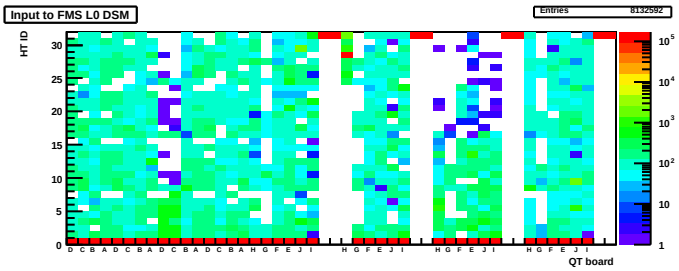

Input to FMS L0 DSM
Entries 122198
QT8(2) sum - simulated
QT board

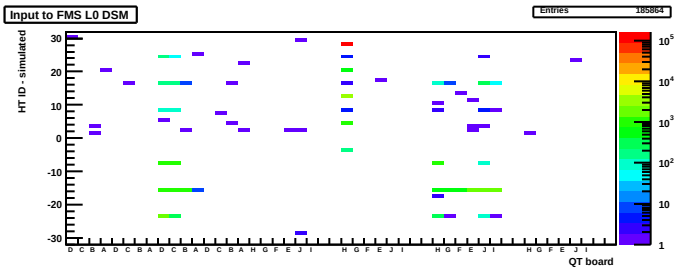

Input to FMS L0 DSM
Entries 8132592
QT8(3) sum
QT board

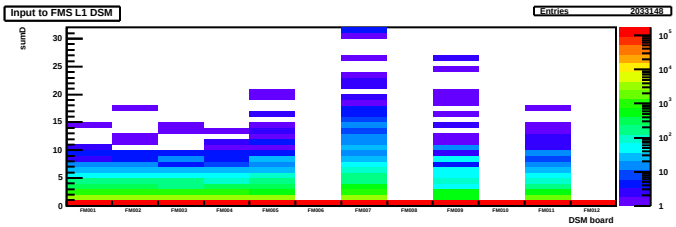

Input to FMS L0 DSM
Entries 164456
QT8(3) sum - simulated
QT board

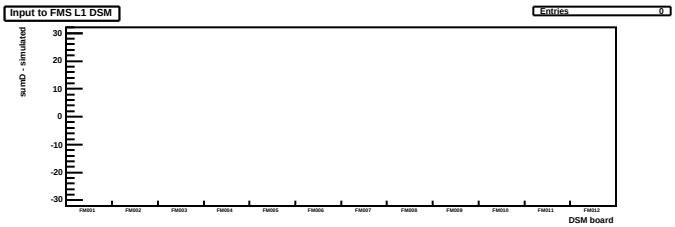

Input to FMS L0 DSM
Entries 8132592
HT ADC
QT board

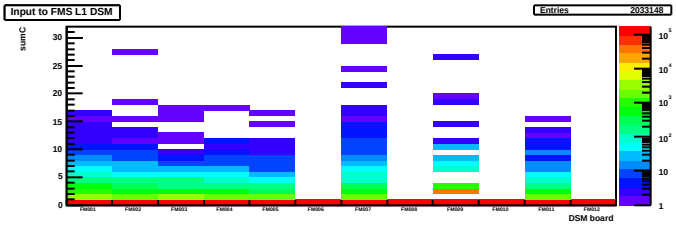

Input to FMS L0 DSM
Entries 13927
HT ADC - simulated
QT board

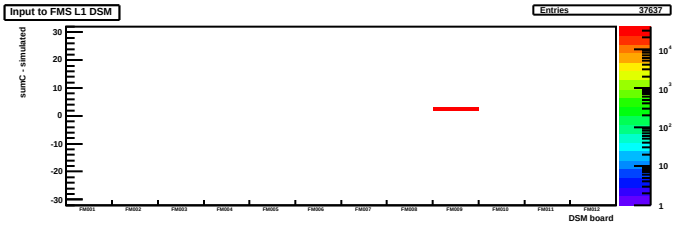

Input to FMS L0 DSM
Entries 8132592
HT ID
QT board

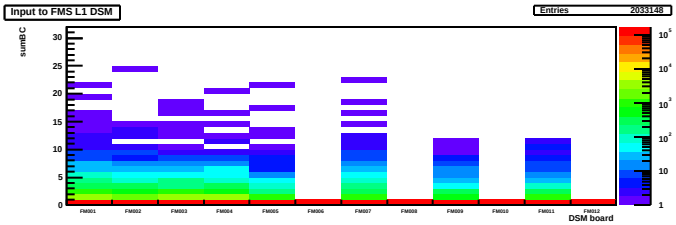

Input to FMS L0 DSM
Entries 185864
HT ID - simulated
QT board

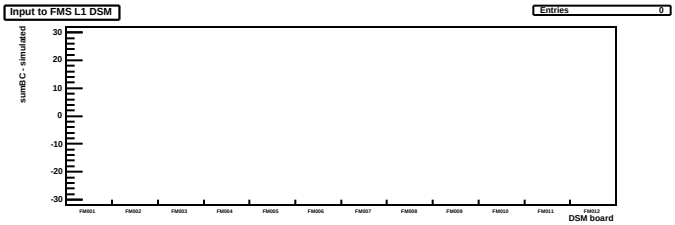

Input to FMS L1 DSM
Entries 2033148
sumD
DSM board

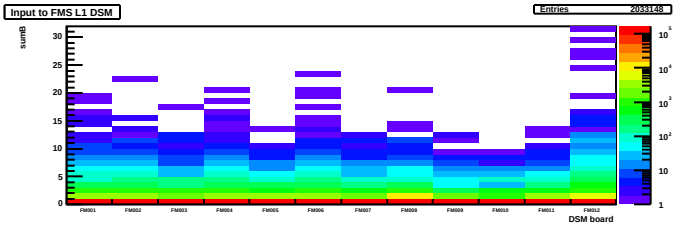

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

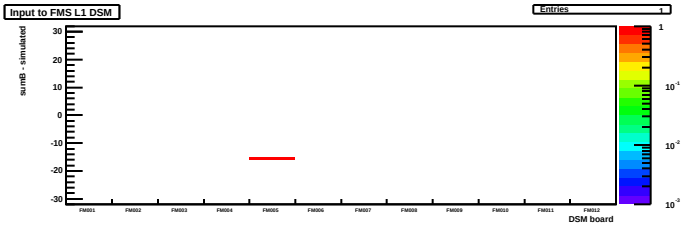

Input to FMS L1 DSM
Entries 2033148
sumC
DSM board

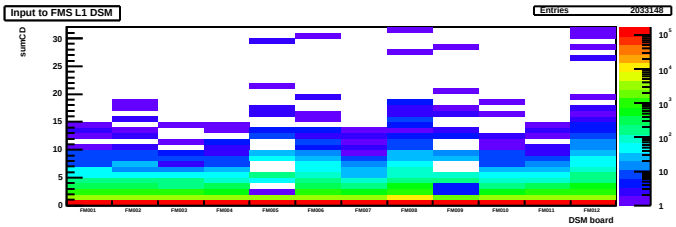

Input to FMS L1 DSM
Entries 37637
sumC - simulated
DSM board

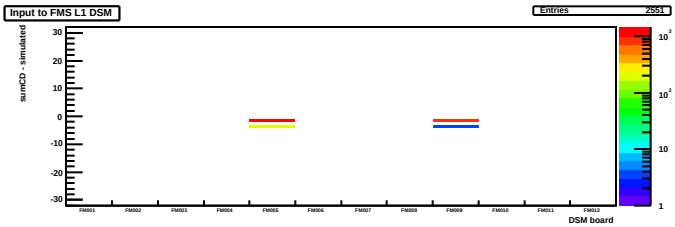

Input to FMS L1 DSM
Entries 2033148
sumBC
DSM board

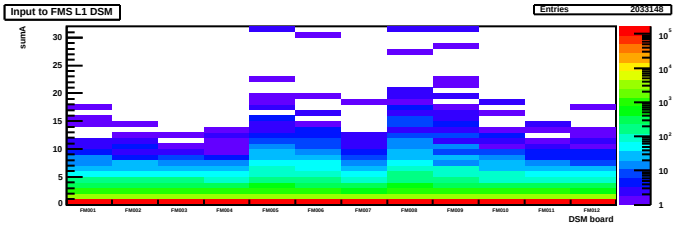

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

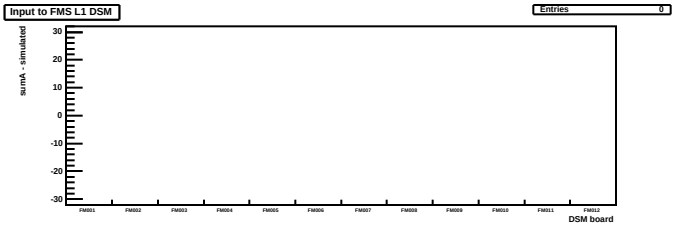

Input to FMS L1 DSM
Entries 2033148
sumB
DSM board

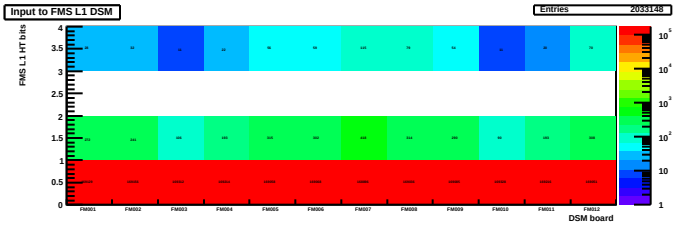

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

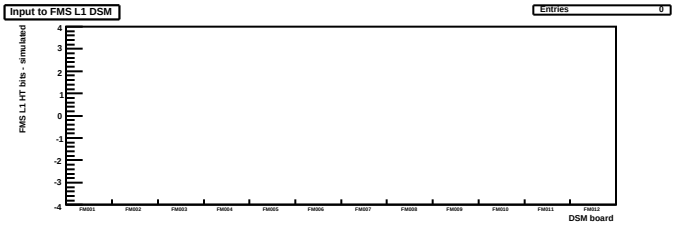

Input to FMS L1 DSM
Entries 2033148
sumCD
DSM board

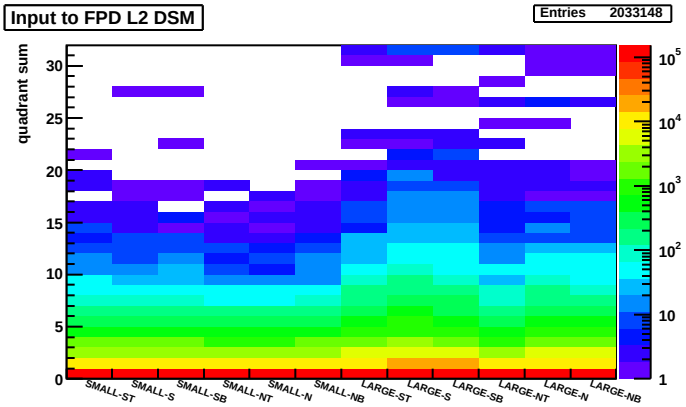

Input to FMS L1 DSM
Entries 2551
sumCD - simulated
DSM board

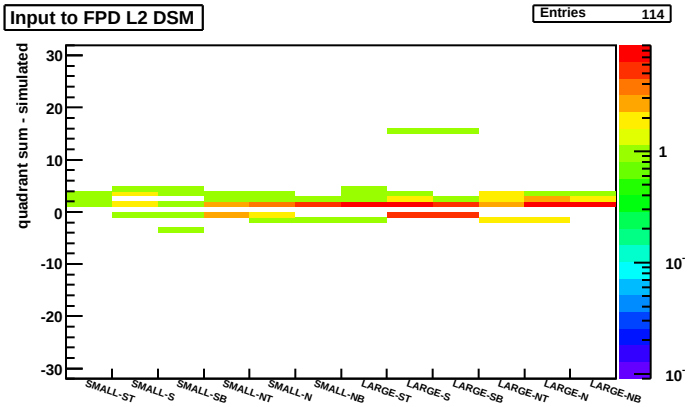

Input to FMS L1 DSM
Entries 2033148
sumA
DSM board

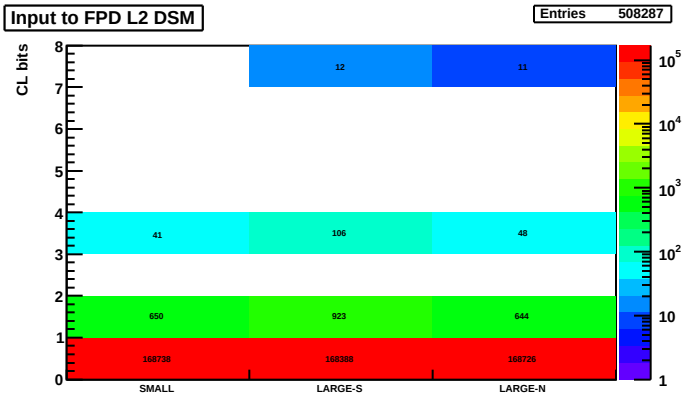

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

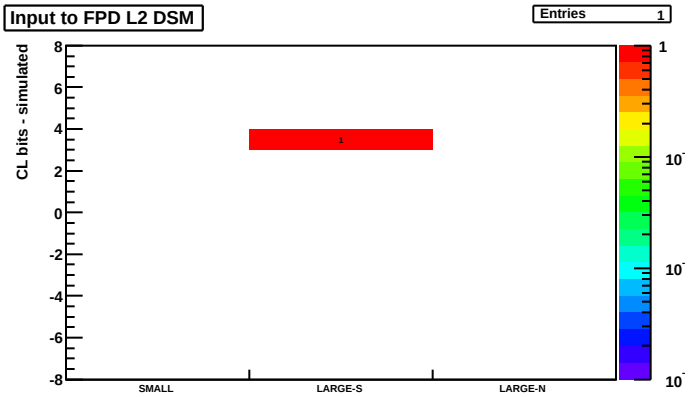

Input to FMS L1 DSM
Entries 2033148
FMS L1 HT bits
DSM board

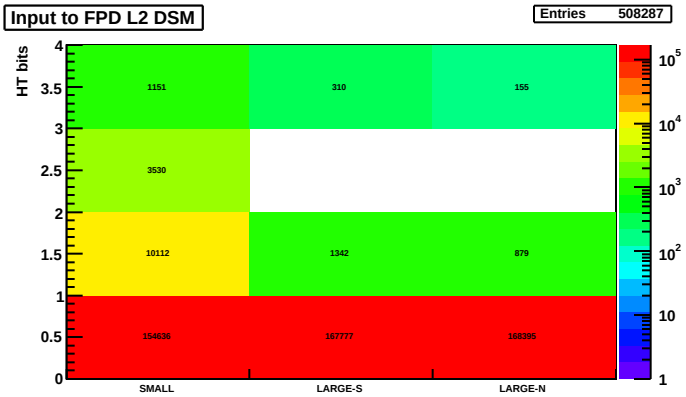

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

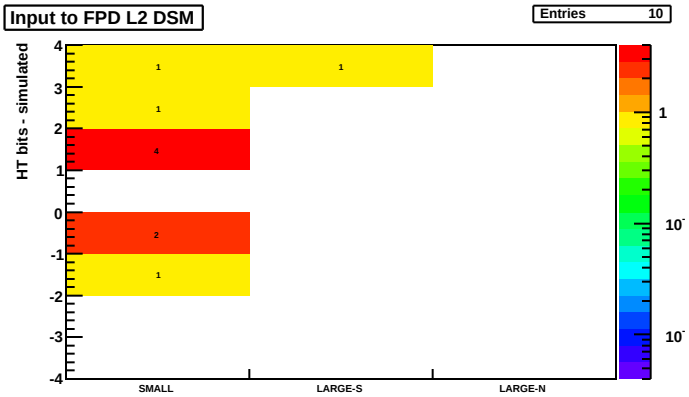

Input to FPD L2 DSM
Entries 2033148
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

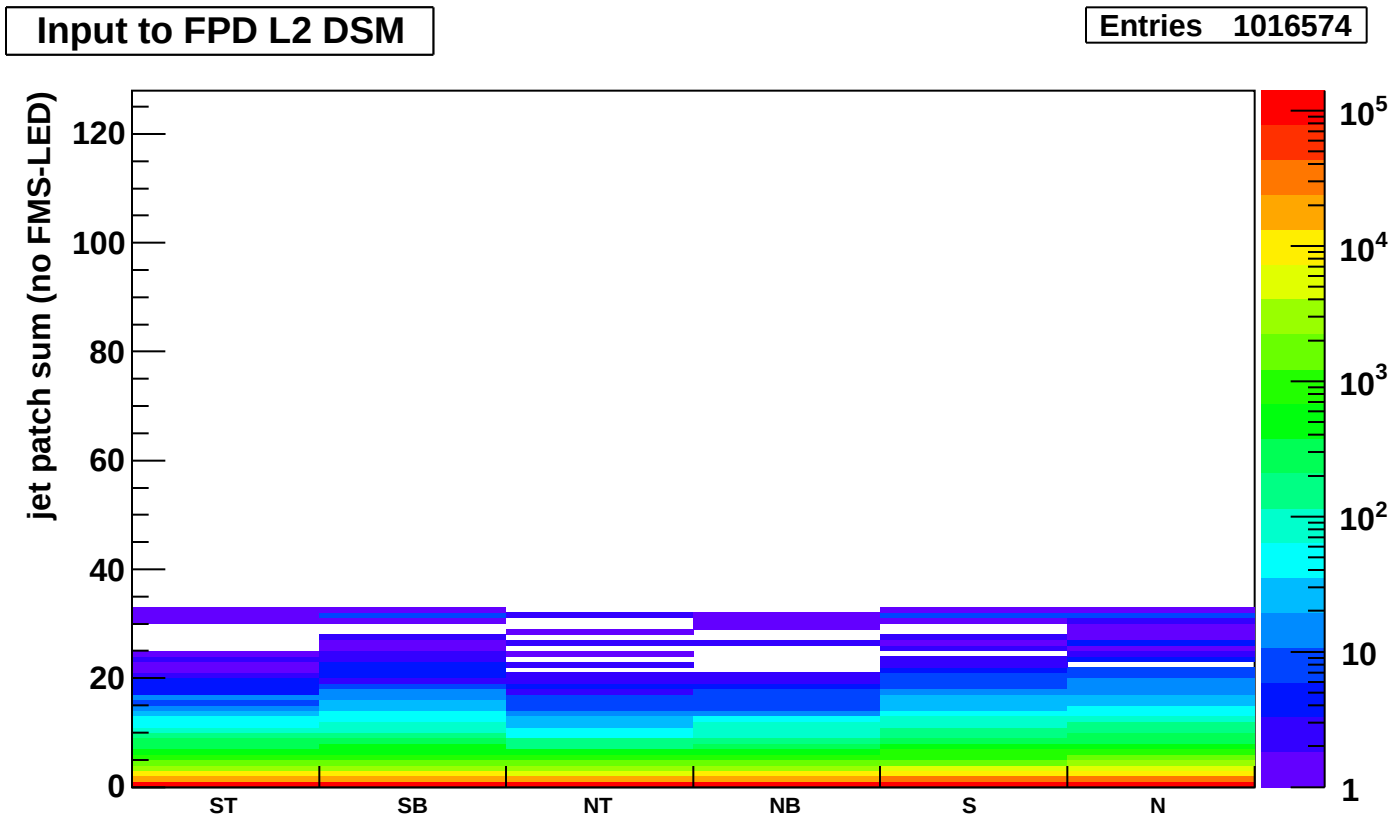

Input to FPD L2 DSM
Entries 114
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

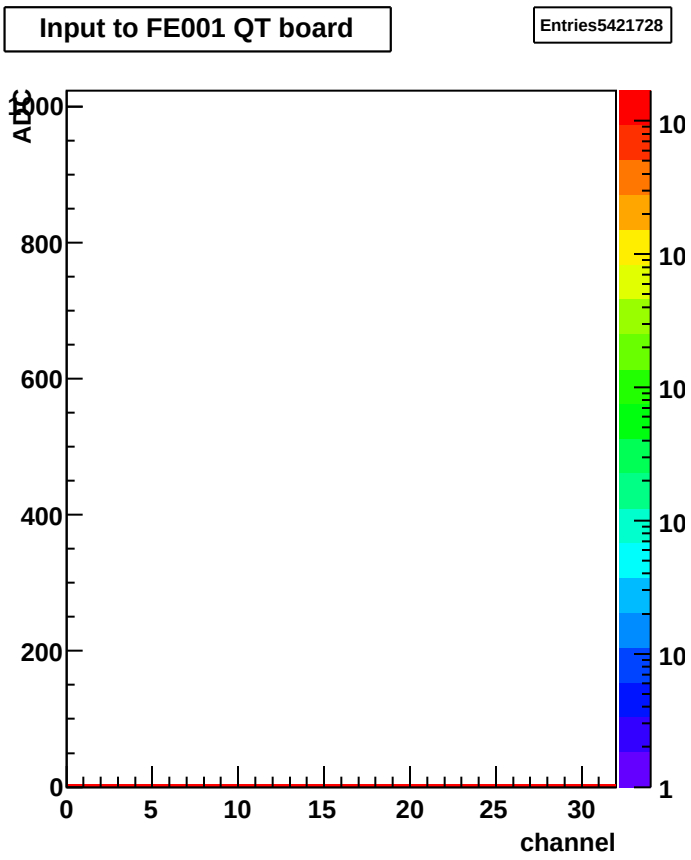

Input to FPD L2 DSM
Entries 508287
CL bits
12
11
41
106
48
650
923
644
168738
168388
168726
SMALL
LARGE-S
LARGE-N

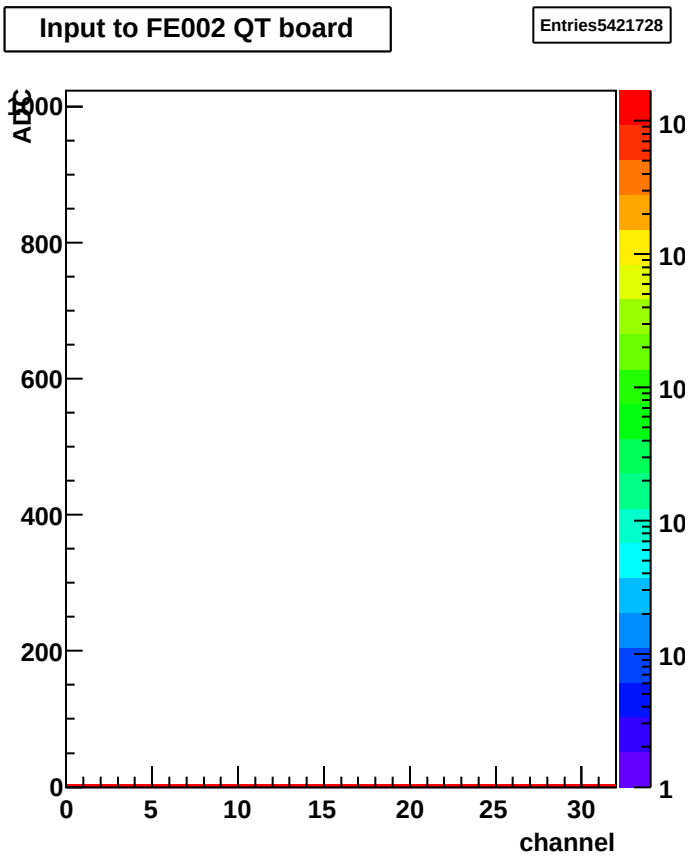

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

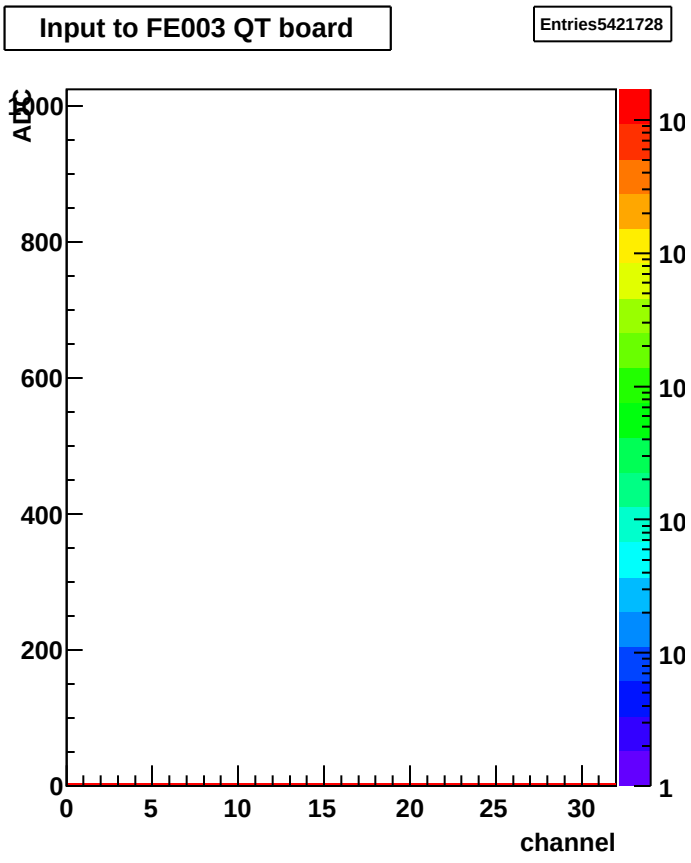

Input to FPD L2 DSM
Entries 508287
HT bits
1151
310
155
3530
10112
1342
879
154636
167777
168395
SMALL
LARGE-S
LARGE-N

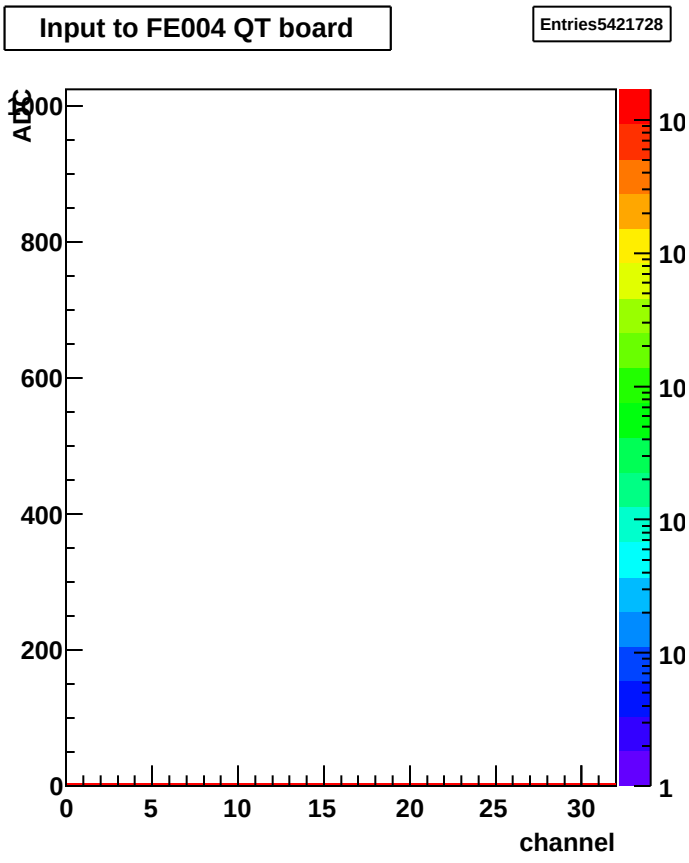

Input to FPD L2 DSM
Entries 10
HT bits - simulated
1
1
1
4
2
1
SMALL
LARGE-S
LARGE-N

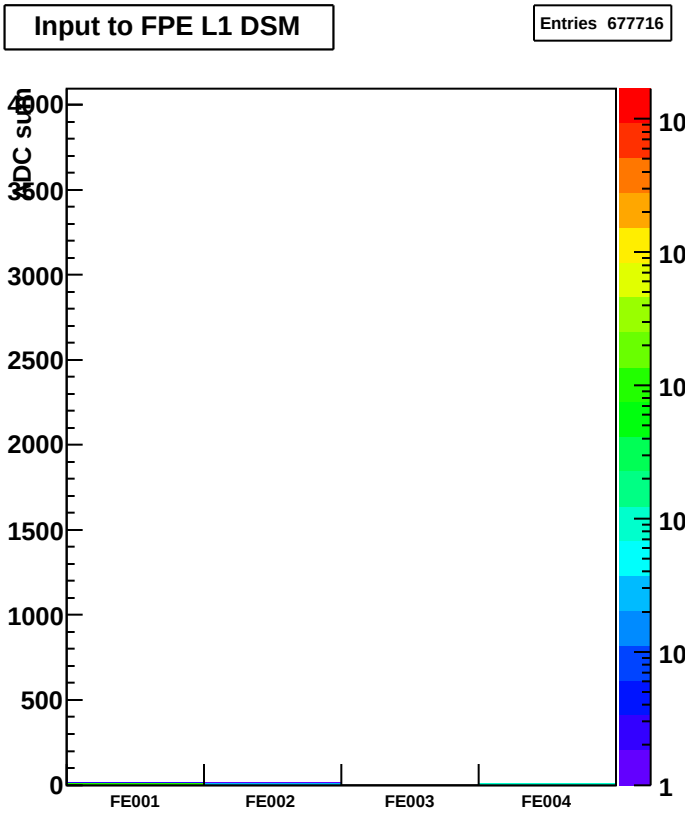

Input to FPD L2 DSM
Entries 1016574
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

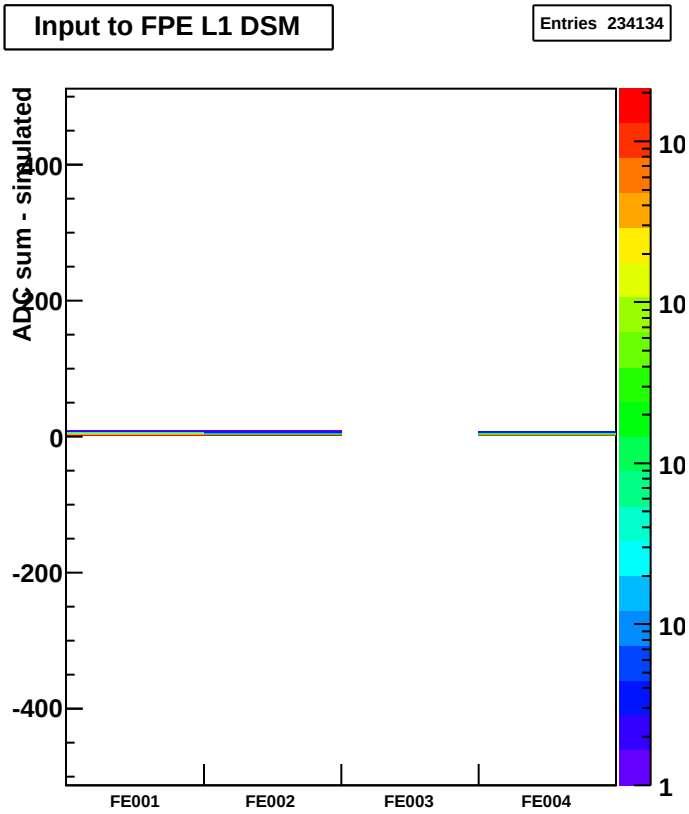

Input to FE001 QT board
Entries5421728
ADC
channel


Input to FE002 QT board
Entries5421728
ADC
channel

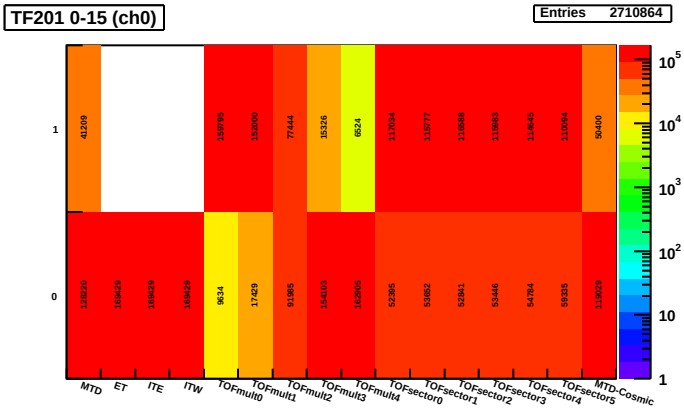

Input to FE003 QT board
Entries5421728
ADC
channel


Input to FE004 QT board
Entries5421728
ADC
channel

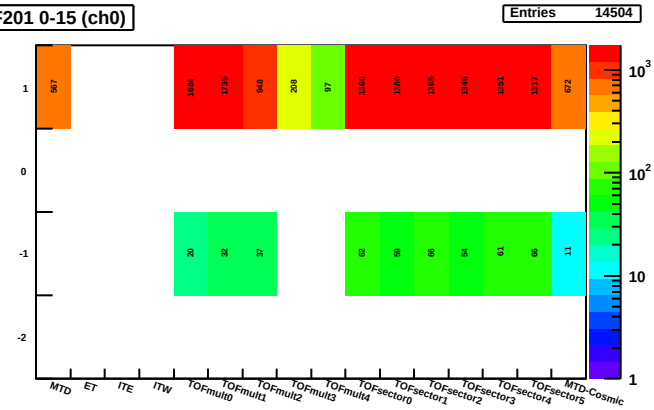

Input to FPE L1 DSM
Entries 677716
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 234134
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

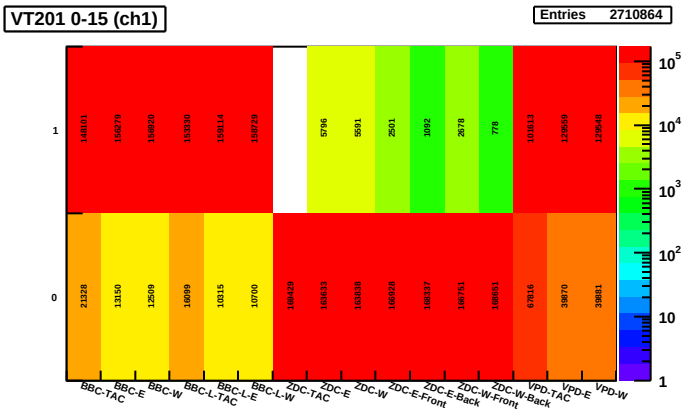
TF201 0-15 (ch0)



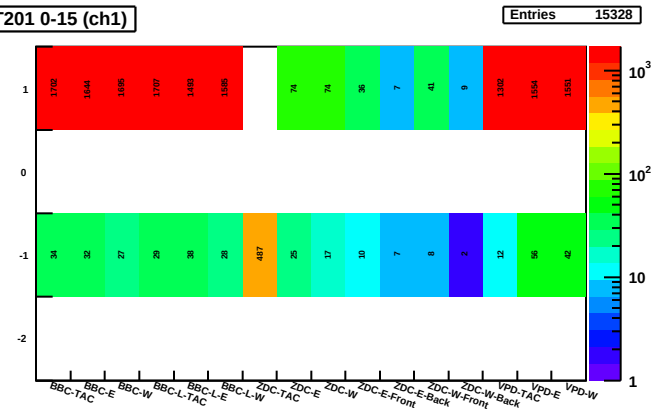
TF201 0-15 (ch0)



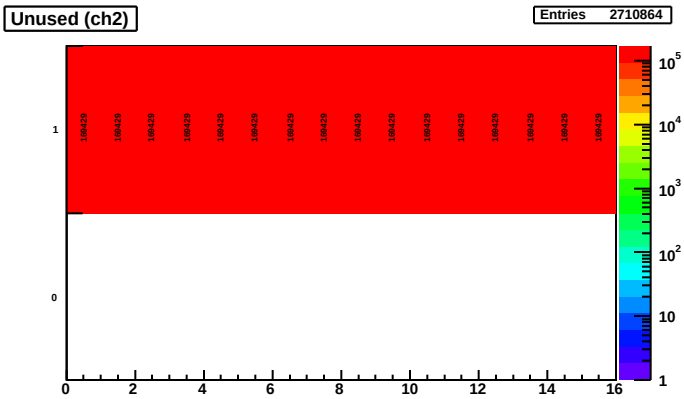
VT201 0-15 (ch1)

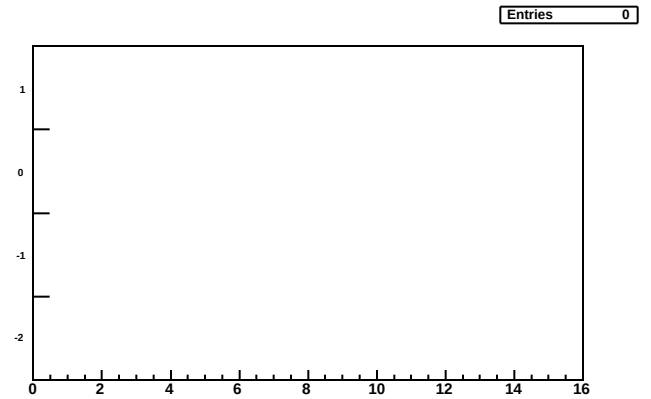


VT201 0-15 (ch1)

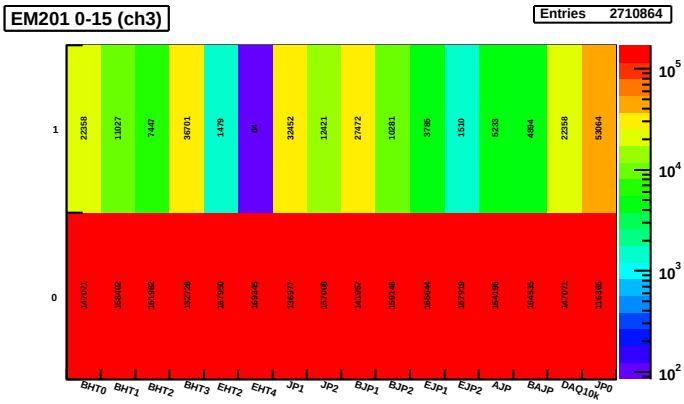


Unused (ch2)

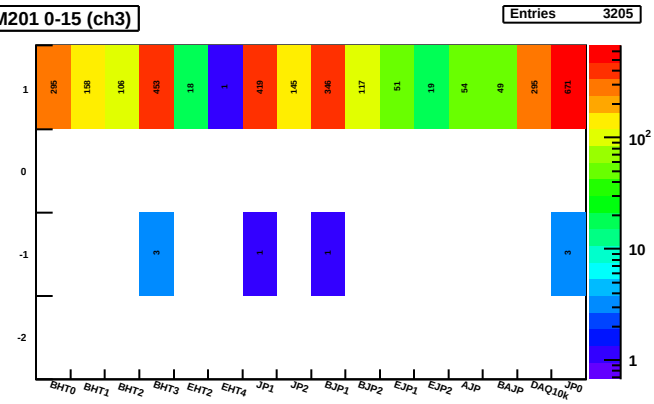




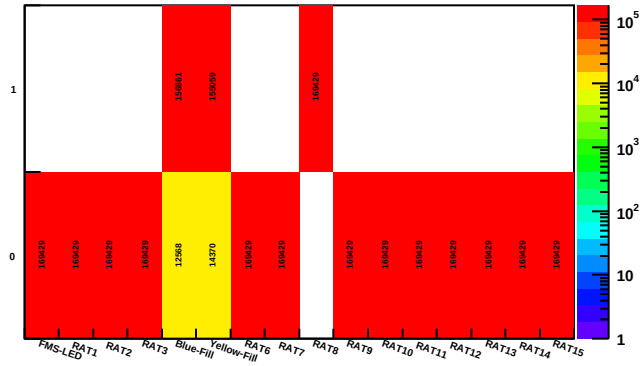
EM201 0-15 (ch3)

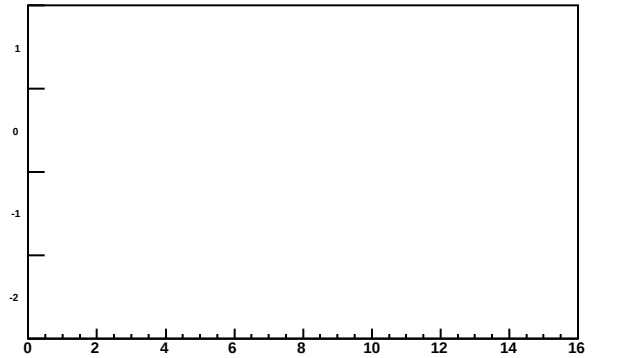


EM201 0-15 (ch3)

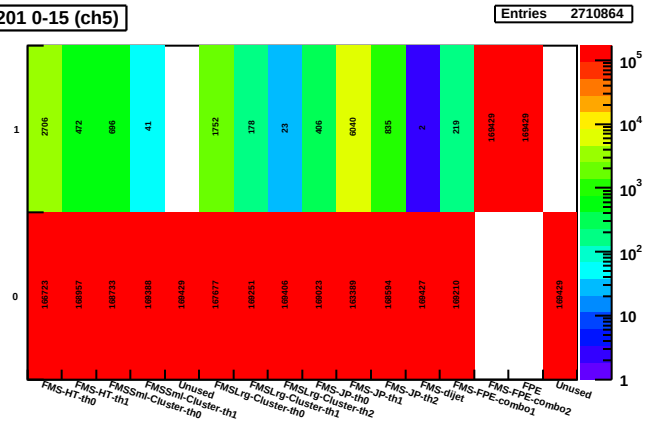


RAT board (ch4)

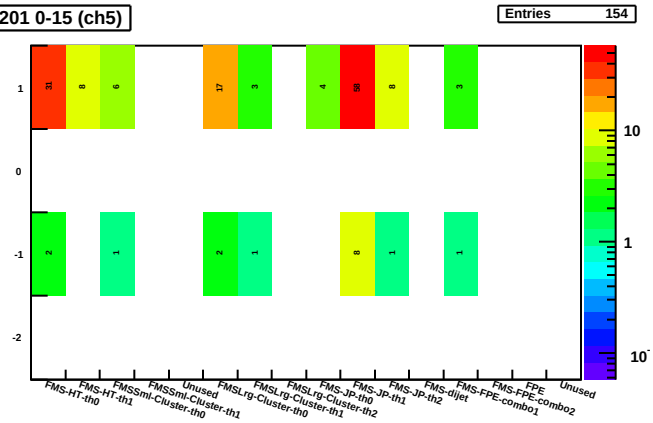




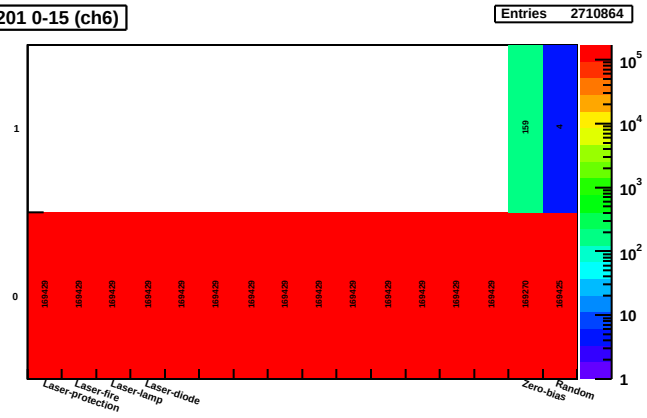
FP201 0-15 (ch5)

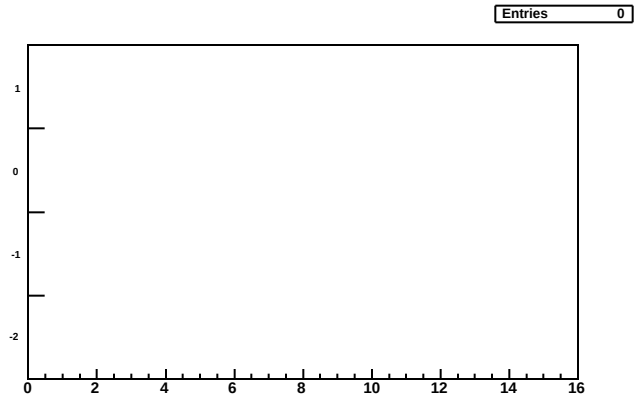
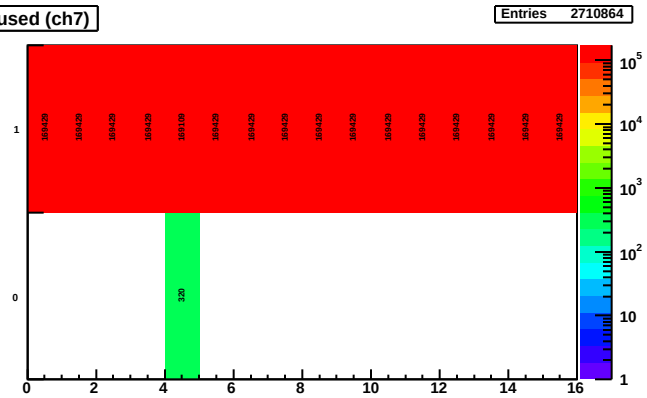
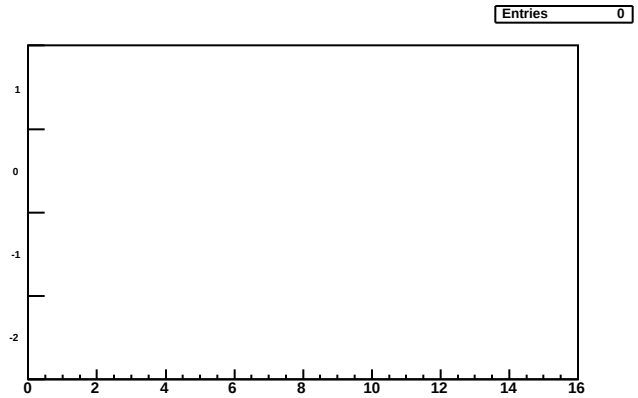


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)