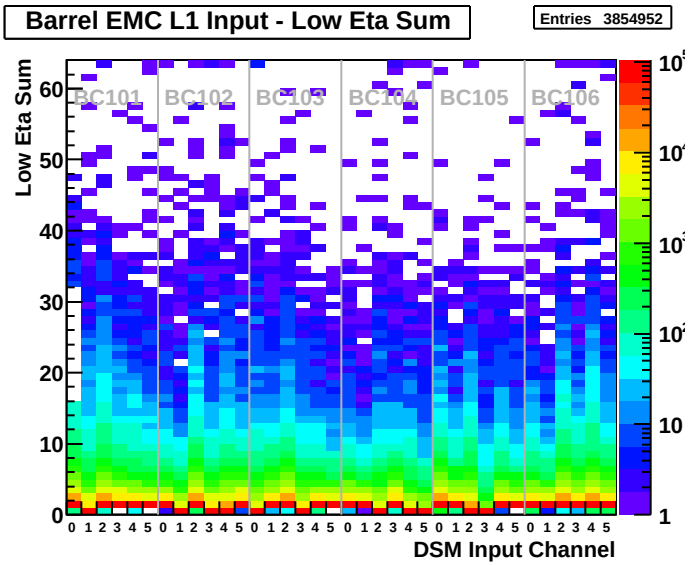
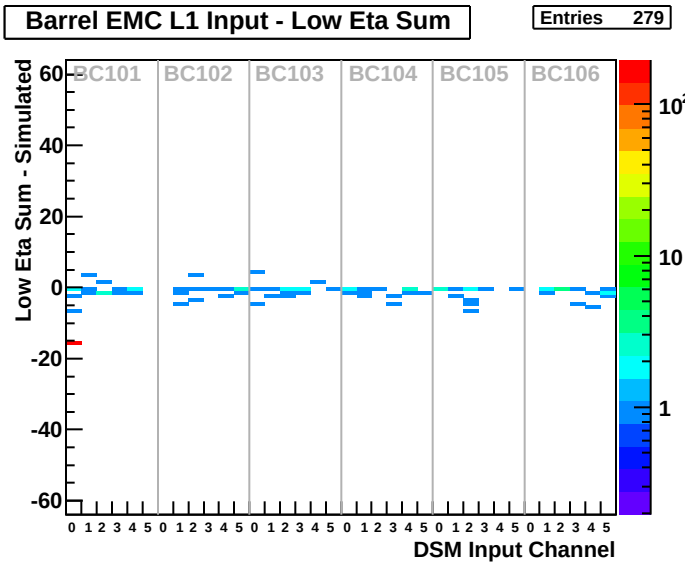
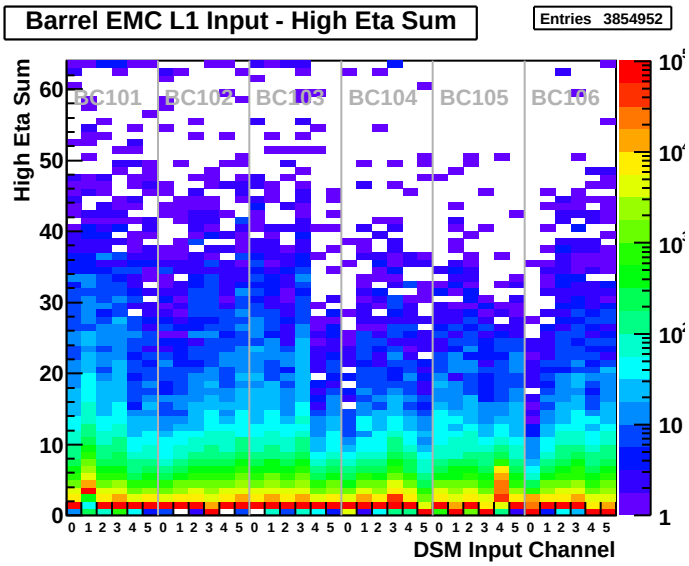
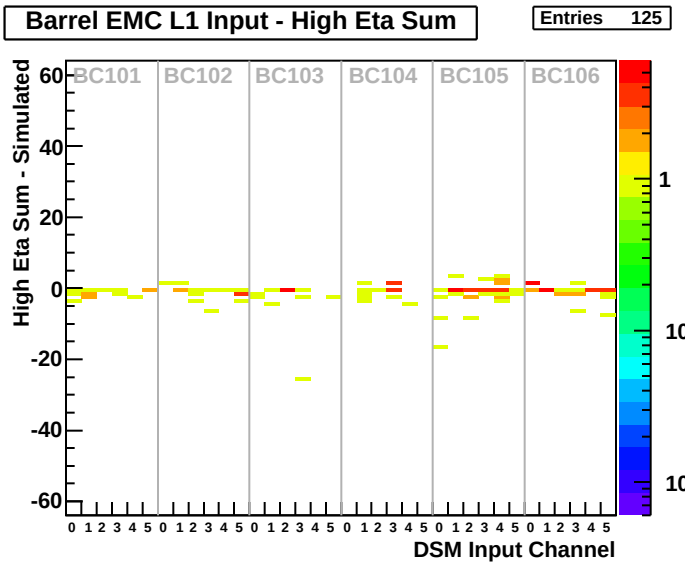
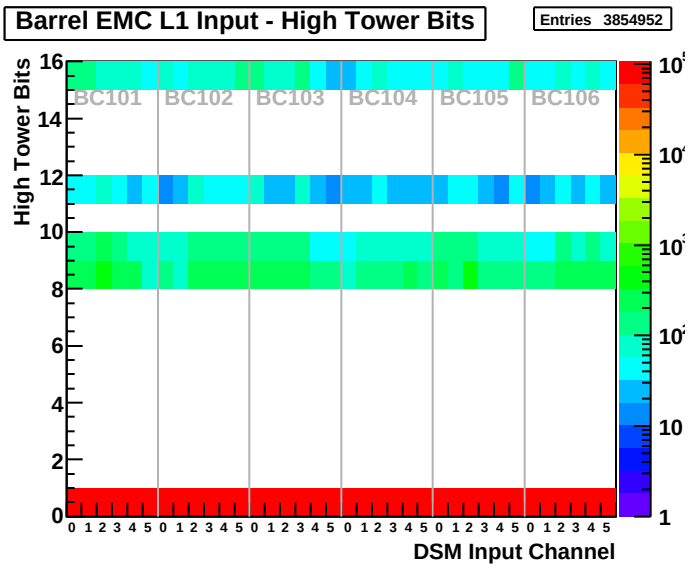
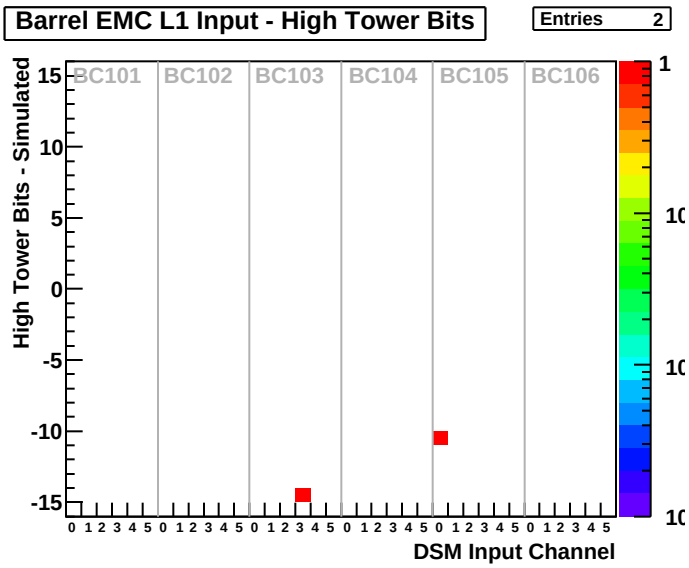

Barrel EMC L1 Input - Low Eta Sum
Entries 3854952
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

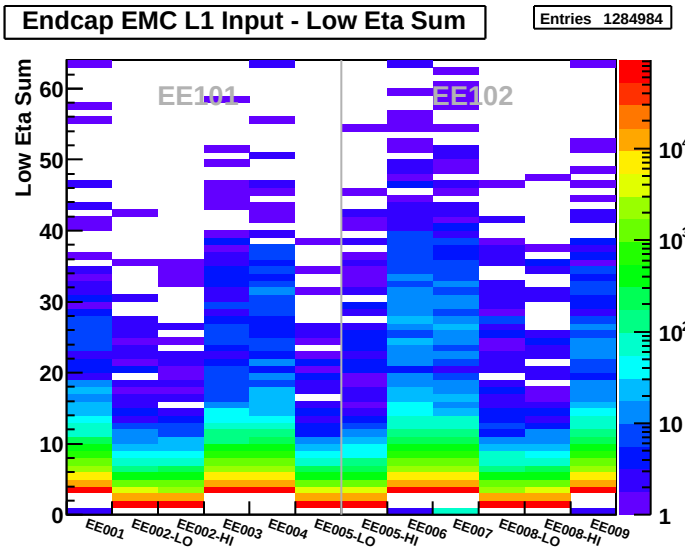

Barrel EMC L1 Input - Low Eta Sum
Entries 279
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

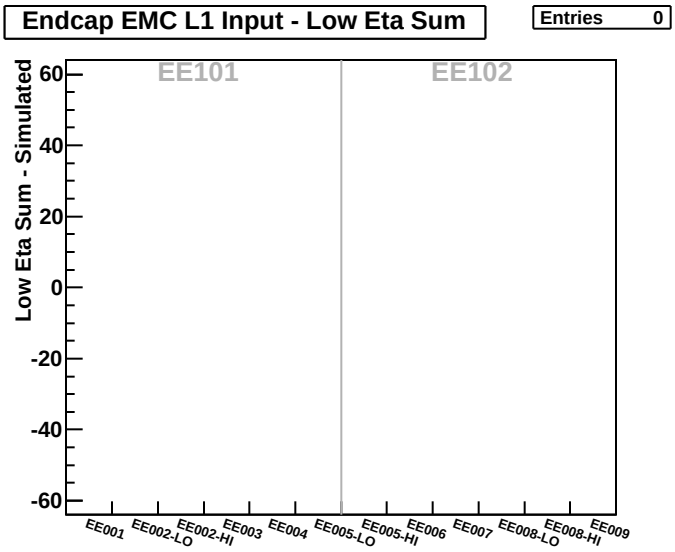

Barrel EMC L1 Input - High Eta Sum
Entries 3854952
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

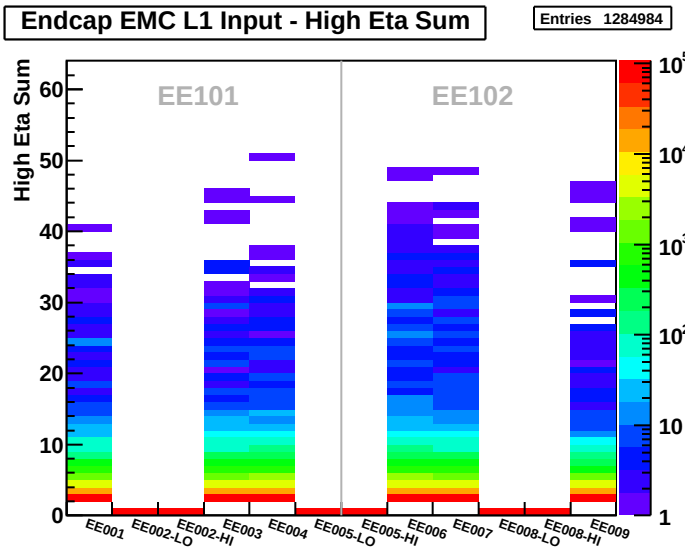

Barrel EMC L1 Input - High Eta Sum
Entries 125
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

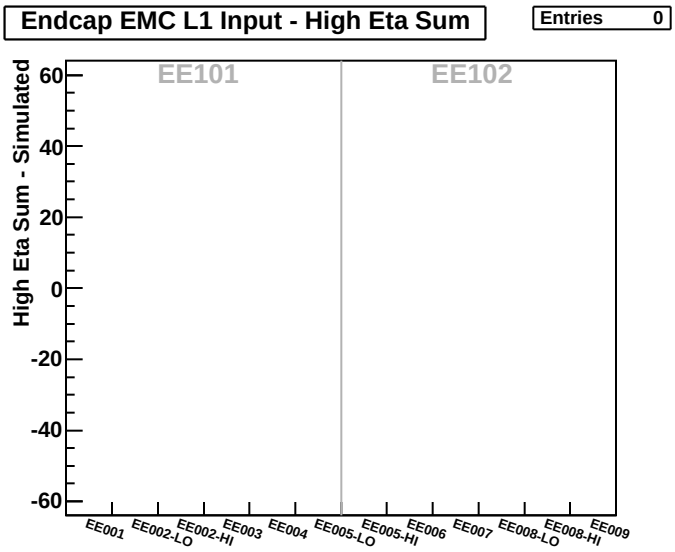

Barrel EMC L1 Input - High Tower Bits
Entries 3854952
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

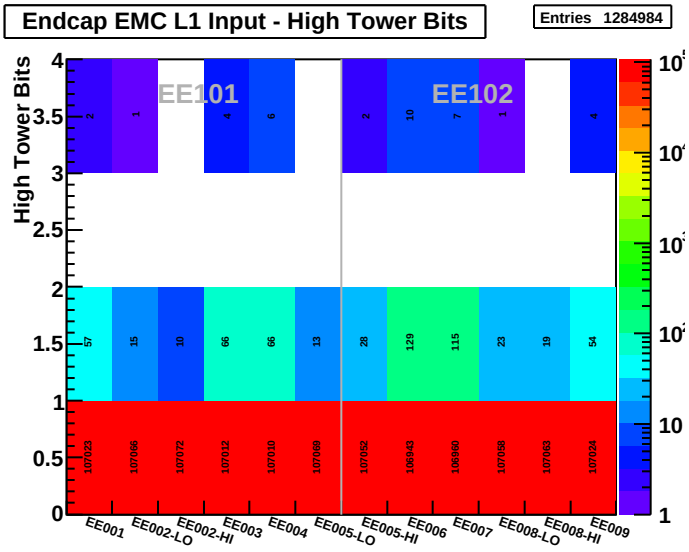

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

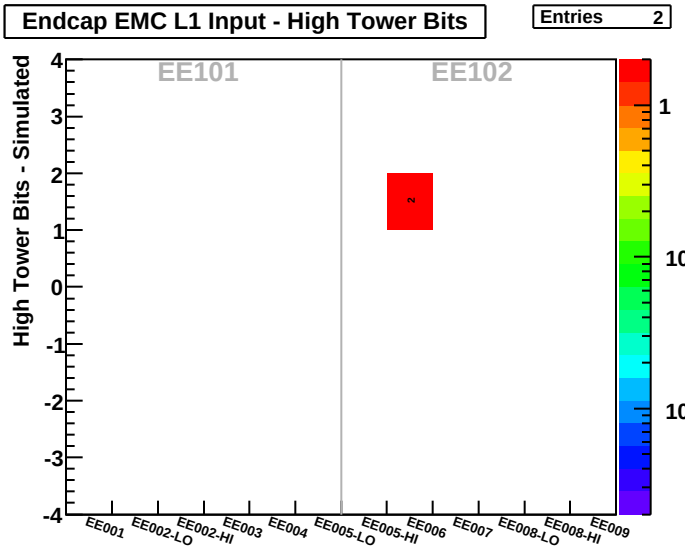

Endcap EMC L1 Input - Low Eta Sum
Entries 1284984
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

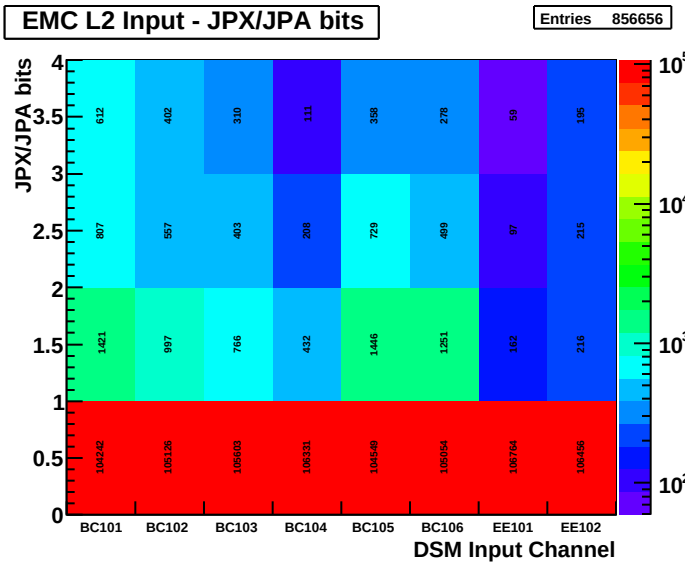

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

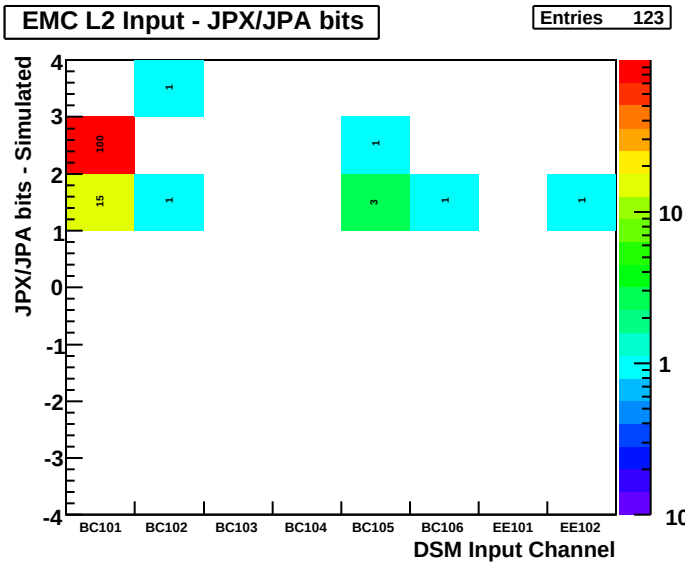

Endcap EMC L1 Input - High Eta Sum
Entries 1284984
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

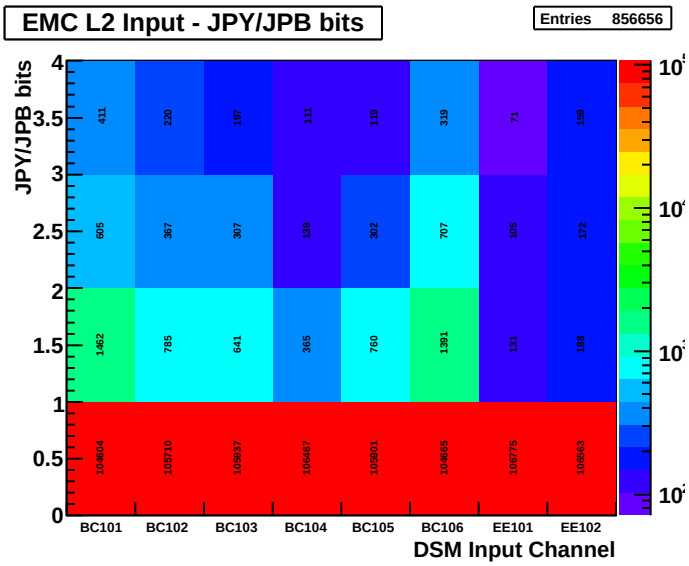

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

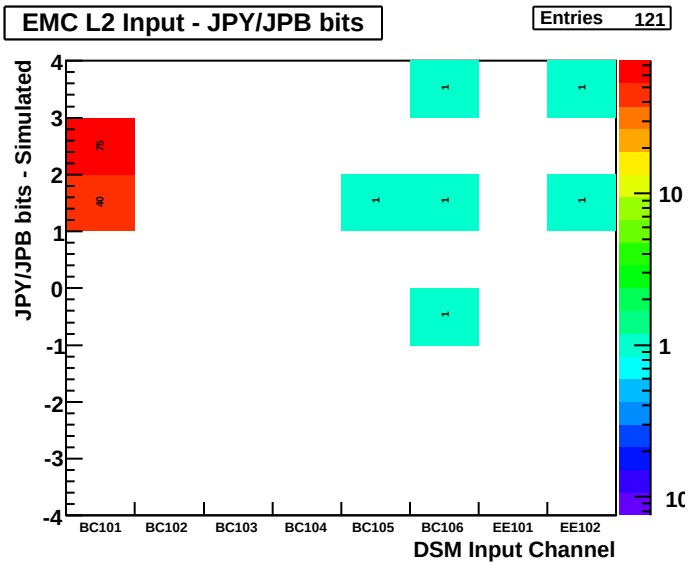

Endcap EMC L1 Input - High Tower Bits
Entries 1284984
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

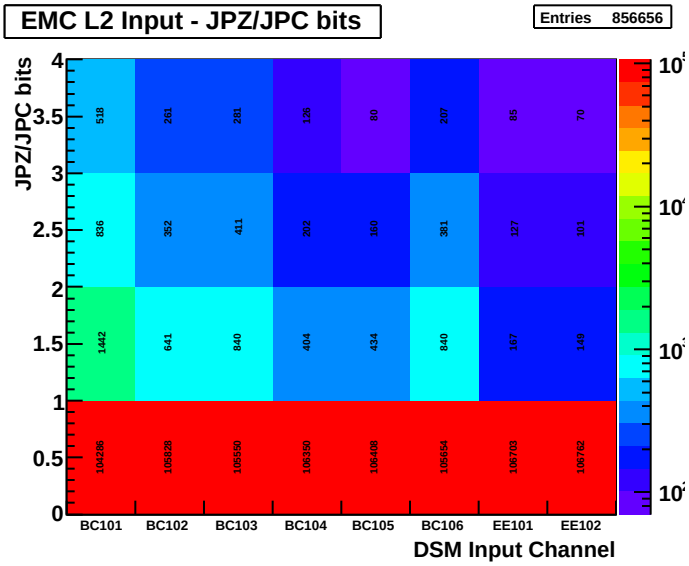

Endcap EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

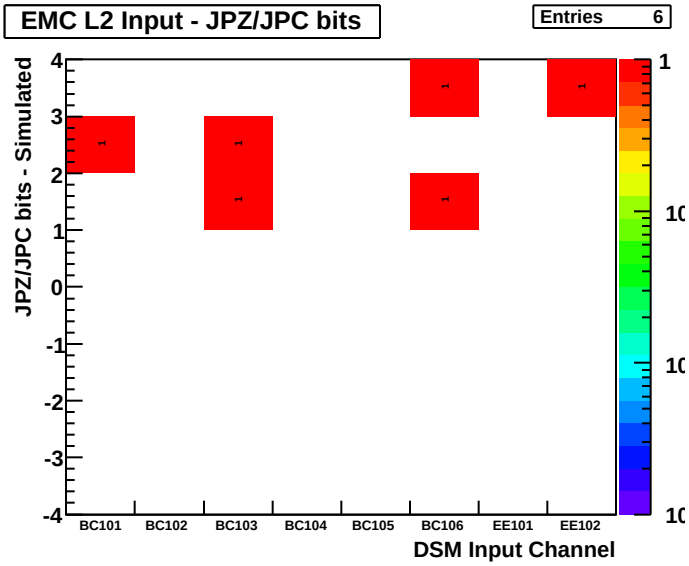

EMC L2 Input - JPX/JPA bits
Entries 856656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

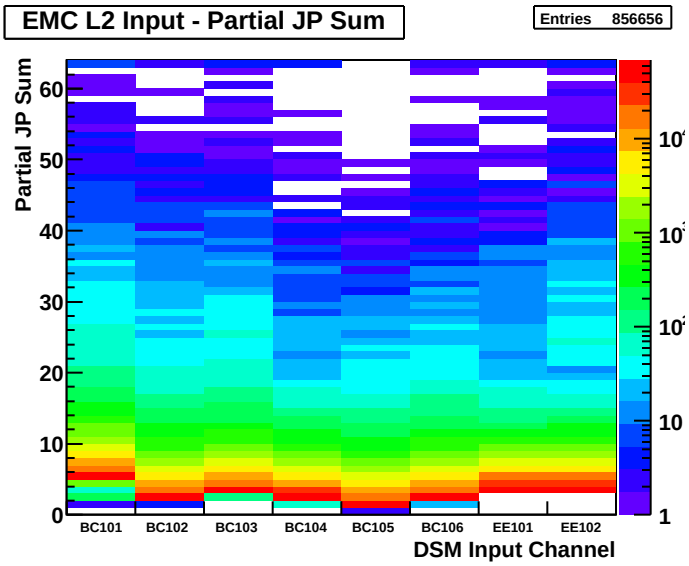

EMC L2 Input - JPX/JPA bits
Entries 123
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

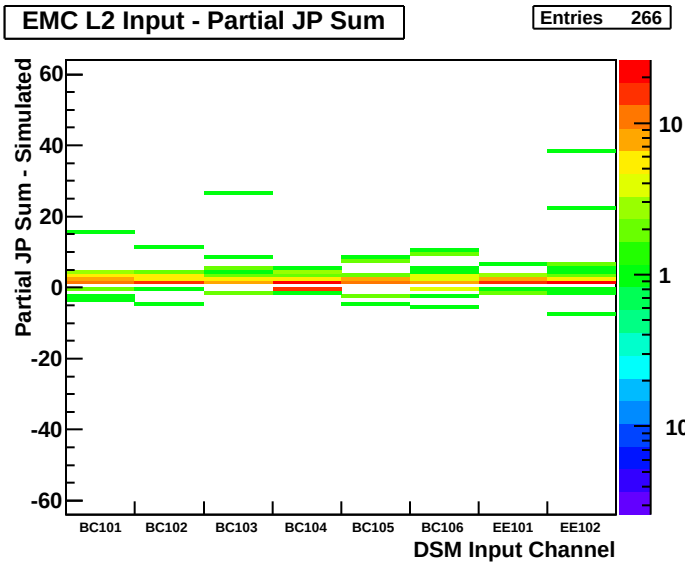

EMC L2 Input - JPY/JPB bits
Entries 856656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

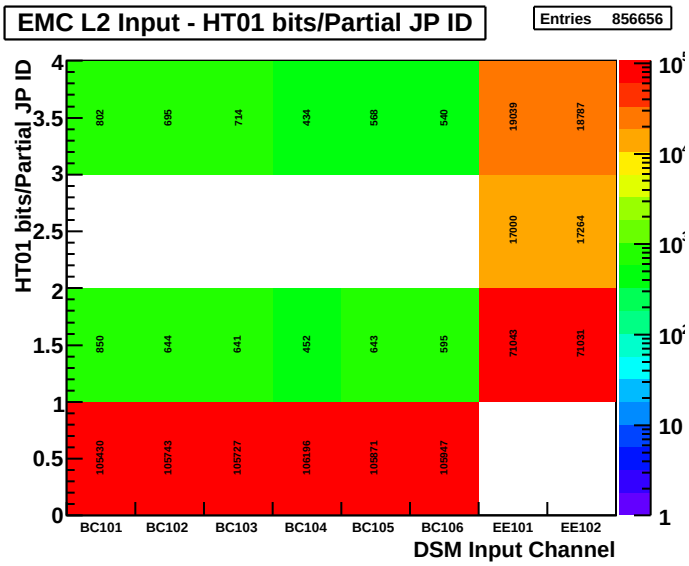

EMC L2 Input - JPY/JPB bits
Entries 121
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

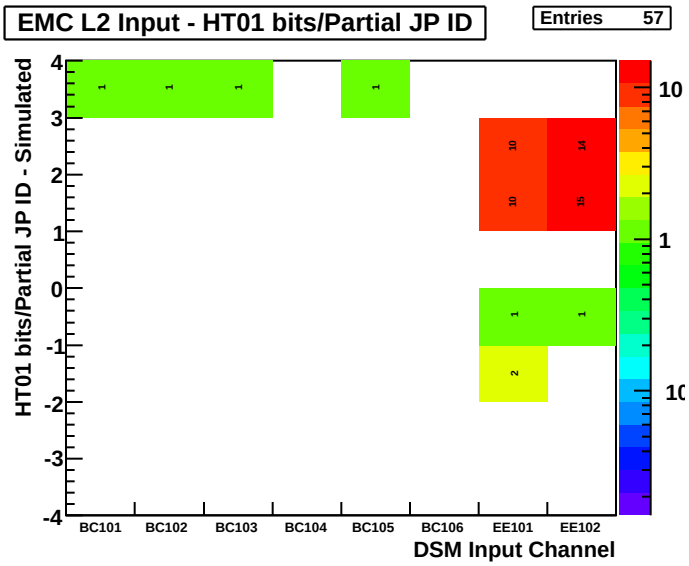

EMC L2 Input - JPZ/JPC bits
Entries 856656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

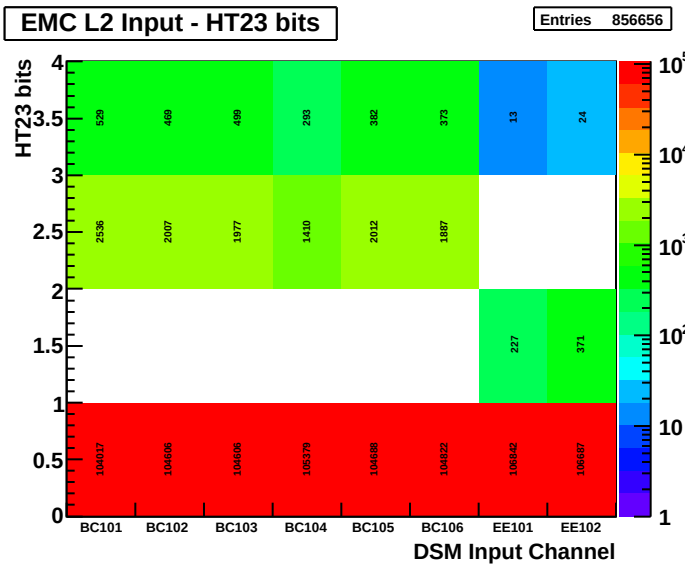

EMC L2 Input - JPZ/JPC bits
Entries 6
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

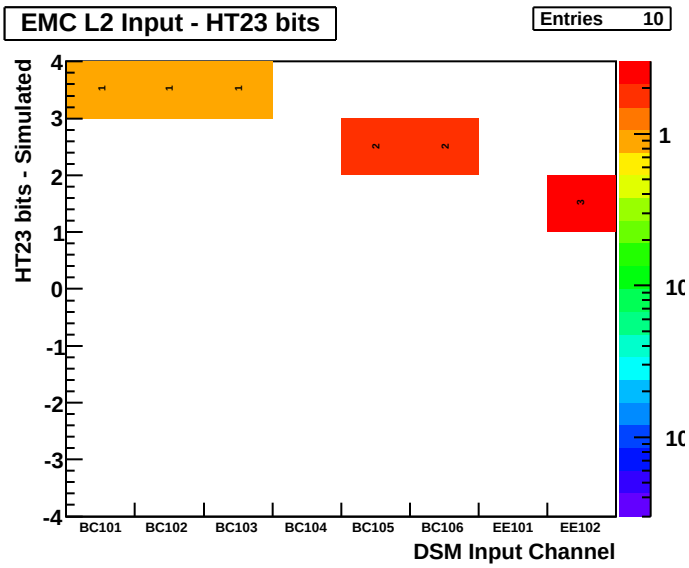

EMC L2 Input - Partial JP Sum
Entries 856656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

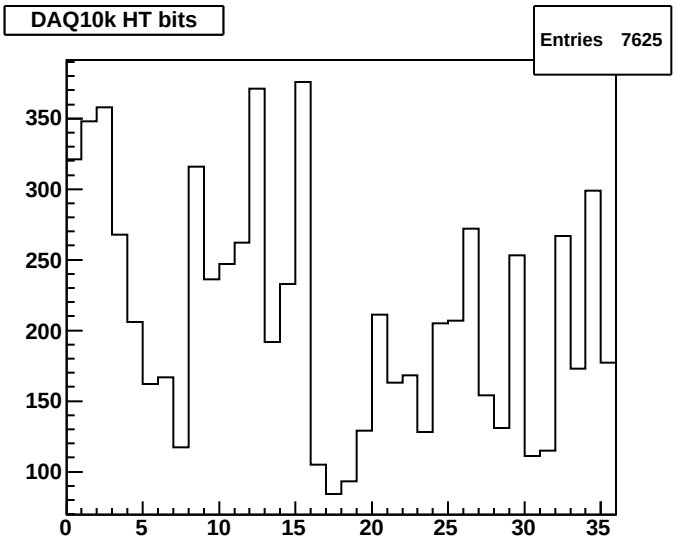

EMC L2 Input - Partial JP Sum
Entries 266
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

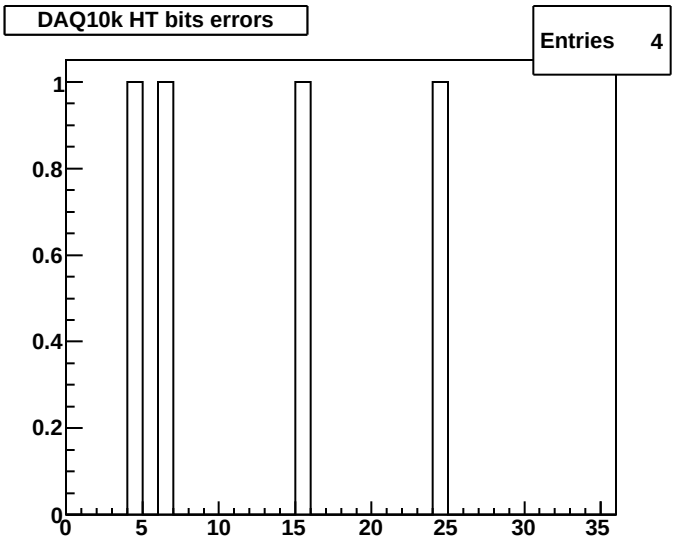

EMC L2 Input - HT01 bits/Partial JP ID
Entries 856656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

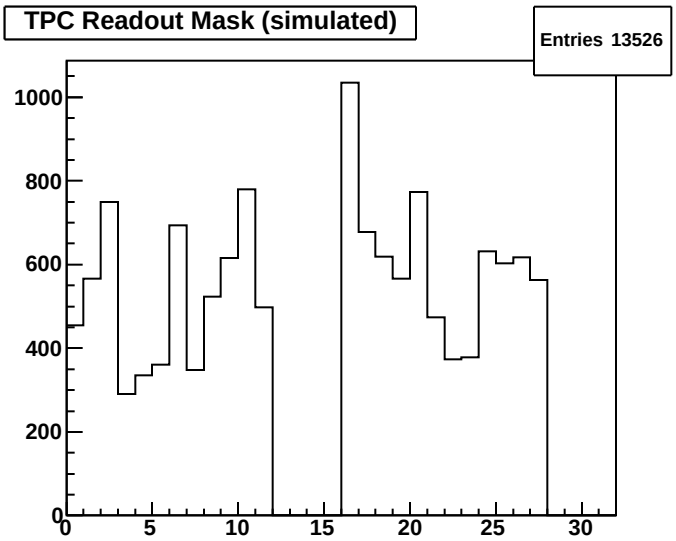

EMC L2 Input - HT01 bits/Partial JP ID
Entries 57
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

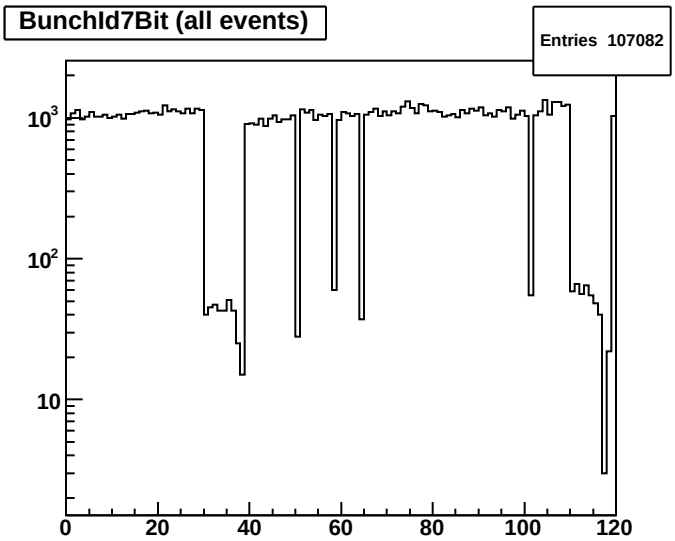

EMC L2 Input - HT23 bits
Entries 856656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

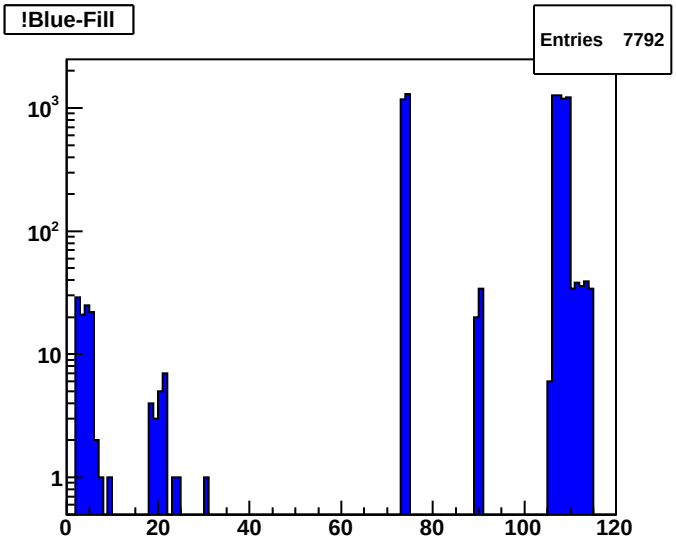

EMC L2 Input - HT23 bits
Entries 10
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

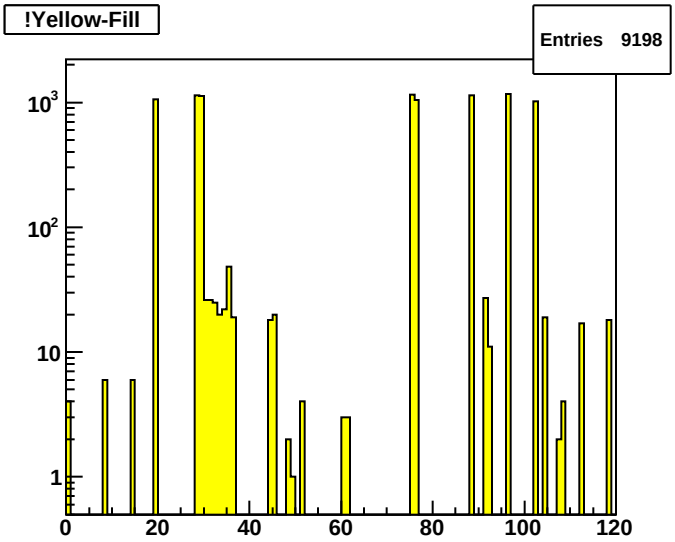

DAQ10k HT bits
Entries 7625

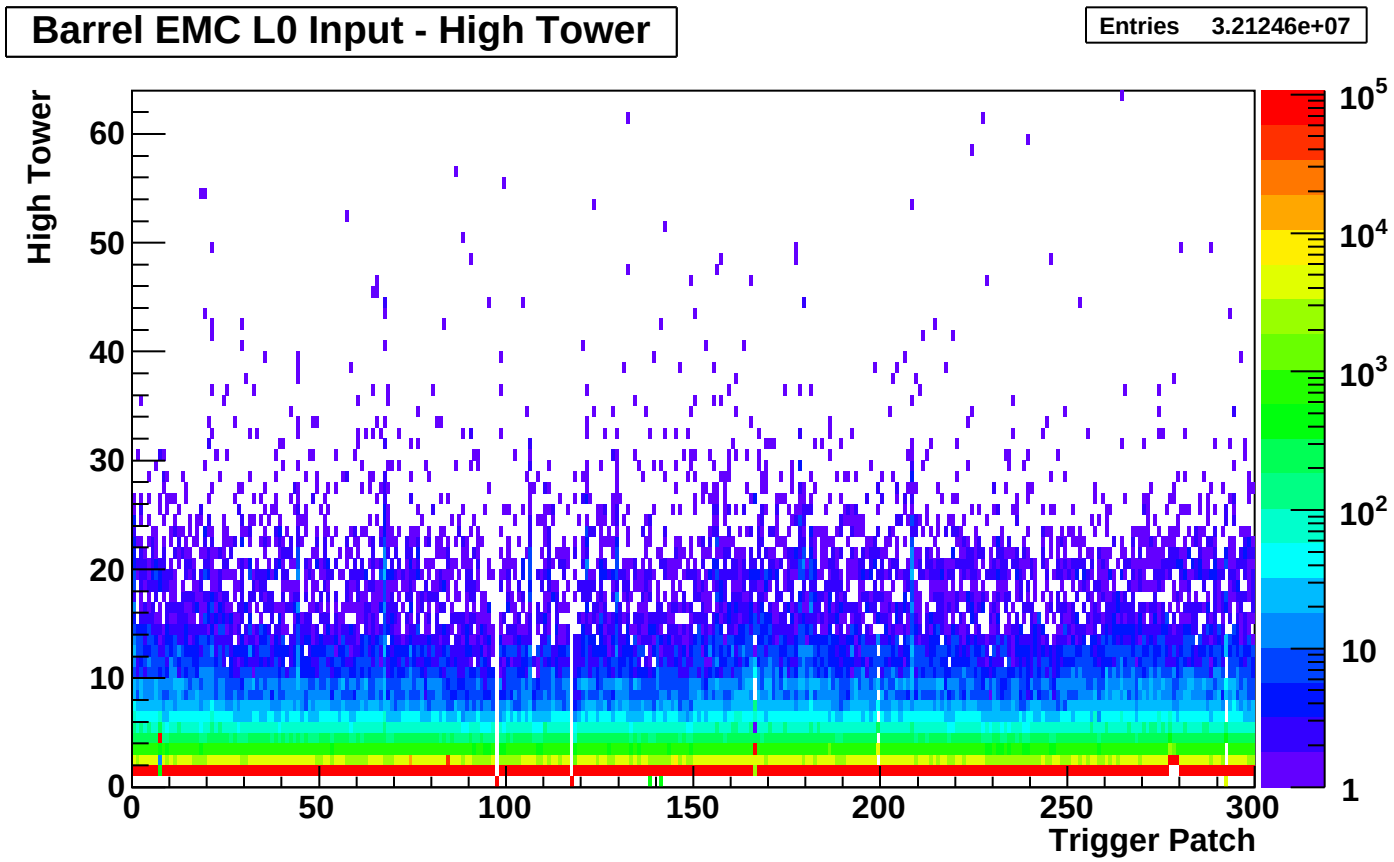

DAQ10k HT bits errors
Entries 4

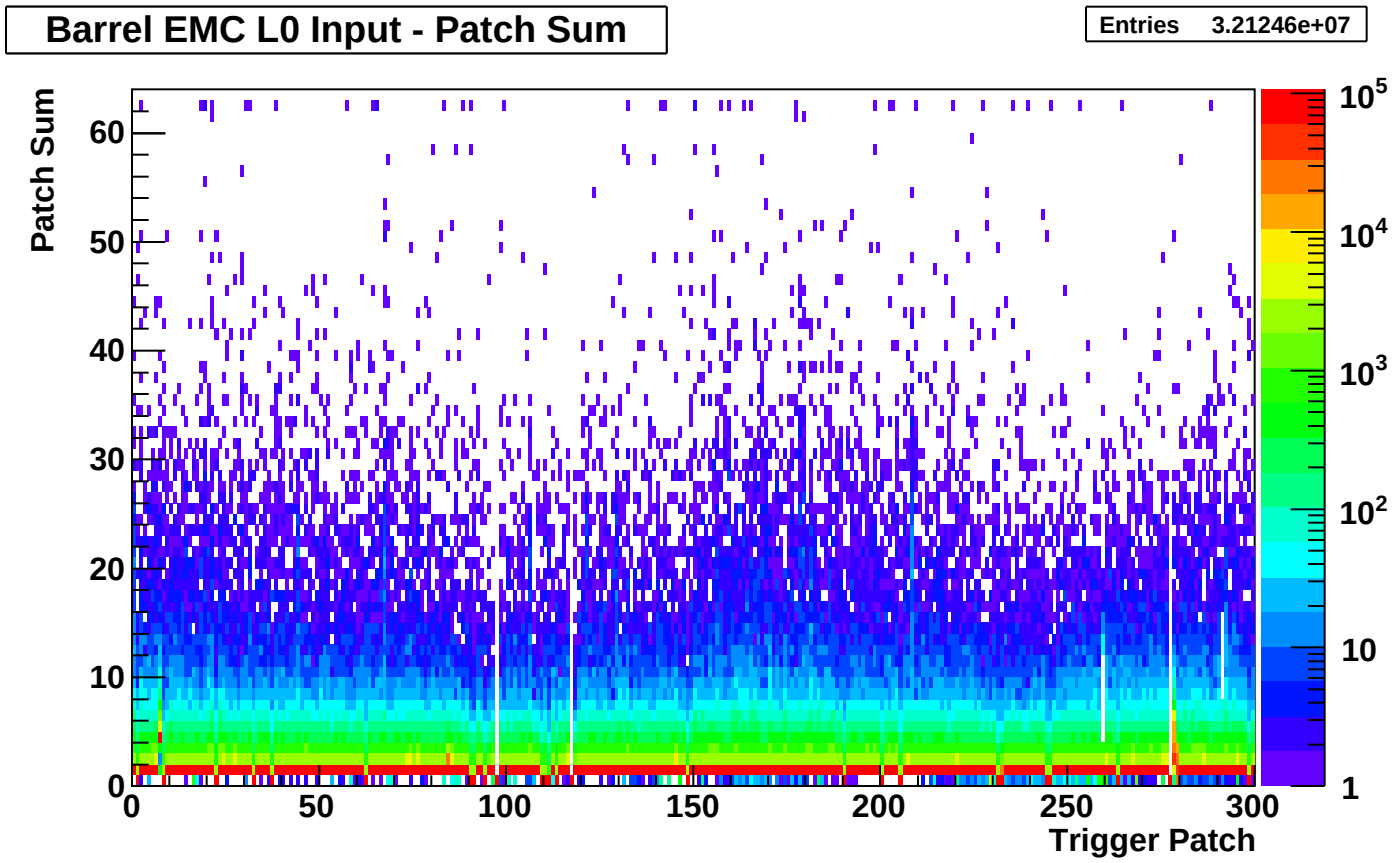

TPC Readout Mask (simulated)
Entries 13526

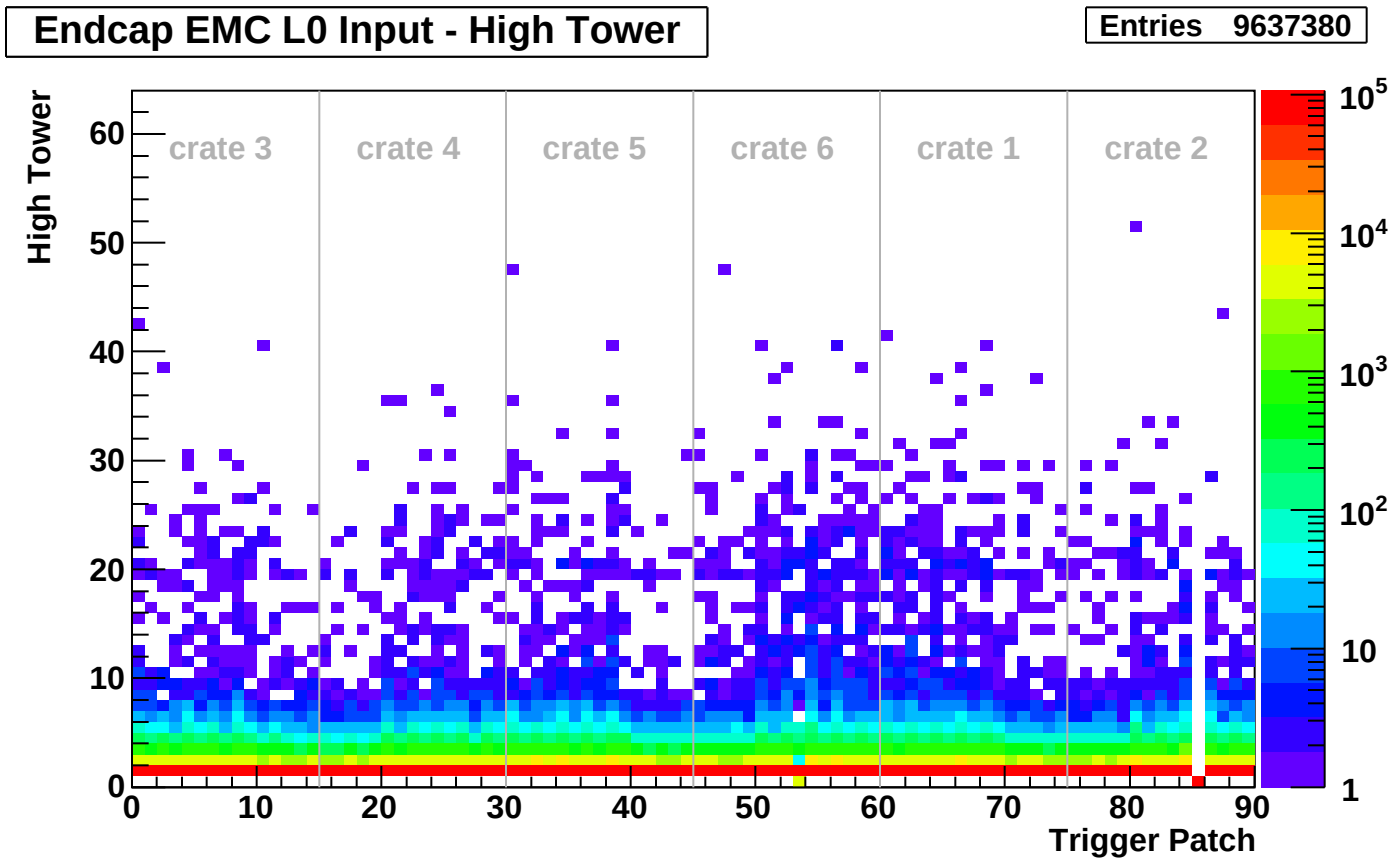

BunchId7Bit (all events)
Entries 107082

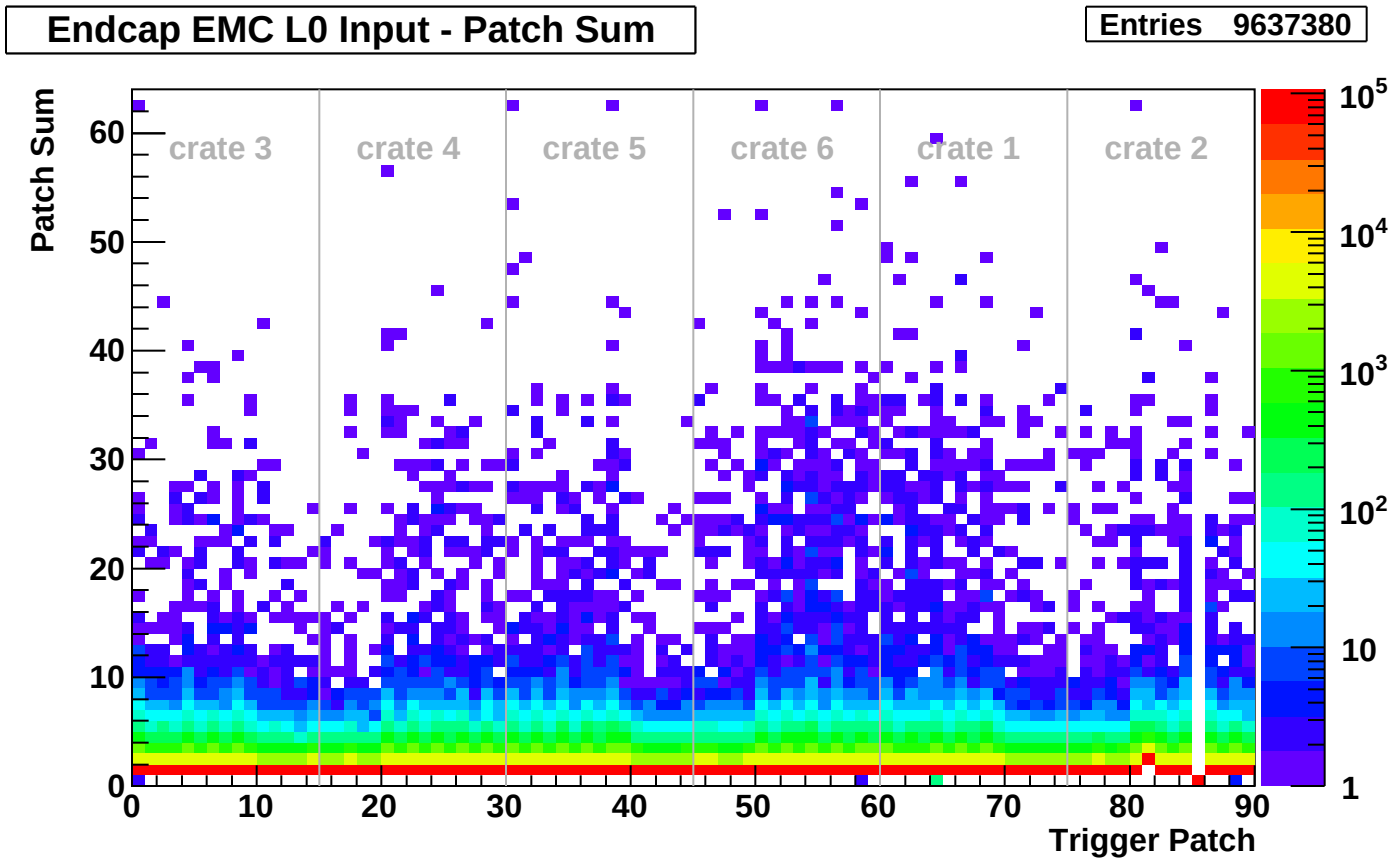

!Blue-Fill
Entries 7792

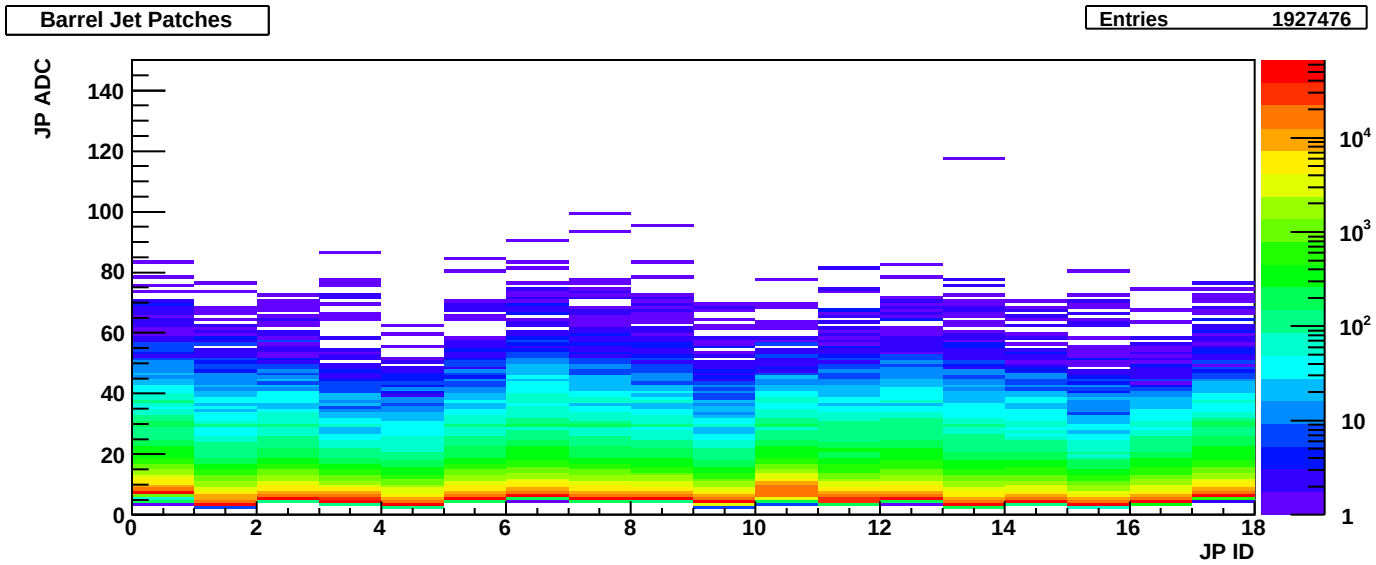

!Yellow-Fill
Entries 9198

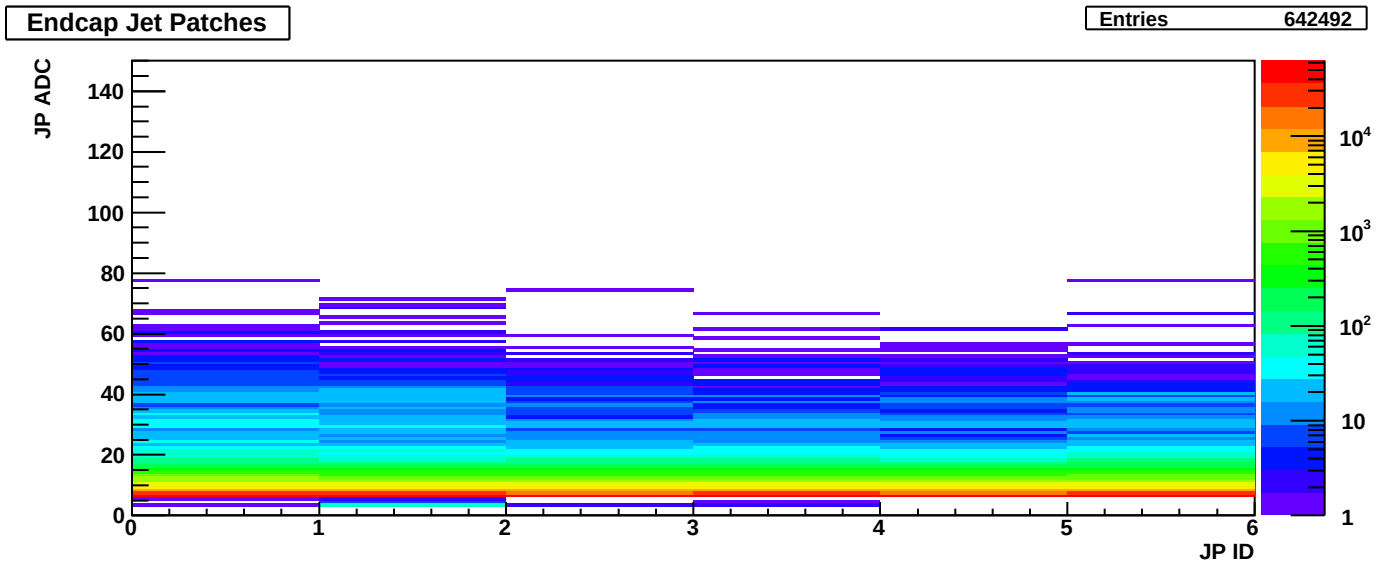

Barrel EMC L0 Input - High Tower
Entries 3.21246e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

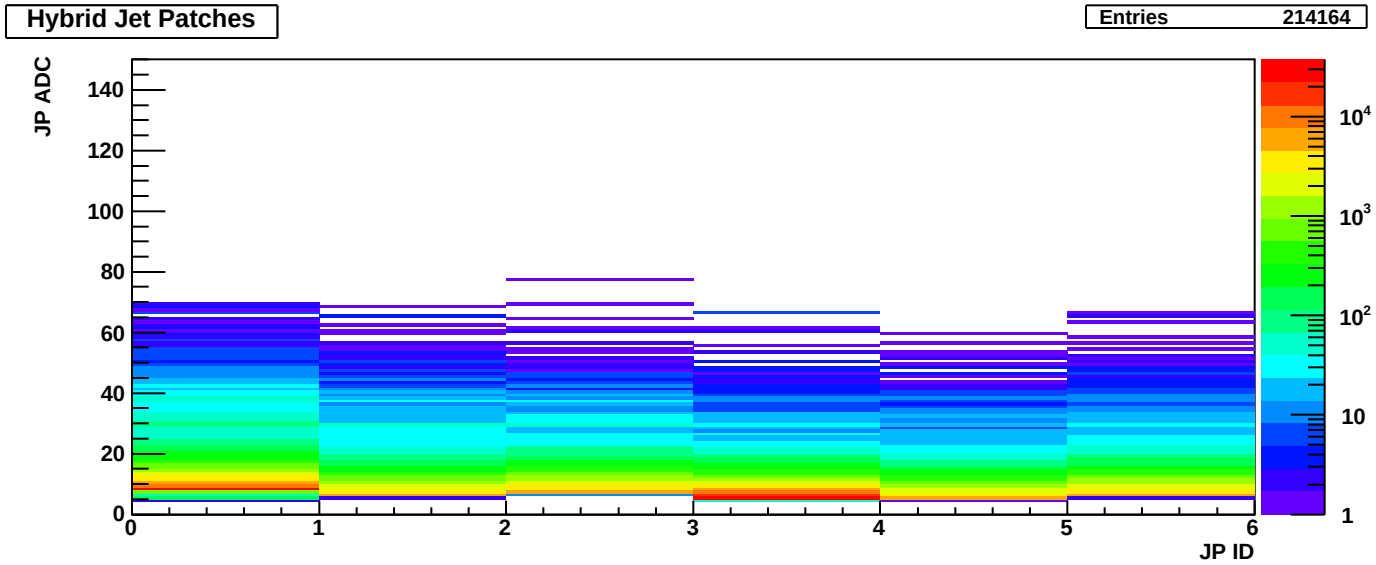

Barrel EMC L0 Input - Patch Sum
Entries 3.21246e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

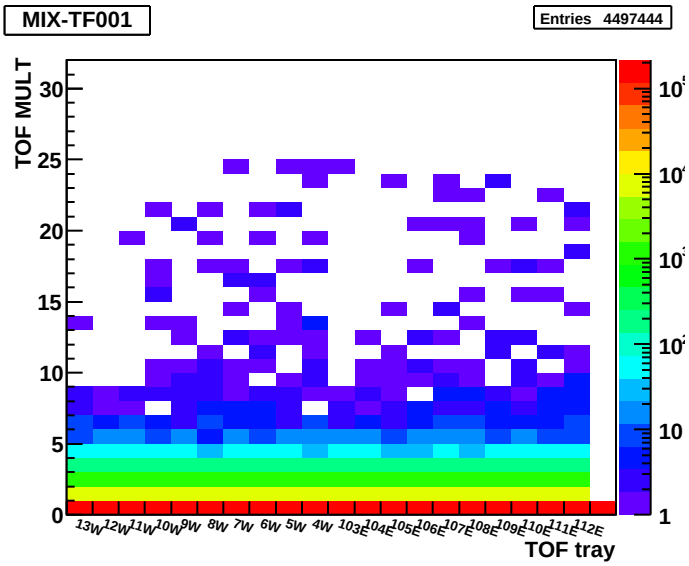

Endcap EMC L0 Input - High Tower
Entries 9637380
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

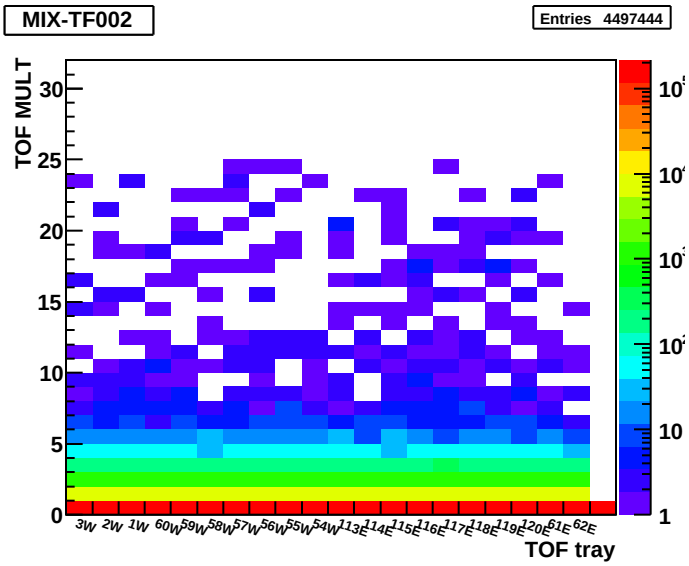

Endcap EMC L0 Input - Patch Sum
Entries 9637380
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

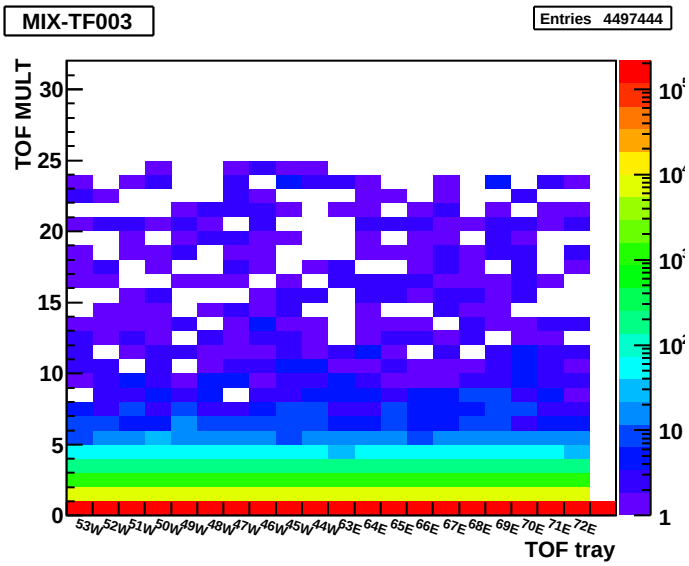

Barrel Jet Patches
Entries 1927476
JP ADC
JP ID

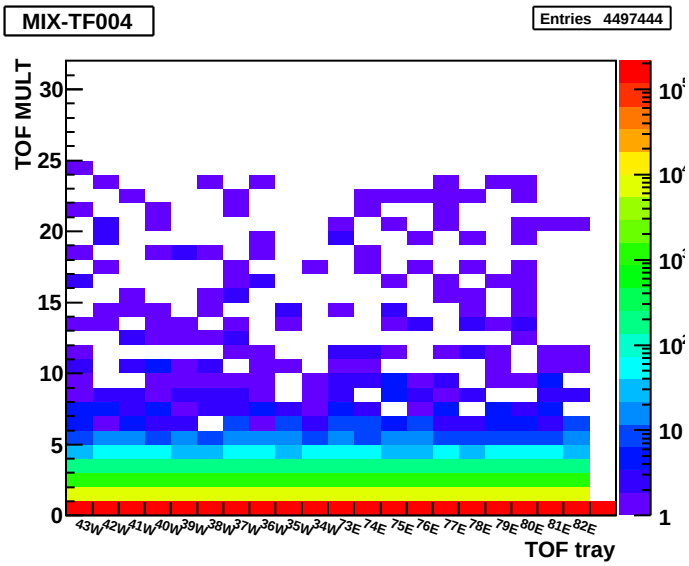

Endcap Jet Patches
Entries 642492
JP ADC
JP ID

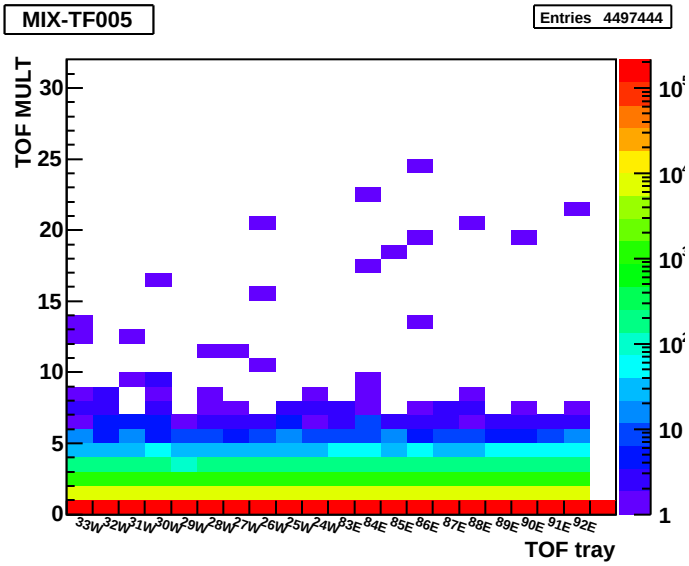

Hybrid Jet Patches
Entries 214164
JP ADC
JP ID

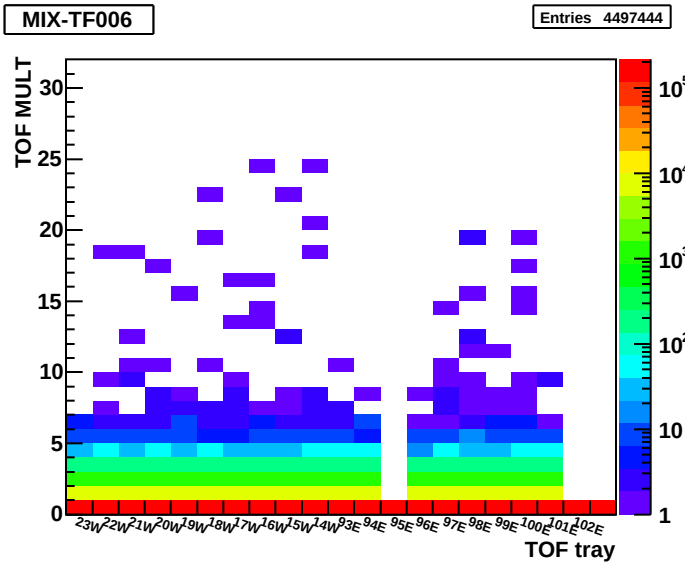

MIX-TF001
Entries 4497444
TOF MULT
TOF tray

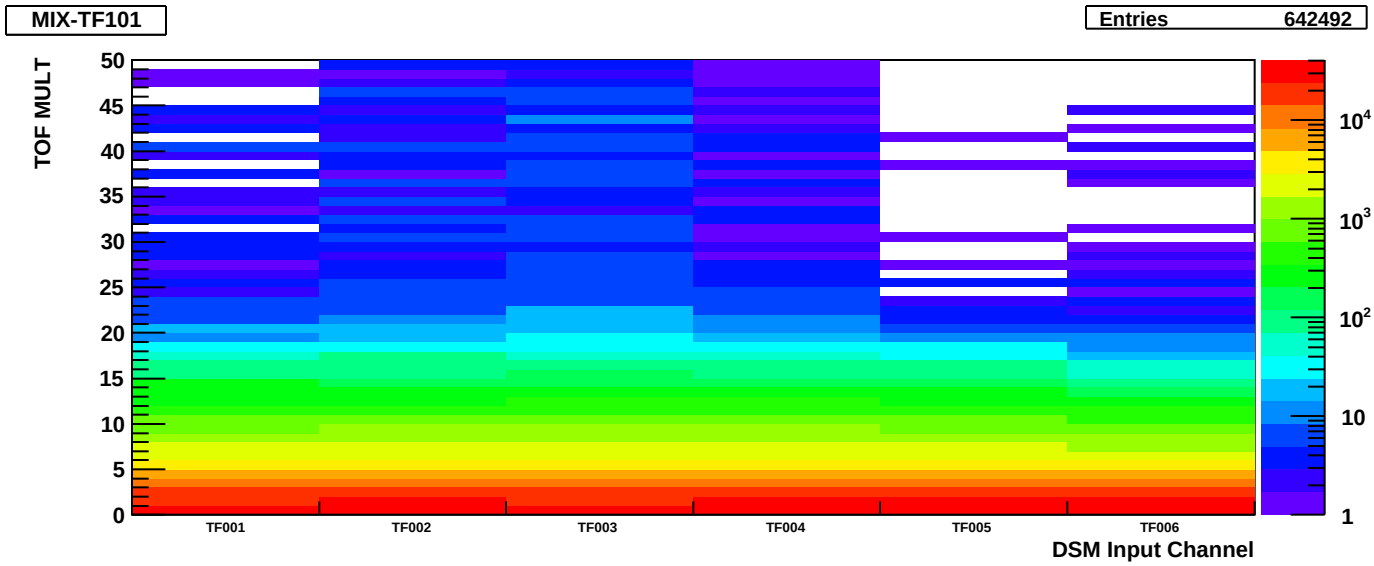

MIX-TF002
Entries 4497444
TOF MULT
TOF tray

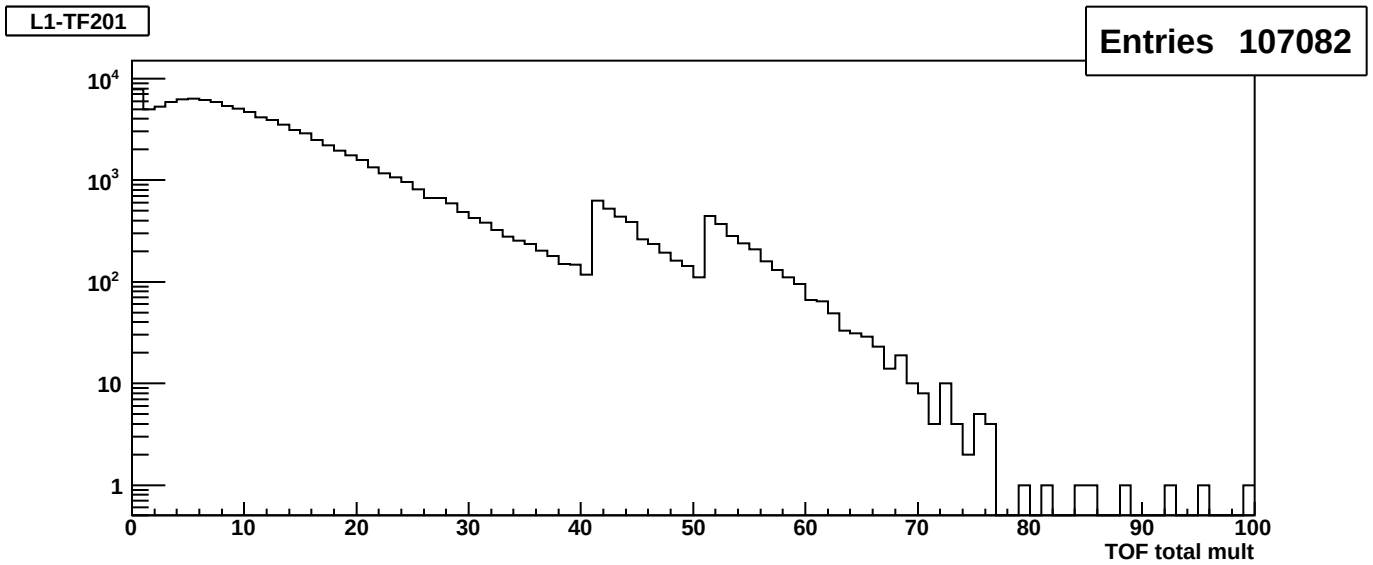

MIX-TF003
Entries 4497444
TOF MULT
TOF tray

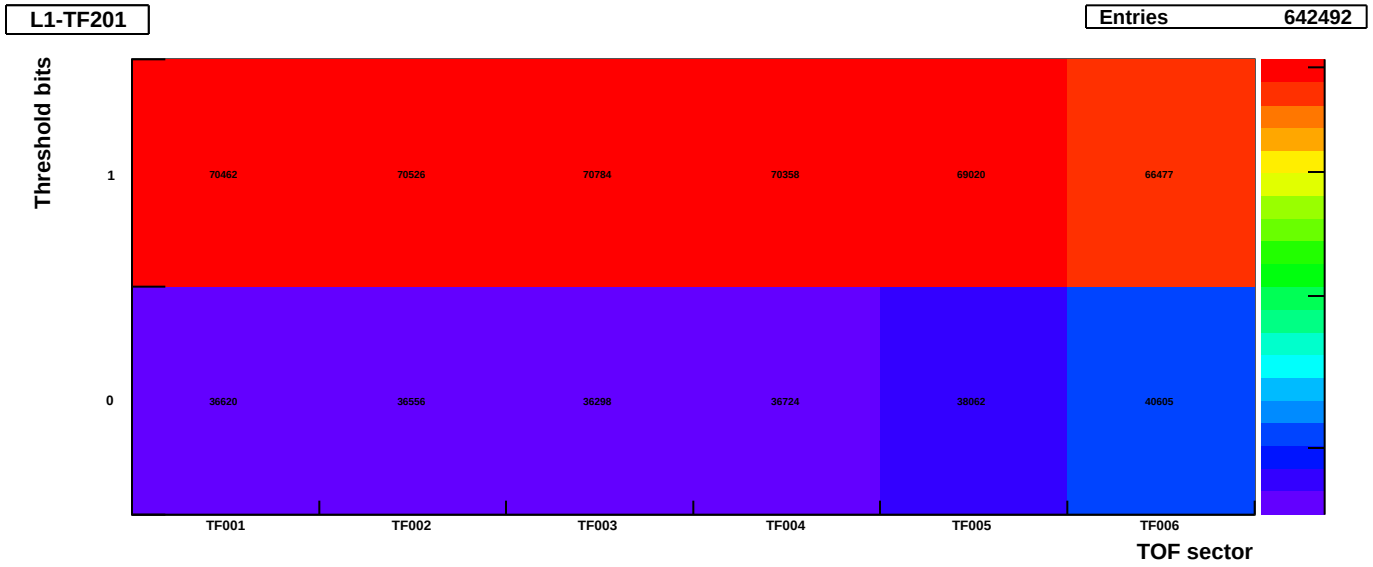

MIX-TF004
Entries 4497444
TOF MULT
TOF tray

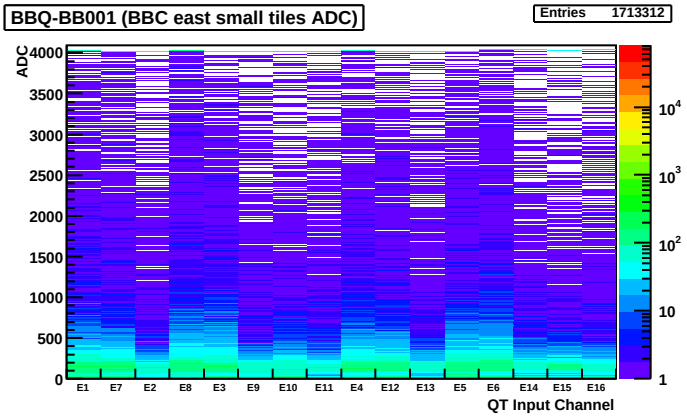

MIX-TF005
Entries 4497444
TOF MULT
TOF tray

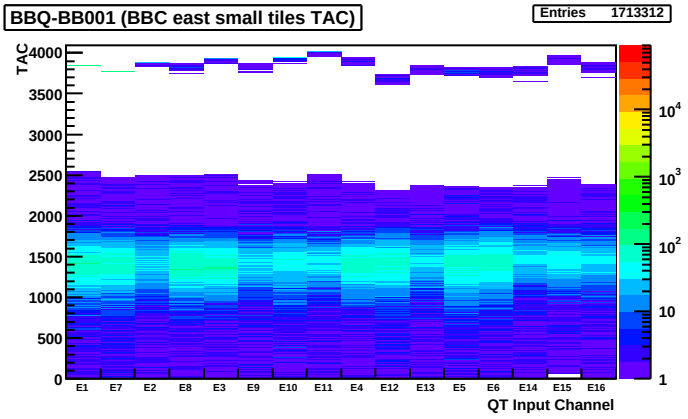

MIX-TF006
Entries 4497444
TOF MULT
TOF tray

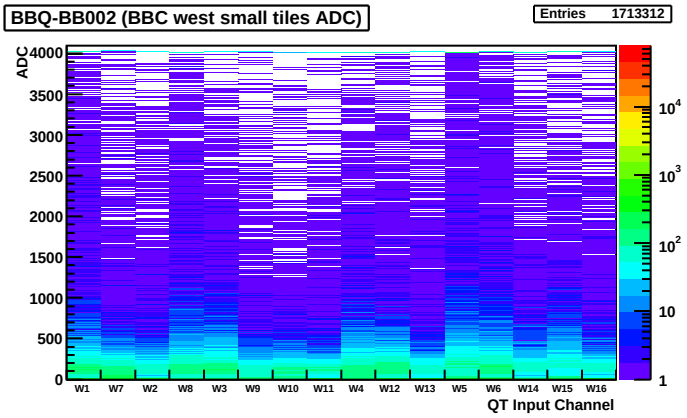

MIX-TF101
Entries 642492
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

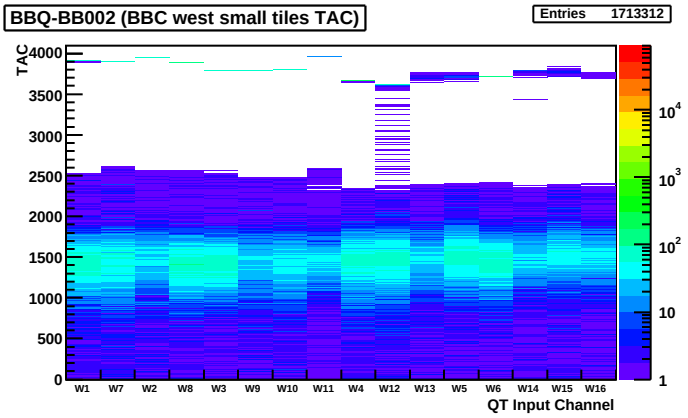

L1-TF201
Entries 107082
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

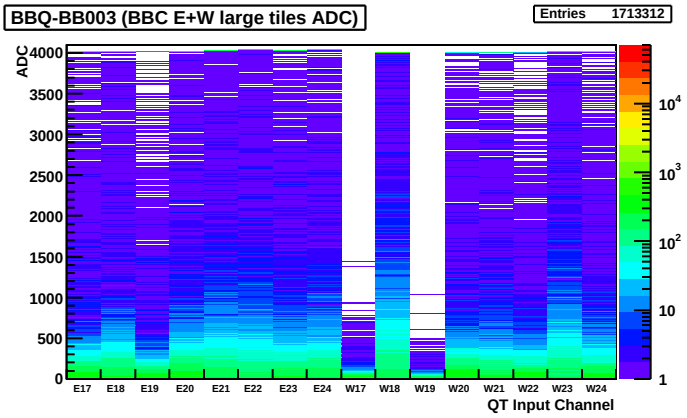

L1-TF201
Entries 642492
Threshold bits
1
0
70462
70526
70784
70358
69020
66477
36620
36556
36298
36724
38062
40605
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

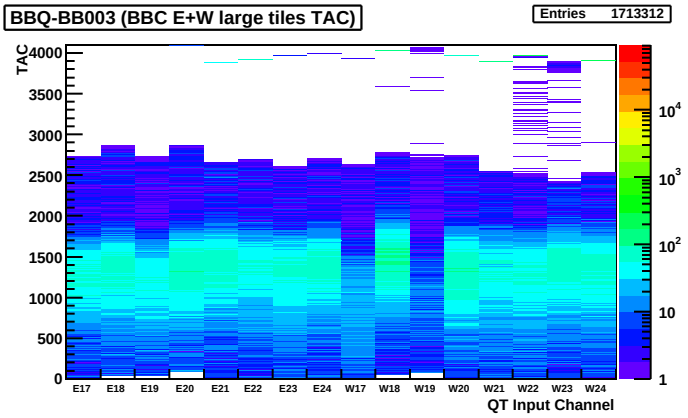

BBQ-BB001 (BBC east small tiles ADC)
Entries 1713312
ADC
QT Input Channel

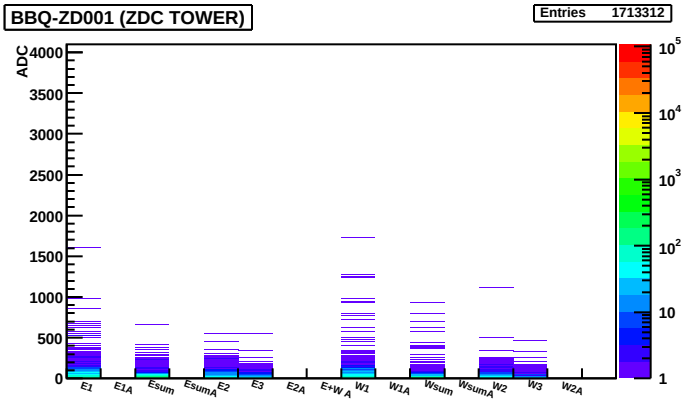

BBQ-BB001 (BBC east small tiles TAC)
Entries 1713312
TAC
QT Input Channel

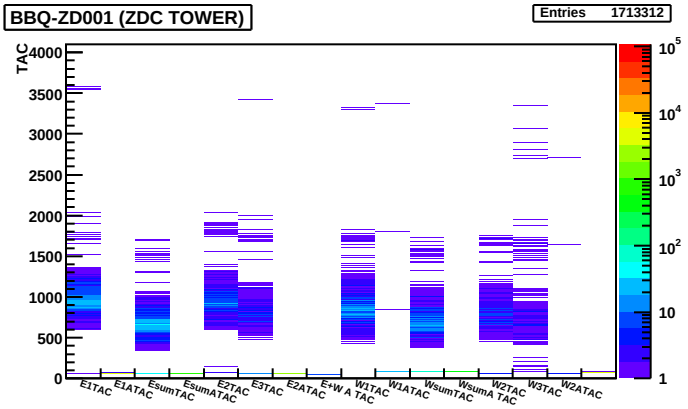

BBQ-BB002 (BBC west small tiles ADC)
Entries 1713312
ADC
QT Input Channel

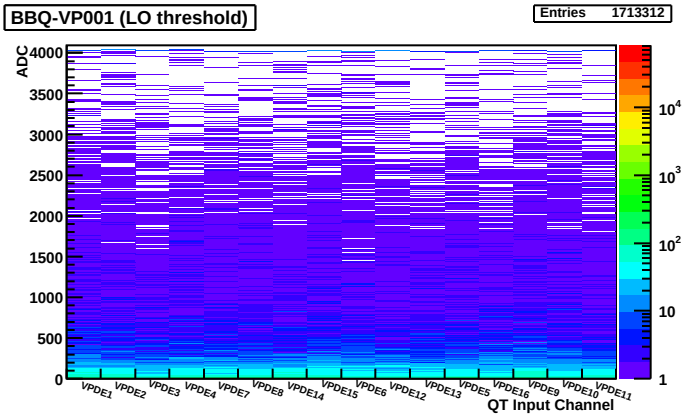

BBQ-BB002 (BBC west small tiles TAC)
Entries 1713312
TAC
QT Input Channel

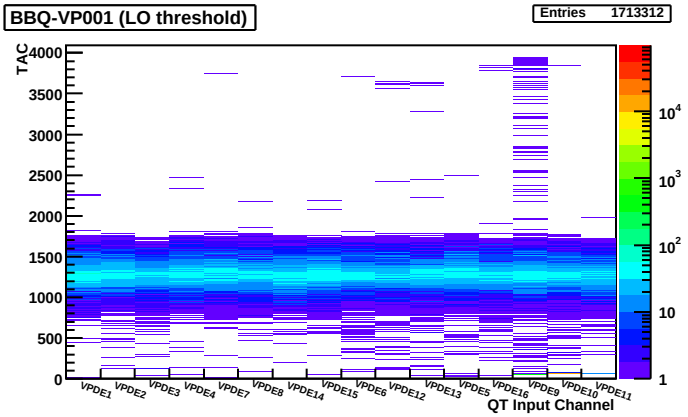

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1713312
ADC
QT Input Channel

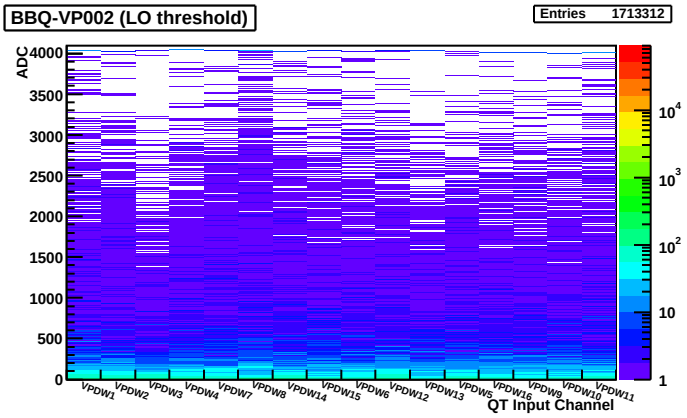

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1713312
TAC
QT Input Channel

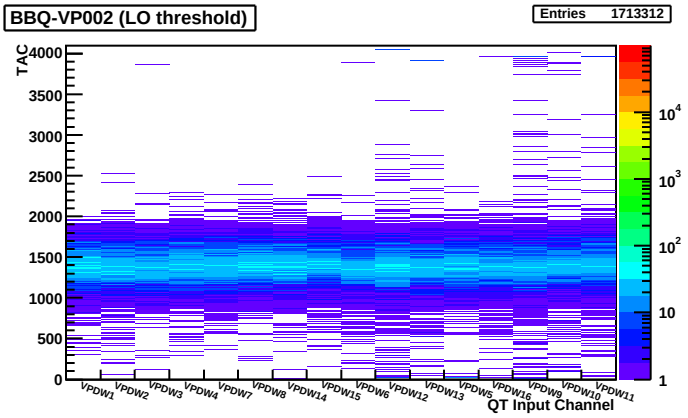

BBQ-ZD001 (ZDC TOWER)
Entries 1713312
ADC

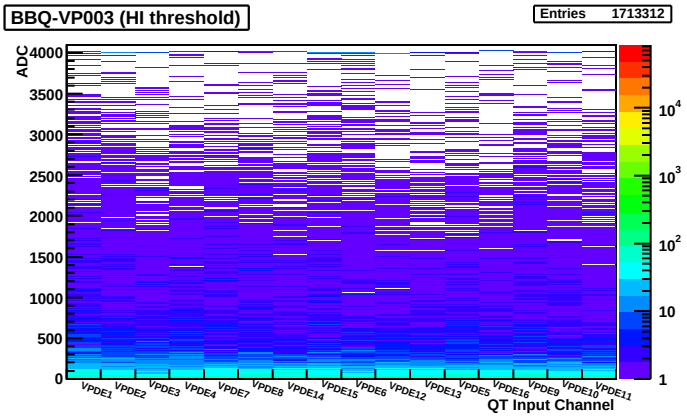

BBQ-ZD001 (ZDC TOWER)
Entries 1713312
TAC

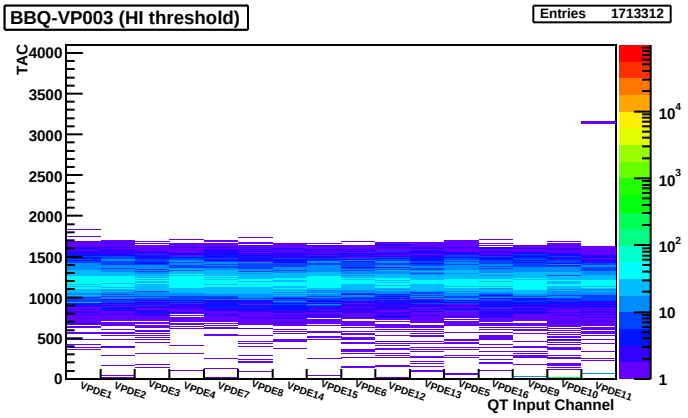

BBQ-VP001 (LO threshold)
Entries 1713312
ADC
QT Input Channel

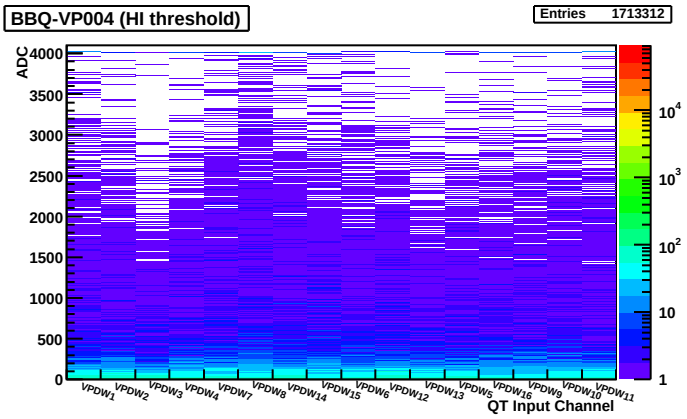

BBQ-VP001 (LO threshold)
Entries 1713312
TAC
QT Input Channel

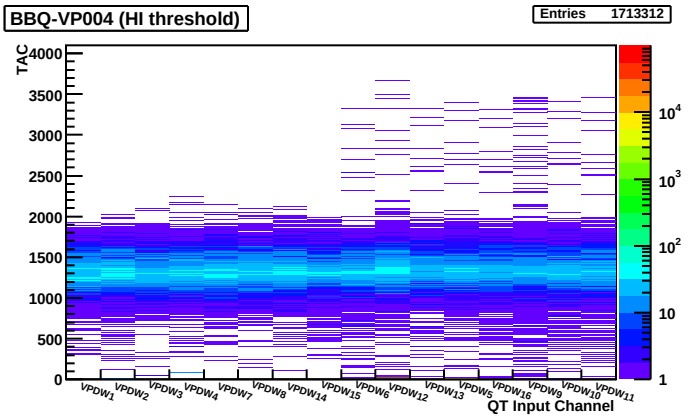

BBQ-VP002 (LO threshold)
Entries 1713312
ADC
QT Input Channel

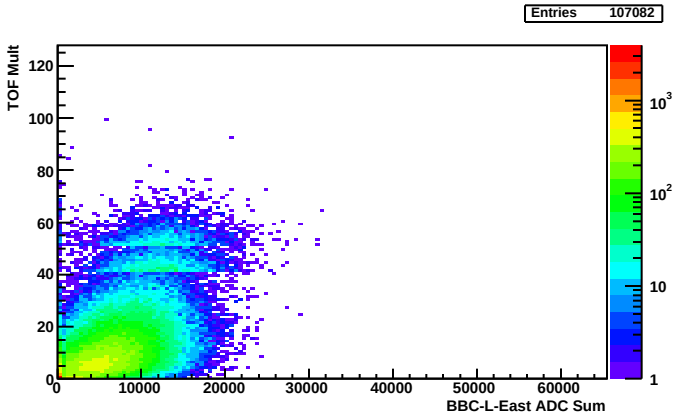

BBQ-VP002 (LO threshold)
Entries 1713312
TAC
QT Input Channel

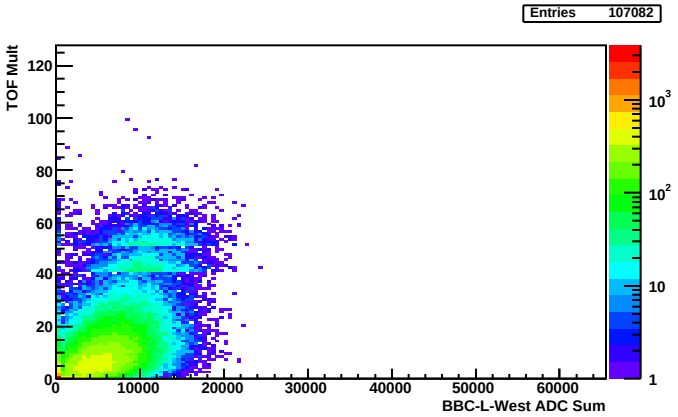

BBQ-VP003 (HI threshold)
Entries 1713312
ADC
QT Input Channel

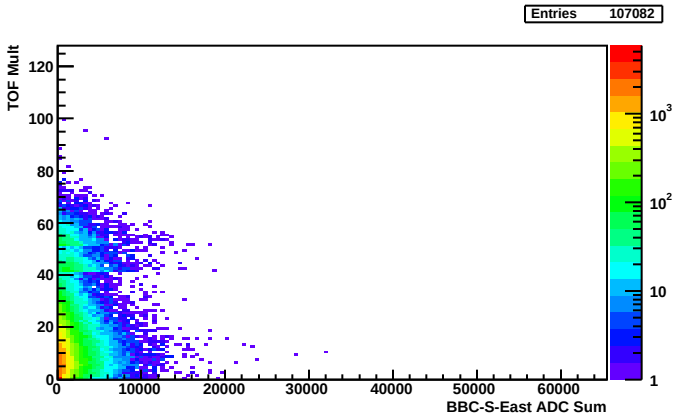

BBQ-VP003 (HI threshold)
Entries 1713312
TAC
QT Input Channel

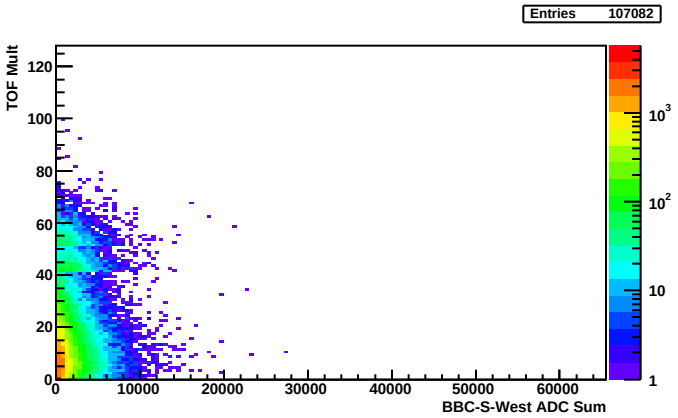

BBQ-VP004 (HI threshold)
Entries 1713312
ADC
QT Input Channel

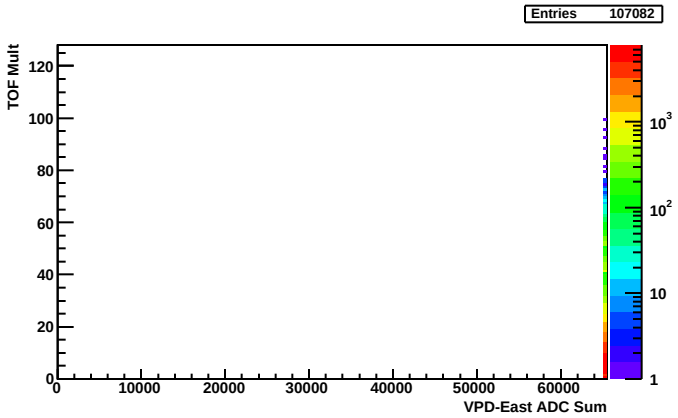

BBQ-VP004 (HI threshold)
Entries 1713312
TAC
QT Input Channel

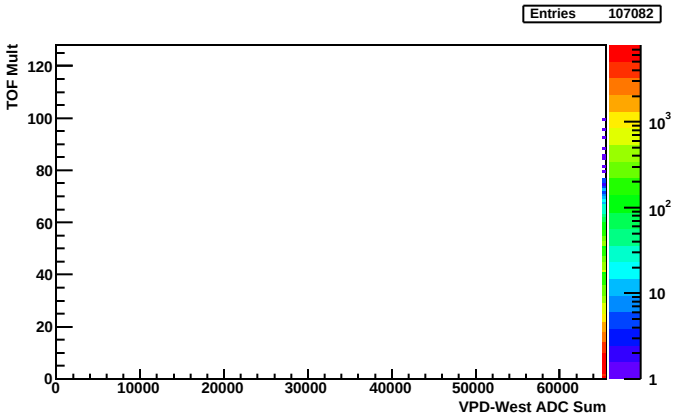

Entries 107082
TOF Mult
BBC-L-East ADC Sum

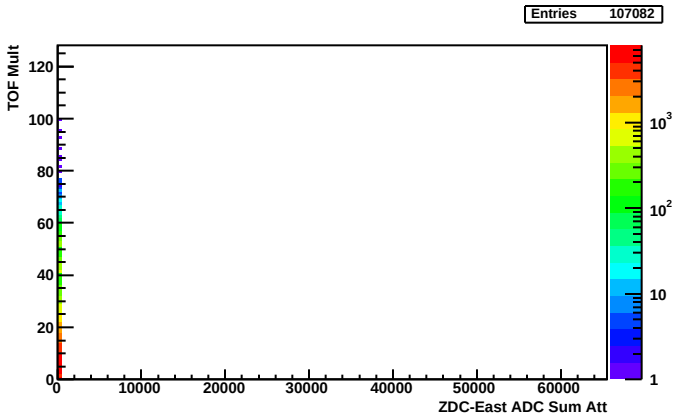

Entries 107082
TOF Mult
BBC-L-West ADC Sum

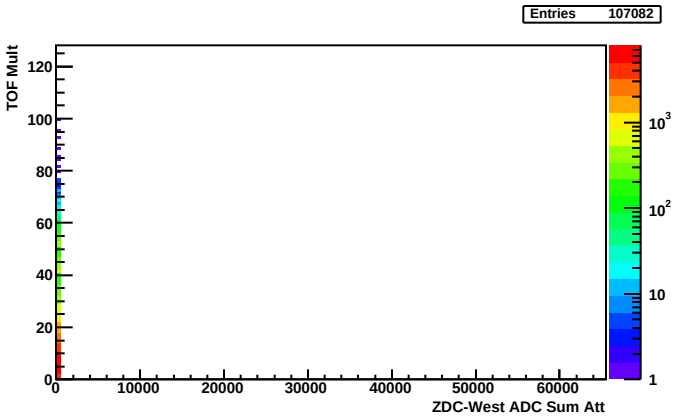

Entries 107082
TOF Mult
BBC-S-East ADC Sum

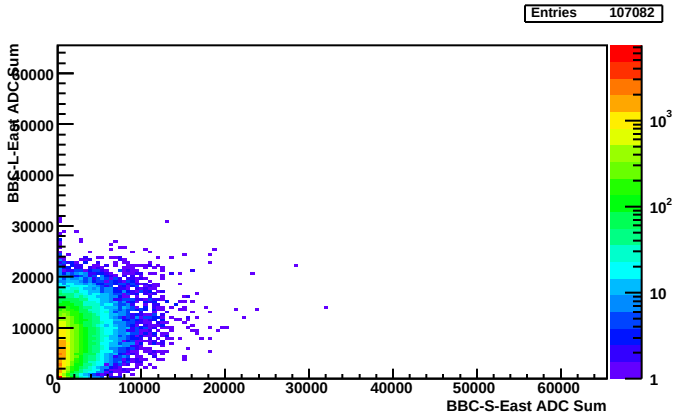

Entries 107082
TOF Mult
BBC-S-West ADC Sum

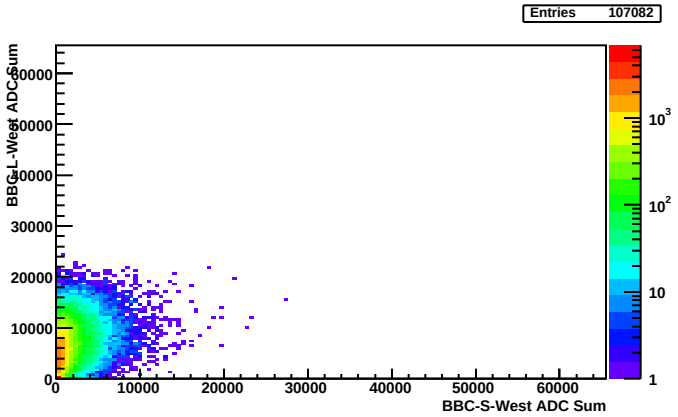

Entries 107082
TOF Mult
VPD-East ADC Sum

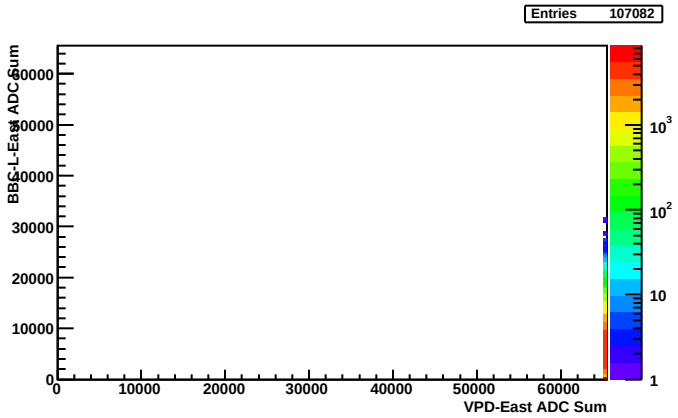

Entries 107082
TOF Mult
VPD-West ADC Sum

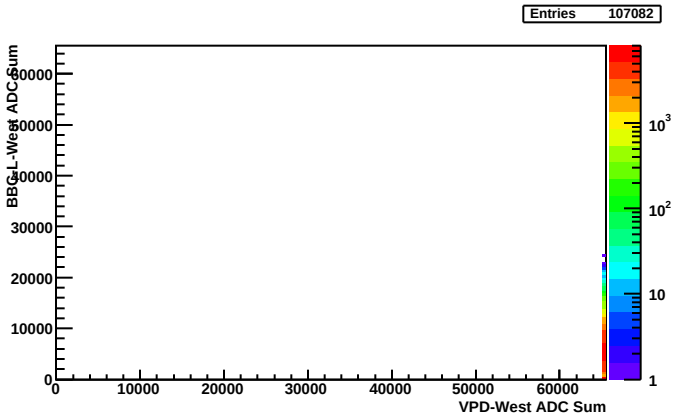

Entries 107082
TOF Mult
ZDC-East ADC Sum Att

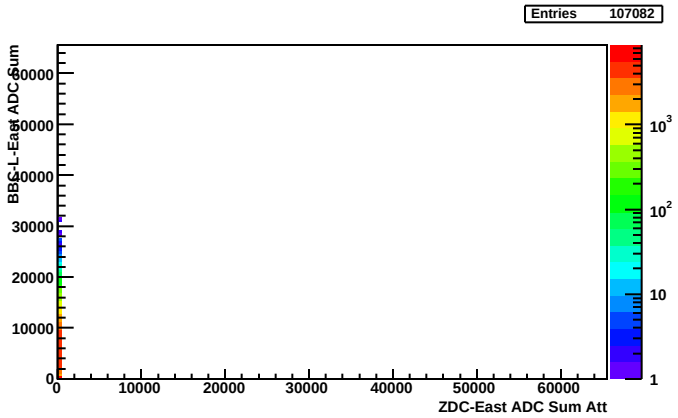

Entries 107082
TOF Mult
ZDC-West ADC Sum Att


Entries 107082
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 107082
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 107082
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 107082
BBC-L-West ADC Sum
VPD-West ADC Sum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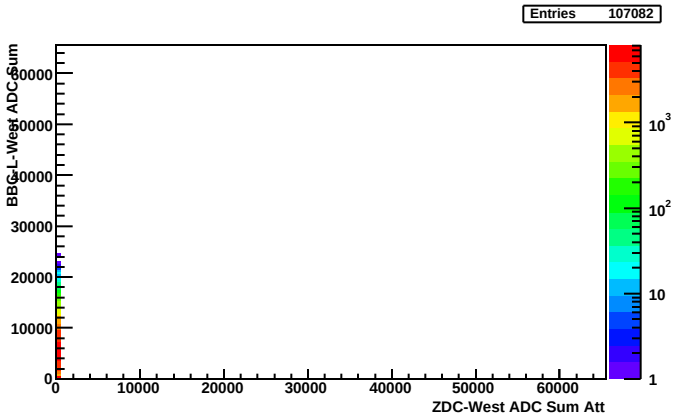
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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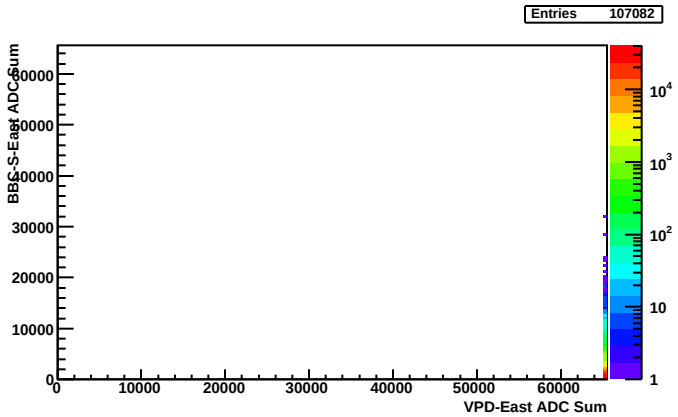
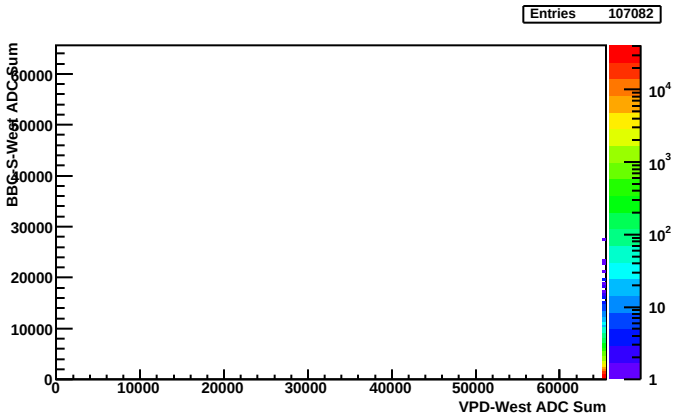
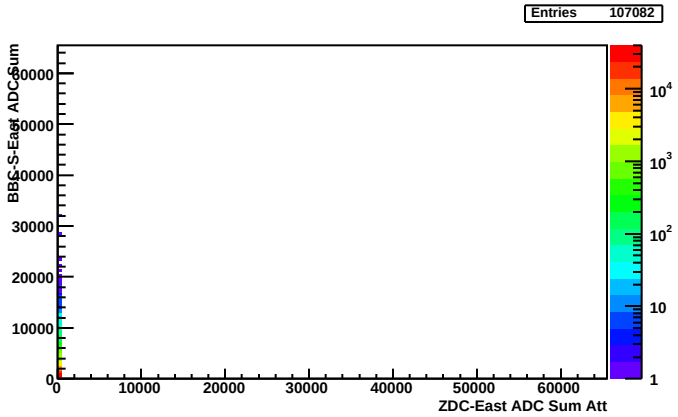
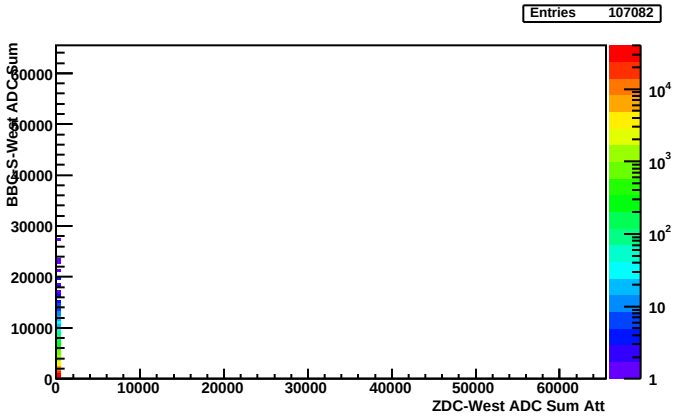
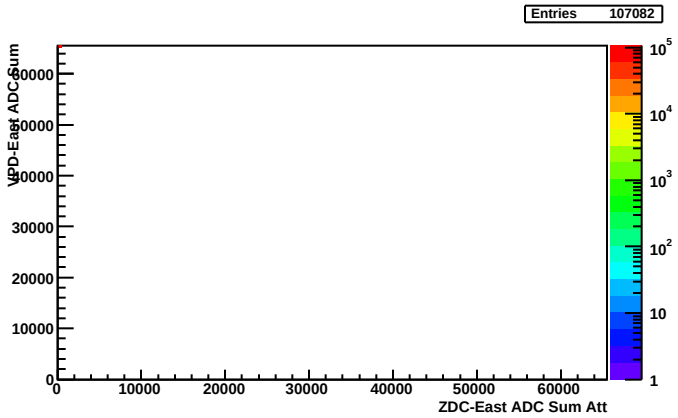
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 107082
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 107082
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 107082
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 107082
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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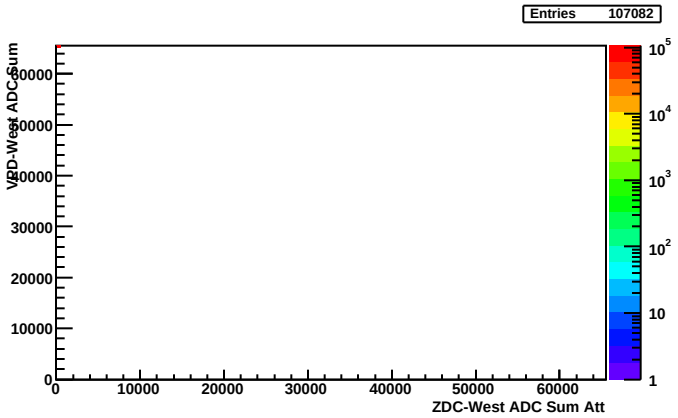
VPD-East ADC Sum
ZDC-East ADC Sum Att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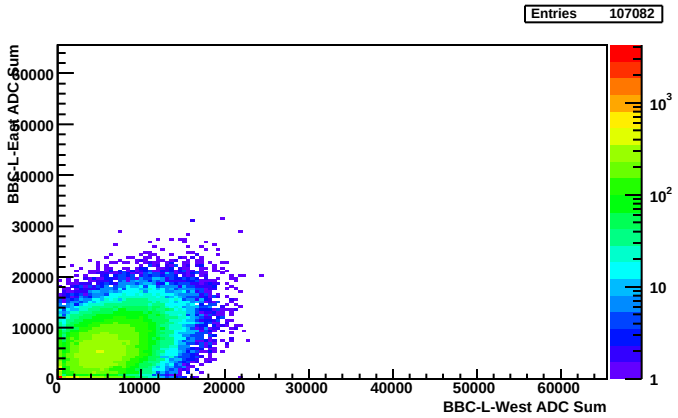
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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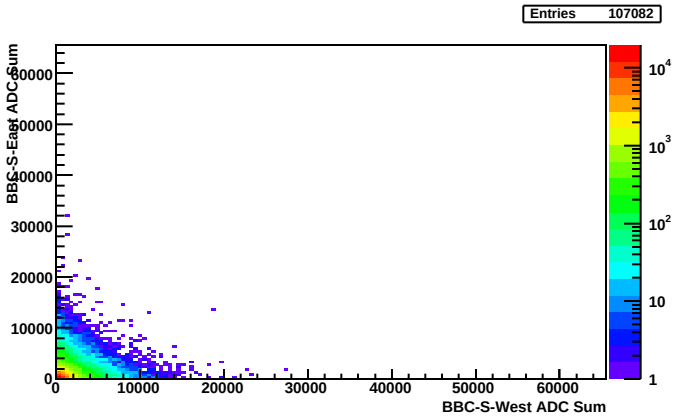
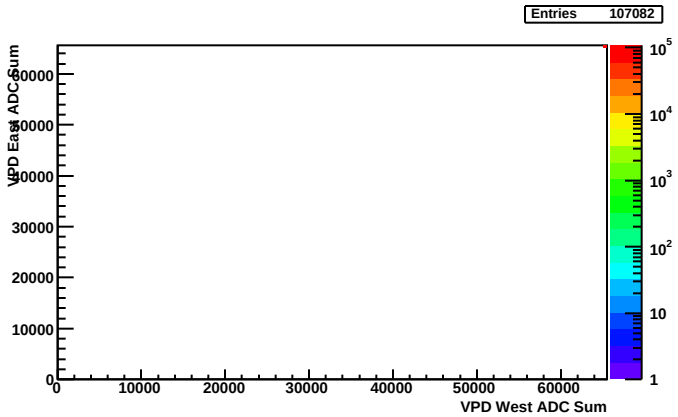
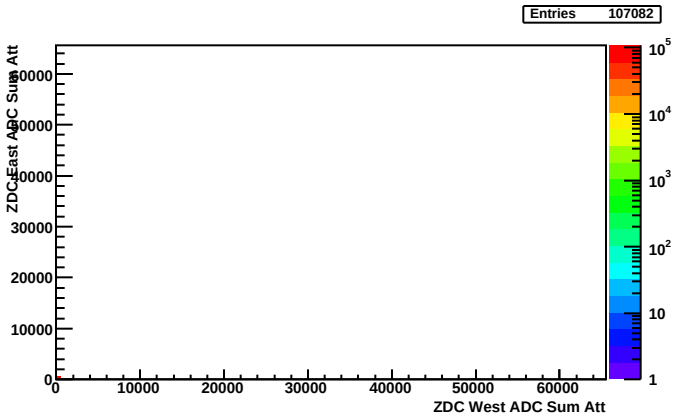
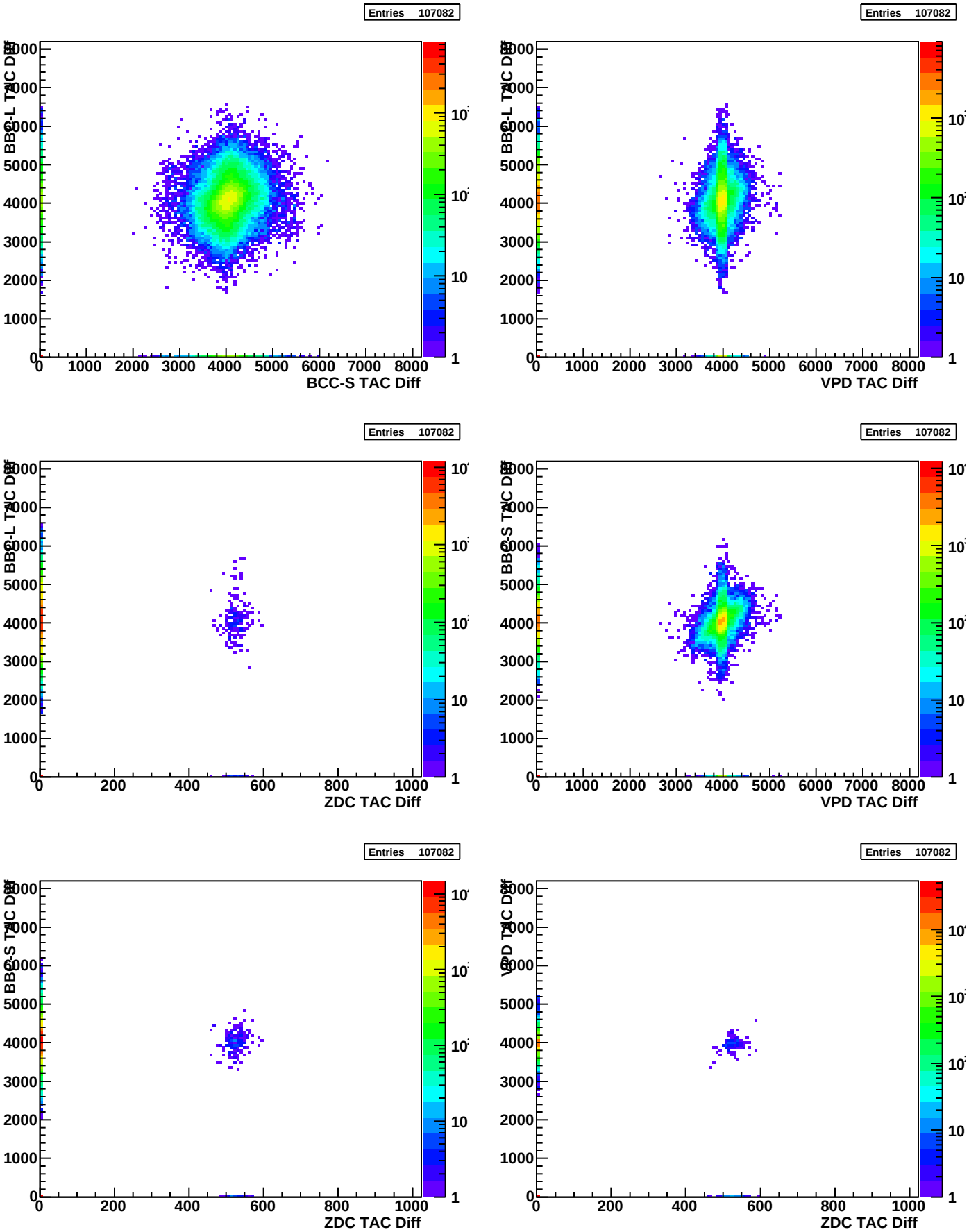
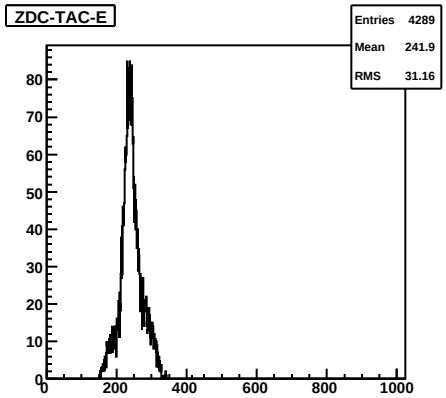
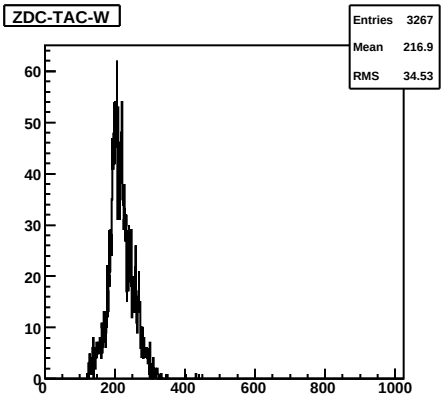

Entries 107082
BBC-S-East ADC Sum
BBC-S-West ADC Sum

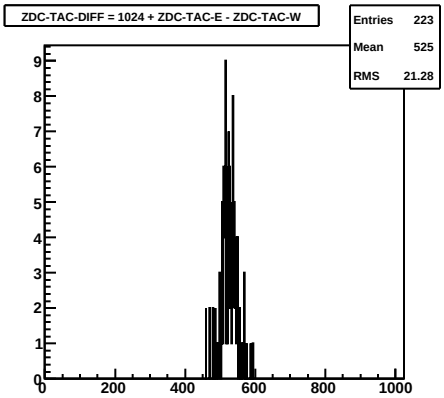

Entries 107082
VPD East ADC Sum
VPD West ADC Sum

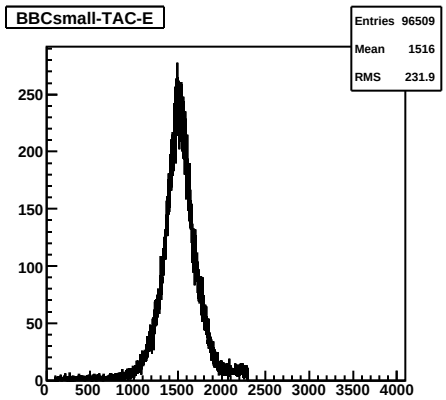

Entries 107082
ZDC East ADC Sum Att
ZDC West ADC Sum Att

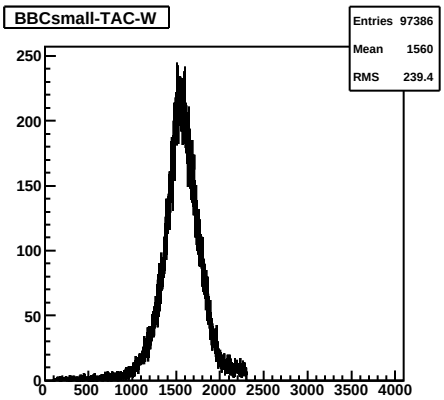

Entries 107082
BBC-L TAC Diff
BCC-S TAC Diff
Entries 107082
BBC-L TAC Diff
VPD TAC Diff
Entries 107082
BBC-L TAC Diff
ZDC TAC Diff
Entries 107082
BBC-S TAC Diff
VPD TAC Diff
Entries 107082
BBC-S TAC Diff
ZDC TAC Diff
Entries 107082
VPD TAC Diff
ZDC TAC Diff

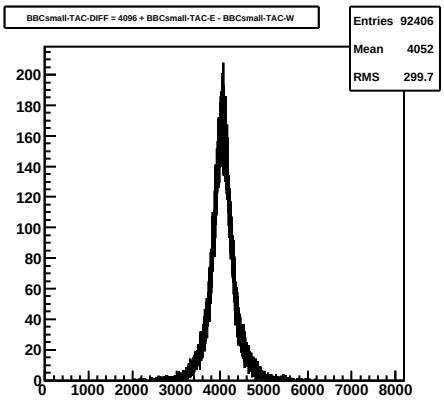

ZDC-TAC-E
Entries 4289
Mean 241.9
RMS 31.16

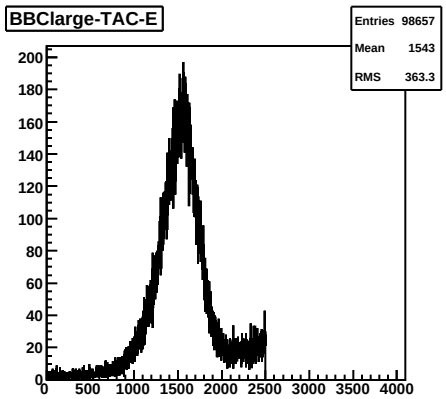

ZDC-TAC-W
Entries 3267
Mean 216.9
RMS 34.53

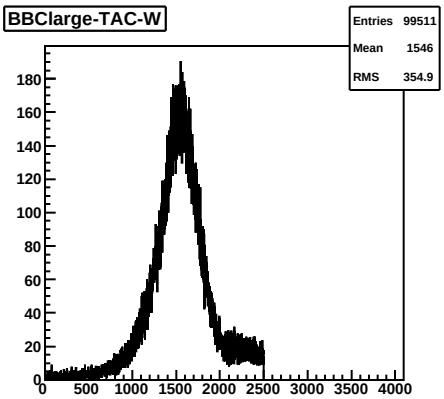

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 223
Mean 525
RMS 21.28

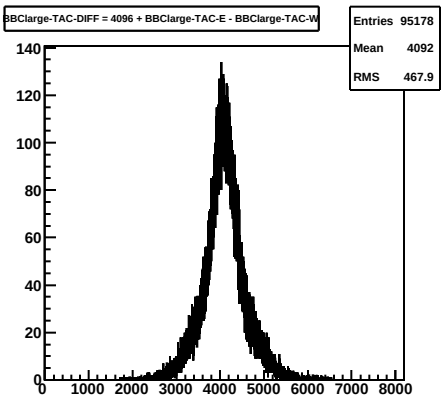

BBCsmall-TAC-E
Entries 96509
Mean 1516
RMS 231.9

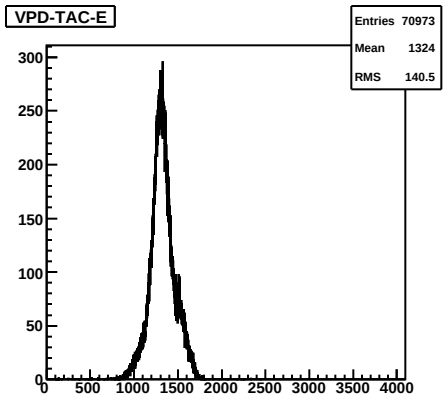

BBCsmall-TAC-W
Entries 97386
Mean 1560
RMS 239.4

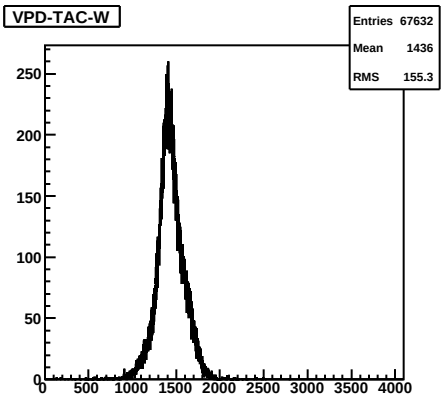

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 92406
Mean 4052
RMS 299.7

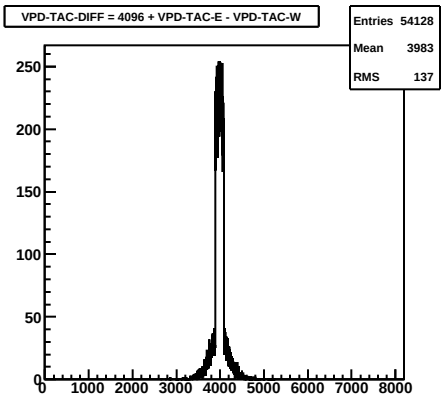

BBClarge-TAC-E
Entries 98657
Mean 1543
RMS 363.3

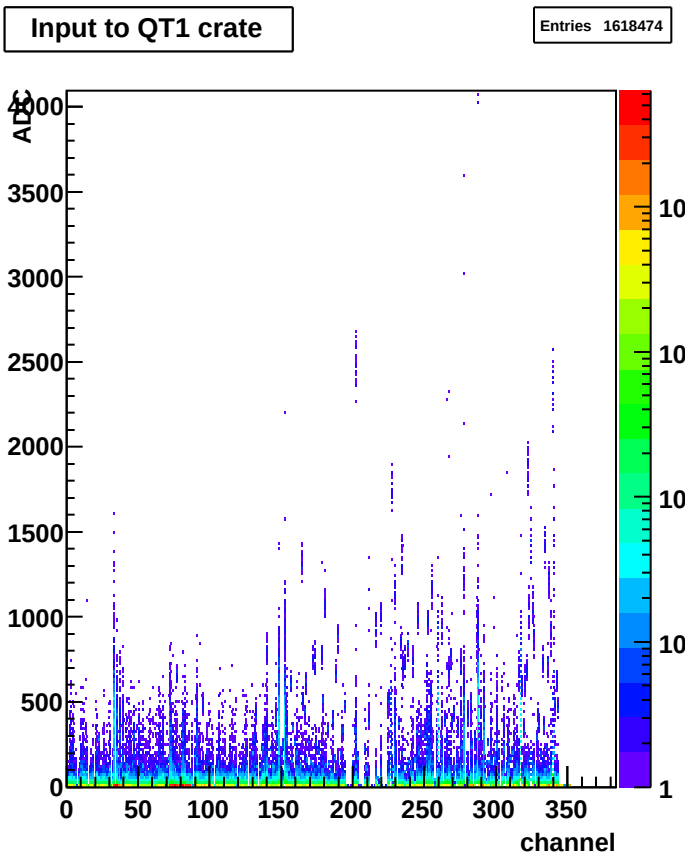

BBClarge-TAC-W
Entries 99511
Mean 1546
RMS 354.9

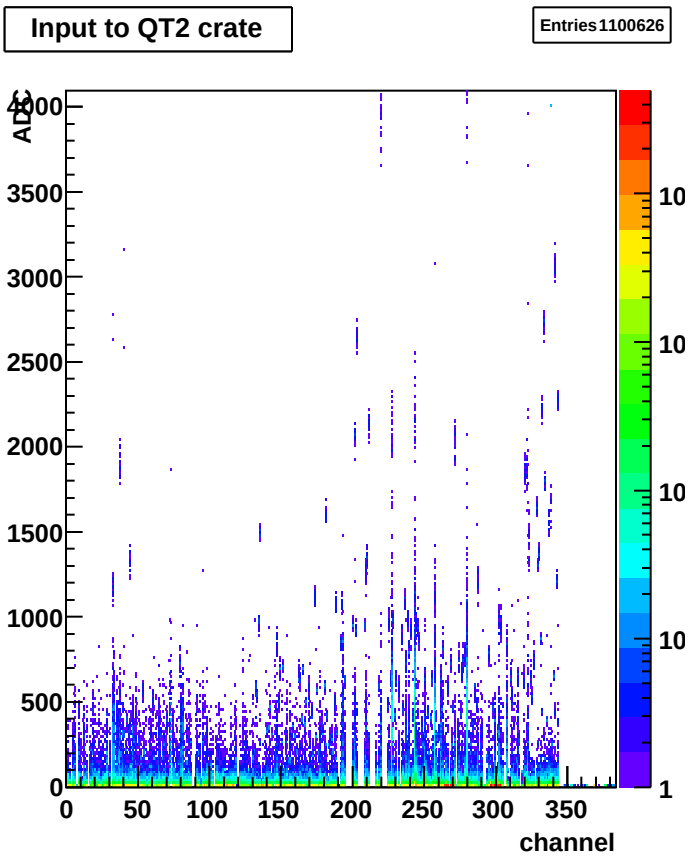

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 95178
Mean 4092
RMS 467.9

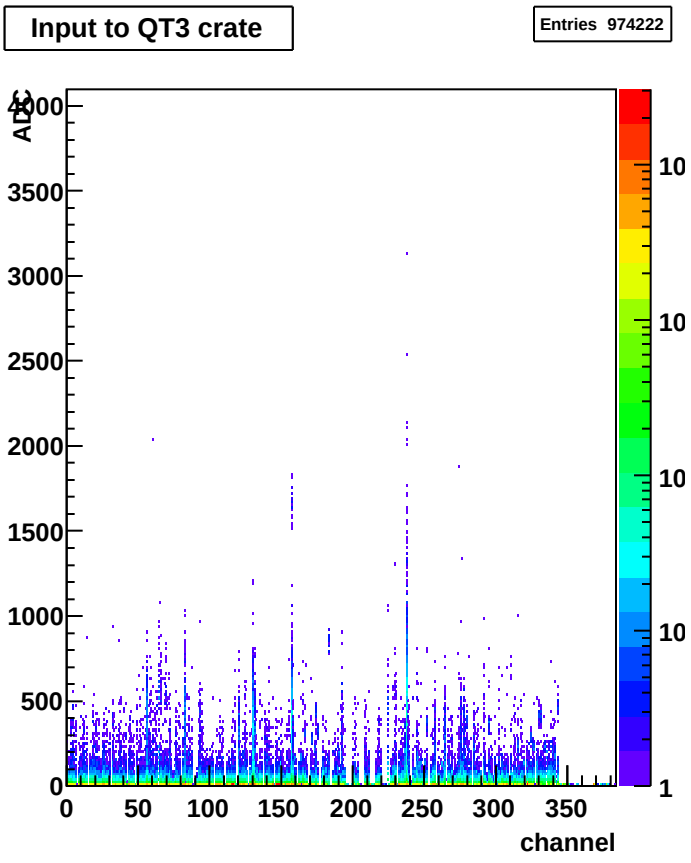

VPD-TAC-E
Entries 70973
Mean 1324
RMS 140.5

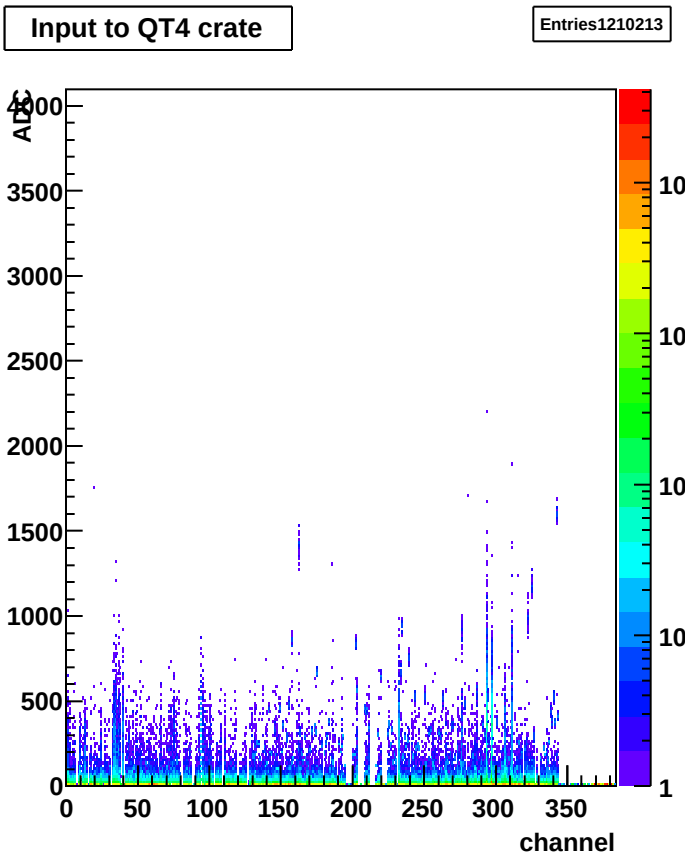

VPD-TAC-W
Entries 67632
Mean 1436
RMS 155.3

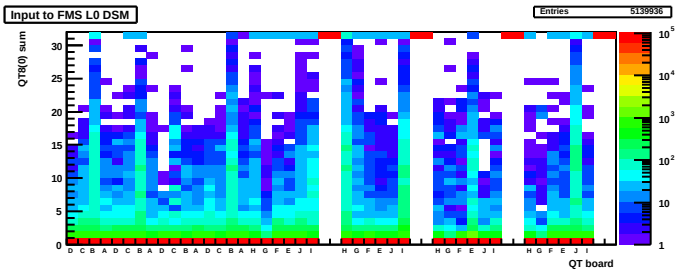

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 54128
Mean 3983
RMS 137

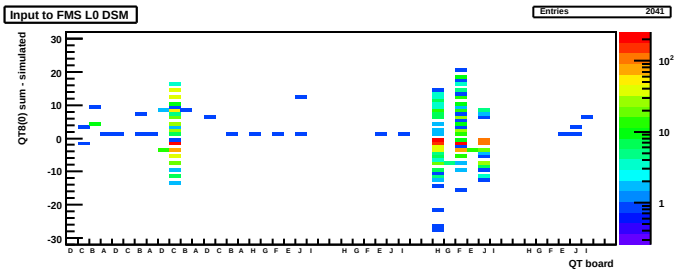

Input to QT1 crate
Entries 1618474
ADC
channel

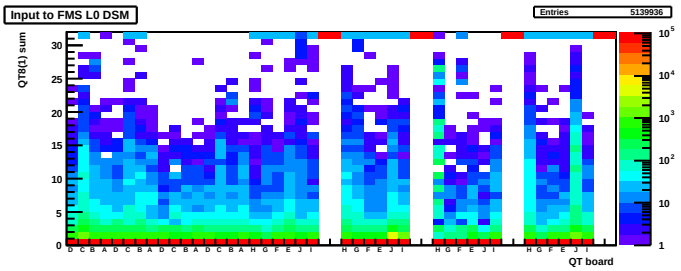

Input to QT2 crate
Entries 1100626
ADC
channel

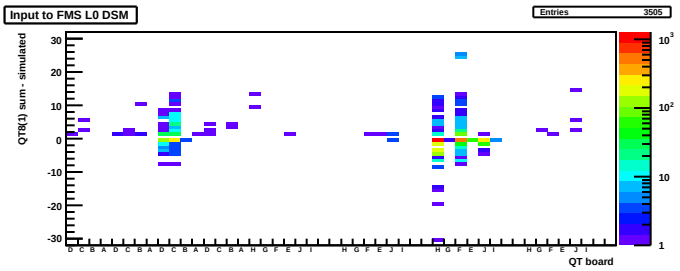

Input to QT3 crate
Entries 974222
ADC
channel

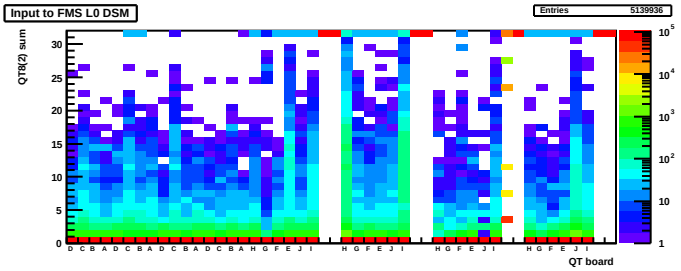

Input to QT4 crate
Entries 1210213
ADC
channel

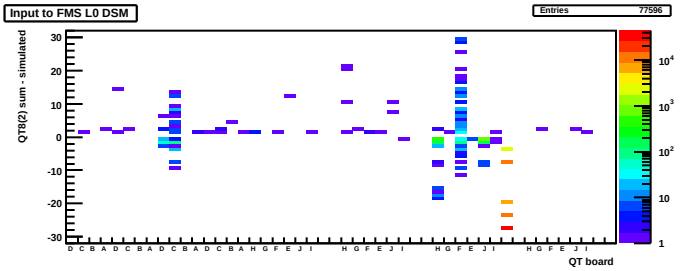

Input to FMS L0 DSM
Entries 5139936
QT8(0) sum
QT board

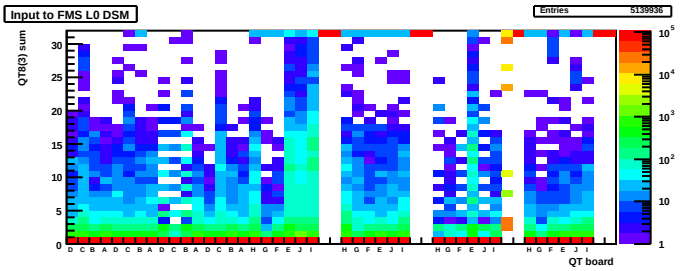

Input to FMS L0 DSM
Entries 2041
QT8(0) sum - simulated
QT board

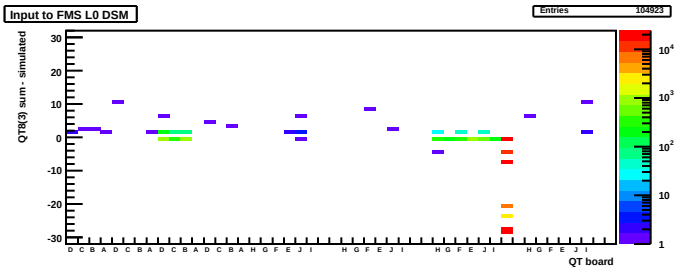

Input to FMS L0 DSM
Entries 5139936
QT8(1) sum
QT board

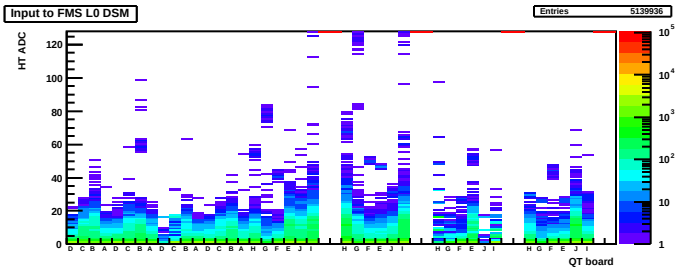

Input to FMS L0 DSM
Entries 3505
QT8(1) sum - simulated
QT board

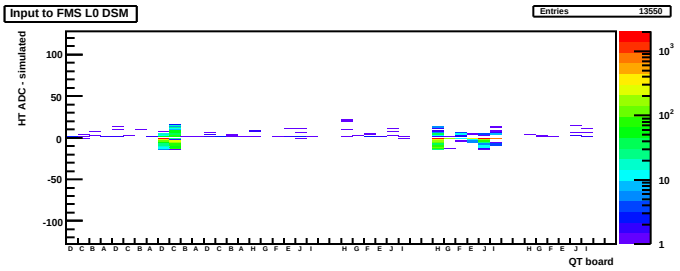

Input to FMS L0 DSM
Entries 5139936
QT8(2) sum
QT board

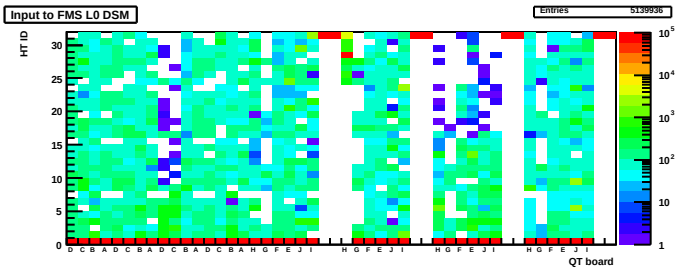

Input to FMS L0 DSM
Entries 77596
QT8(2) sum - simulated
QT board

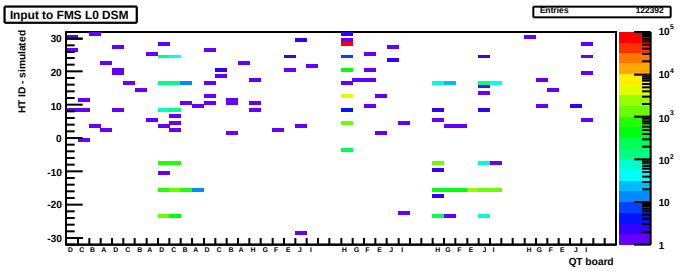

Input to FMS L0 DSM
Entries 5139936
QT8(3) sum
QT board

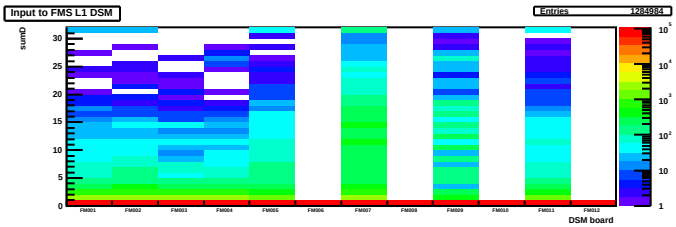

Input to FMS L0 DSM
Entries 104923
QT8(3) sum - simulated
QT board

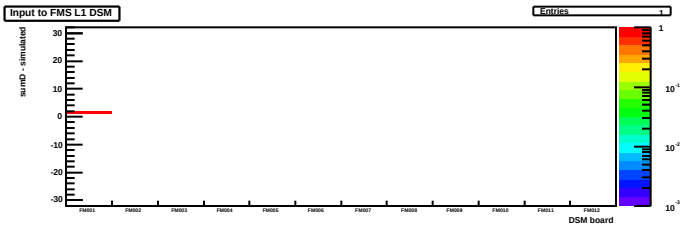

Input to FMS L0 DSM
Entries 5139936
HT ADC
QT board

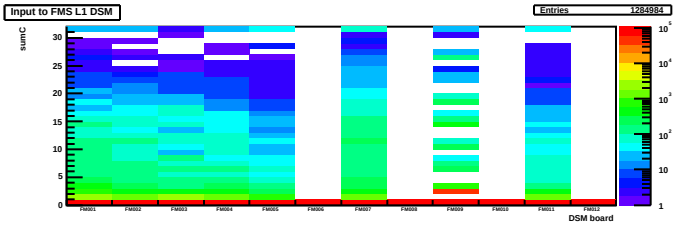

Input to FMS L0 DSM
Entries 13550
HT ADC - simulated
QT board

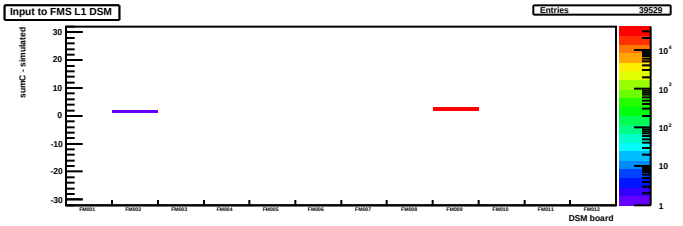

Input to FMS L0 DSM
Entries 5139936
HT ID
QT board

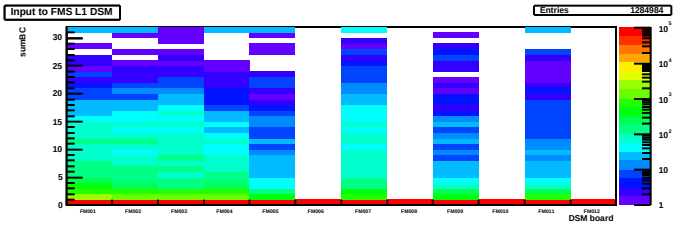

Input to FMS L0 DSM
Entries 122392
HT ID - simulated
QT board

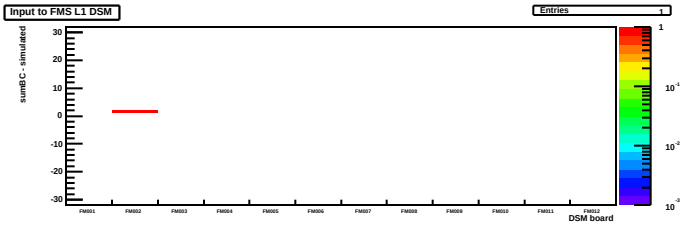

Input to FMS L1 DSM
Entries 1284984
sumD
DSM board

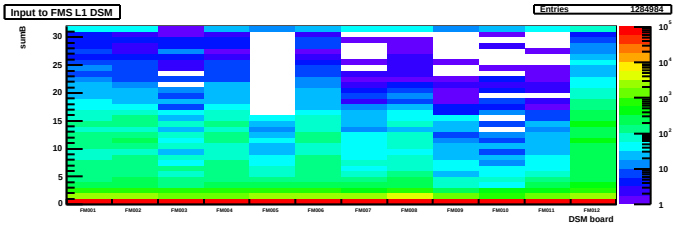

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

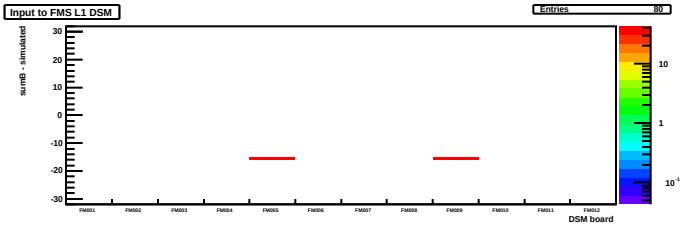

Input to FMS L1 DSM
Entries 1284984
sumC
DSM board

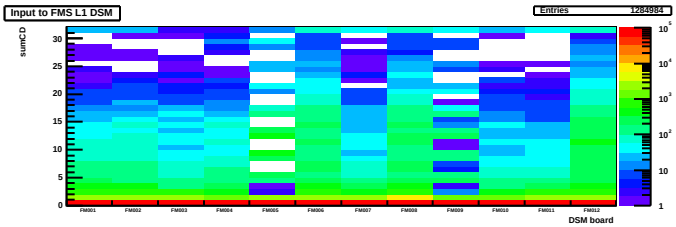

Input to FMS L1 DSM
Entries 39529
sumC - simulated
DSM board

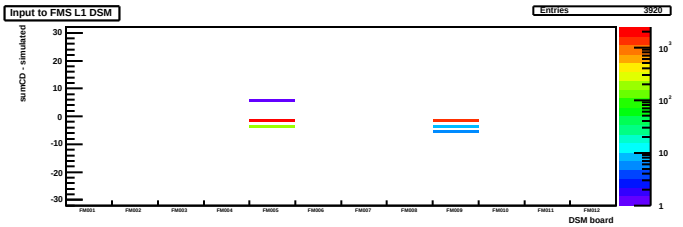

Input to FMS L1 DSM
Entries 1284984
sumBC
DSM board

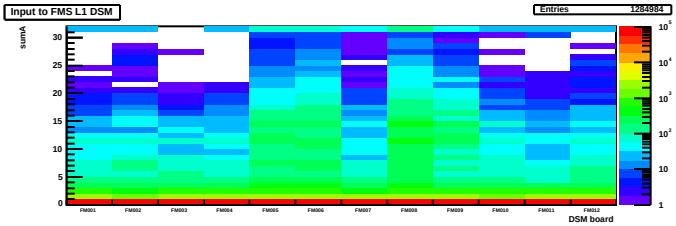

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

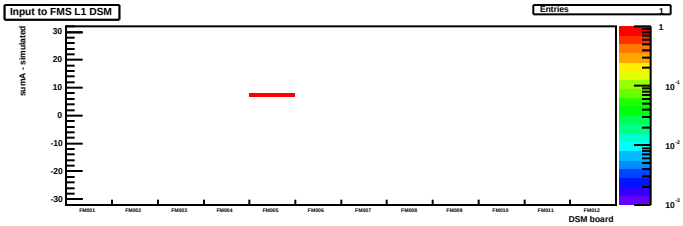

Input to FMS L1 DSM
Entries 1284984
sumB
DSM board

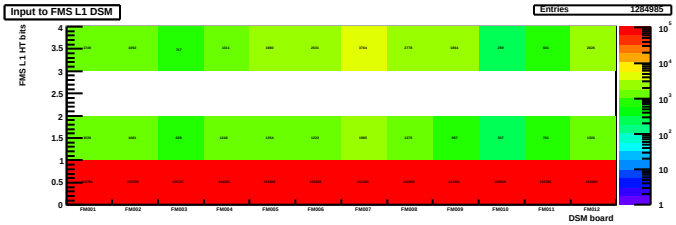

Input to FMS L1 DSM
Entries 80
sumB - simulated
DSM board

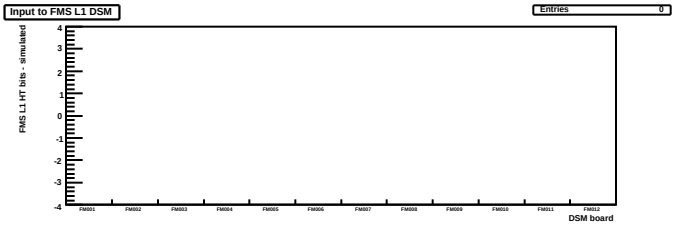

Input to FMS L1 DSM
Entries 1284984
sumCD
DSM board

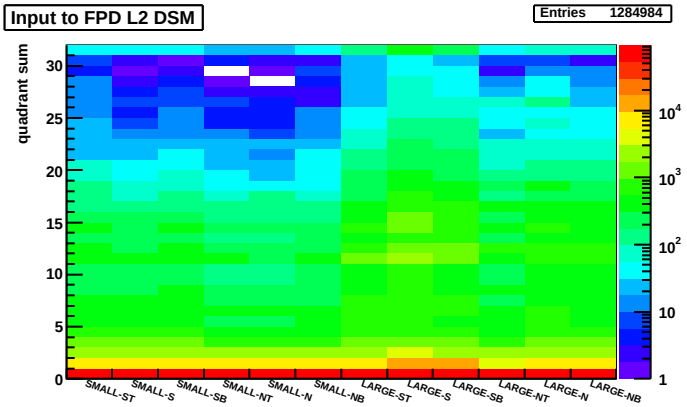

Input to FMS L1 DSM
Entries 3920
sumCD - simulated
DSM board

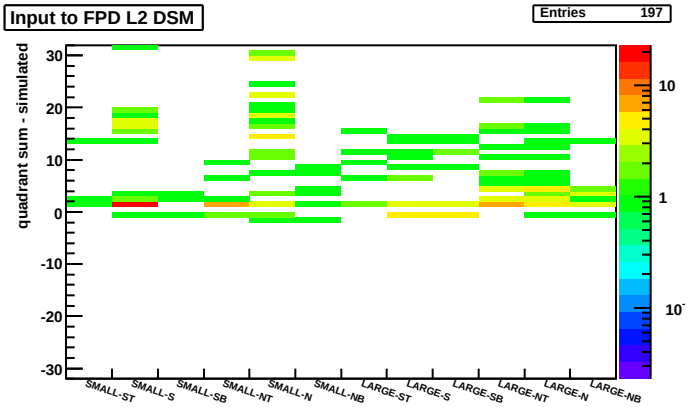

Input to FMS L1 DSM
Entries 1284984
sumA
DSM board

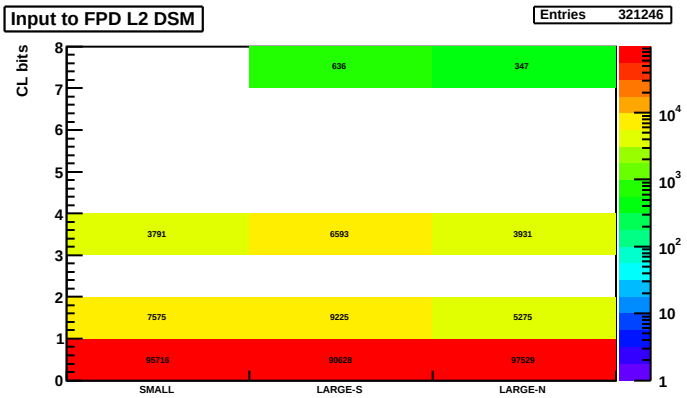

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

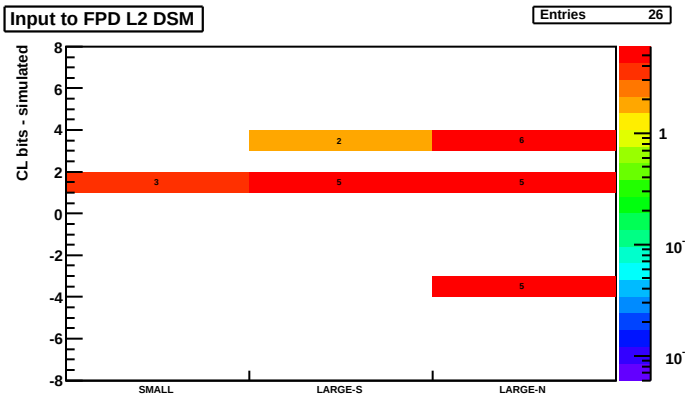

Input to FMS L1 DSM
Entries 1284985
FMS L1 HT bits
DSM board

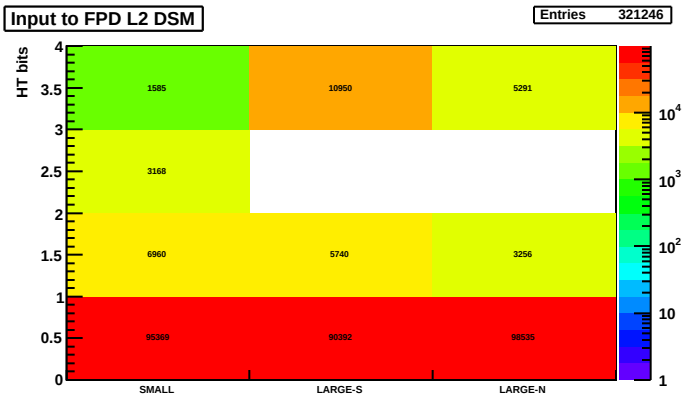

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

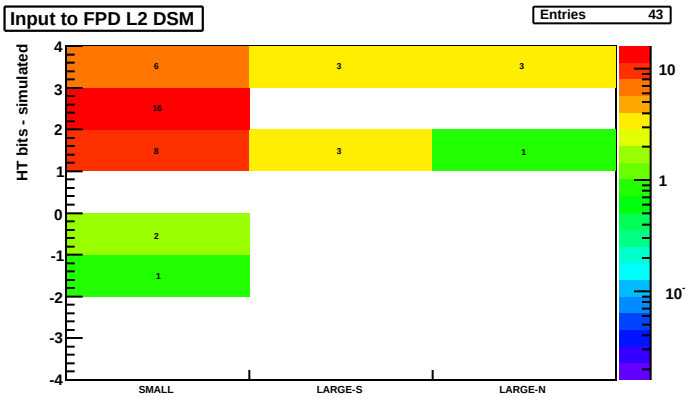

Input to FPD L2 DSM
Entries 1284984
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

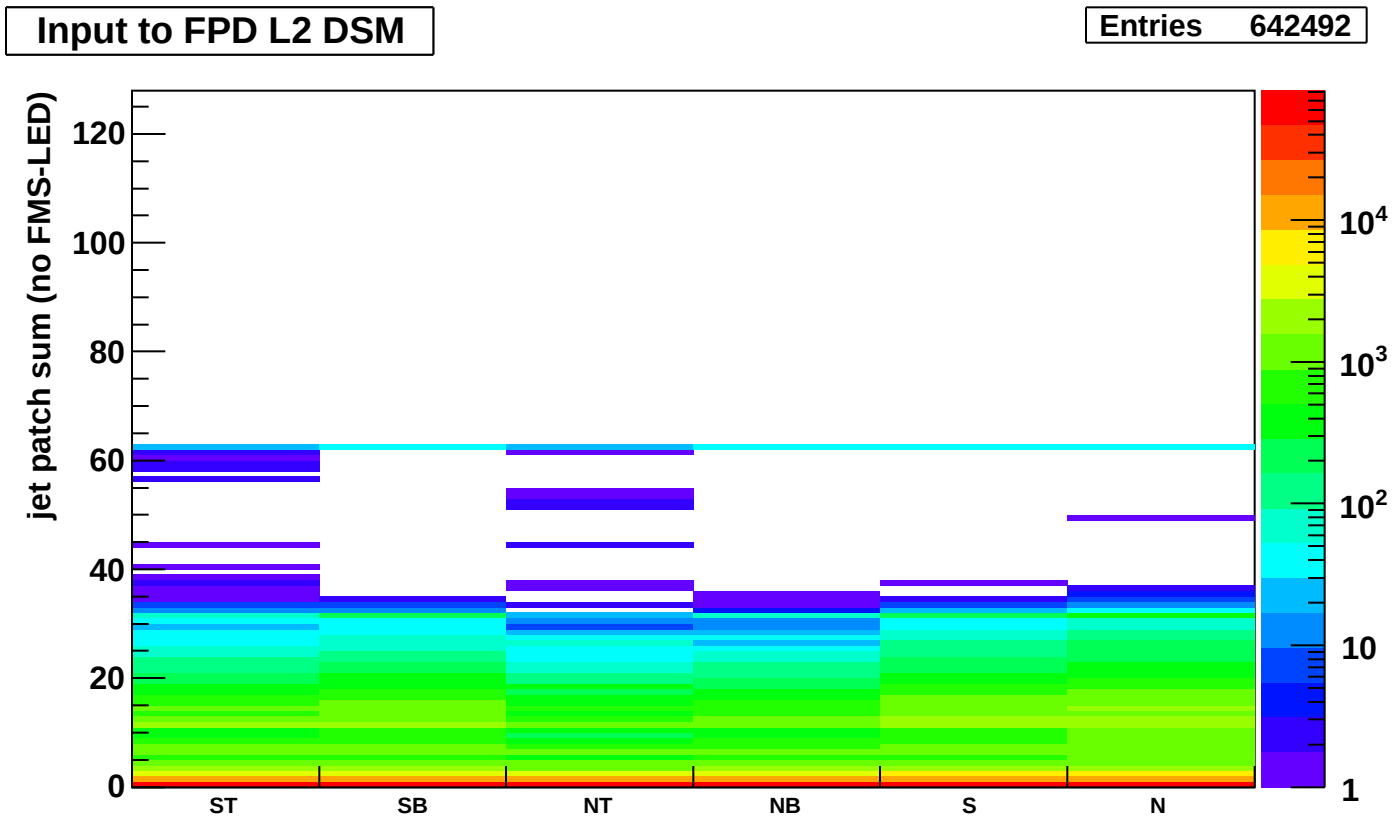

Input to FPD L2 DSM
Entries 197
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

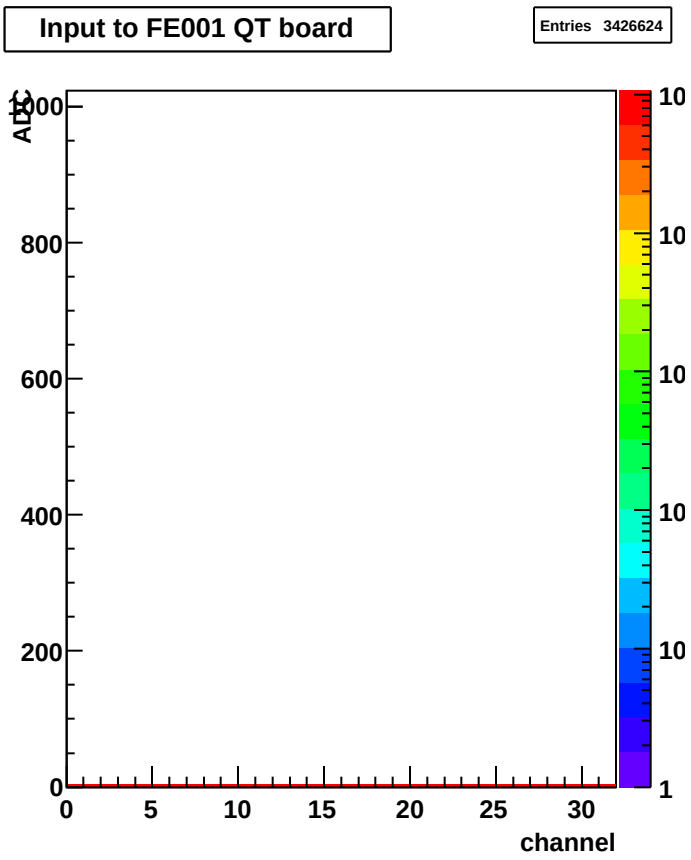

Input to FPD L2 DSM
Entries 321246
CL bits
636
347
3791
6593
3931
7575
9225
5275
95716
90628
97529
SMALL
LARGE-S
LARGE-N

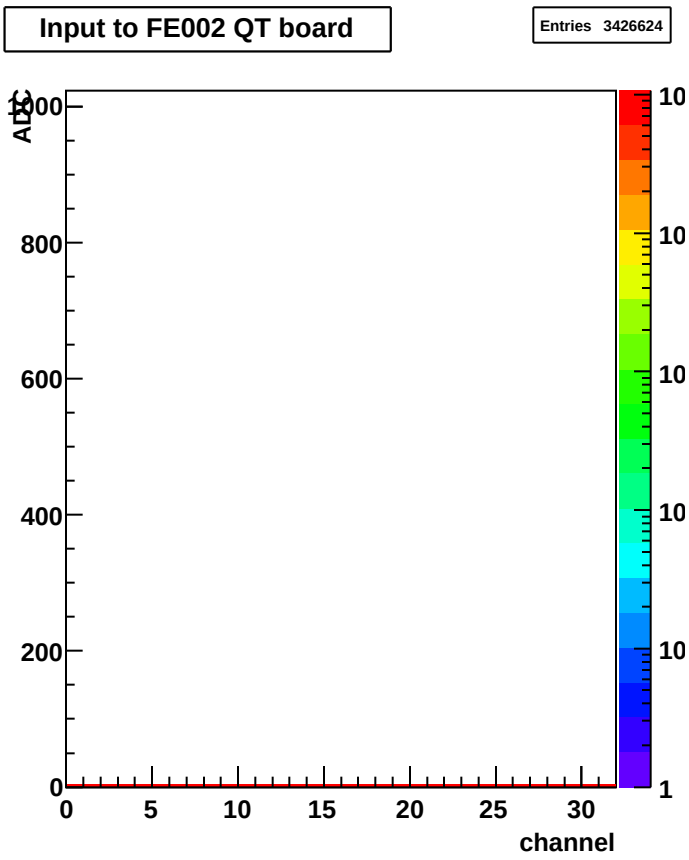

Input to FPD L2 DSM
Entries 26
CL bits - simulated
2
6
3
5
5
5
SMALL
LARGE-S
LARGE-N

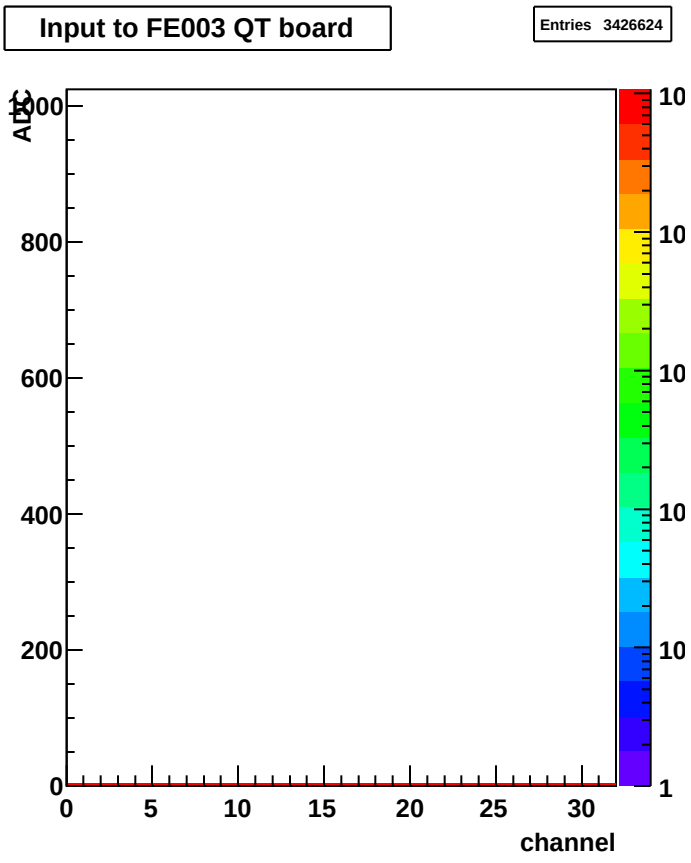

Input to FPD L2 DSM
Entries 321246
HT bits
1585
10950
5291
3168
6960
5740
3256
95369
90392
98535
SMALL
LARGE-S
LARGE-N

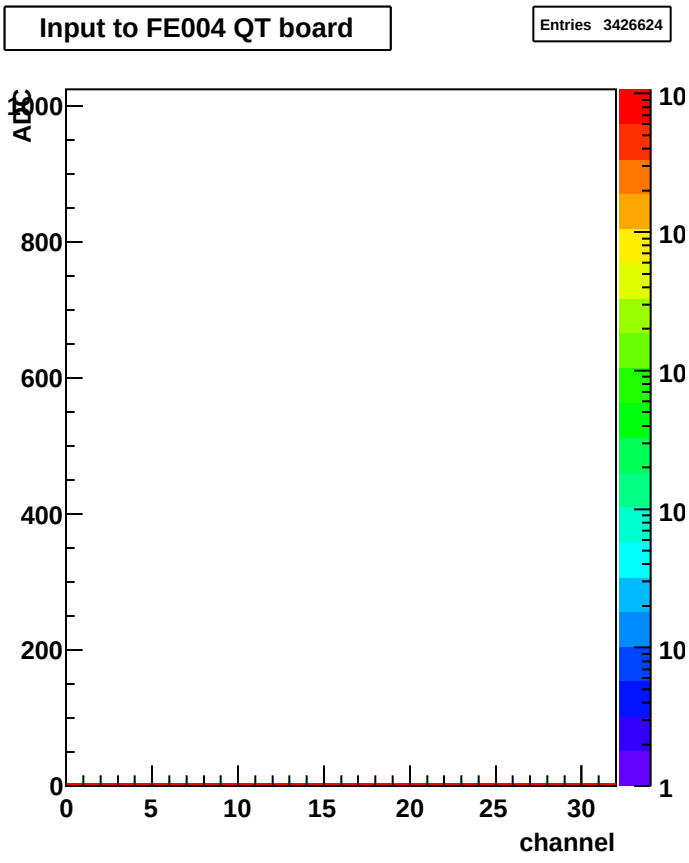

Input to FPD L2 DSM
Entries 43
HT bits - simulated
6
3
3
16
8
3
1
2
1
SMALL
LARGE-S
LARGE-N

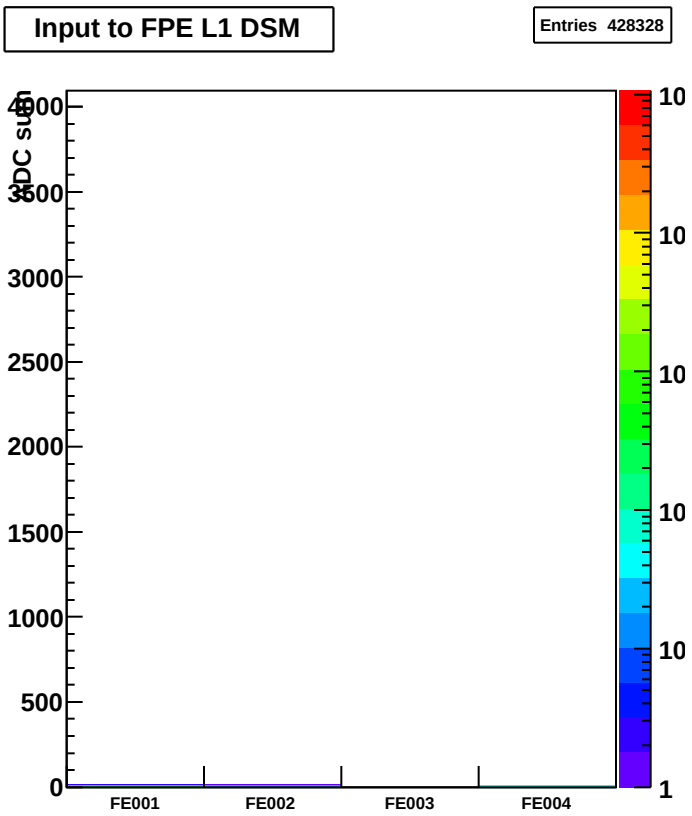

Input to FPD L2 DSM
Entries 642492
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

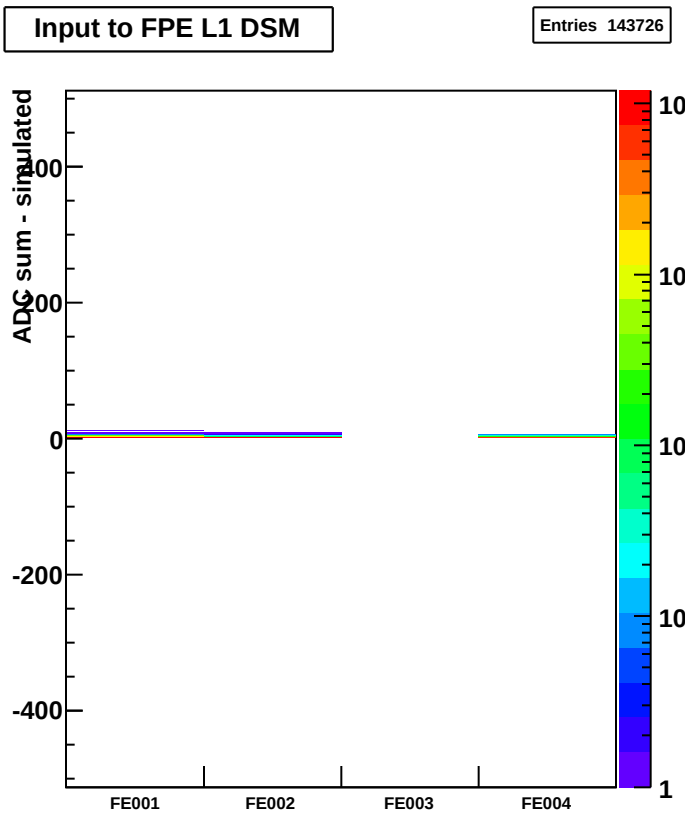

Input to FE001 QT board
Entries 3426624
ADC
channel

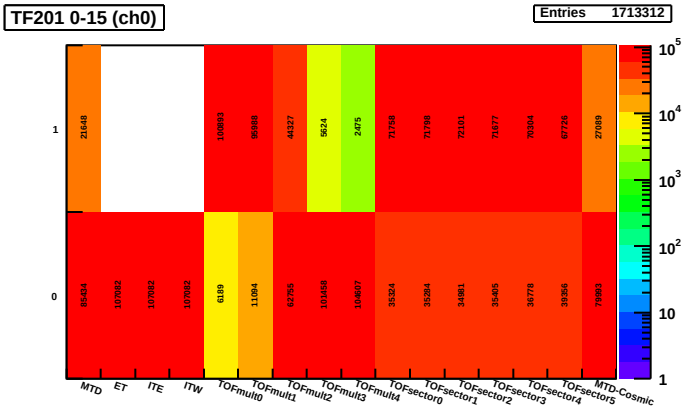

Input to FE002 QT board
Entries 3426624
ADC
channel

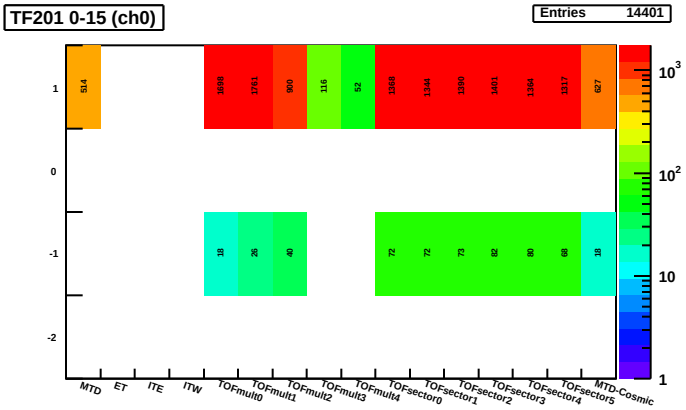

Input to FE003 QT board
Entries 3426624
ADC
channel

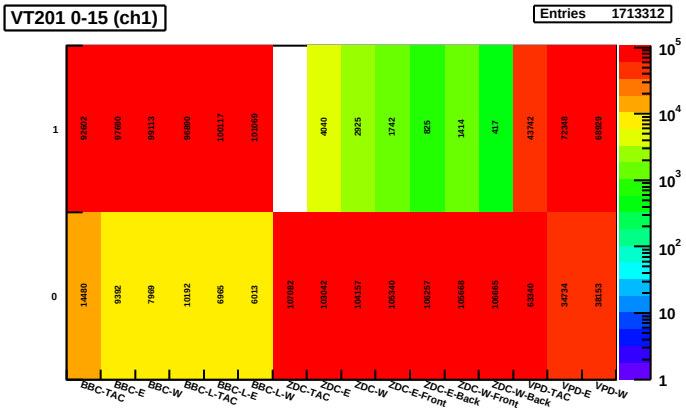

Input to FE004 QT board
Entries 3426624
ADC
channel

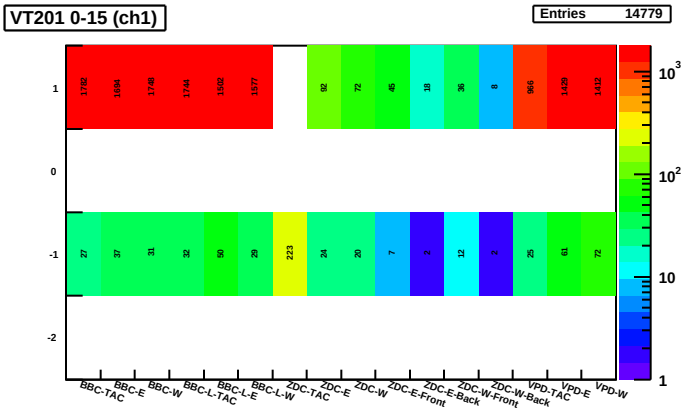

Input to FPE L1 DSM
Entries 428328
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

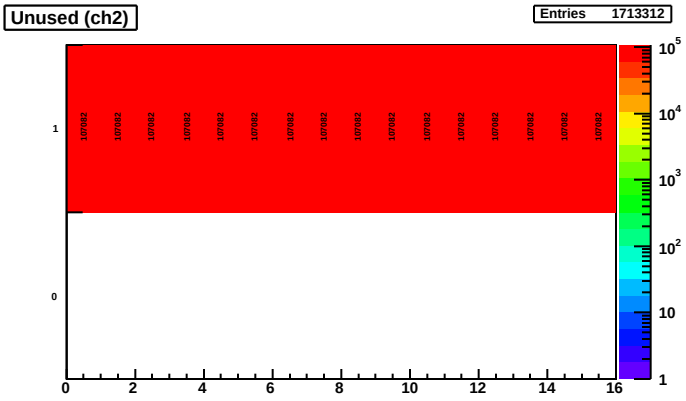

Input to FPE L1 DSM
Entries 143726
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

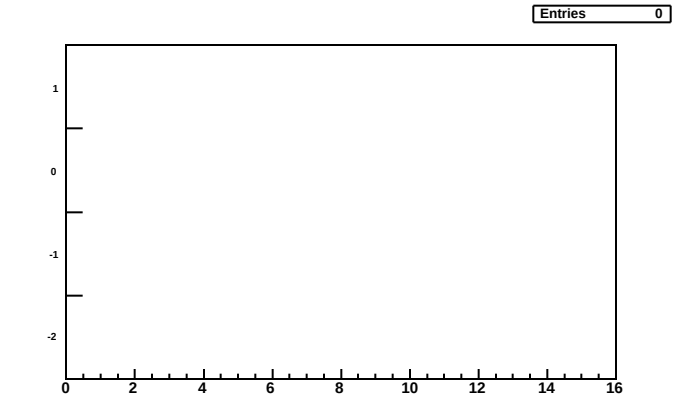

TF201 0-15 (ch0)
Entries 1713312
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

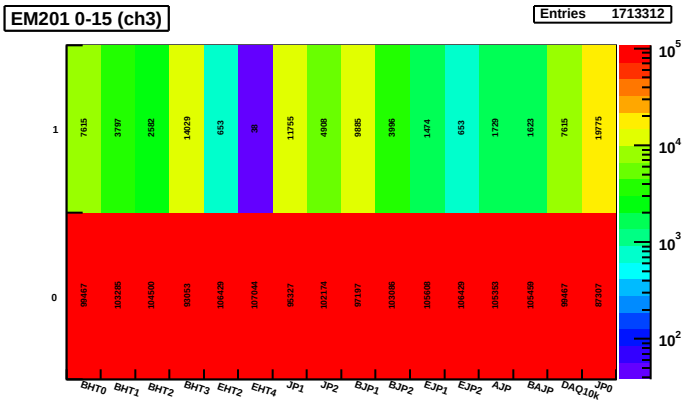

TF201 0-15 (ch0)
Entries 14401
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1713312
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 14779
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1713312


Entries 0


EM201 0-15 (ch3)
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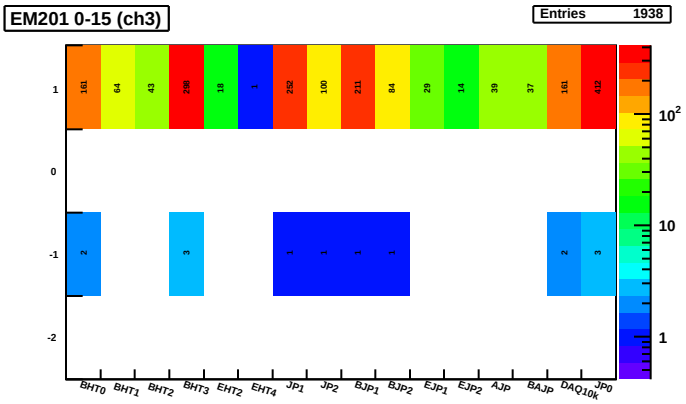
EM201 0-15 (ch3)
Entries 1938
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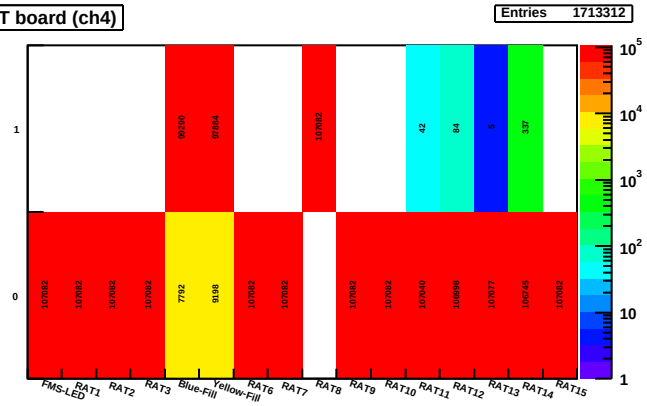
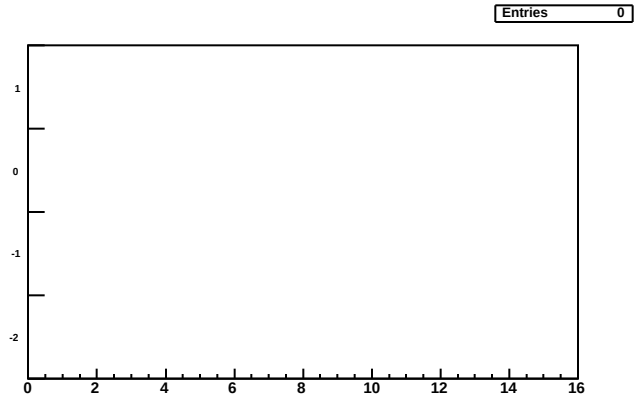
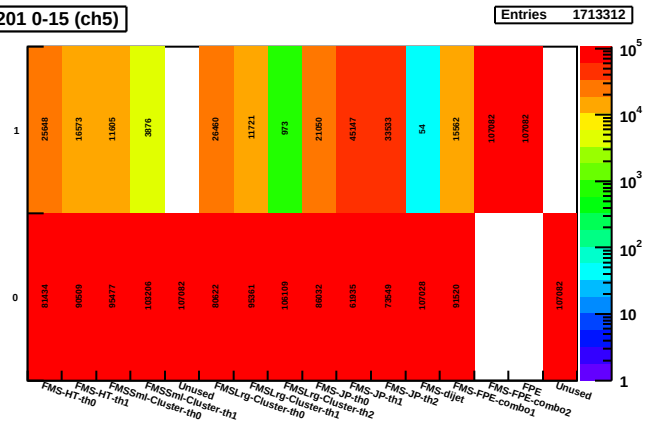
RAT board (ch4)





FP201 0-15 (ch5)



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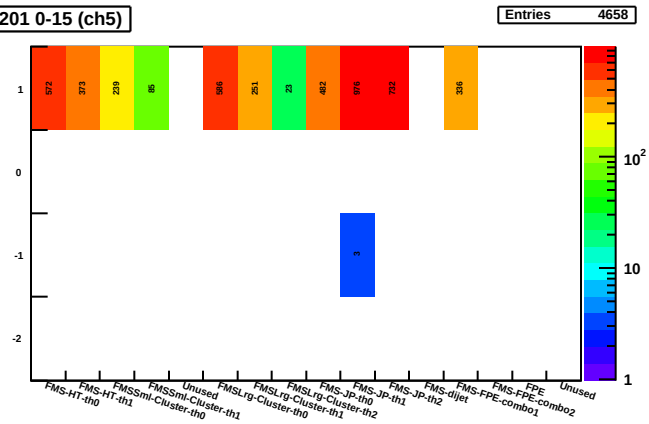
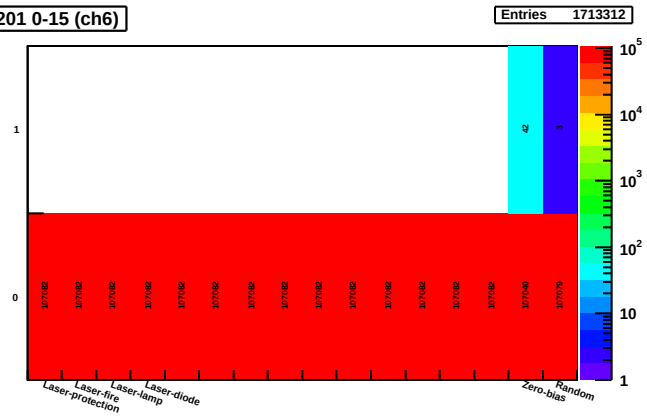
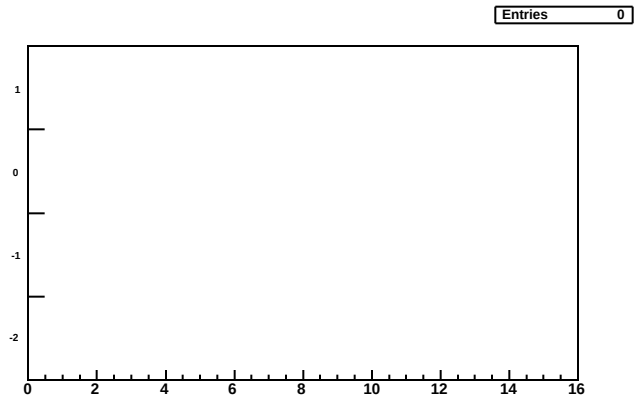
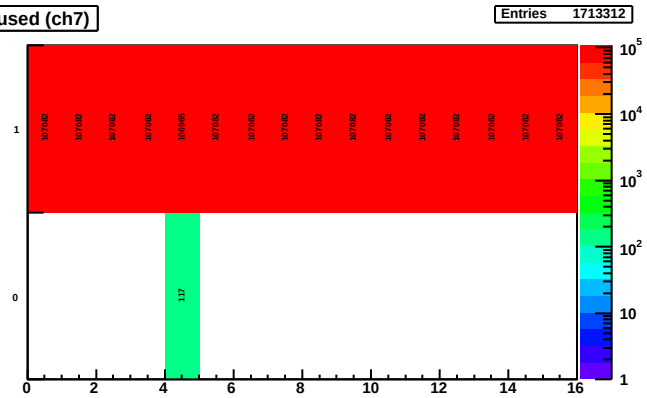
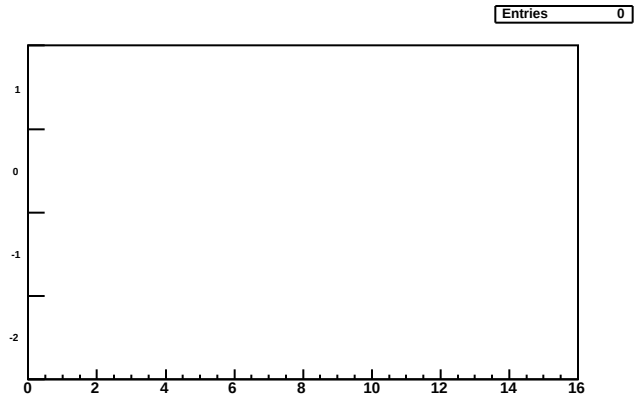


ST201 0-15 (ch6)





Unused (ch7)