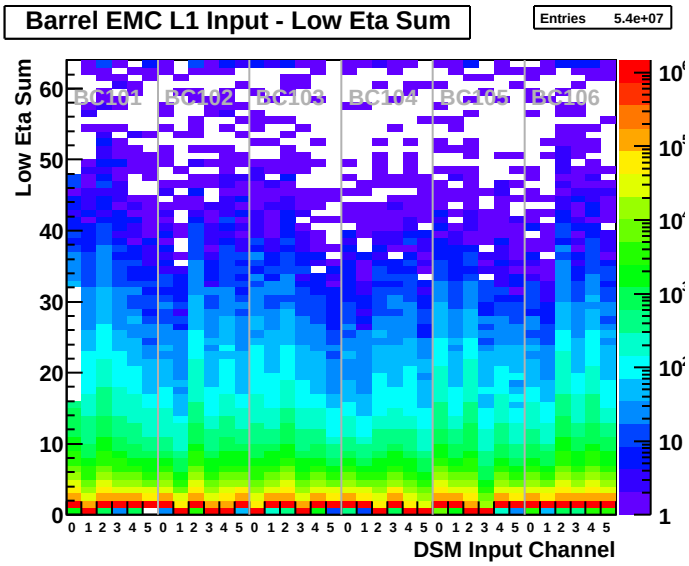
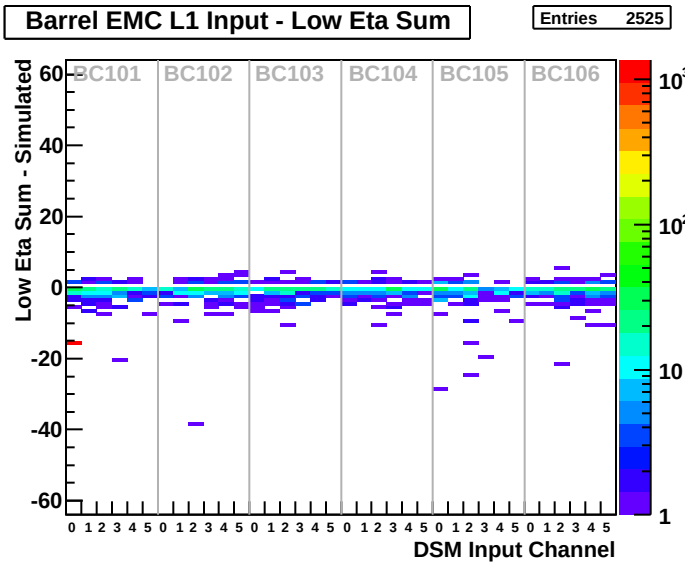
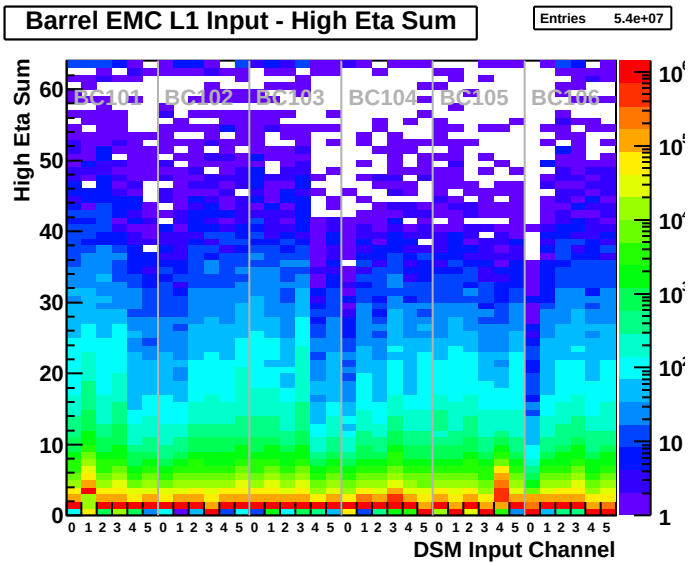
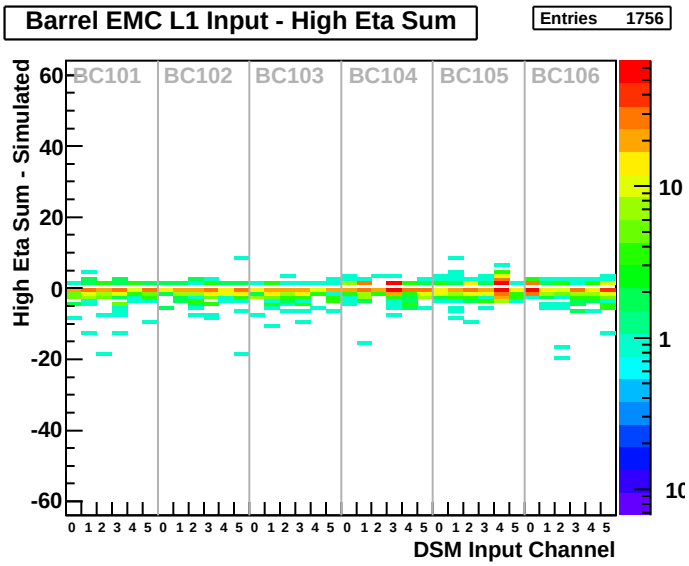
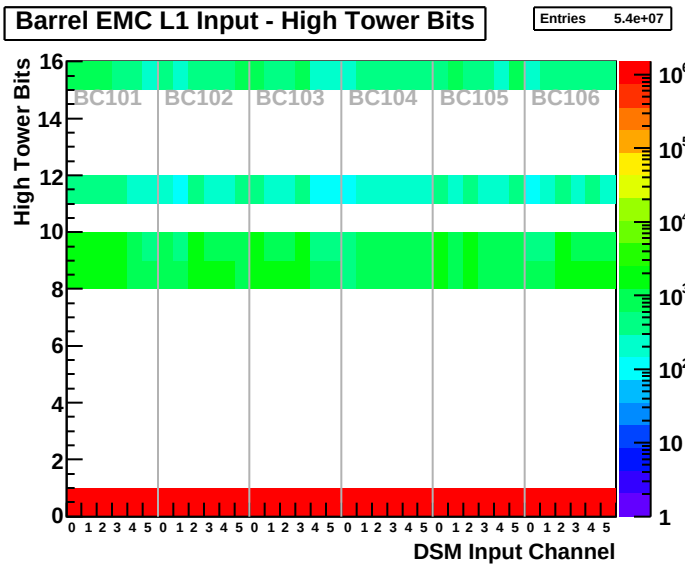
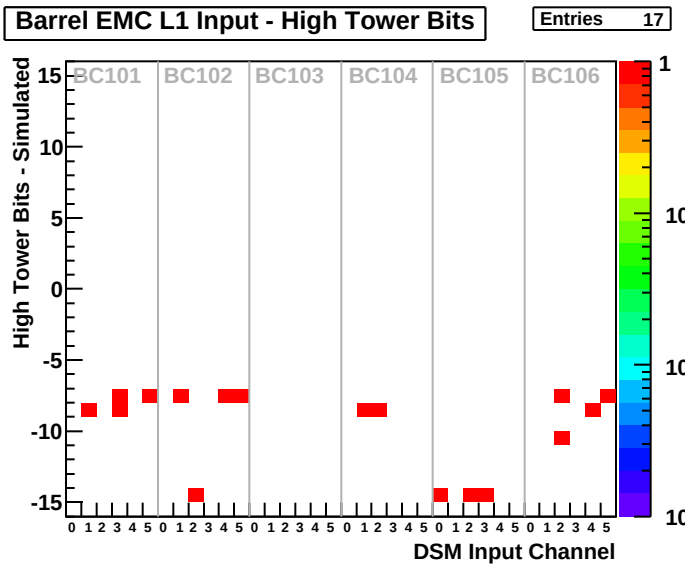

Barrel EMC L1 Input - Low Eta Sum
Entries 5.4e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

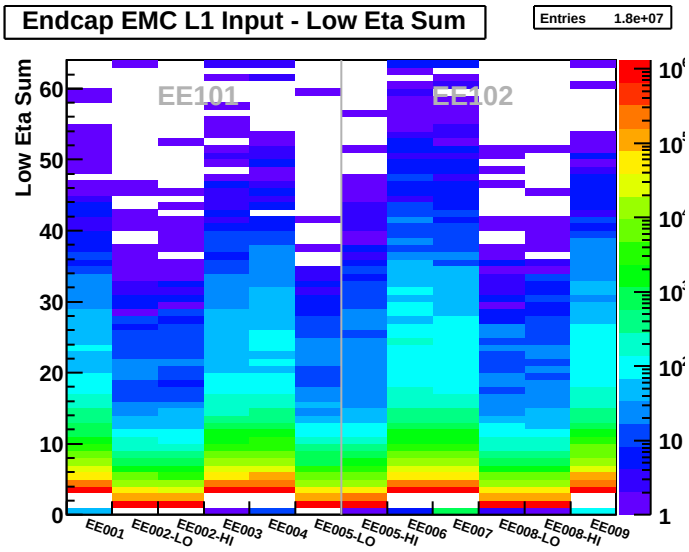

Barrel EMC L1 Input - Low Eta Sum
Entries 2525
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

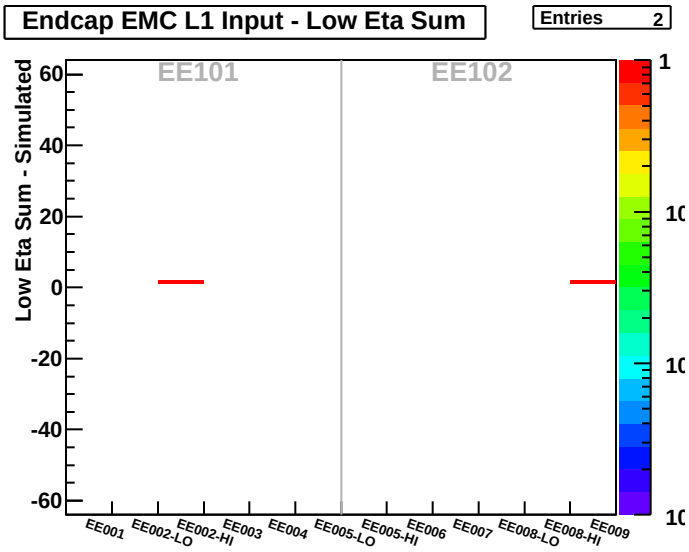

Barrel EMC L1 Input - High Eta Sum
Entries 5.4e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

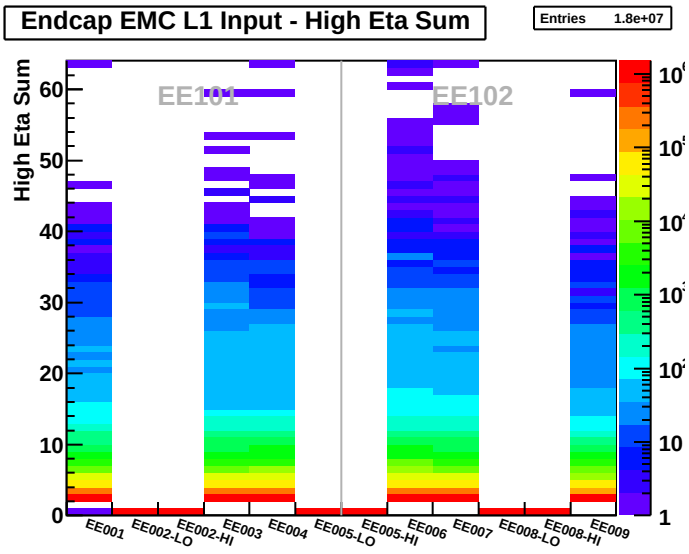

Barrel EMC L1 Input - High Eta Sum
Entries 1756
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

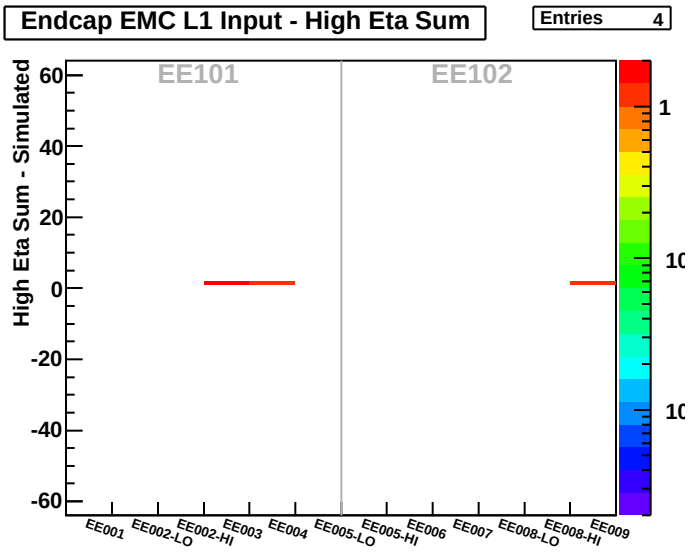

Barrel EMC L1 Input - High Tower Bits
Entries 5.4e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

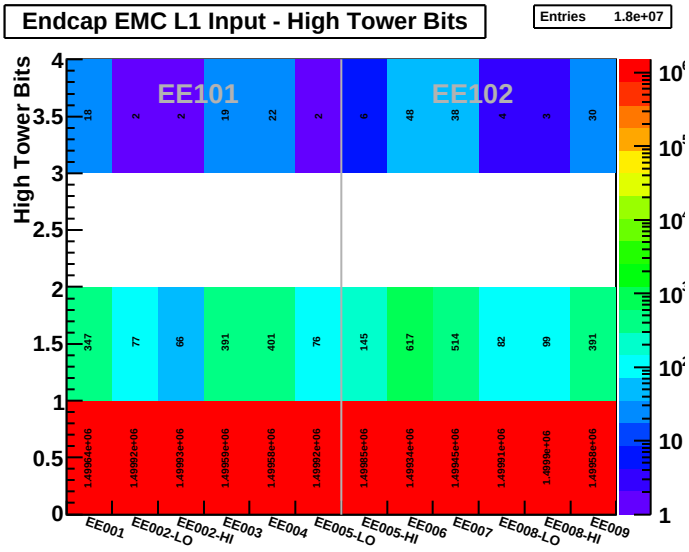

Barrel EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

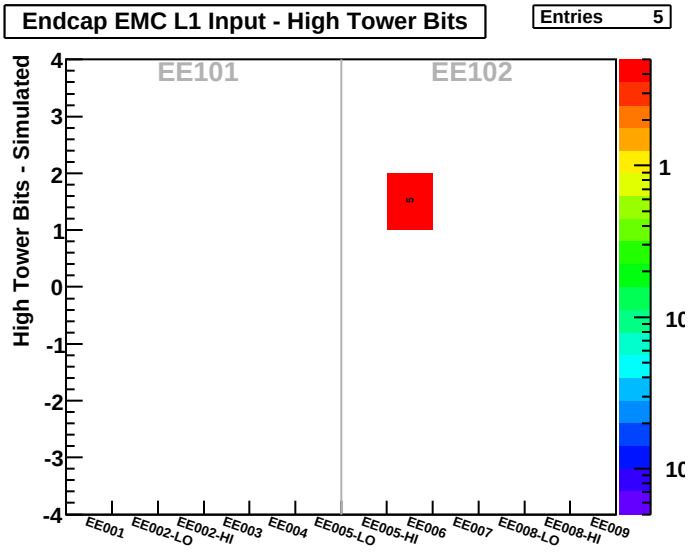

Endcap EMC L1 Input - Low Eta Sum
Entries 1.8e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

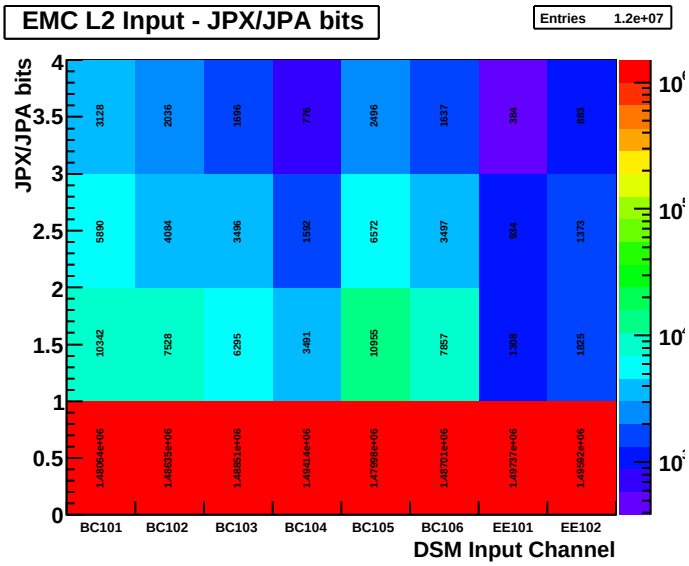

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

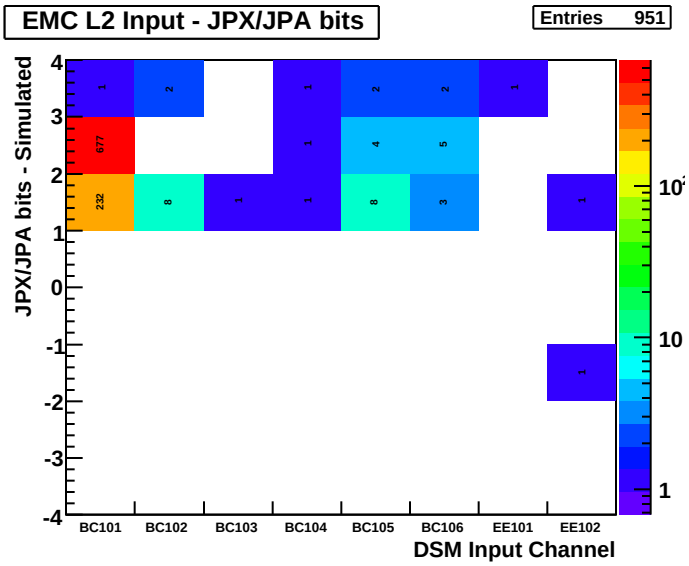

Endcap EMC L1 Input - High Eta Sum
Entries 1.8e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

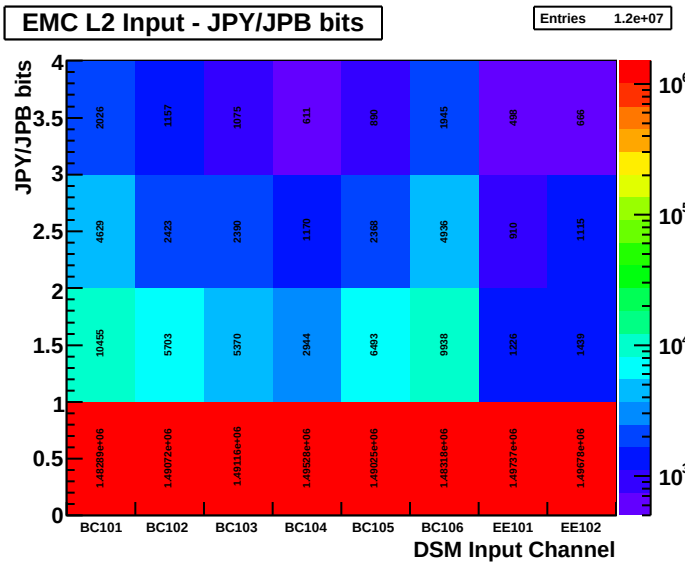

Endcap EMC L1 Input - High Eta Sum
Entries 4
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

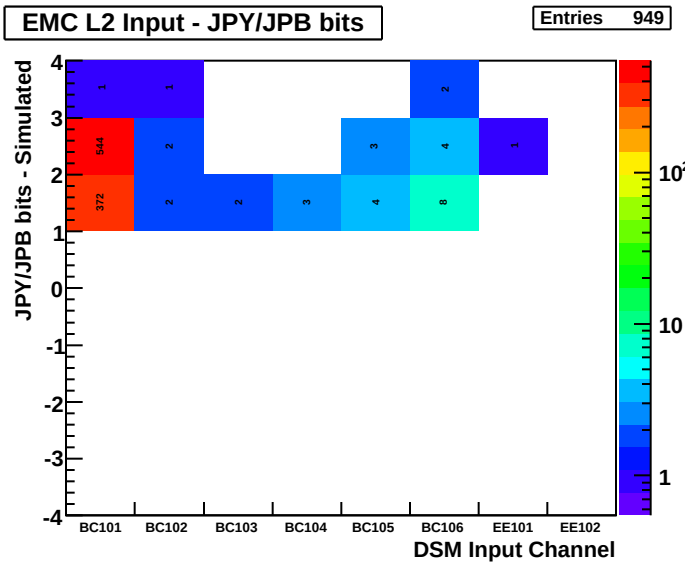

Endcap EMC L1 Input - High Tower Bits
Entries 1.8e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

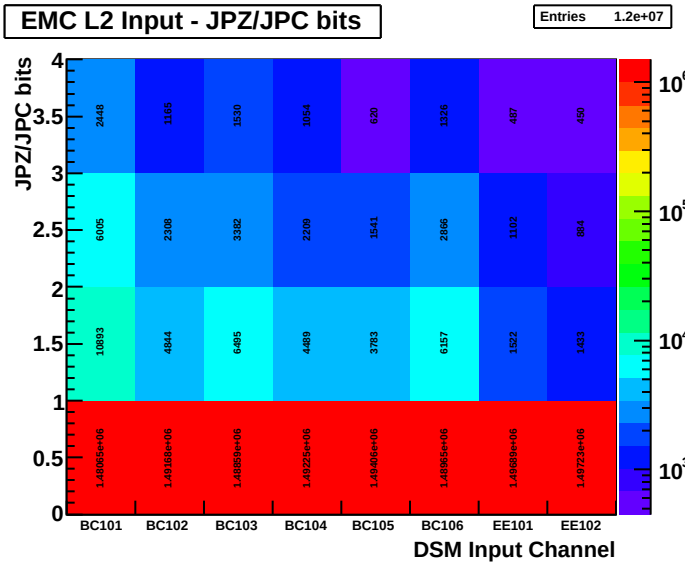

Endcap EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

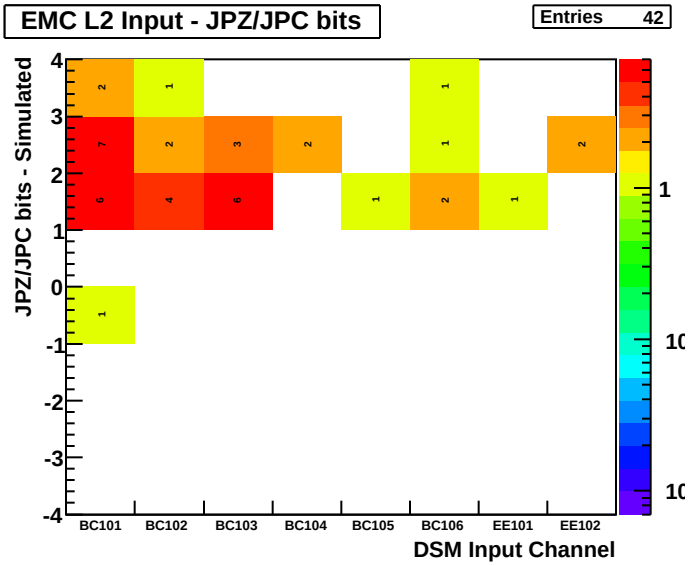

EMC L2 Input - JPX/JPA bits
Entries 1.2e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

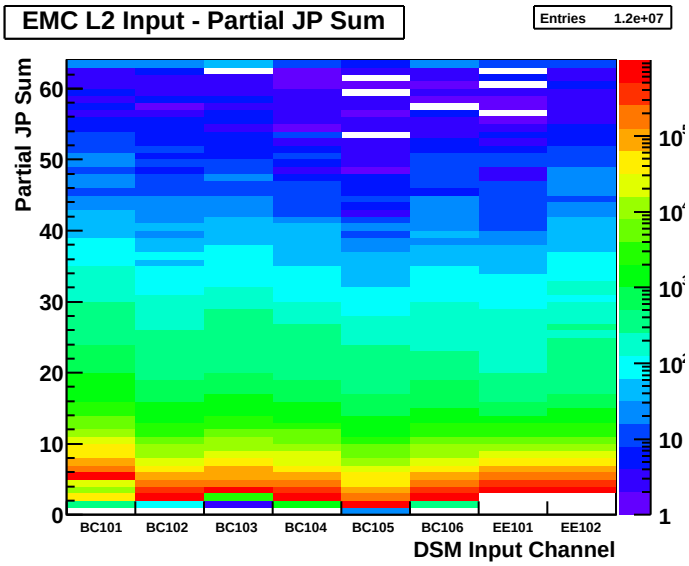

EMC L2 Input - JPX/JPA bits
Entries 951
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

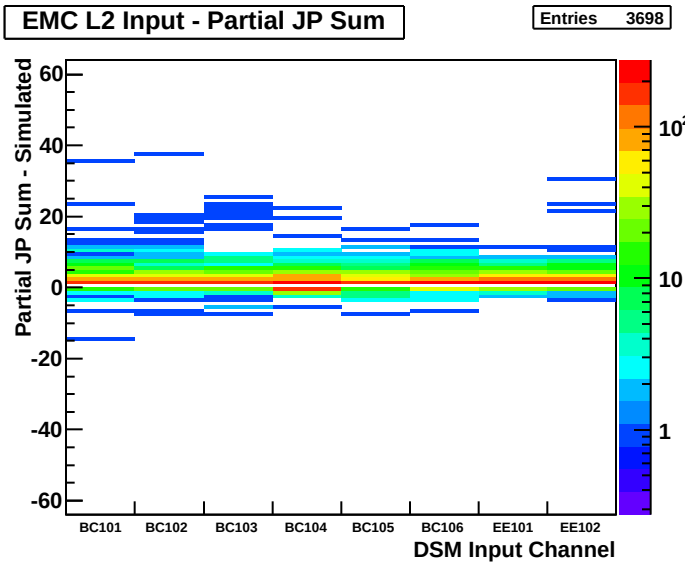

EMC L2 Input - JPY/JPB bits
Entries 1.2e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

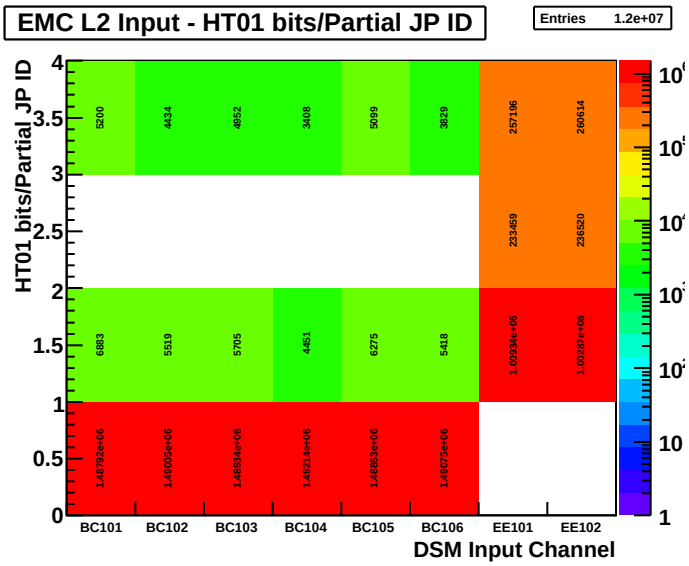

EMC L2 Input - JPY/JPB bits
Entries 949
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

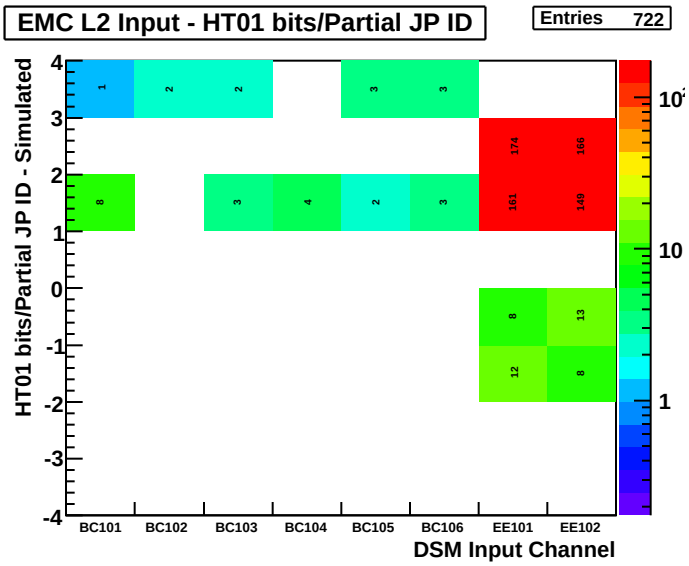

EMC L2 Input - JPZ/JPC bits
Entries 1.2e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

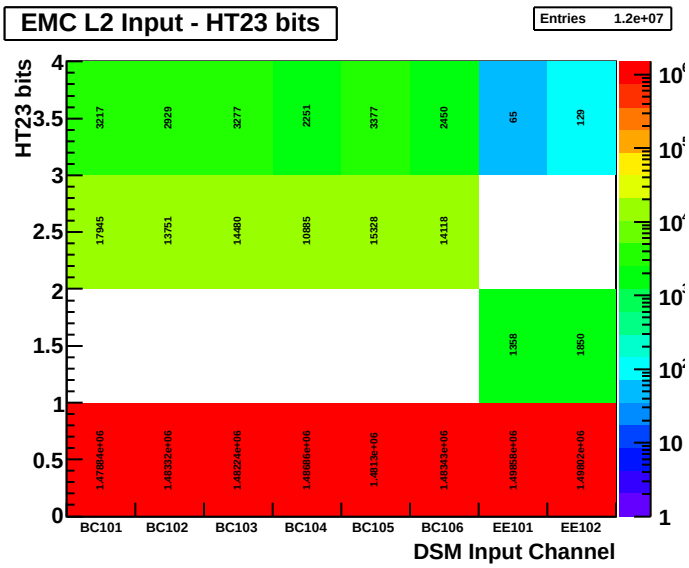

EMC L2 Input - JPZ/JPC bits
Entries 42
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

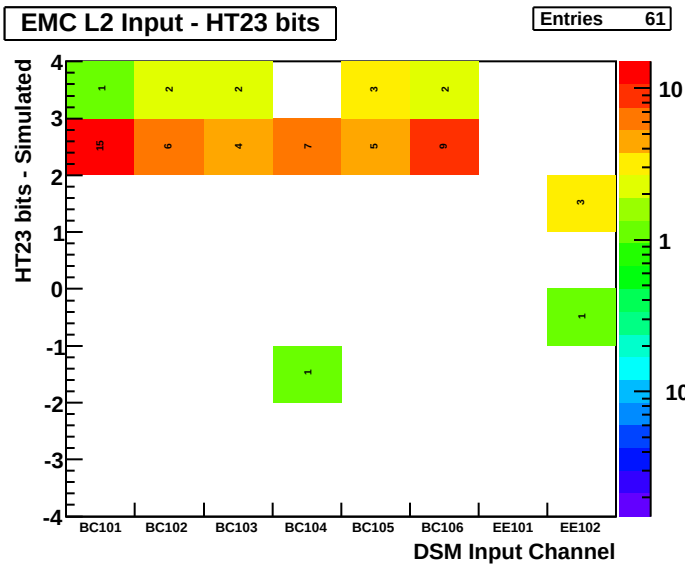

EMC L2 Input - Partial JP Sum
Entries 1.2e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

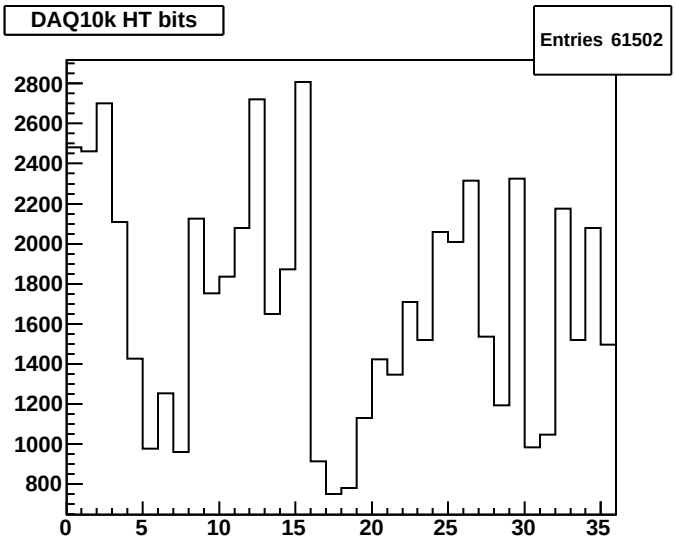

EMC L2 Input - Partial JP Sum
Entries 3698
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

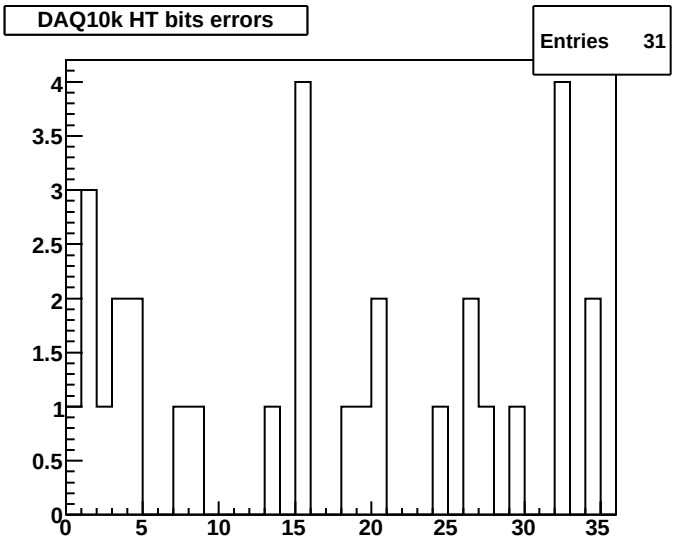

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.2e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

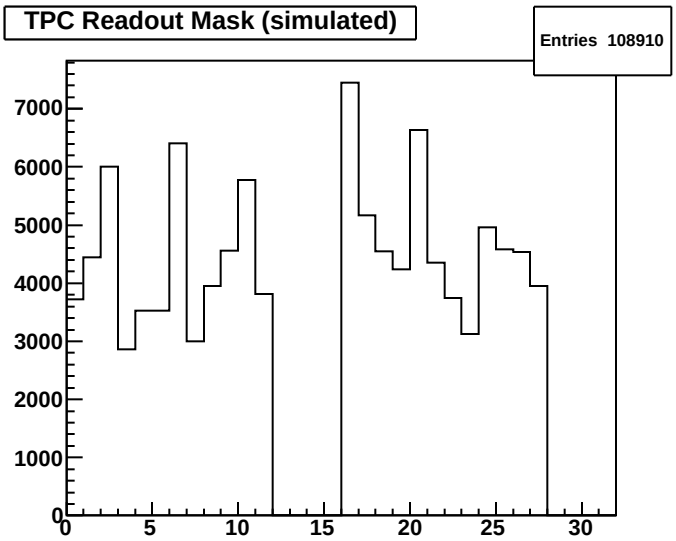

EMC L2 Input - HT01 bits/Partial JP ID
Entries 722
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

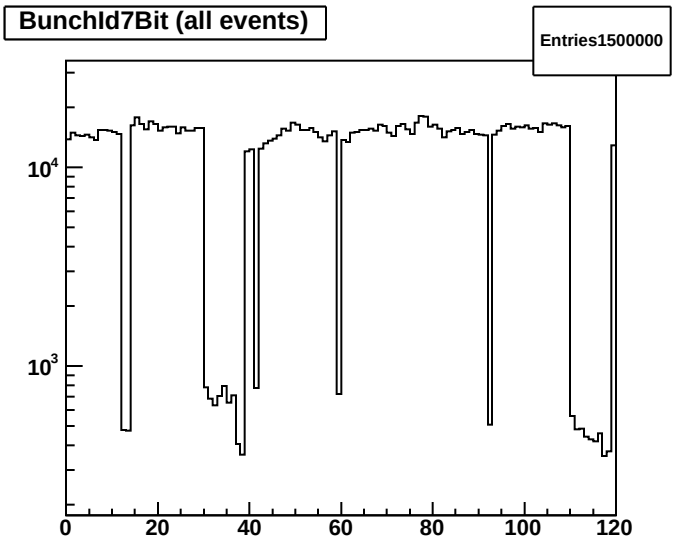

EMC L2 Input - HT23 bits
Entries 1.2e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

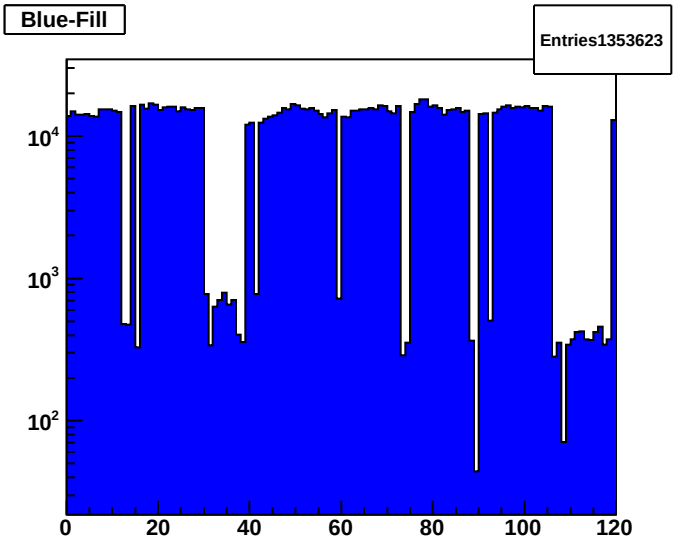

EMC L2 Input - HT23 bits
Entries 61
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

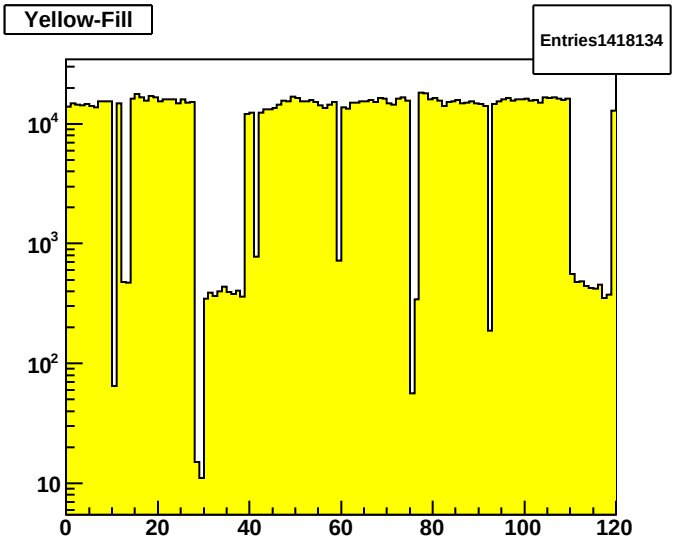

DAQ10k HT bits
Entries 61502

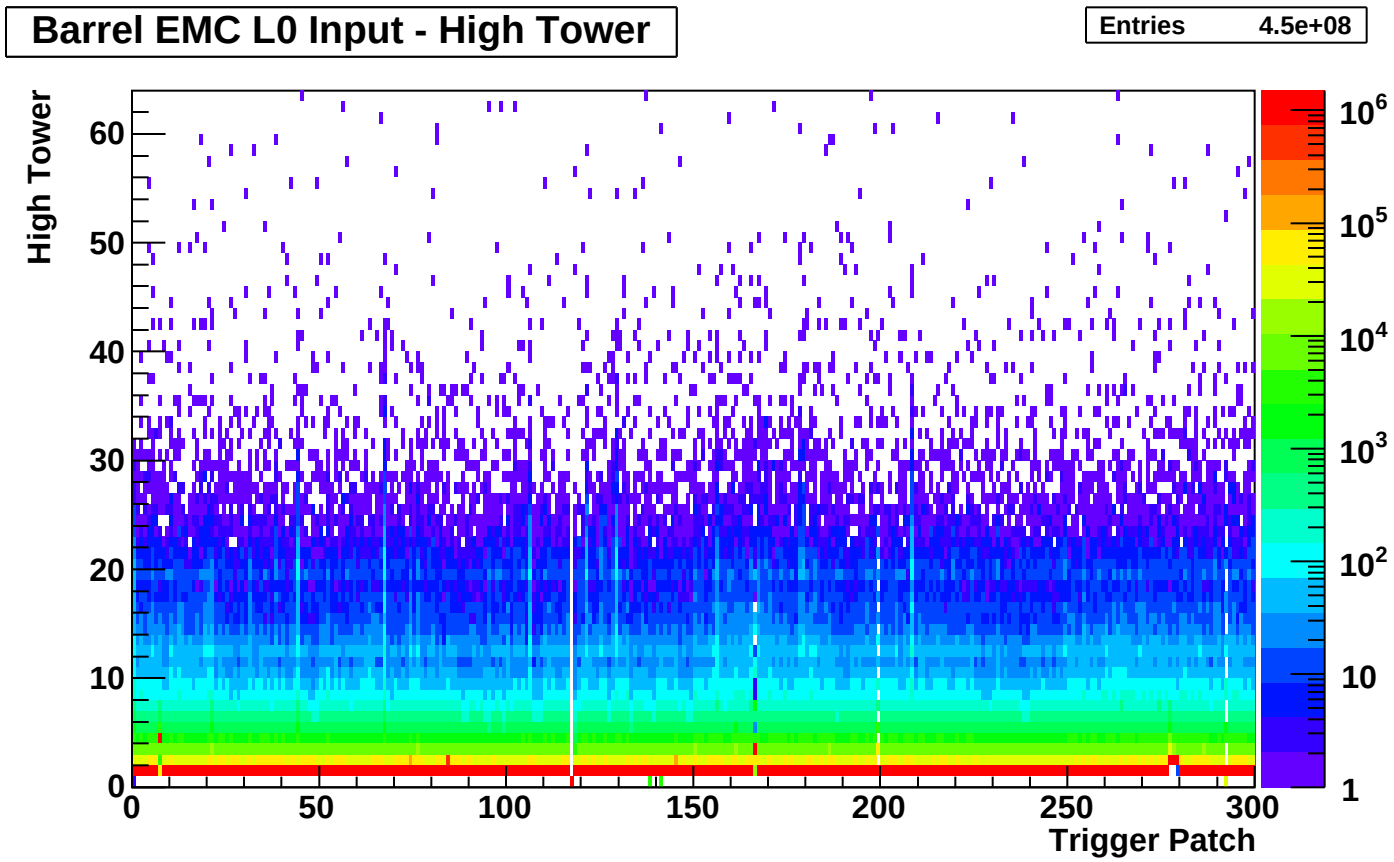

DAQ10k HT bits errors
Entries 31

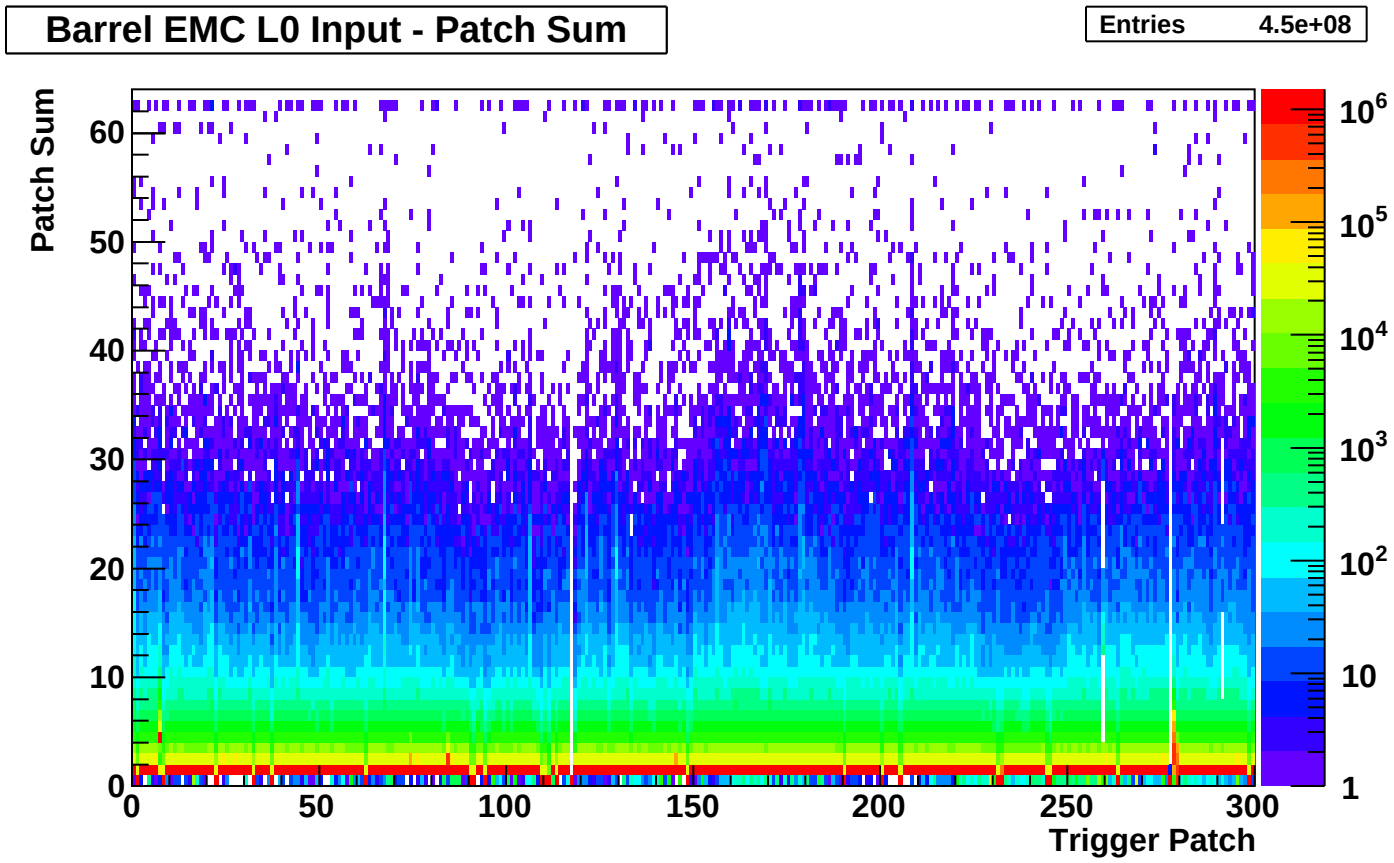

TPC Readout Mask (simulated)
Entries 108910

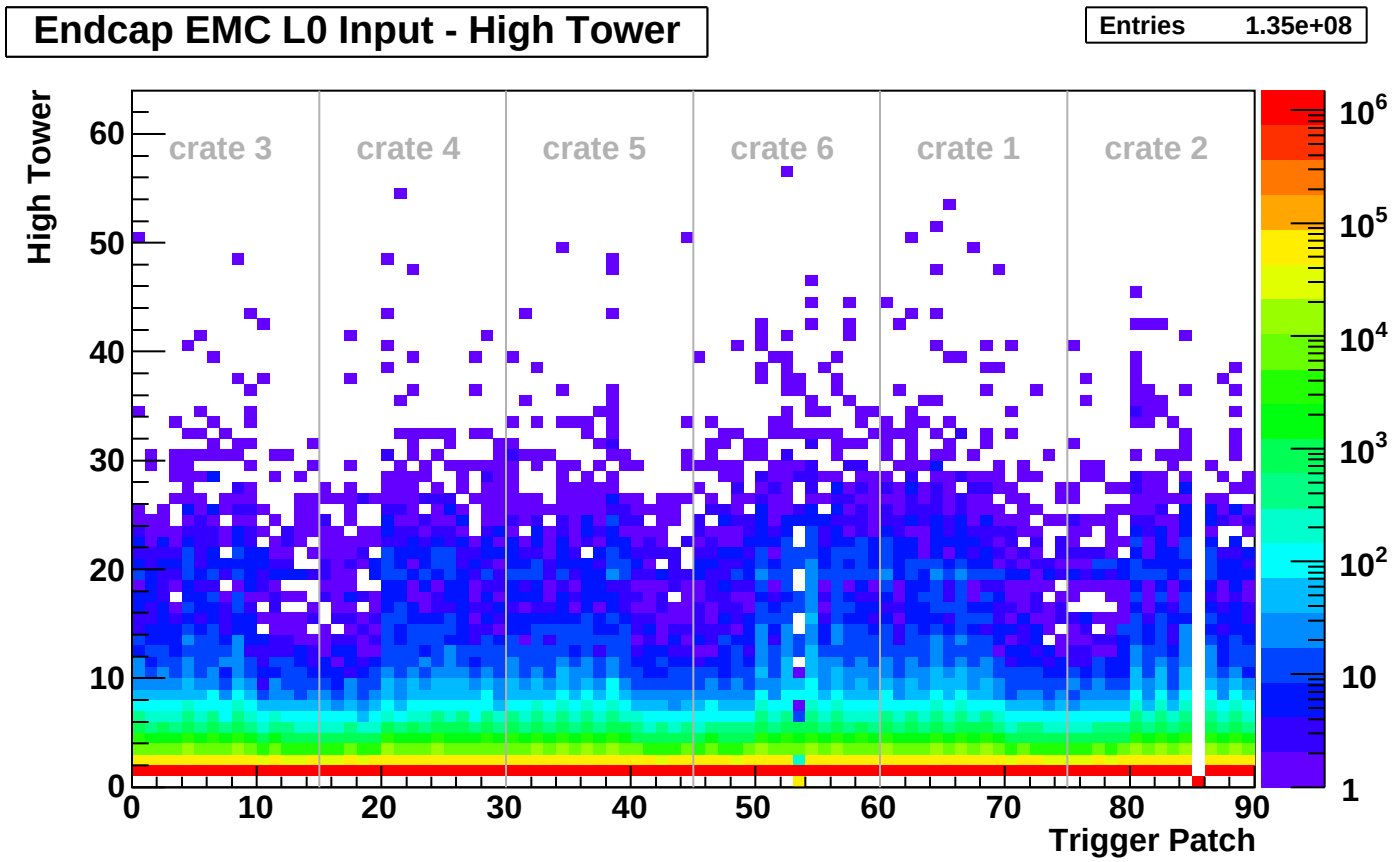

BunchId7Bit (all events)
Entries1500000

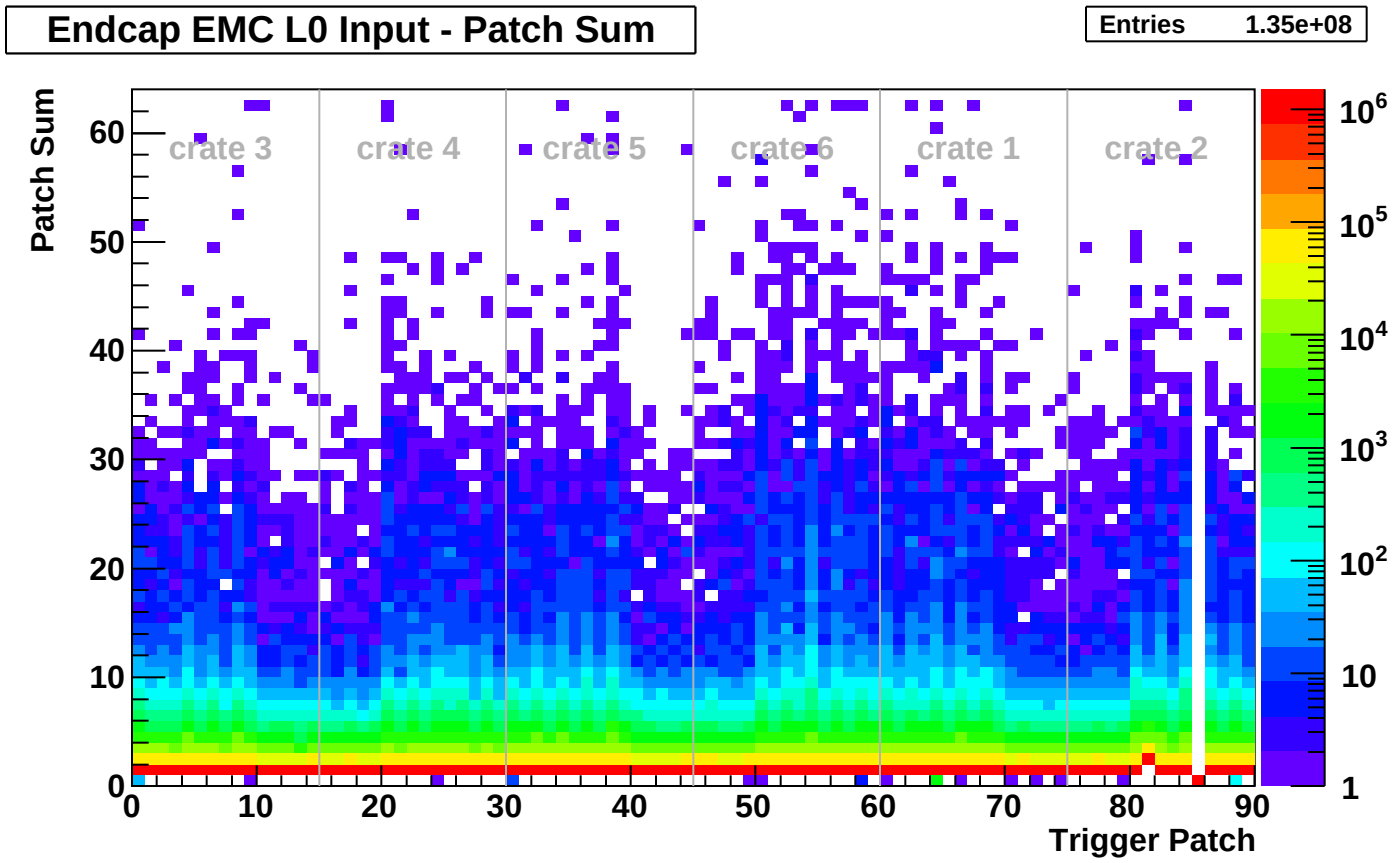

Blue-Fill
Entries1353623

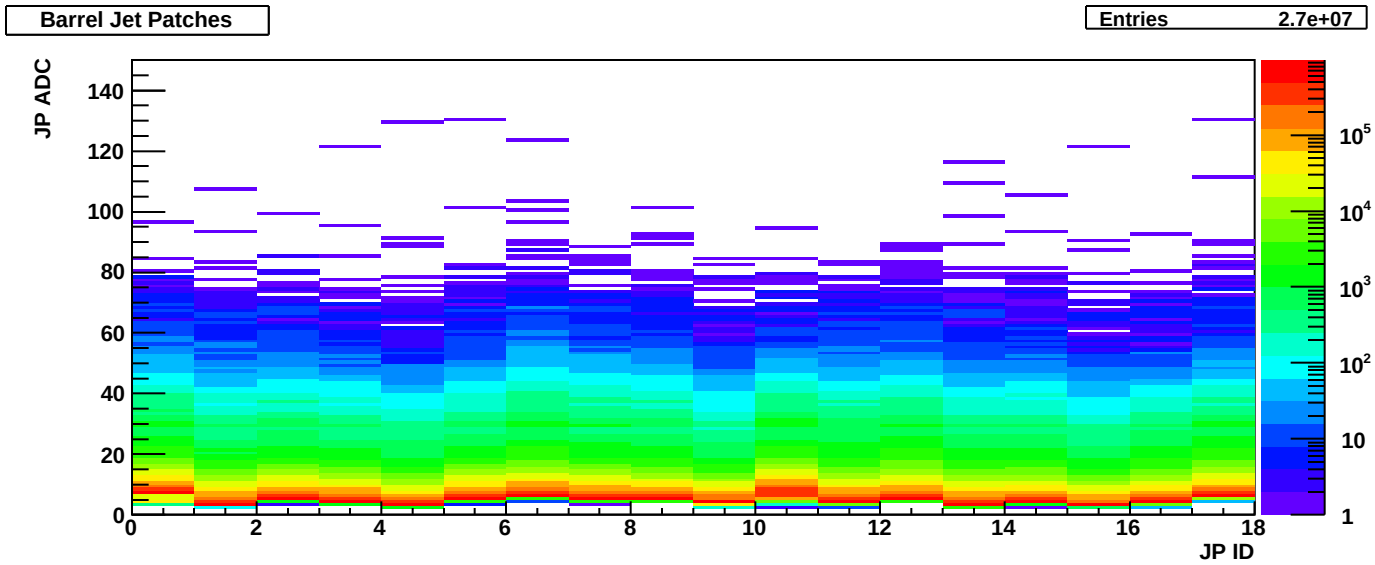

Yellow-Fill
Entries1418134

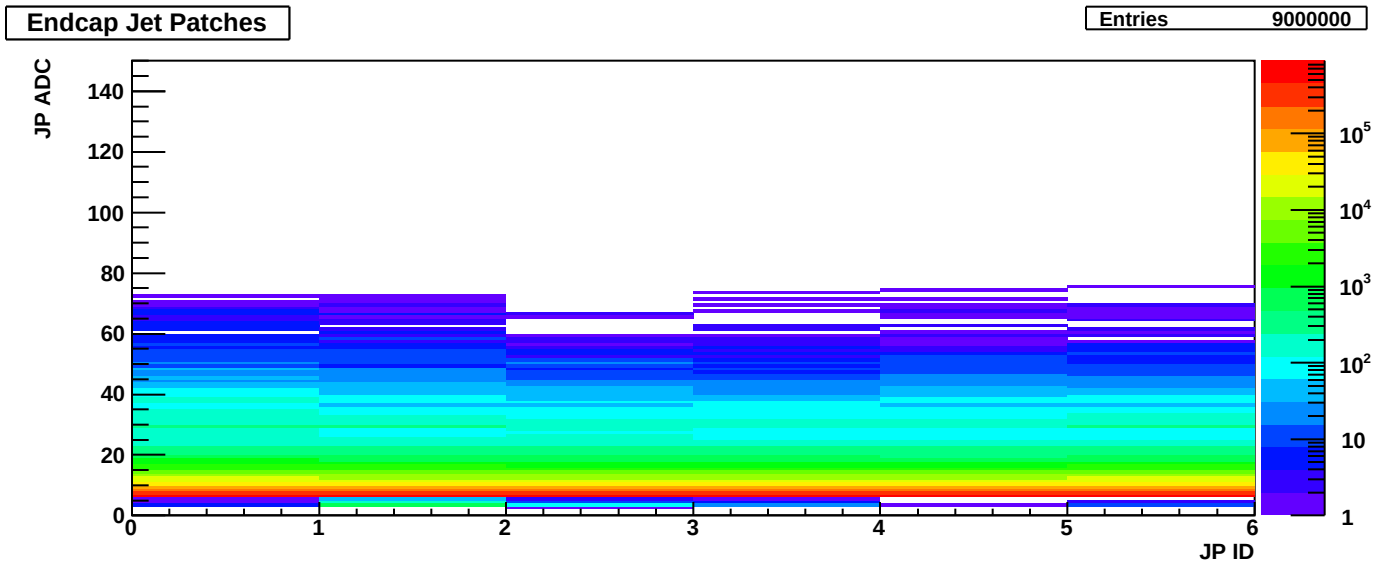

Barrel EMC L0 Input - High Tower
Entries 4.5e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

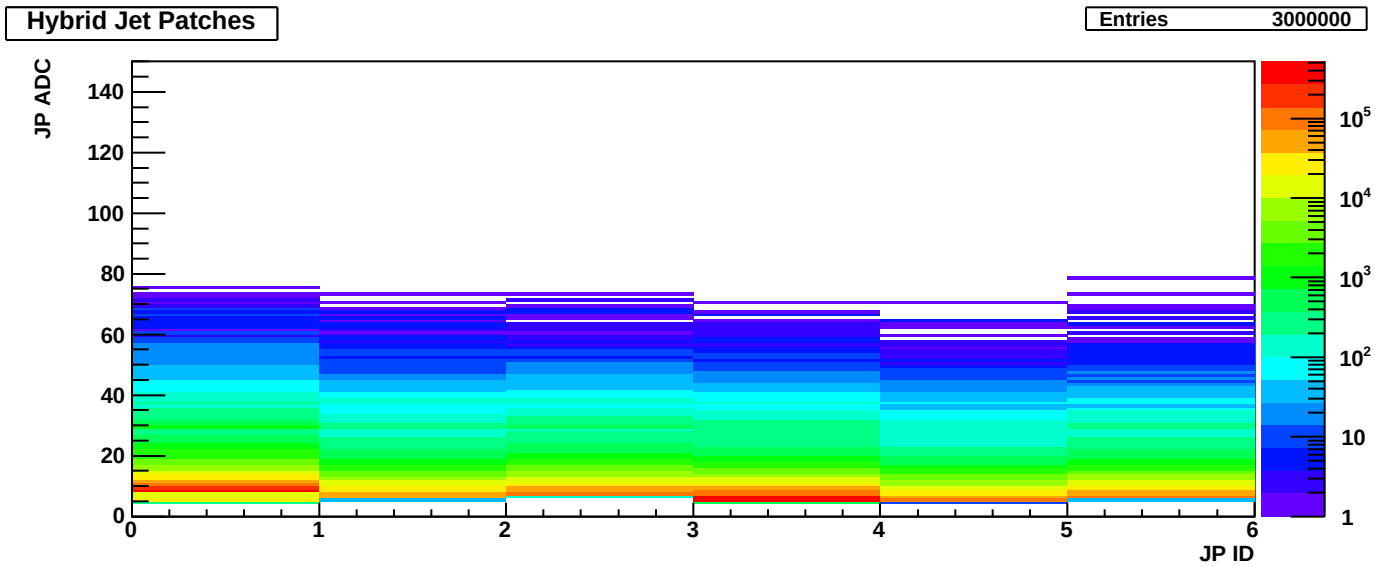

Barrel EMC L0 Input - Patch Sum
Entries 4.5e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

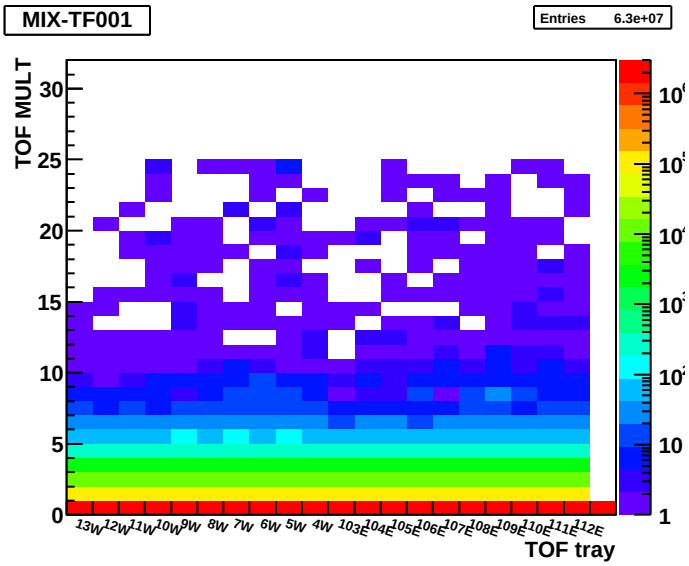

Endcap EMC L0 Input - High Tower
Entries 1.35e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

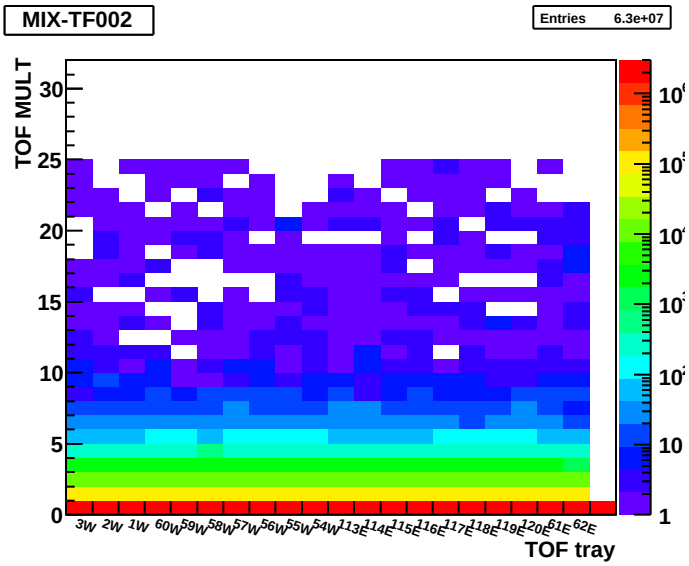

Endcap EMC L0 Input - Patch Sum
Entries 1.35e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

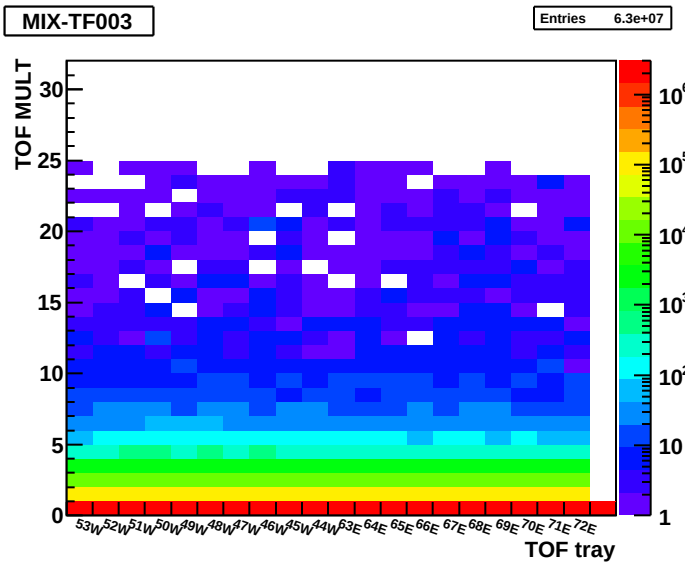

Barrel Jet Patches
Entries 2.7e+07
JP ADC
JP ID

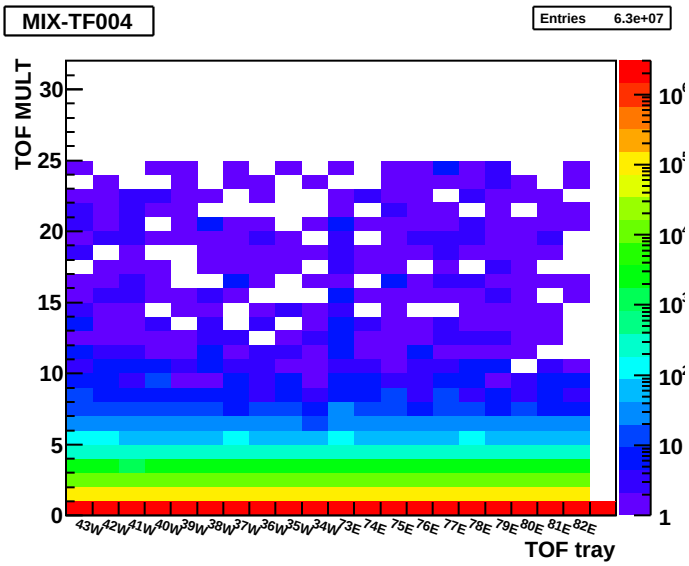

Endcap Jet Patches
Entries 9000000
JP ADC
JP ID

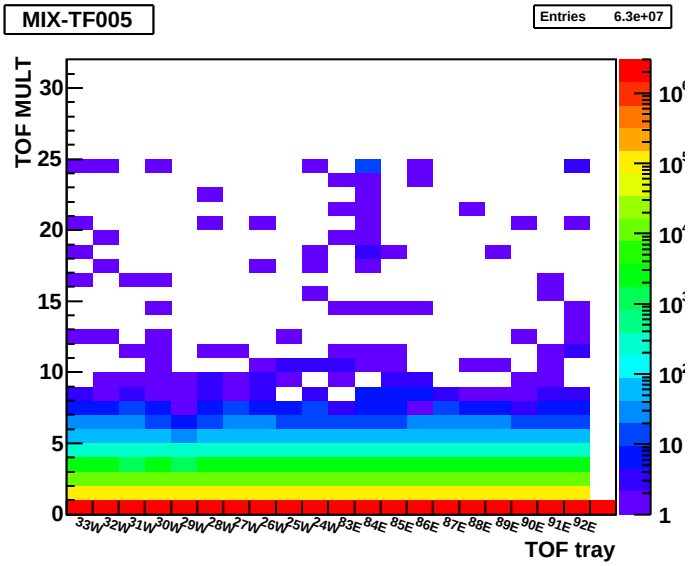

Hybrid Jet Patches
Entries 3000000
JP ADC
JP ID

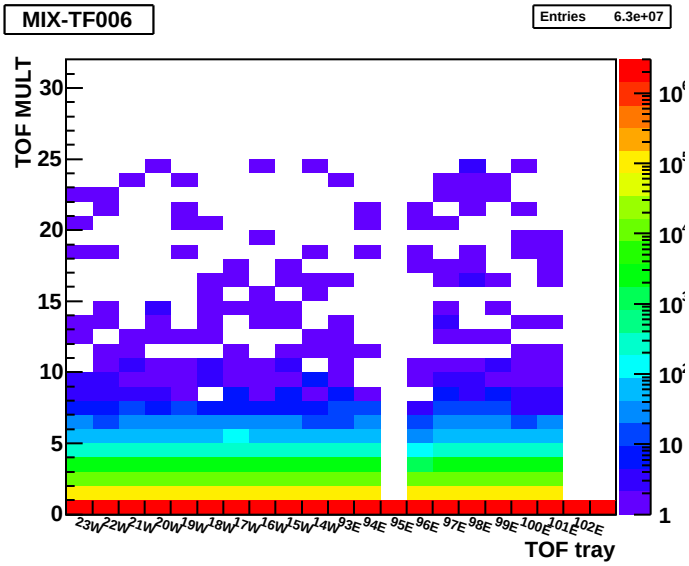

MIX-TF001
Entries 6.3e+07
TOF MULT
TOF tray

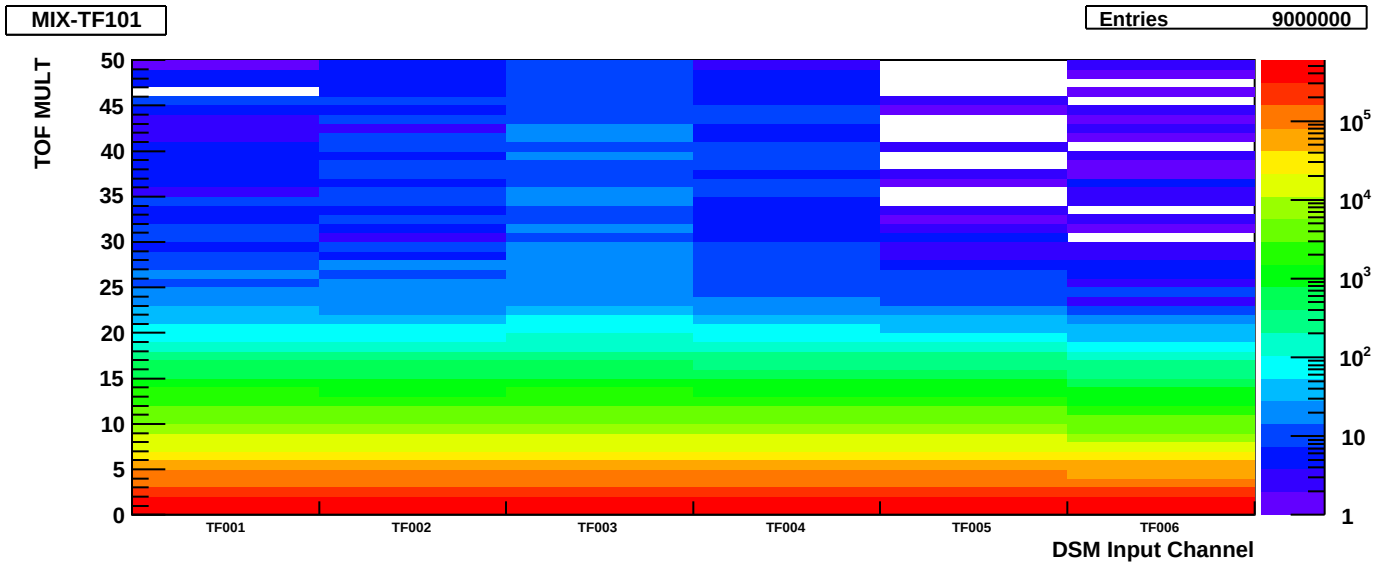

MIX-TF002
Entries 6.3e+07
TOF MULT
TOF tray

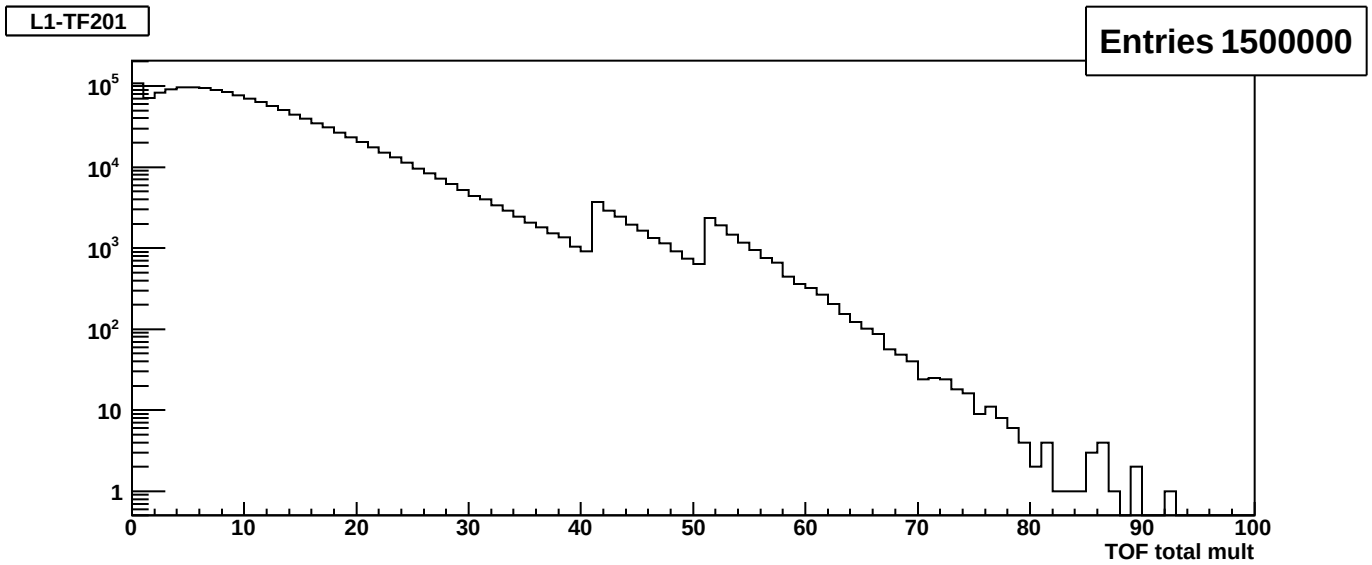

MIX-TF003
Entries 6.3e+07
TOF MULT
TOF tray

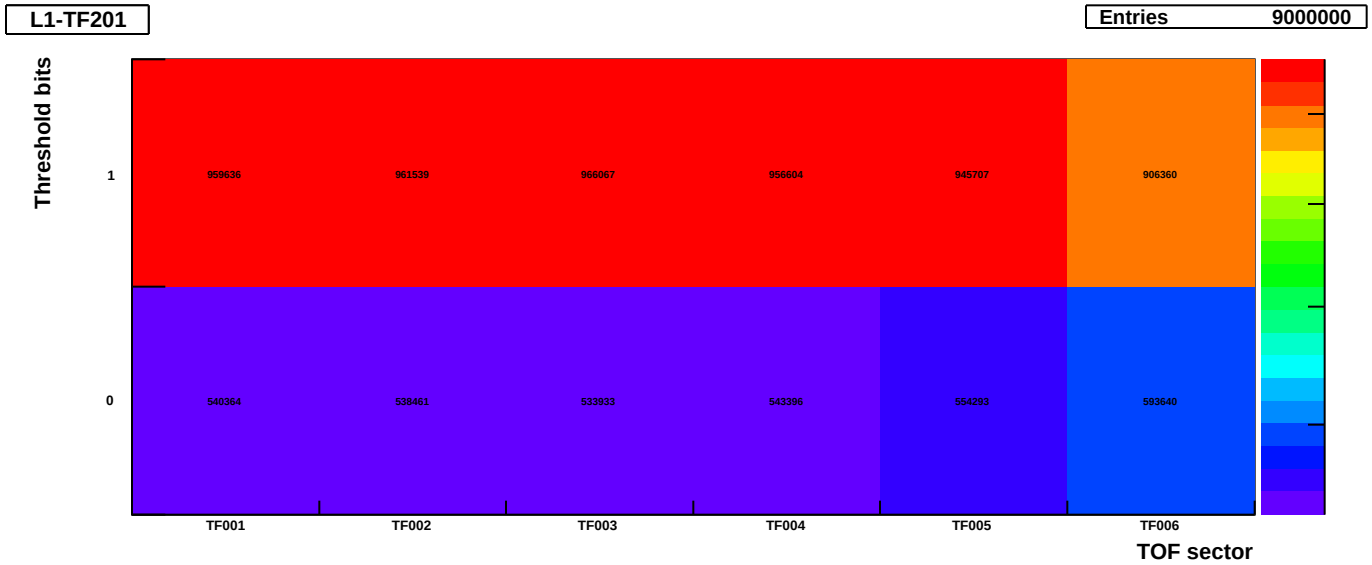

MIX-TF004
Entries 6.3e+07
TOF MULT
TOF tray

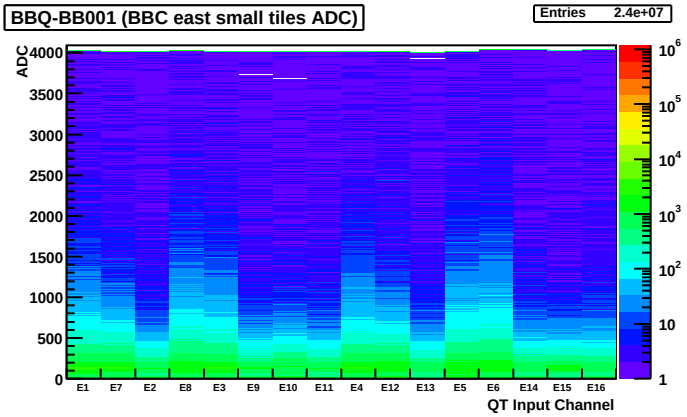

MIX-TF005
Entries 6.3e+07
TOF MULT
TOF tray

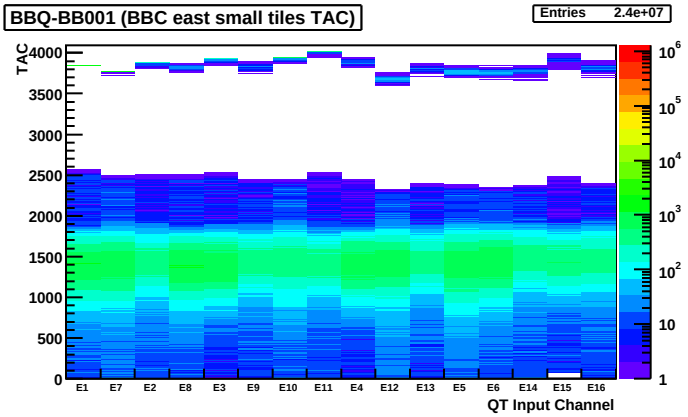

MIX-TF006
Entries 6.3e+07
TOF MULT
TOF tray

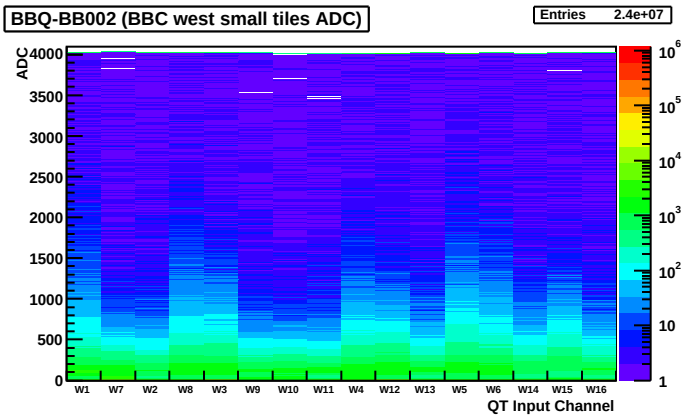

MIX-TF101
Entries 9000000
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

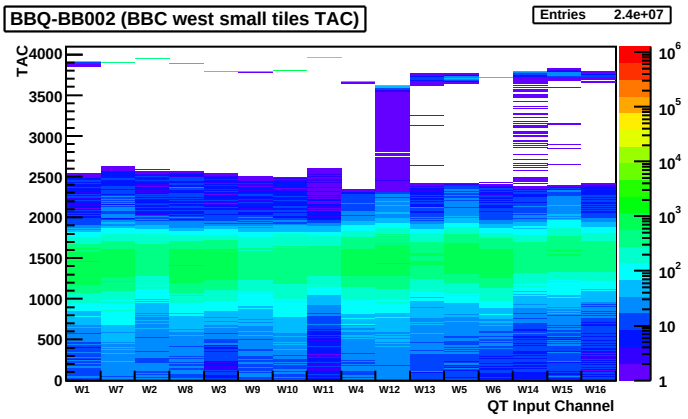

L1-TF201
Entries 1500000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

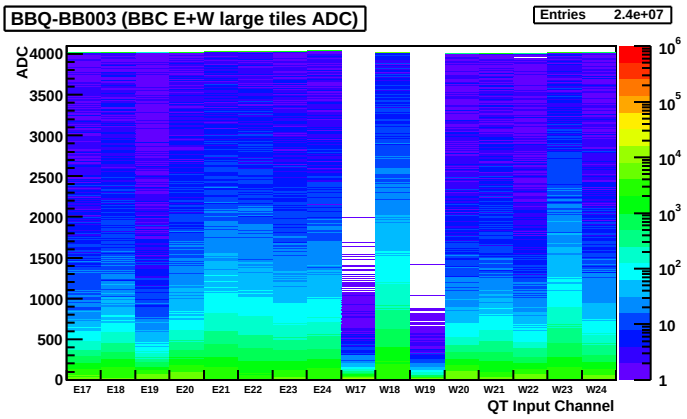

L1-TF201
Entries 9000000
Threshold bits
1
0
959636
961539
966067
956604
945707
906360
540364
538461
533933
543396
554293
593640
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

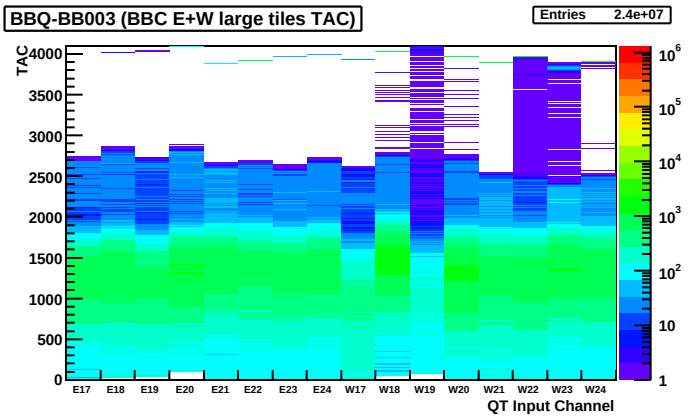

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.4e+07
ADC
QT Input Channel

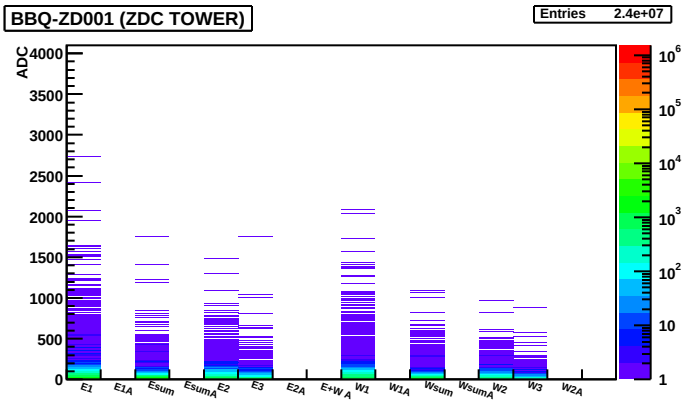

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.4e+07
TAC
QT Input Channel

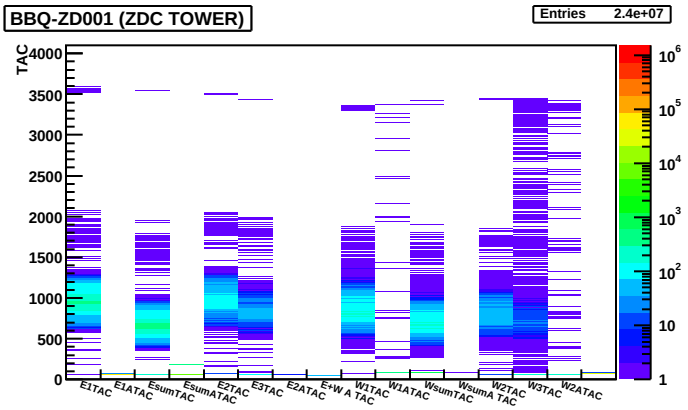

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.4e+07
ADC
QT Input Channel

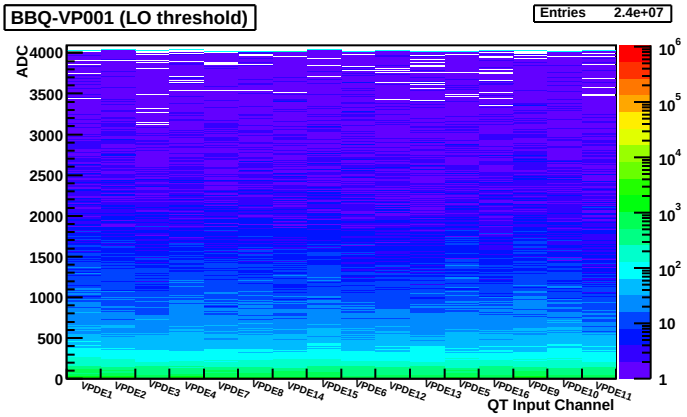

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.4e+07
TAC
QT Input Channel

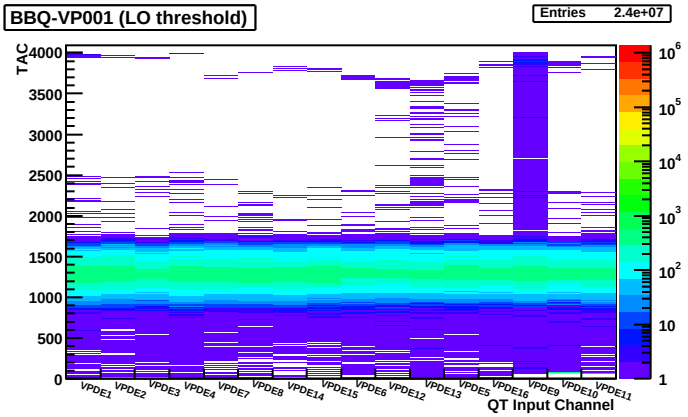

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.4e+07
ADC
QT Input Channel

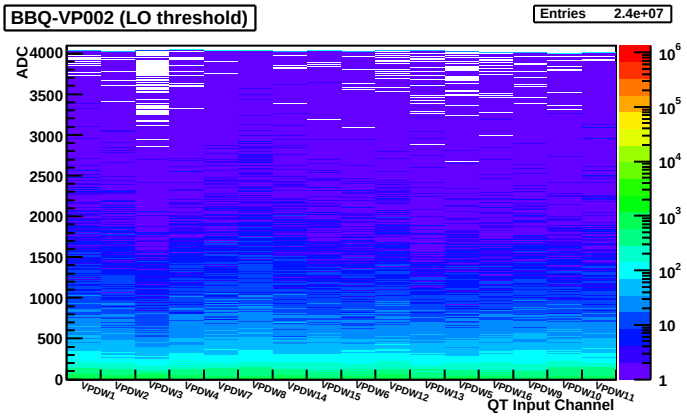

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.4e+07
TAC
QT Input Channel

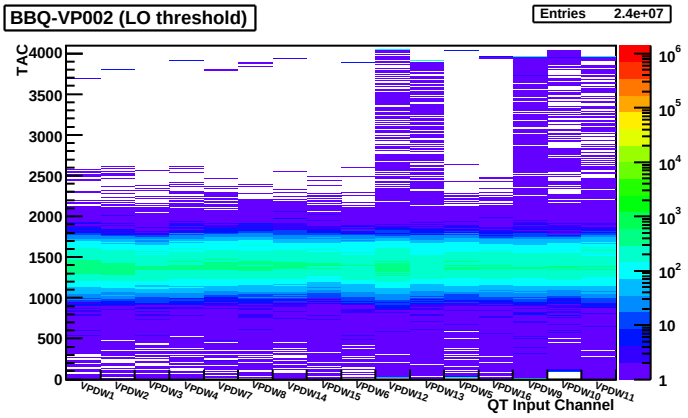

BBQ-ZD001 (ZDC TOWER)
Entries 2.4e+07
ADC

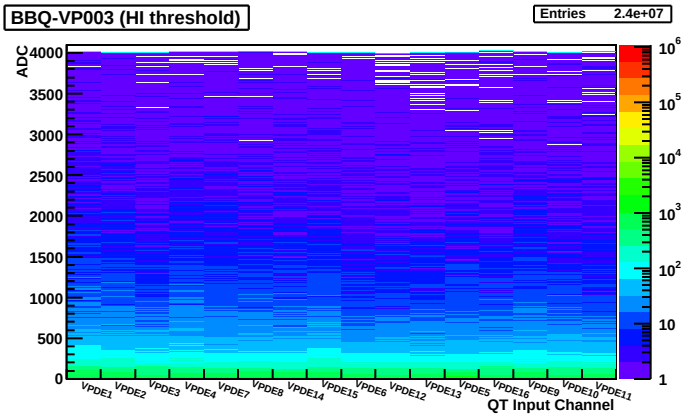

BBQ-ZD001 (ZDC TOWER)
Entries 2.4e+07
TAC

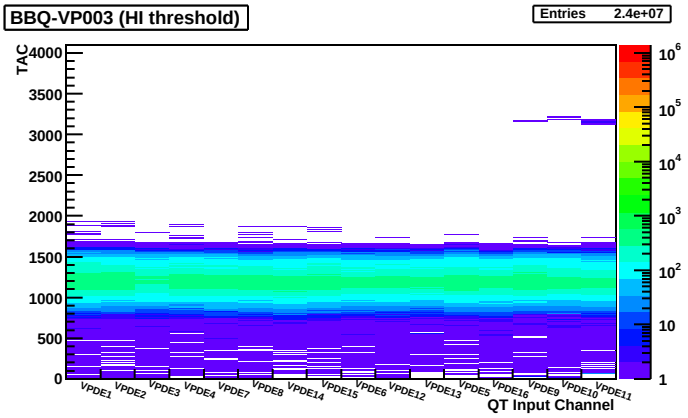

BBQ-VP001 (LO threshold)
Entries 2.4e+07
ADC
QT Input Channel

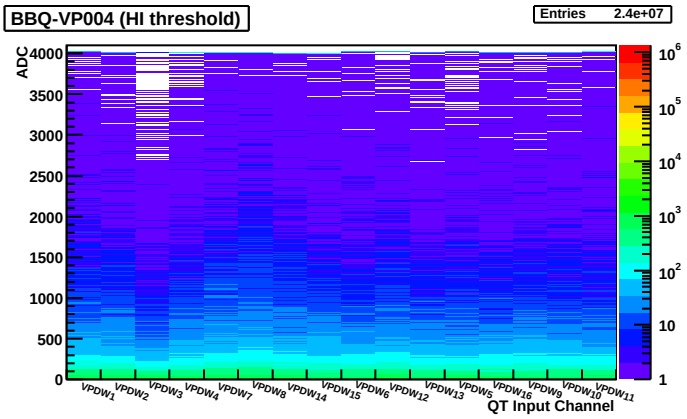

BBQ-VP001 (LO threshold)
Entries 2.4e+07
TAC
QT Input Channel

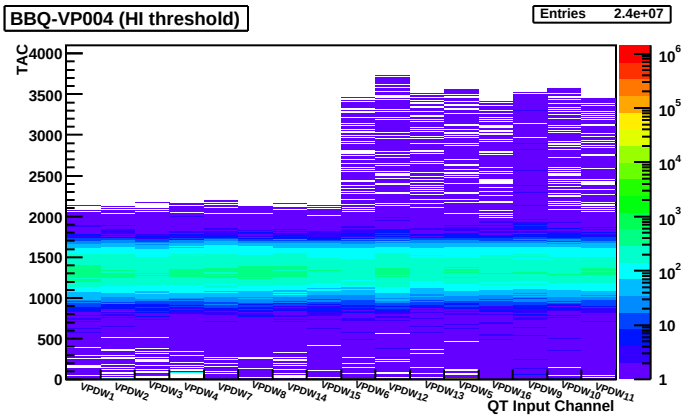

BBQ-VP002 (LO threshold)
Entries 2.4e+07
ADC
QT Input Channel

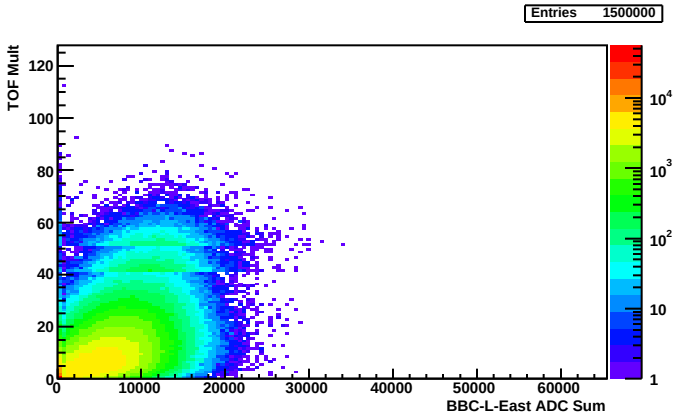

BBQ-VP002 (LO threshold)
Entries 2.4e+07
TAC
QT Input Channel

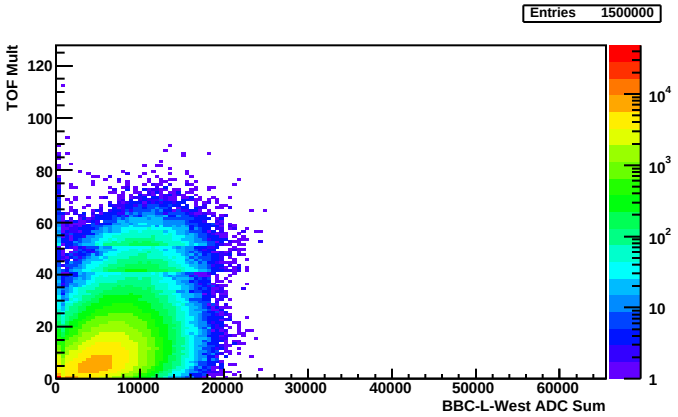

BBQ-VP003 (HI threshold)
Entries 2.4e+07
ADC
QT Input Channel

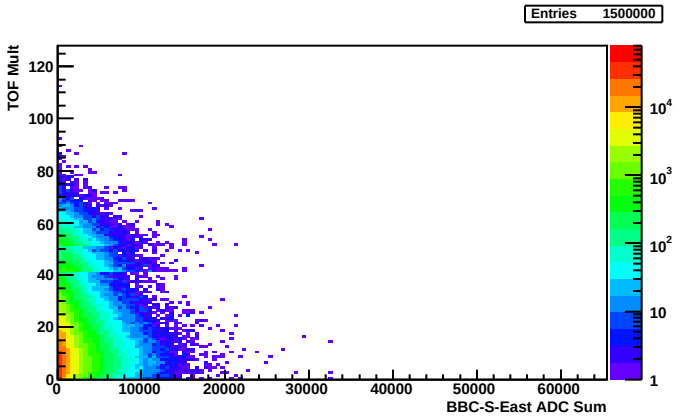

BBQ-VP003 (HI threshold)
Entries 2.4e+07
TAC
QT Input Channel

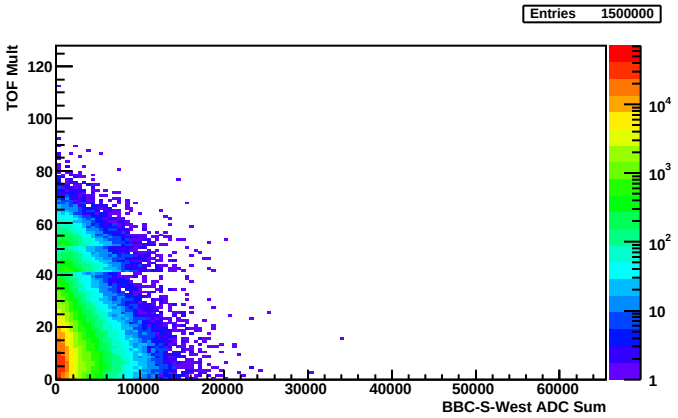

BBQ-VP004 (HI threshold)
Entries 2.4e+07
ADC
QT Input Channel

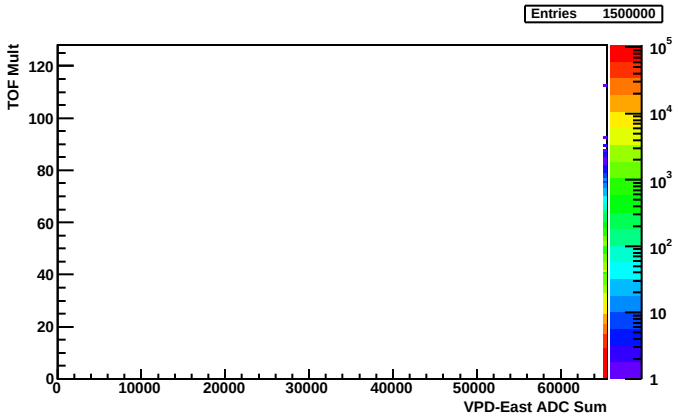

BBQ-VP004 (HI threshold)
Entries 2.4e+07
TAC
QT Input Channel

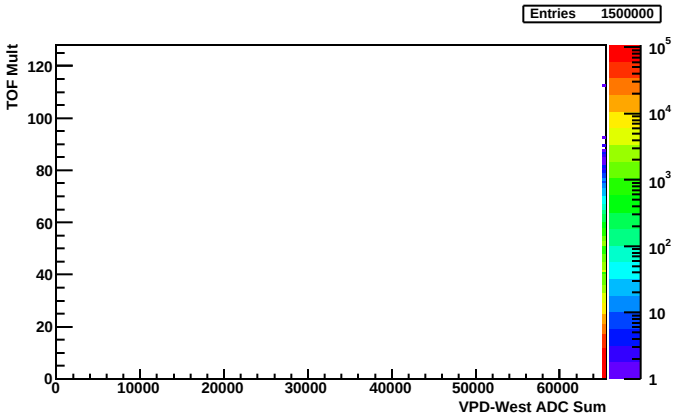

Entries 1500000
TOF Mult
BBC-L-East ADC Sum

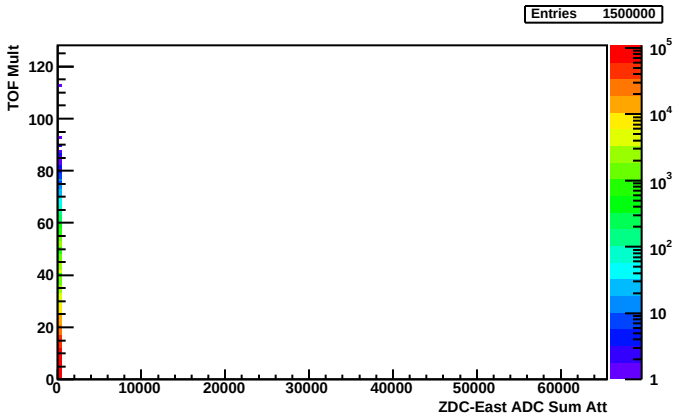

Entries 1500000
TOF Mult
BBC-L-West ADC Sum

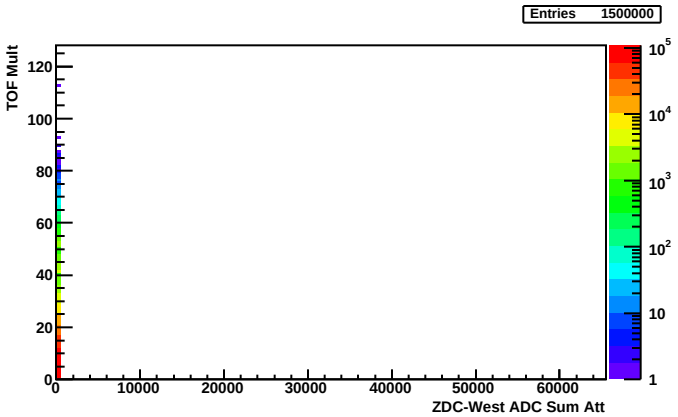

Entries 1500000
TOF Mult
BBC-S-East ADC Sum

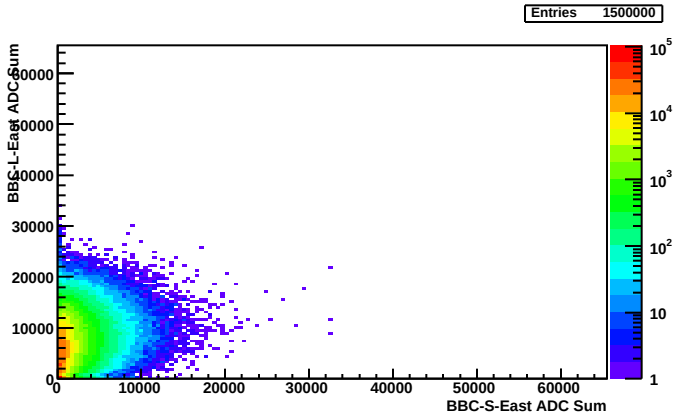

Entries 1500000
TOF Mult
BBC-S-West ADC Sum

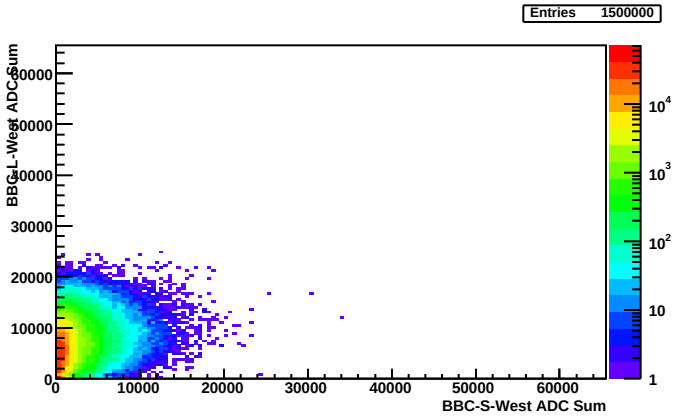

Entries 1500000
TOF Mult
VPD-East ADC Sum

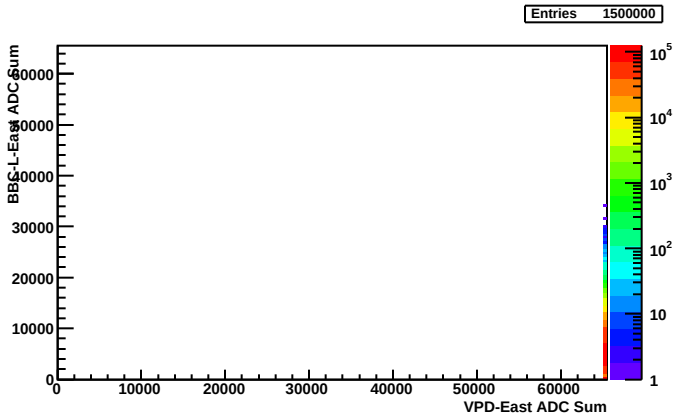

Entries 1500000
TOF Mult
VPD-West ADC Sum

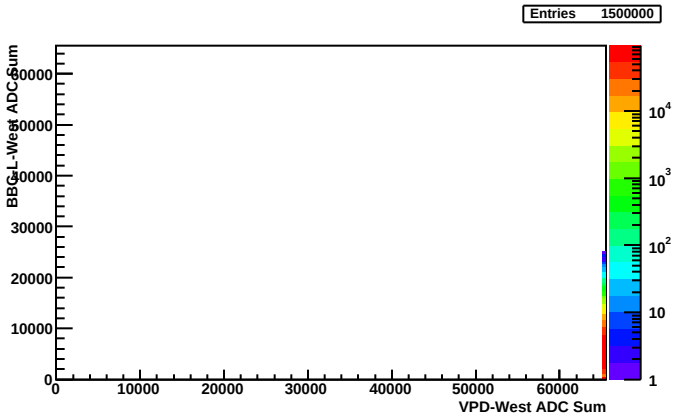

Entries 1500000
TOF Mult
ZDC-East ADC Sum Att

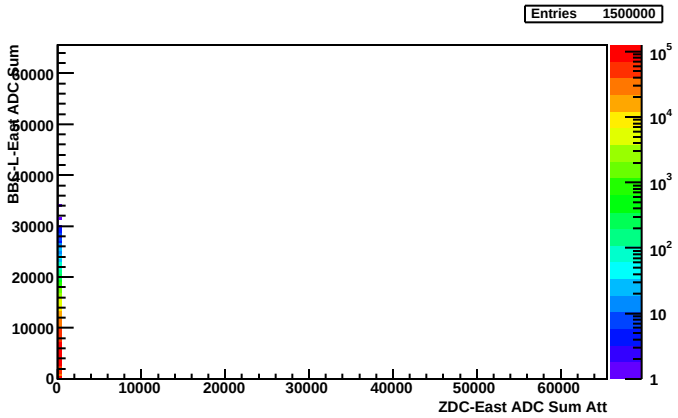

Entries 1500000
TOF Mult
ZDC-West ADC Sum Att

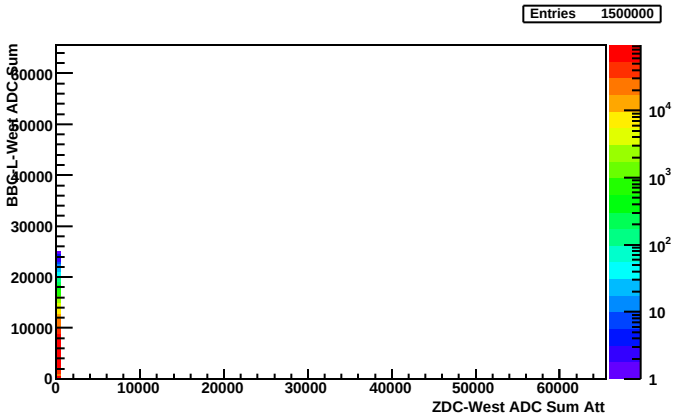

Entries 1500000
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1500000
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1500000
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1500000
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 1500000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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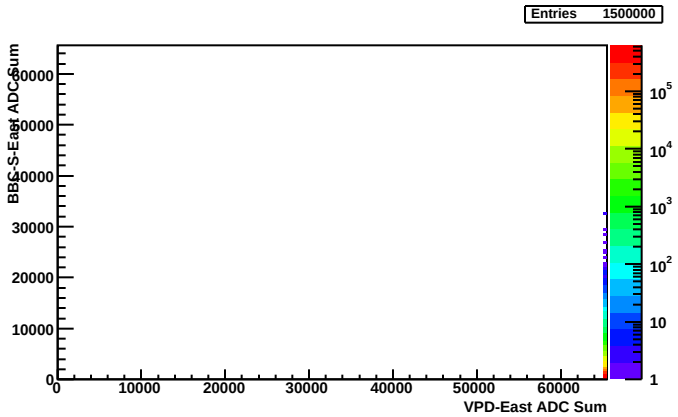
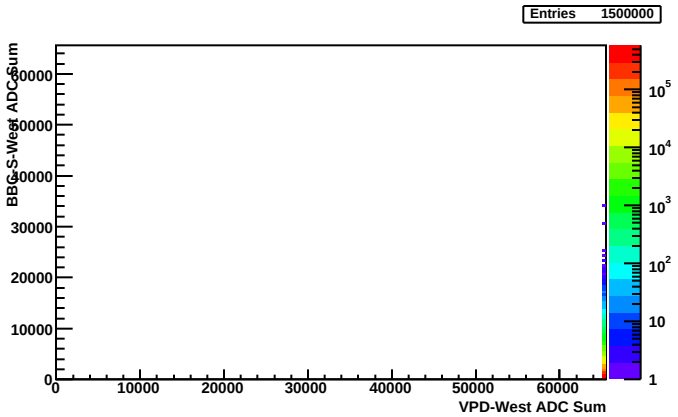
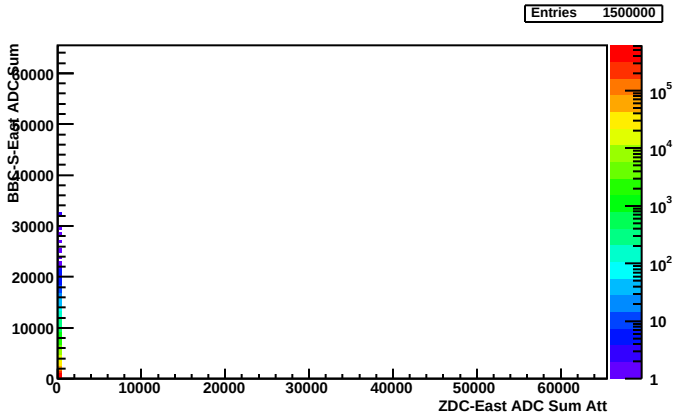
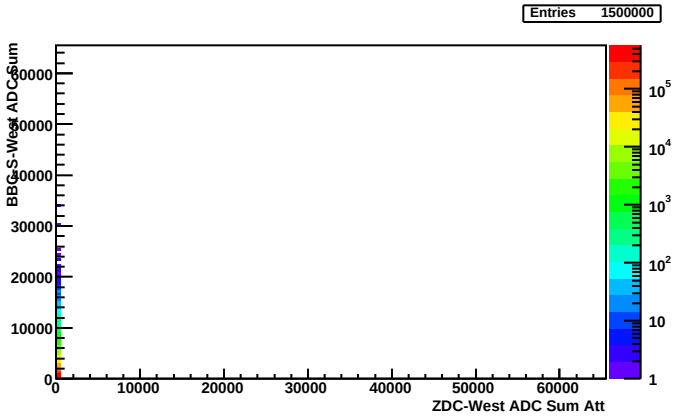
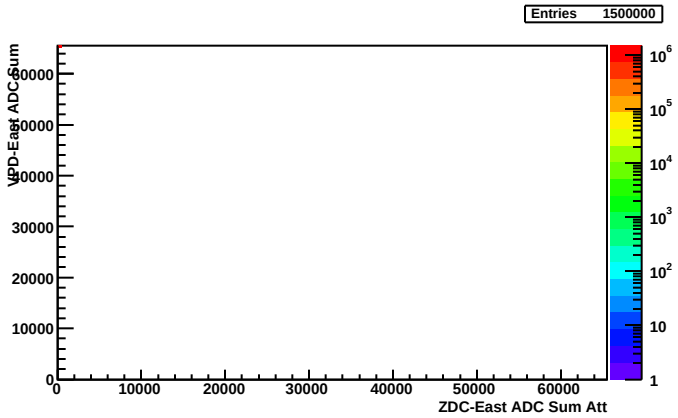
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

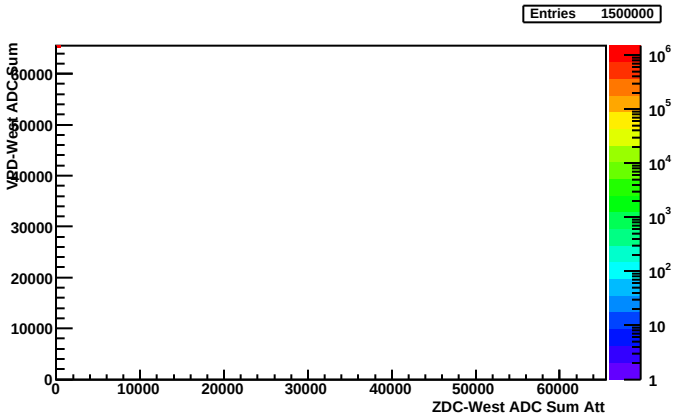

Entries 1500000
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1500000
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1500000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1500000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1500000
VPD-East ADC Sum
ZDC-East ADC Sum Att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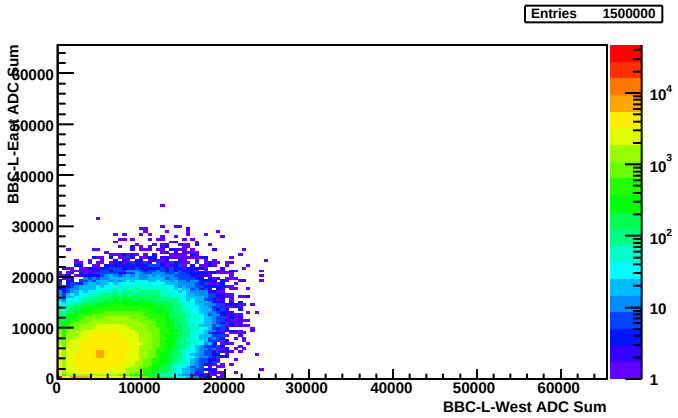
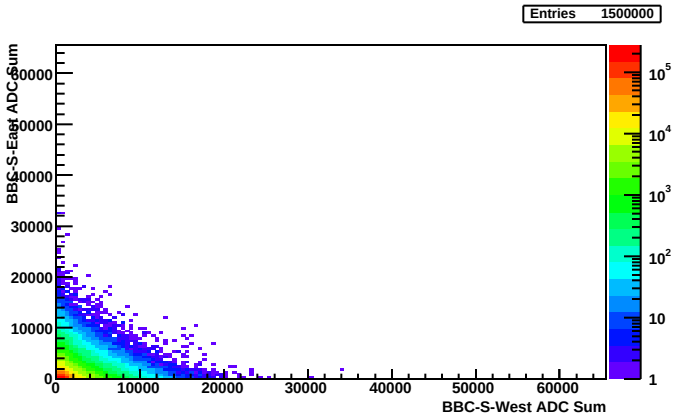
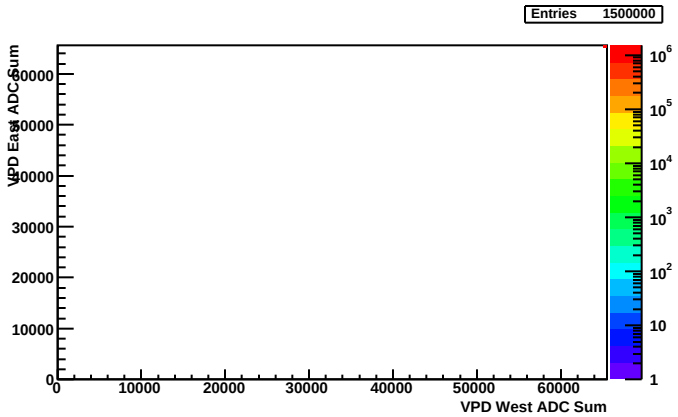
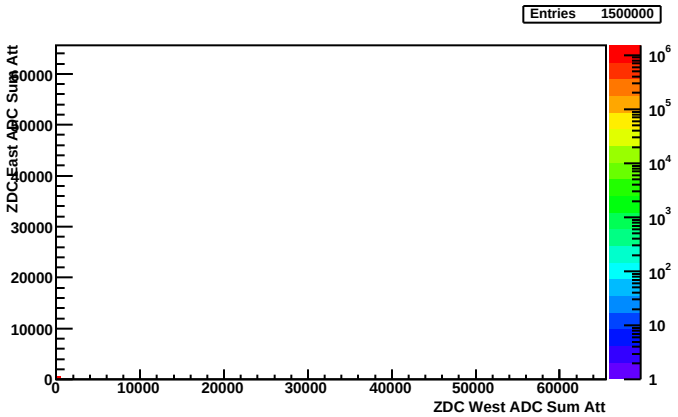
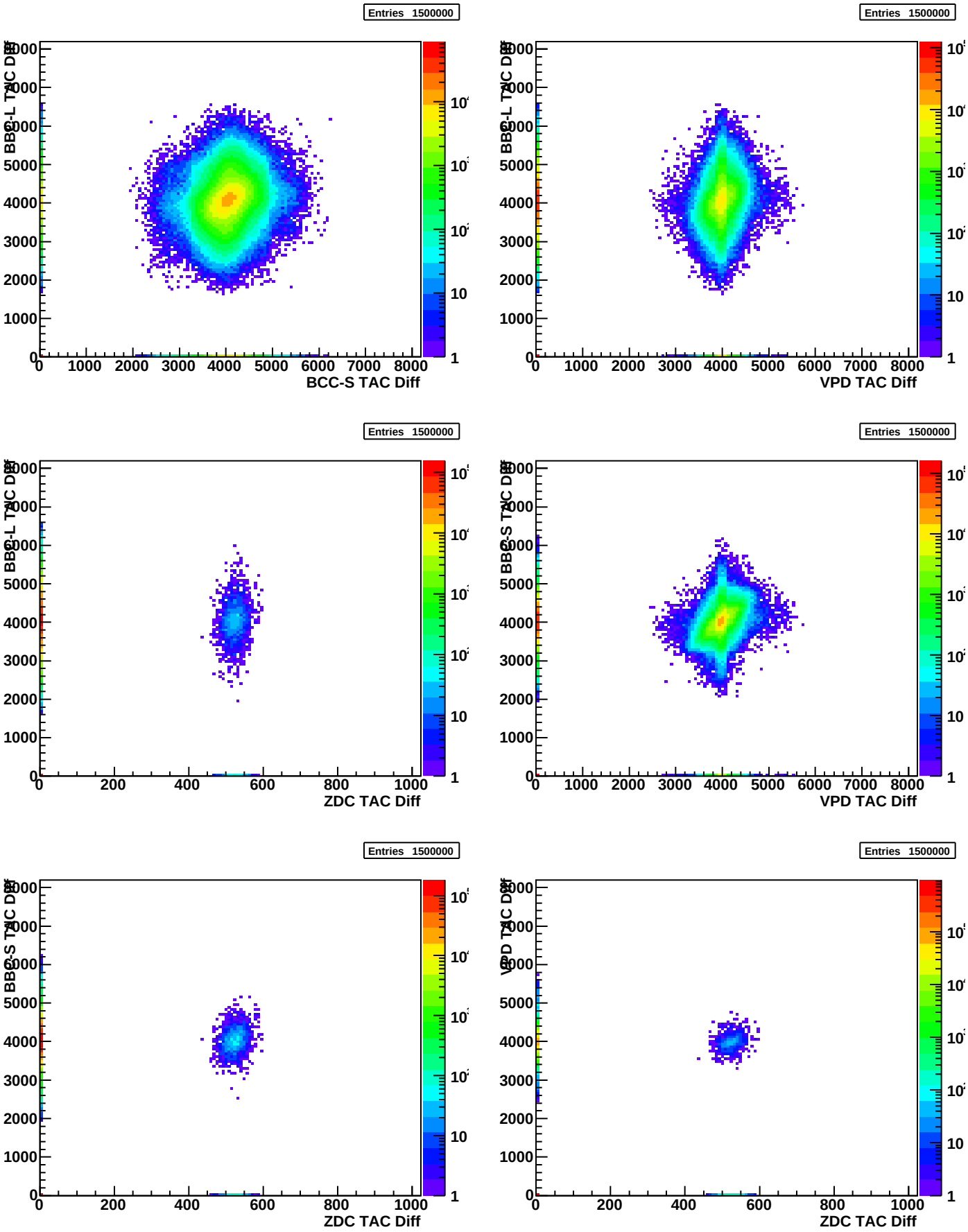
VPD-West ADC Sum
ZDC-West ADC Sum Att

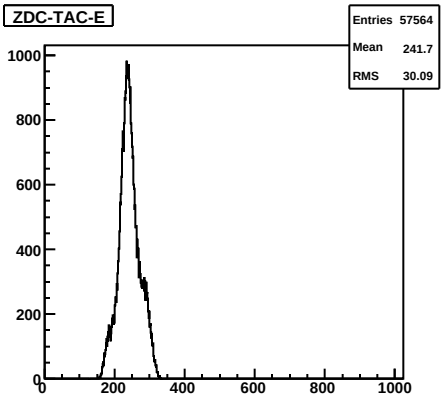

Entries 1500000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

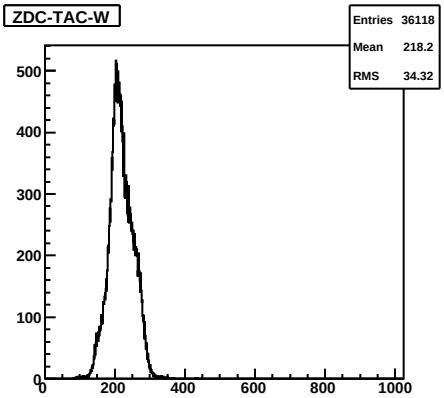

Entries 1500000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

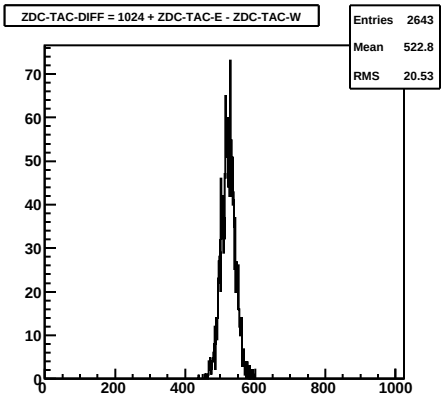

Entries 1500000
VPD East ADC Sum
VPD West ADC Sum

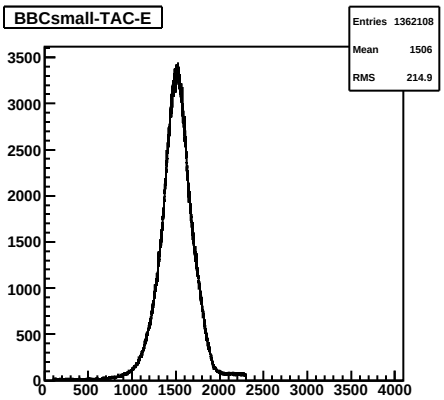

Entries 1500000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

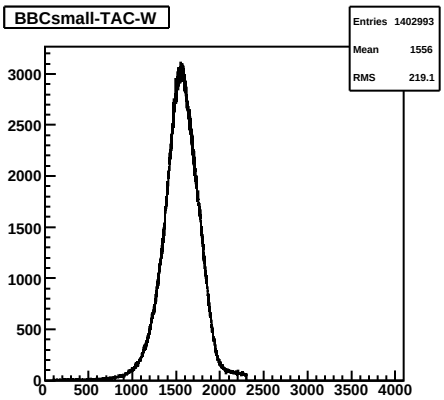

Entries 1500000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1500000
BBC-L TAC Diff
VPD TAC Diff
Entries 1500000
BBC-L TAC Diff
ZDC TAC Diff
Entries 1500000
BBC-S TAC Diff
VPD TAC Diff
Entries 1500000
BBC-S TAC Diff
ZDC TAC Diff
Entries 1500000
VPD TAC Diff
ZDC TAC Diff

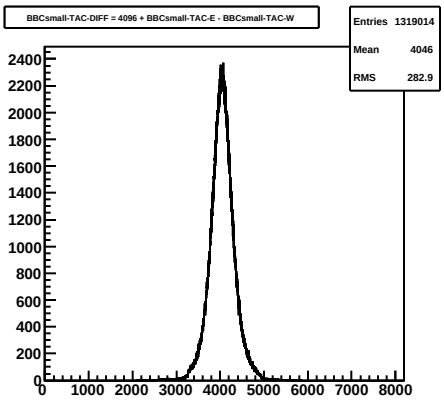

ZDC-TAC-E
Entries 57564
Mean 241.7
RMS 30.09

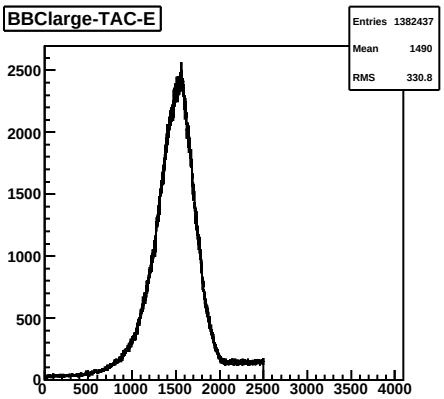

ZDC-TAC-W
Entries 36118
Mean 218.2
RMS 34.32

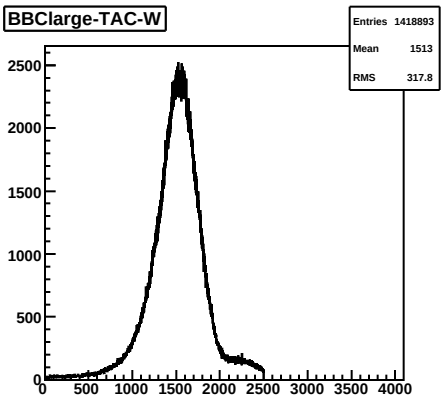

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2643
Mean 522.8
RMS 20.53

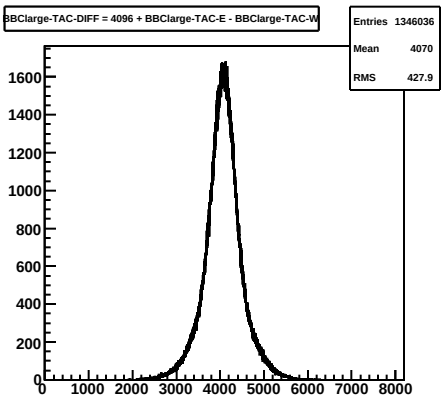

BBCsmall-TAC-E
Entries 1362108
Mean 1506
RMS 214.9

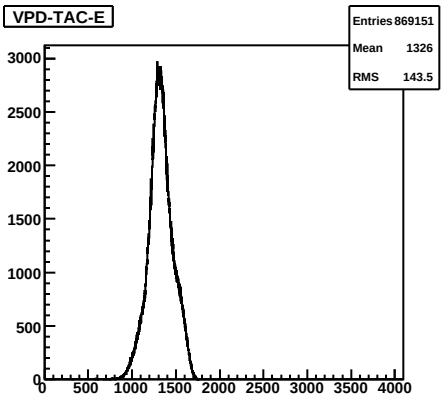

BBCsmall-TAC-W
Entries 1402993
Mean 1556
RMS 219.1

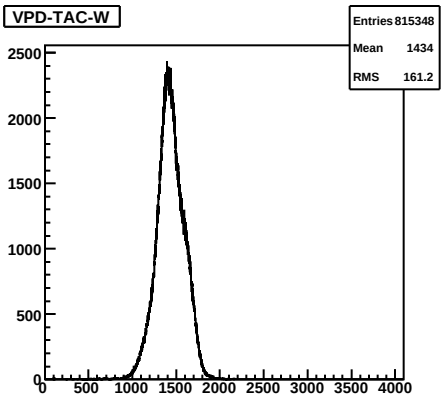

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1319014
Mean 4046
RMS 282.9

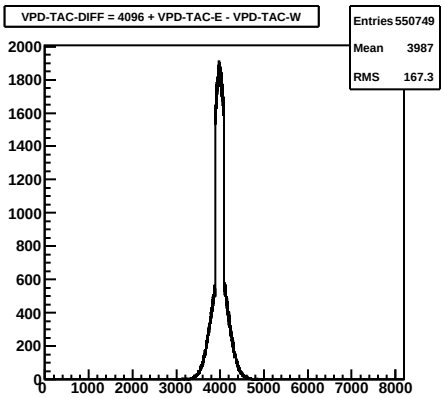

BBClarge-TAC-E
Entries 1382437
Mean 1490
RMS 330.8

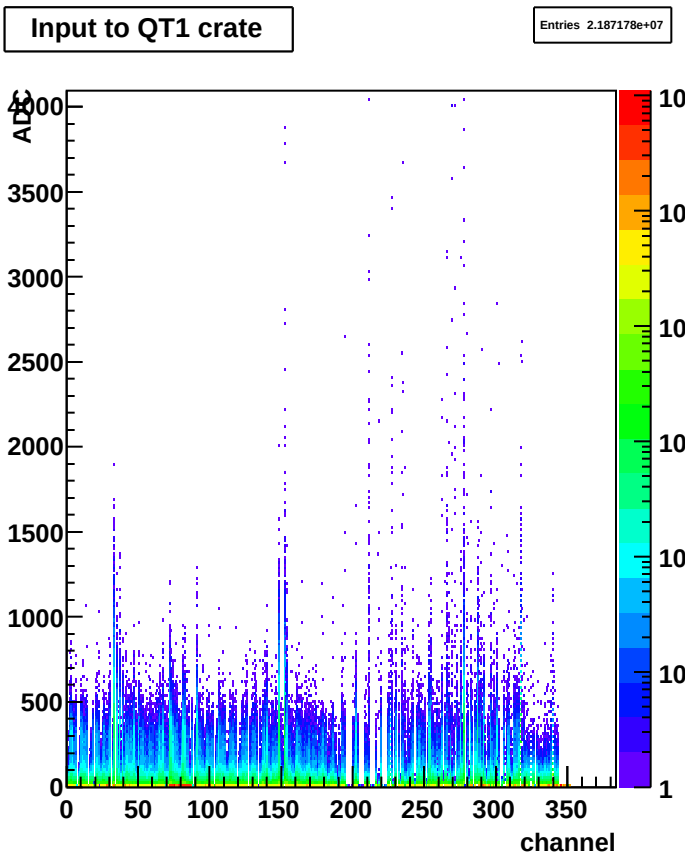

BBClarge-TAC-W
Entries 1418893
Mean 1513
RMS 317.8

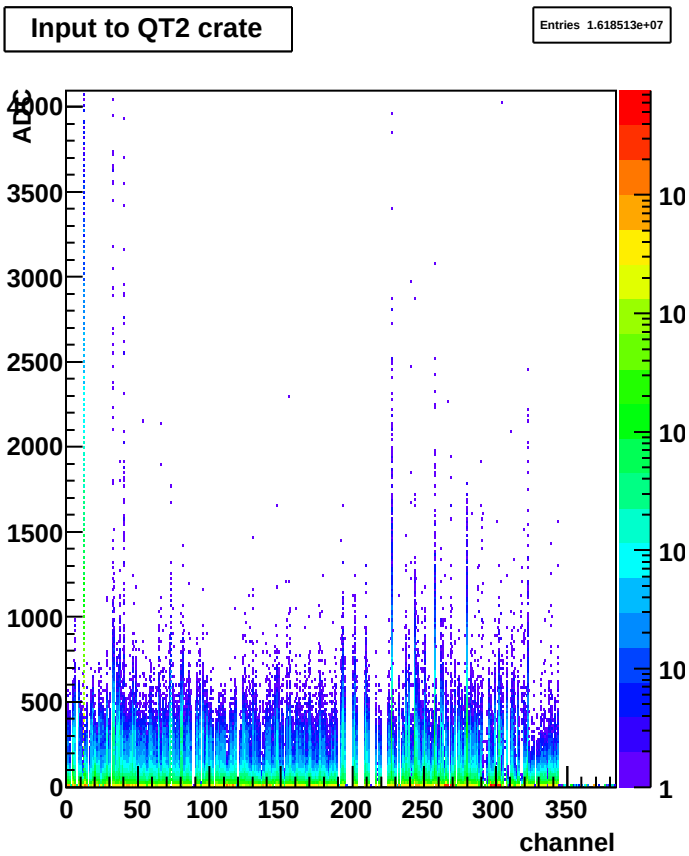

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1346036
Mean 4070
RMS 427.9

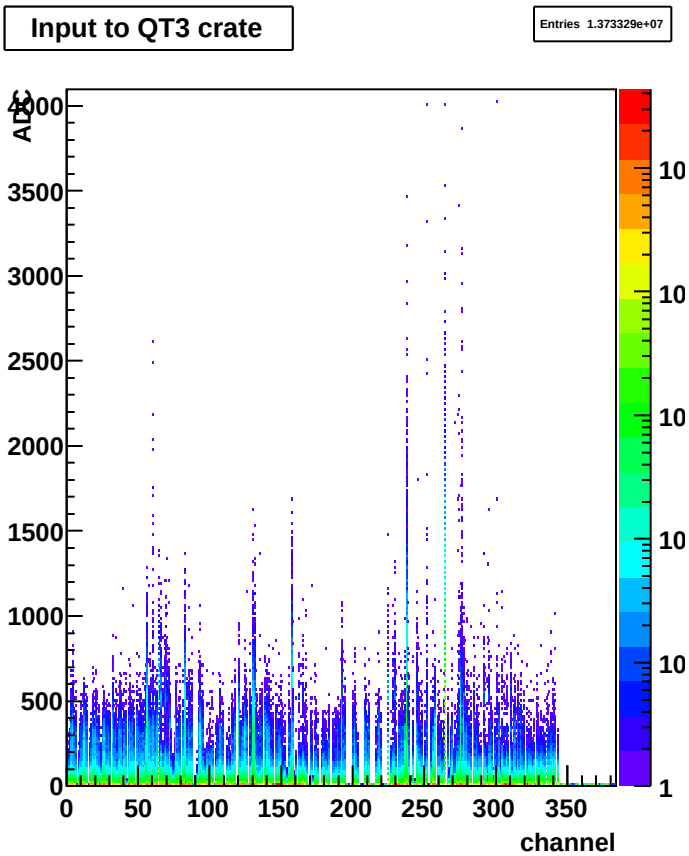

VPD-TAC-E
Entries 869151
Mean 1326
RMS 143.5

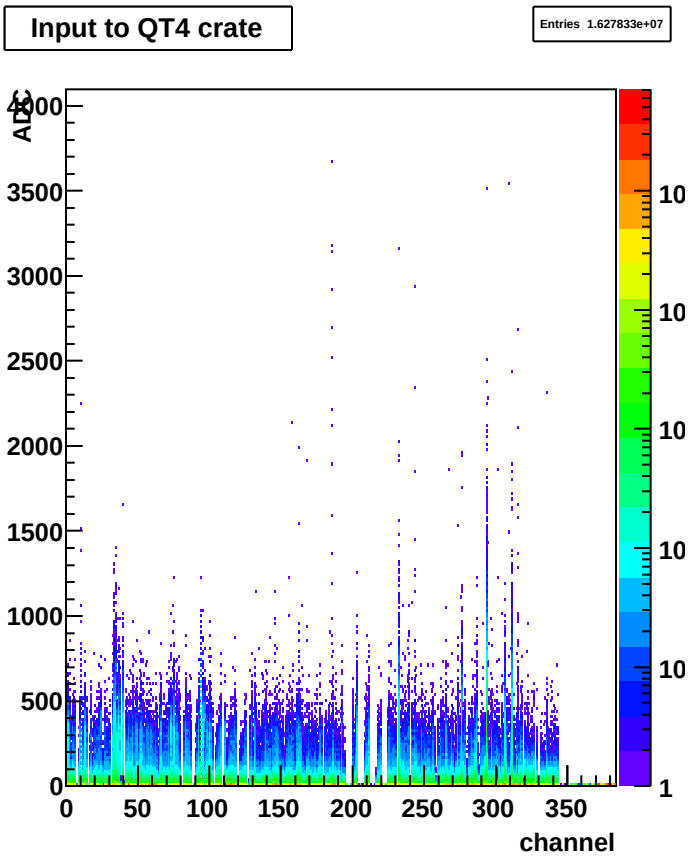

VPD-TAC-W
Entries 815348
Mean 1434
RMS 161.2

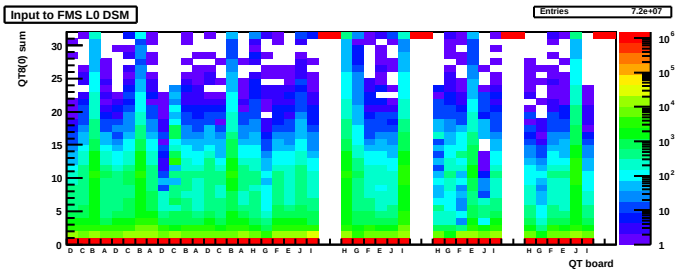

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 550749
Mean 3987
RMS 167.3

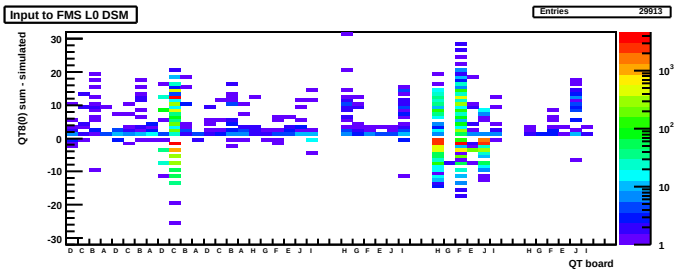

Input to QT1 crate
Entries 2.187178e+07
ADC
channel

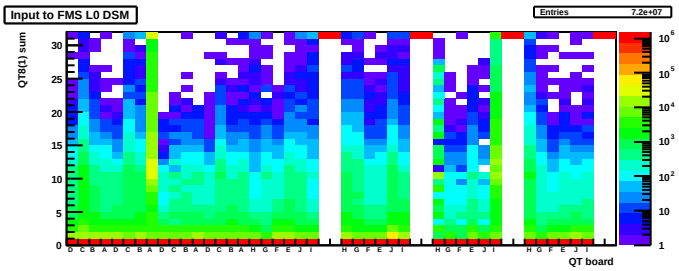

Input to QT2 crate
Entries 1.618513e+07
ADC
channel

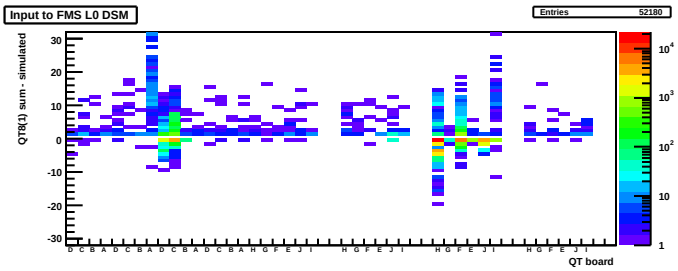

Input to QT3 crate
Entries 1.373329e+07
ADC
channel

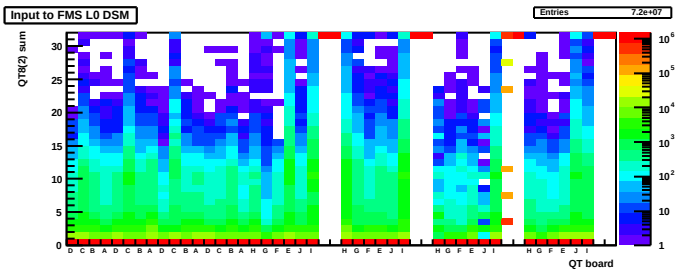

Input to QT4 crate
Entries 1.627833e+07
ADC
channel

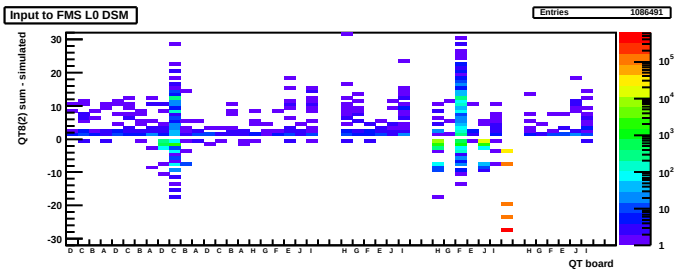

Input to FMS L0 DSM
Entries 7.2e+07
QT8(0) sum
QT board

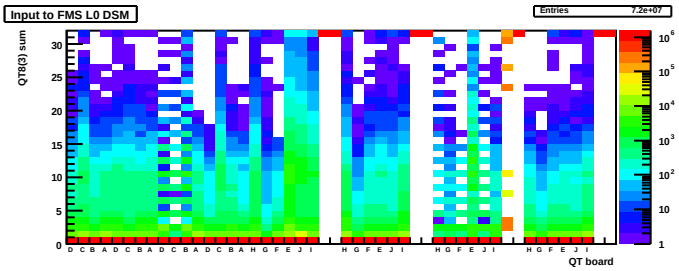

Input to FMS L0 DSM
Entries 29913
QT8(0) sum - simulated
QT board

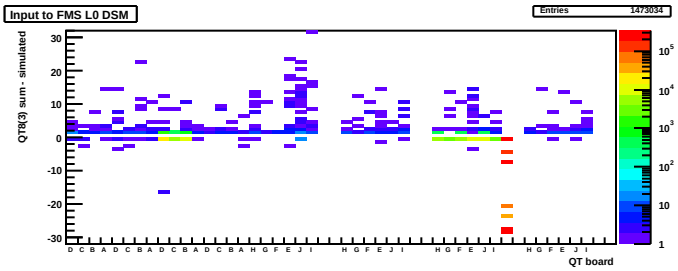

Input to FMS L0 DSM
Entries 7.2e+07
QT8(1) sum
QT board

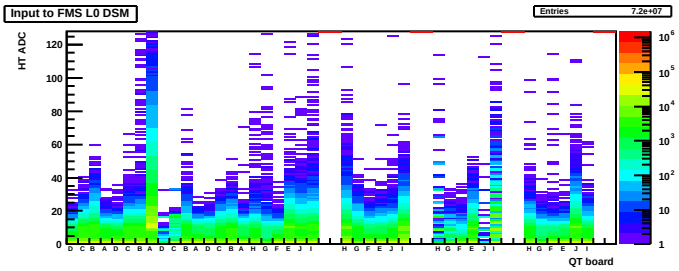

Input to FMS L0 DSM
Entries 52180
QT8(1) sum - simulated
QT board

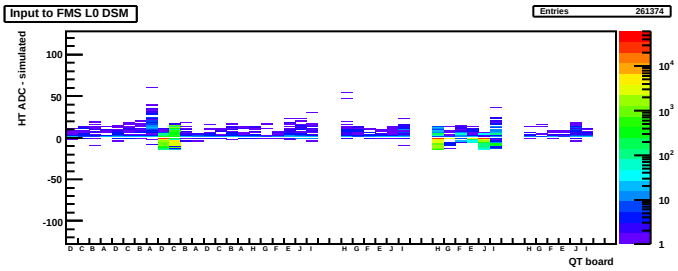

Input to FMS L0 DSM
Entries 7.2e+07
QT8(2) sum
QT board

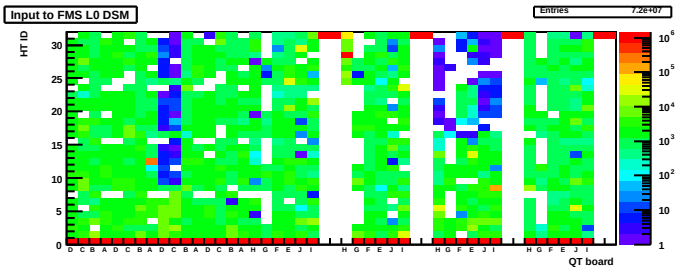

Input to FMS L0 DSM
Entries 1086491
QT8(2) sum - simulated
QT board

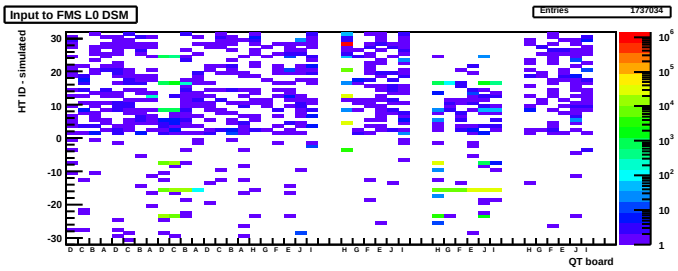

Input to FMS L0 DSM
Entries 7.2e+07
QT8(3) sum
QT board

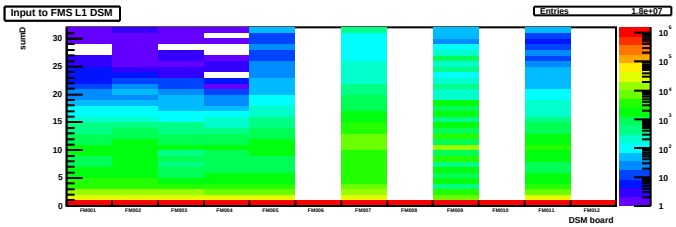

Input to FMS L0 DSM
Entries 1473034
QT8(3) sum - simulated
QT board

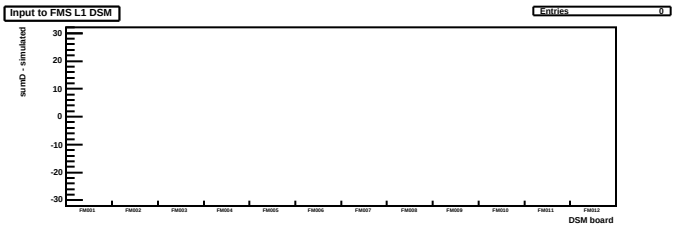

Input to FMS L0 DSM
Entries 7.2e+07
HT ADC
QT board

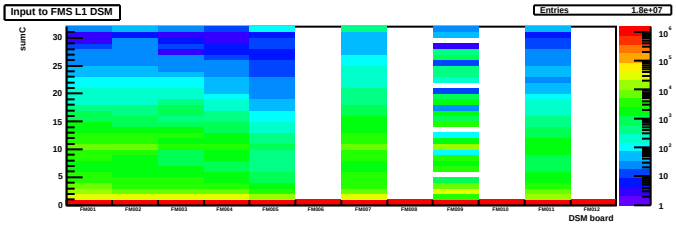

Input to FMS L0 DSM
Entries 261374
HT ADC - simulated
QT board

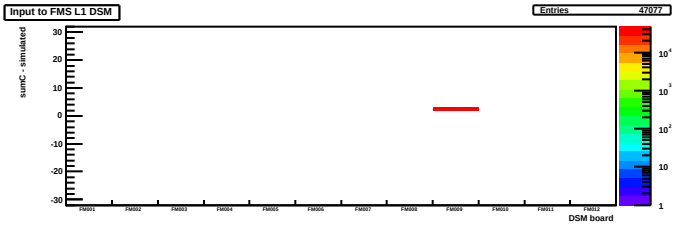

Input to FMS L0 DSM
Entries 7.2e+07
HT ID
QT board

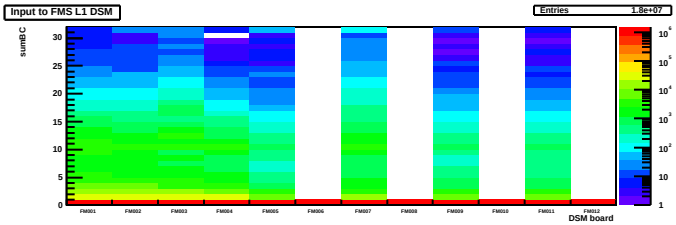

Input to FMS L0 DSM
Entries 1737034
HT ID - simulated
QT board

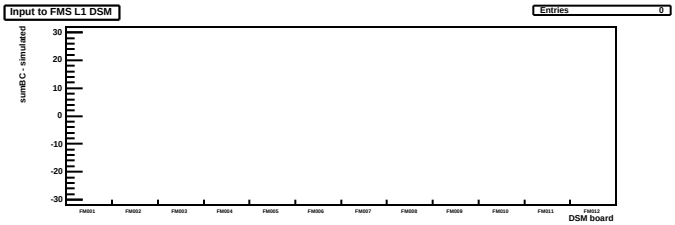

Input to FMS L1 DSM
Entries 1.8e+07
sumD
DSM board

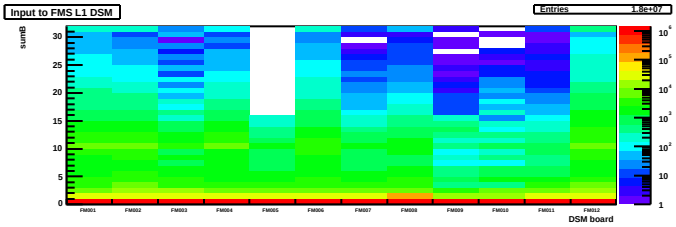

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

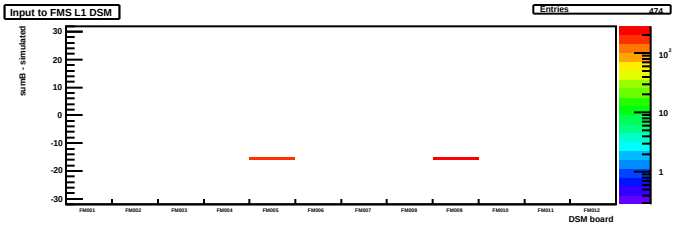

Input to FMS L1 DSM
Entries 1.8e+07
sumC
DSM board

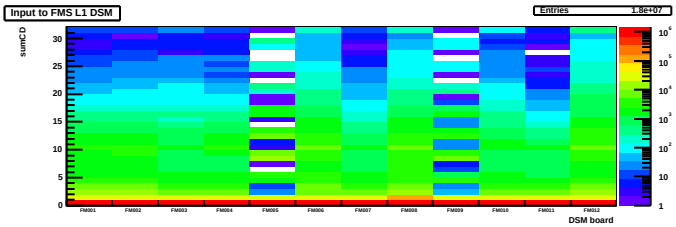

Input to FMS L1 DSM
Entries 47077
sumC - simulated
DSM board

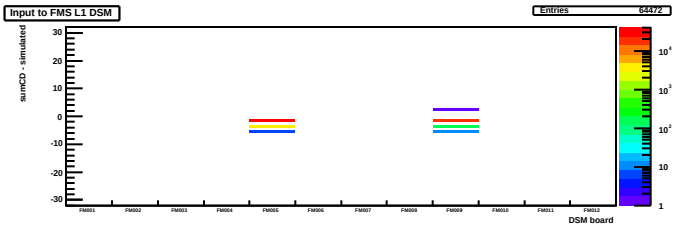

Input to FMS L1 DSM
Entries 1.8e+07
sumBC
DSM board

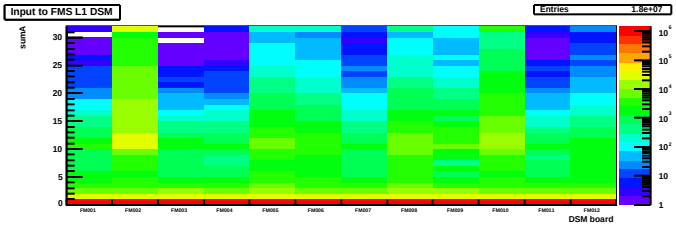

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

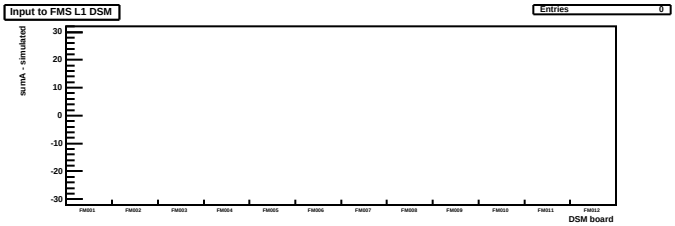

Input to FMS L1 DSM
Entries 1.8e+07
sumB
DSM board

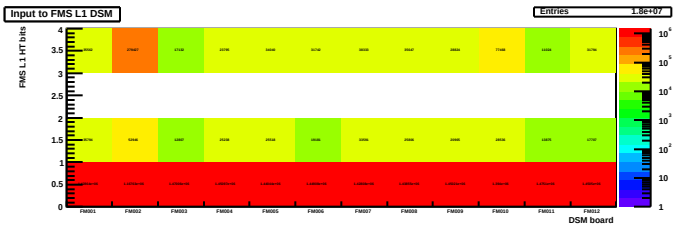

Input to FMS L1 DSM
Entries 474
sumB - simulated
DSM board

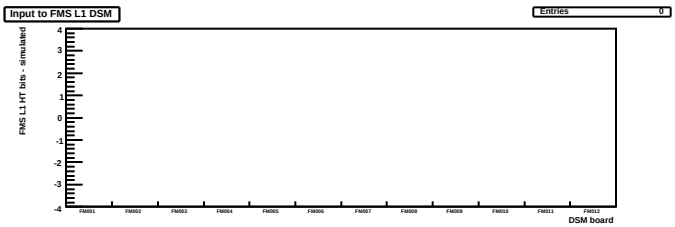

Input to FMS L1 DSM
Entries 1.8e+07
sumCD
DSM board

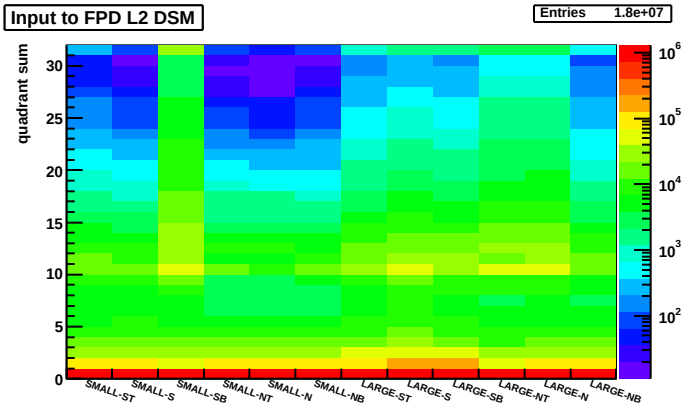

Input to FMS L1 DSM
Entries 64472
sumCD - simulated
DSM board

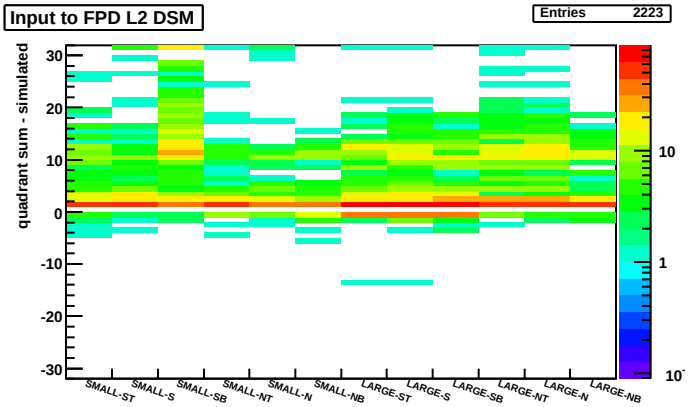

Input to FMS L1 DSM
Entries 1.8e+07
sumA
DSM board

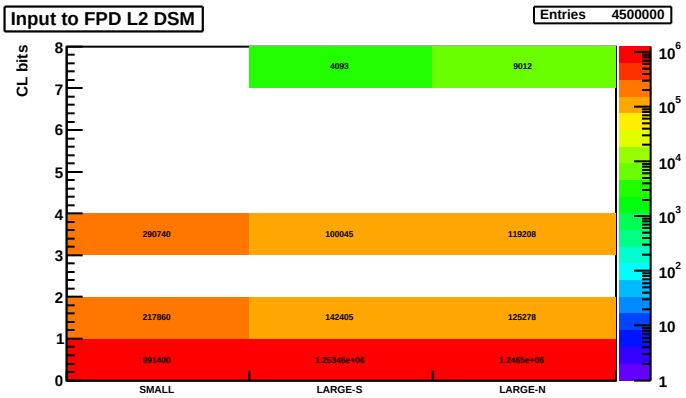

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

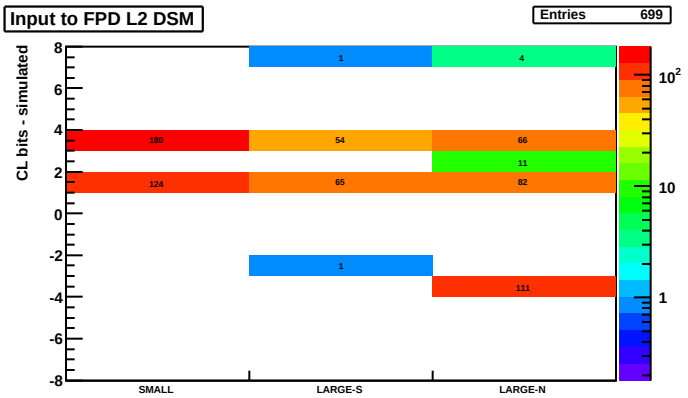

Input to FMS L1 DSM
Entries 1.8e+07
FMS L1 HT bits
DSM board

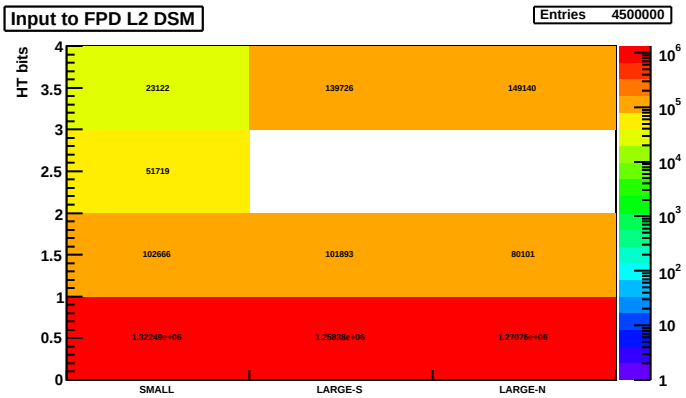

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

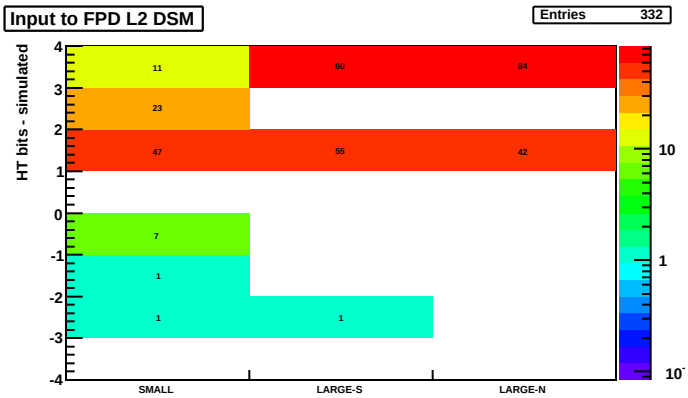

Input to FPD L2 DSM
Entries 1.8e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

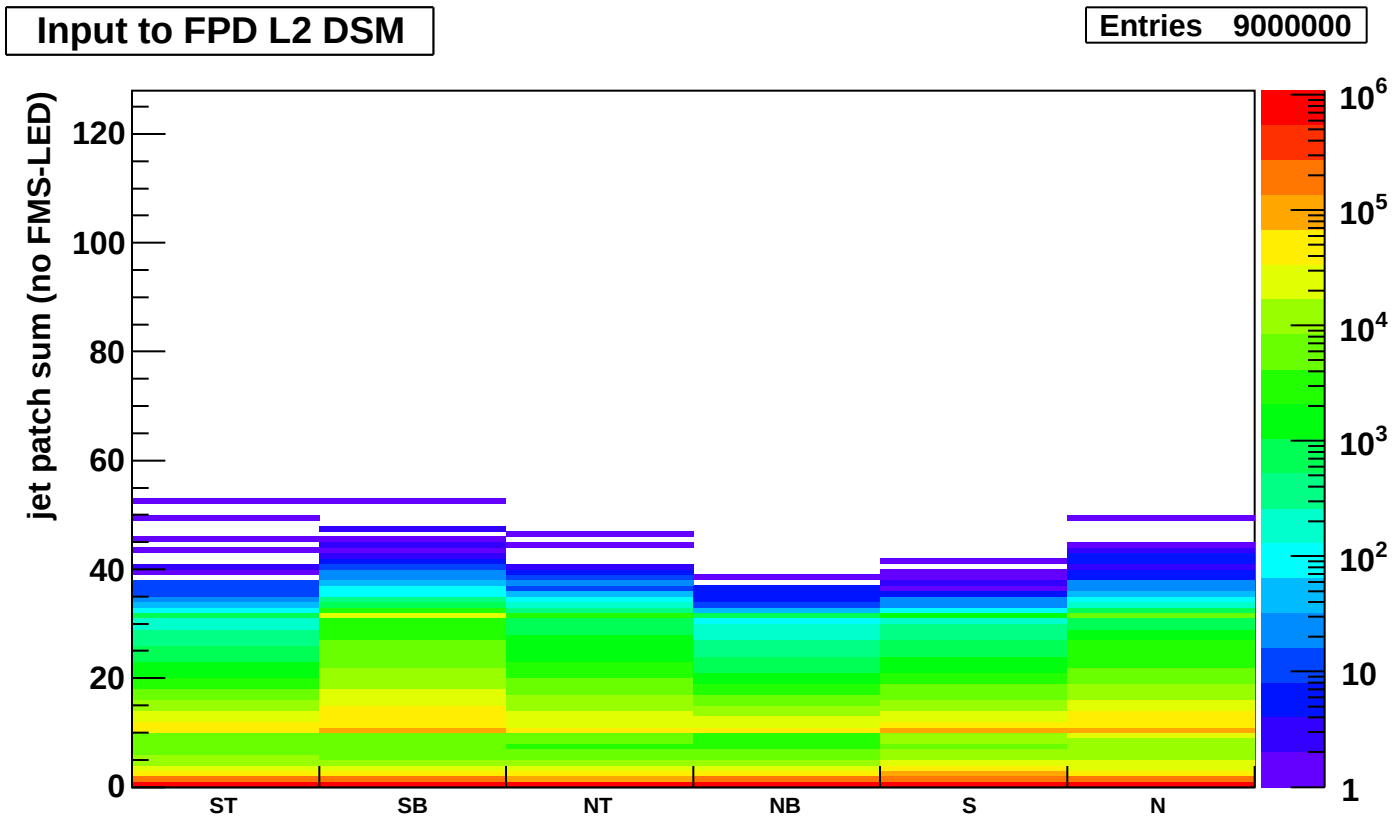

Input to FPD L2 DSM
Entries 2223
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

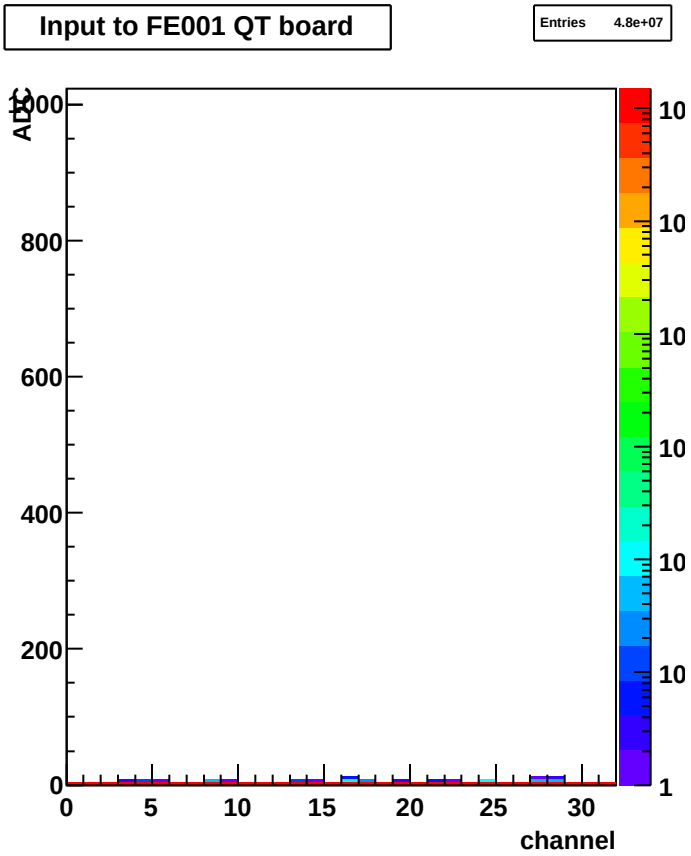

Input to FPD L2 DSM
Entries 4500000
CL bits
4093
9012
290740
100045
119208
217860
142405
125278
991400
1.25346e+06
1.2465e+06
SMALL
LARGE-S
LARGE-N

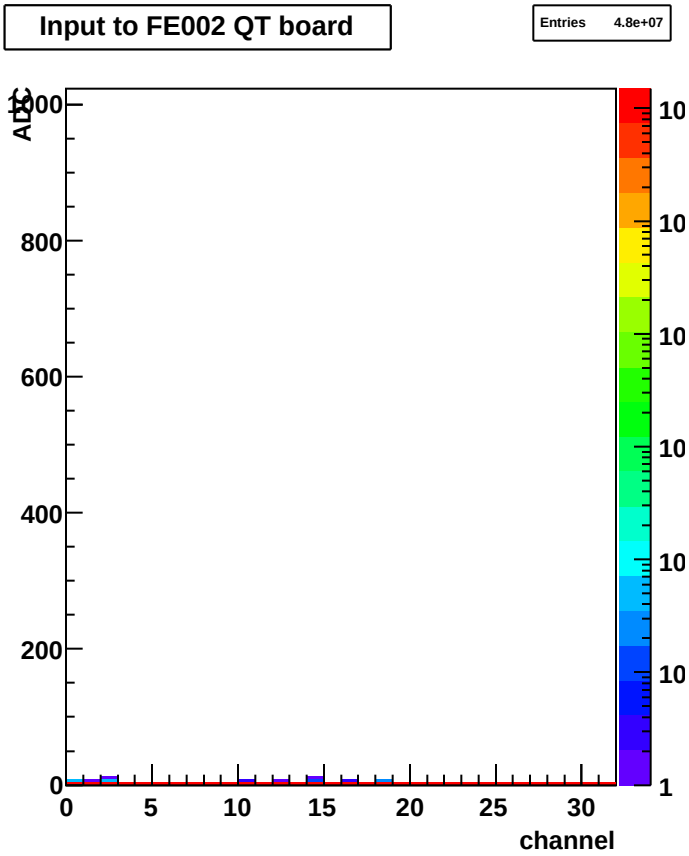

Input to FPD L2 DSM
Entries 699
CL bits - simulated
1
4
180
54
66
11
124
65
82
1
111
SMALL
LARGE-S
LARGE-N

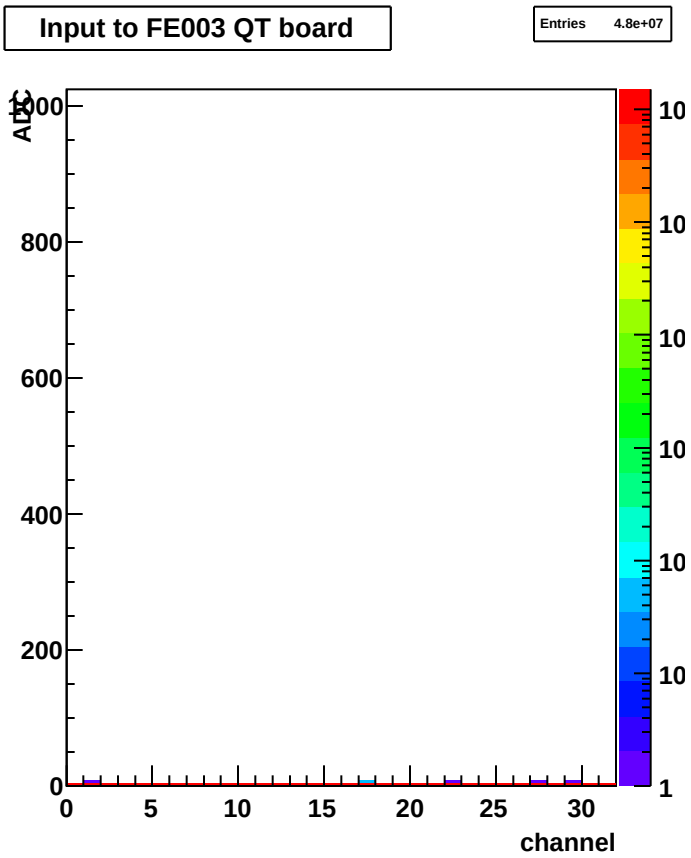

Input to FPD L2 DSM
Entries 4500000
HT bits
23122
139726
149140
51719
102666
101893
80101
1.32249e+06
1.25838e+06
1.27076e+06
SMALL
LARGE-S
LARGE-N

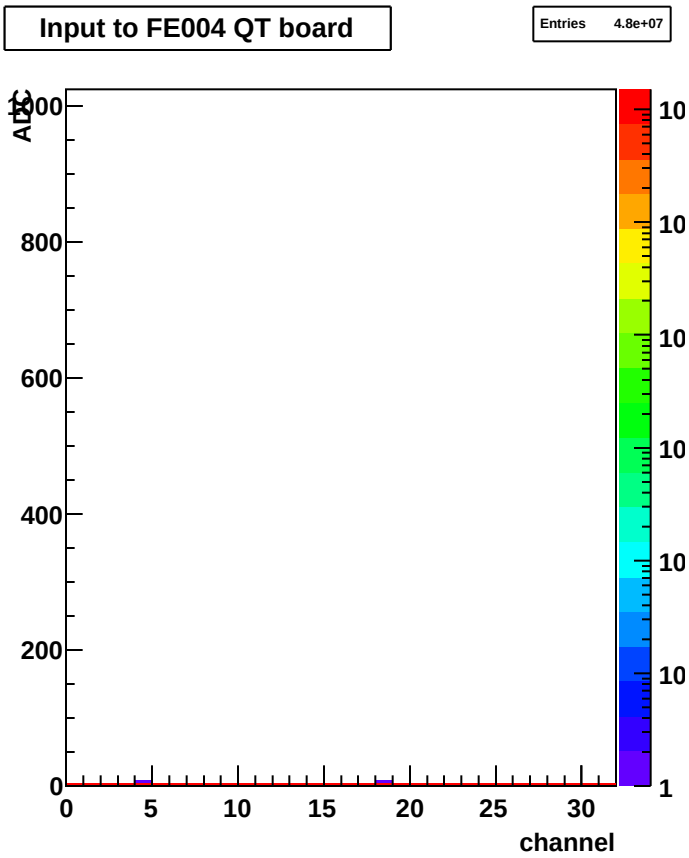

Input to FPD L2 DSM
Entries 332
HT bits - simulated
11
60
84
23
47
55
42
7
1
1
1
SMALL
LARGE-S
LARGE-N

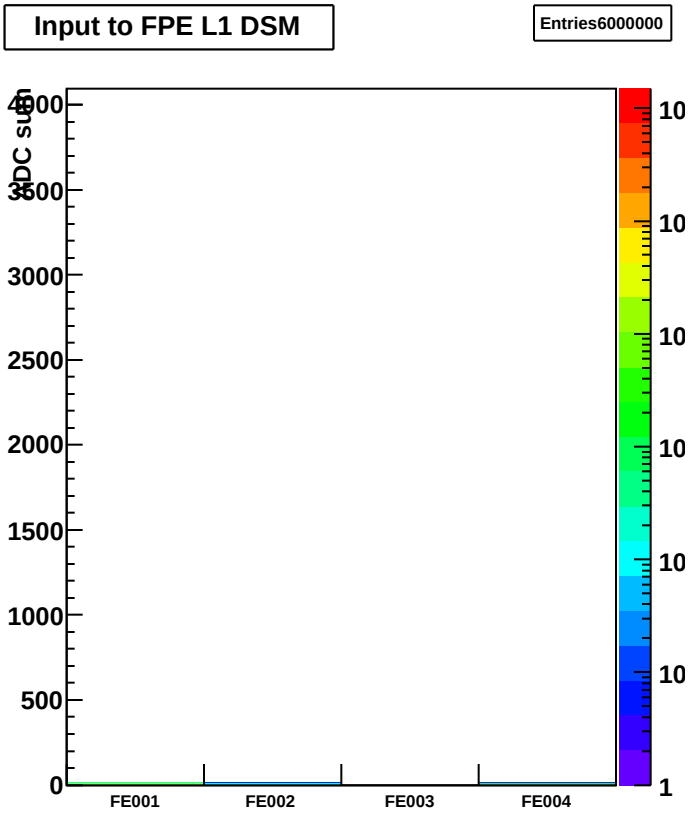

Input to FPD L2 DSM
Entries 9000000
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

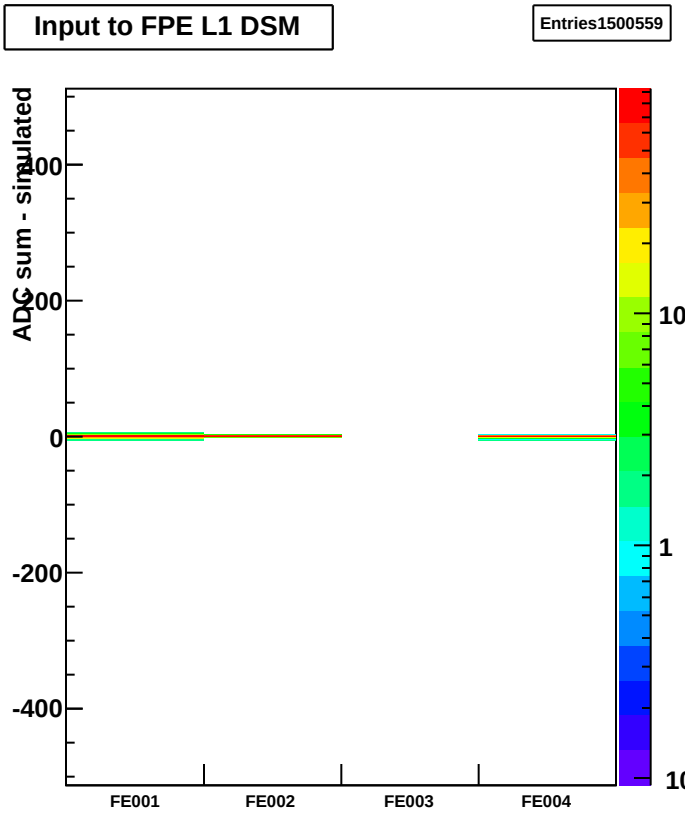

Input to FE001 QT board
Entries 4.8e+07
ADC
channel


Input to FE002 QT board
Entries 4.8e+07
ADC
channel

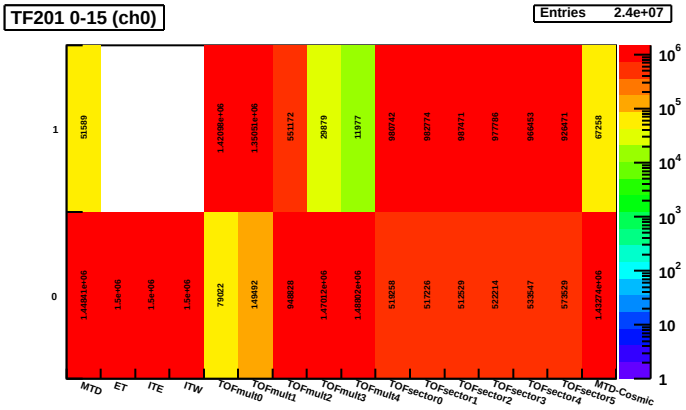

Input to FE003 QT board
Entries 4.8e+07
ADC
channel


Input to FE004 QT board
Entries 4.8e+07
ADC
channel

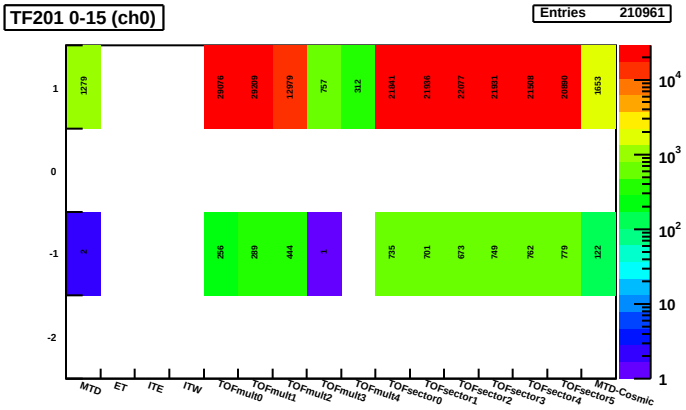

Input to FPE L1 DSM
Entries6000000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries1500559
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
1

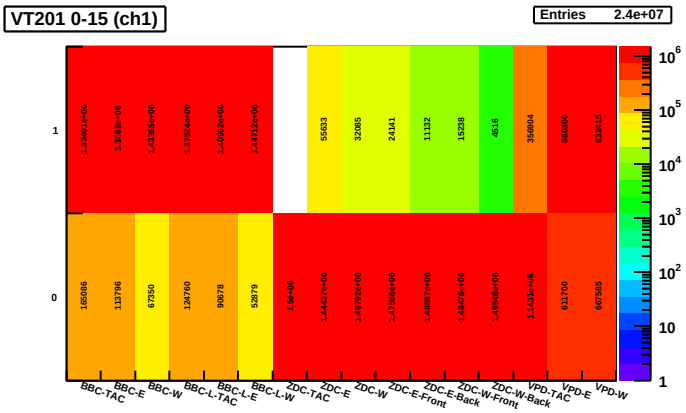
TF201 0-15 (ch0)



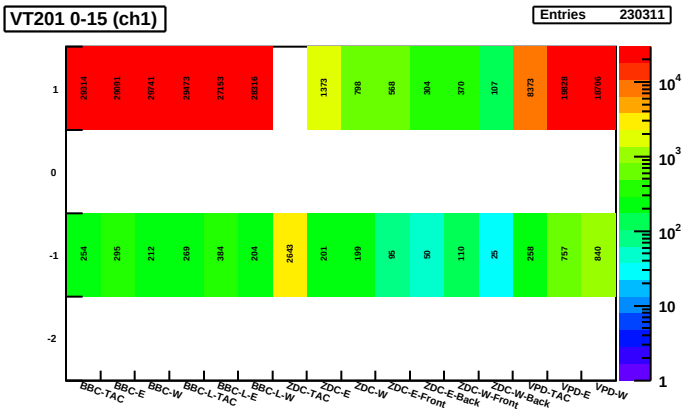
TF201 0-15 (ch0)



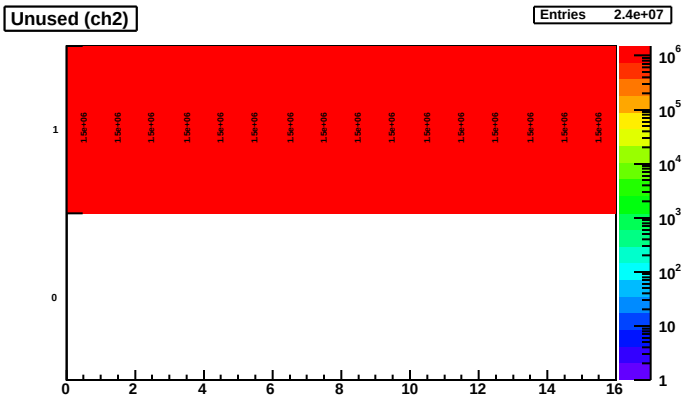
VT201 0-15 (ch1)

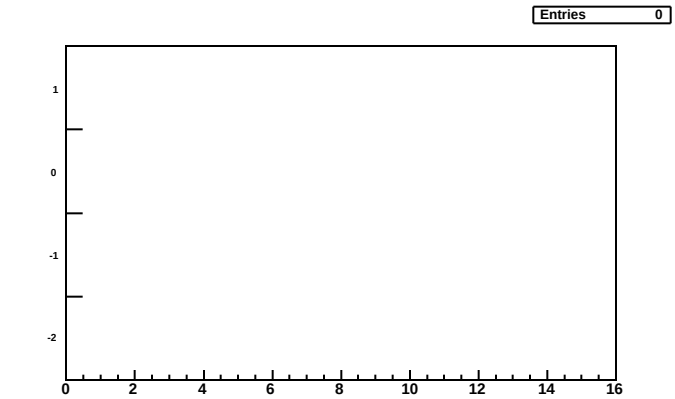


VT201 0-15 (ch1)

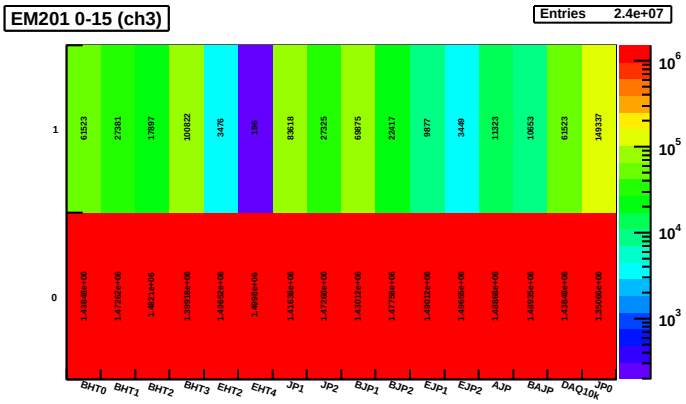


Unused (ch2)





EM201 0-15 (ch3)



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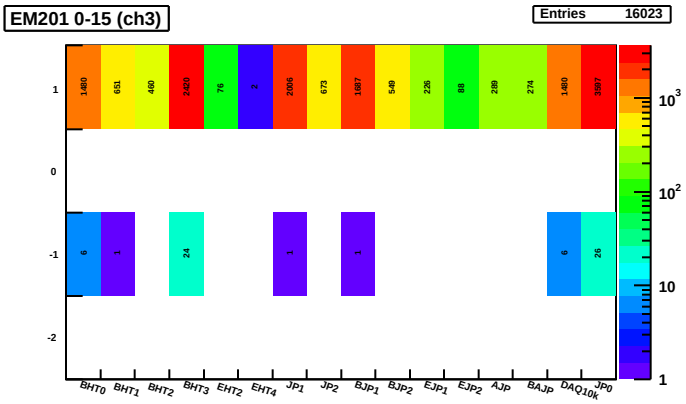
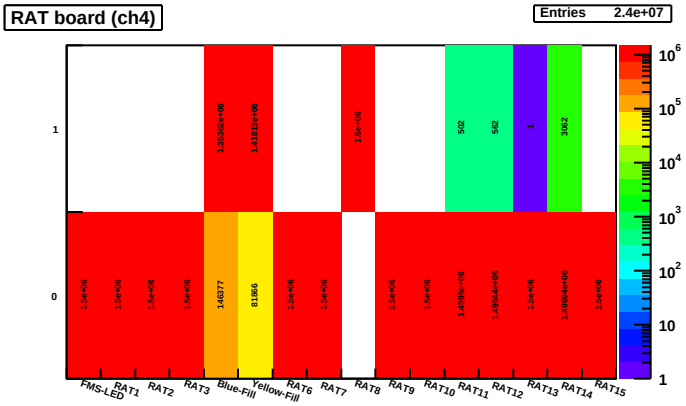
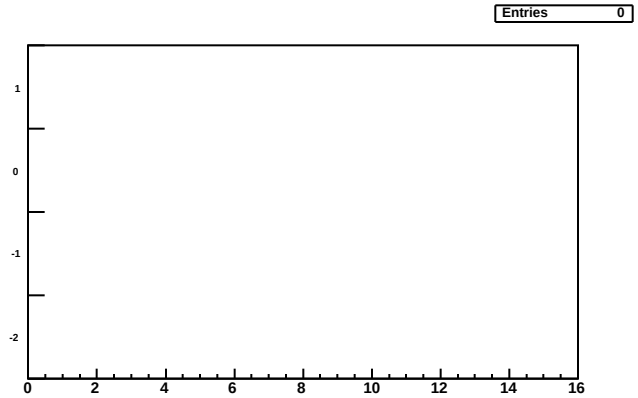
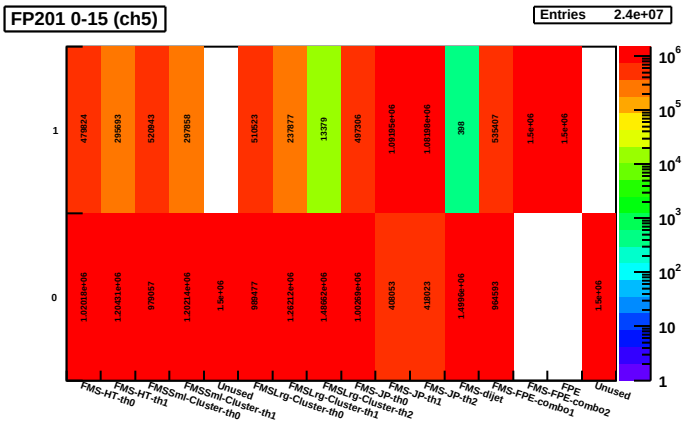


RAT board (ch4)





FP201 0-15 (ch5)



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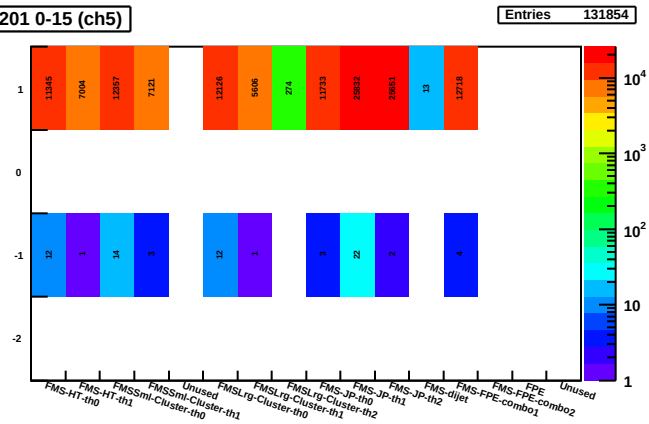

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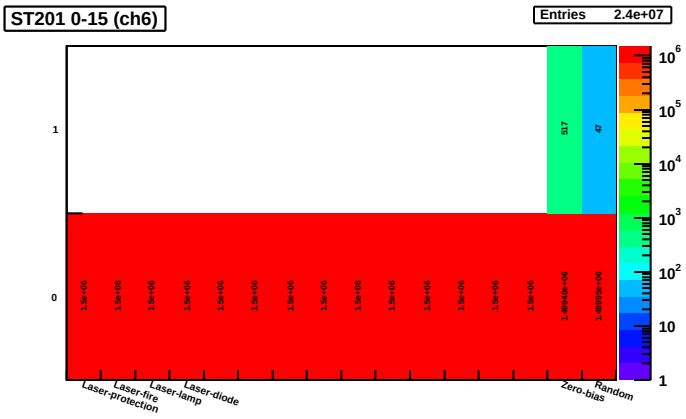

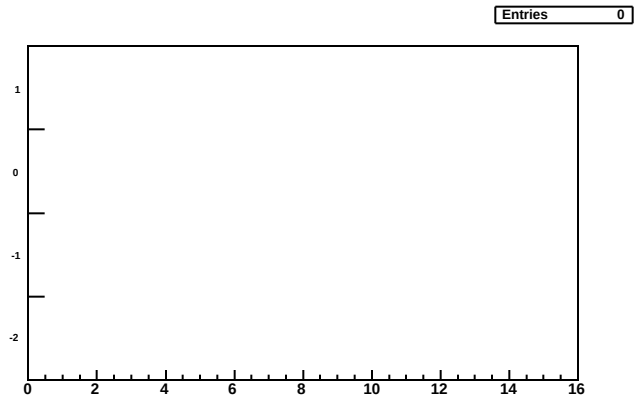
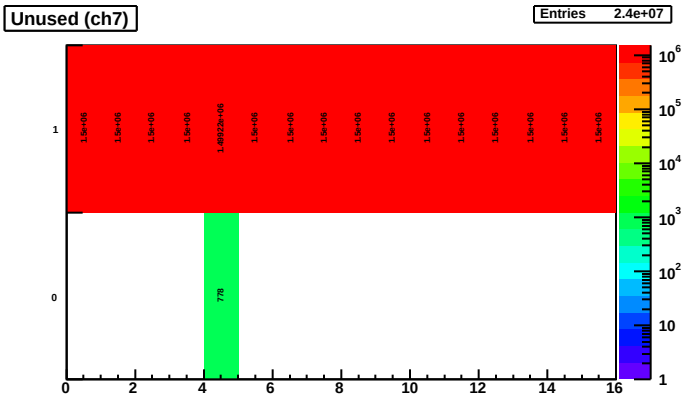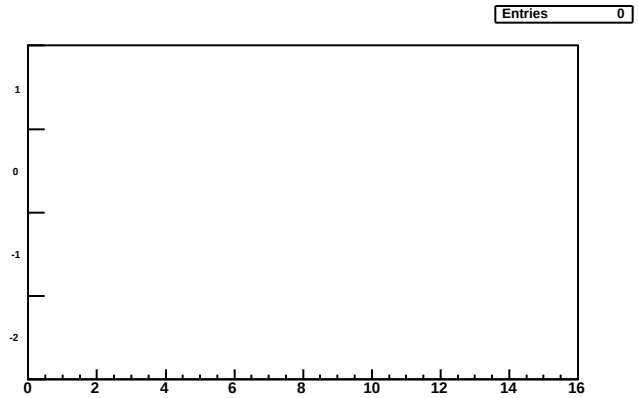
Unused (ch7)