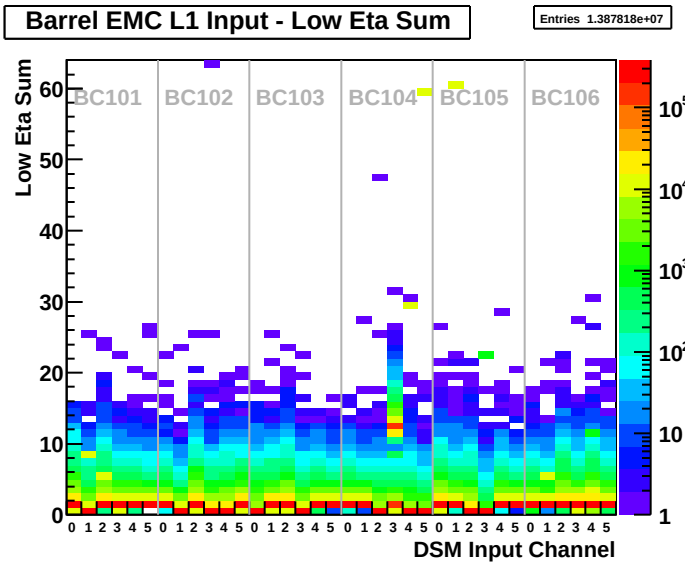
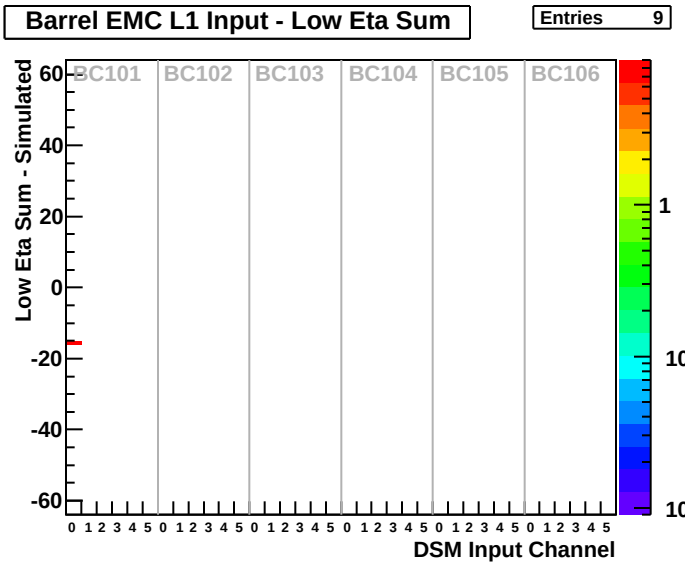
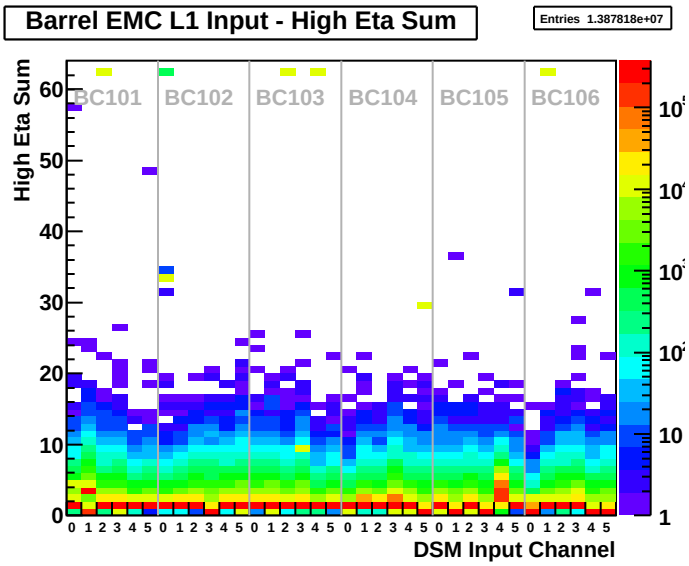
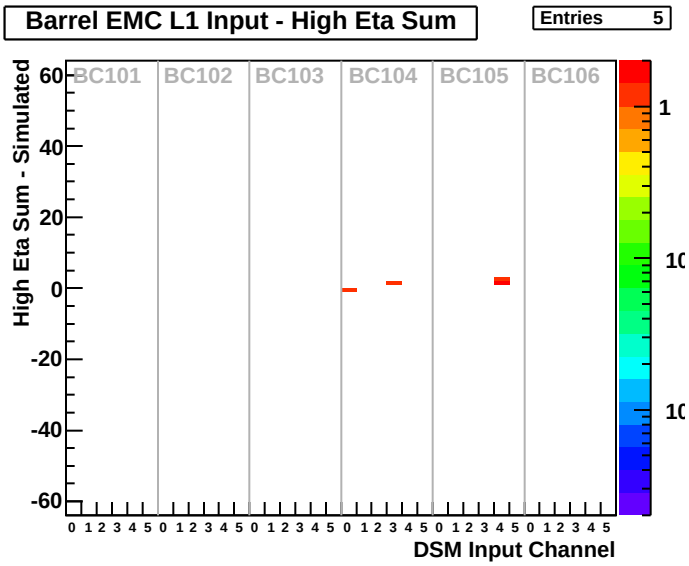
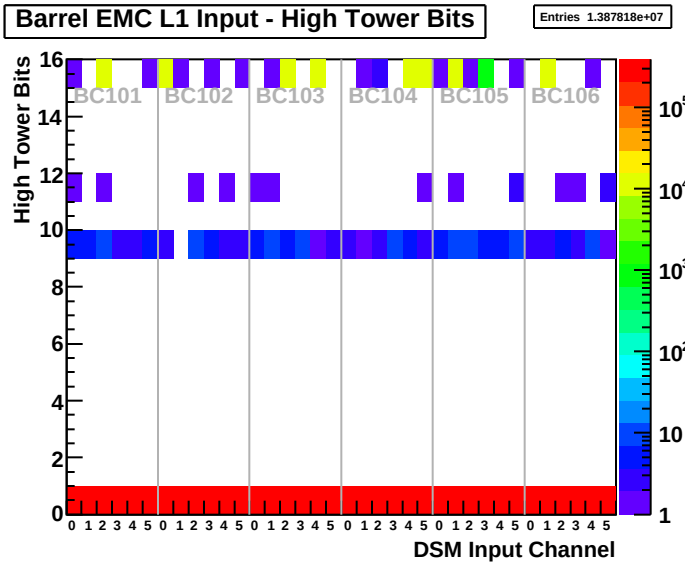
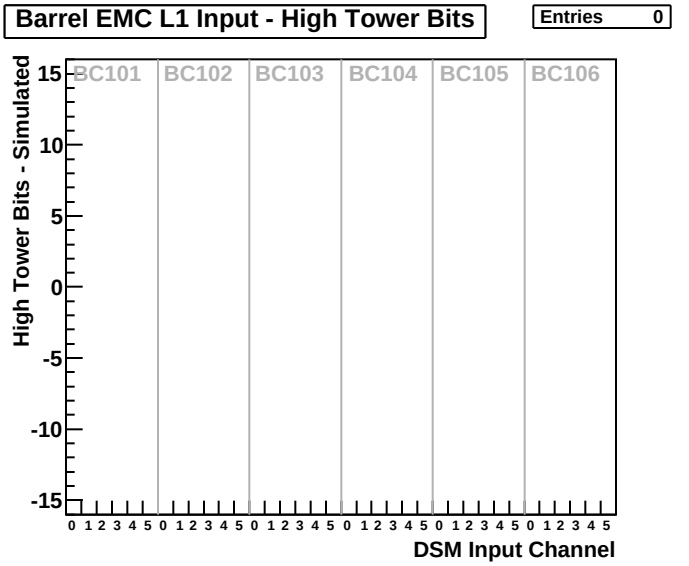

Barrel EMC L1 Input - Low Eta Sum
Entries 1.387818e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

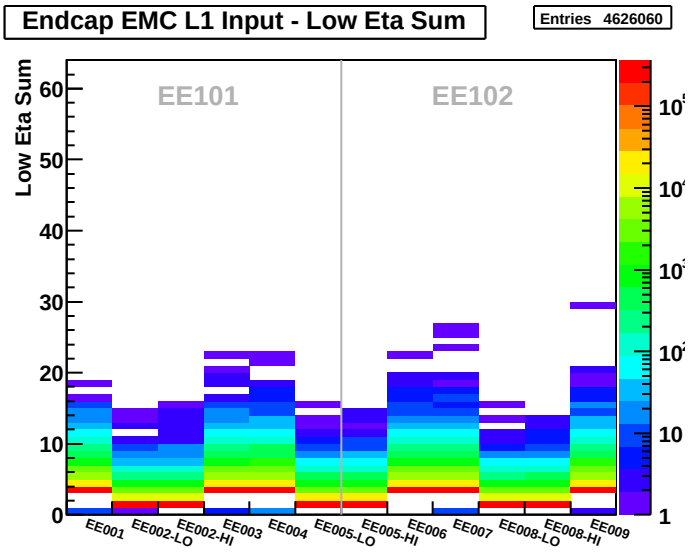

Barrel EMC L1 Input - Low Eta Sum
Entries 9
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

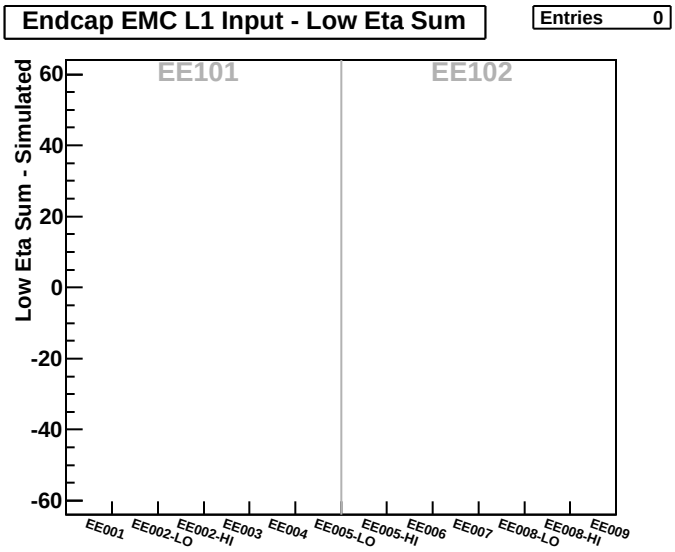

Barrel EMC L1 Input - High Eta Sum
Entries 1.387818e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

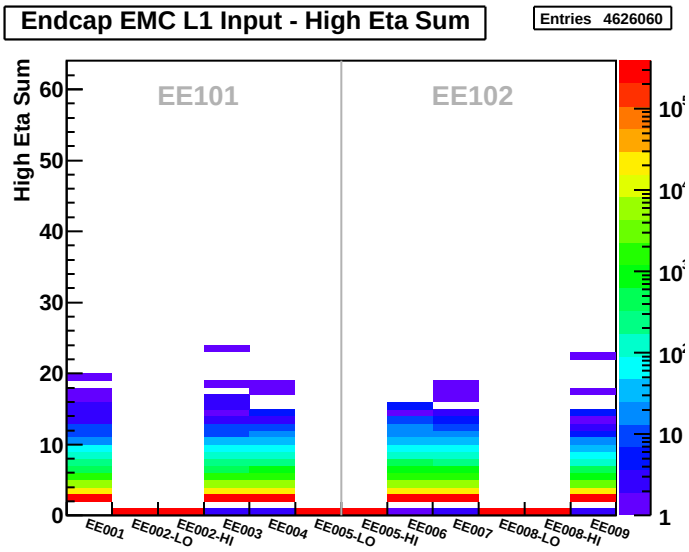

Barrel EMC L1 Input - High Eta Sum
Entries 5
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

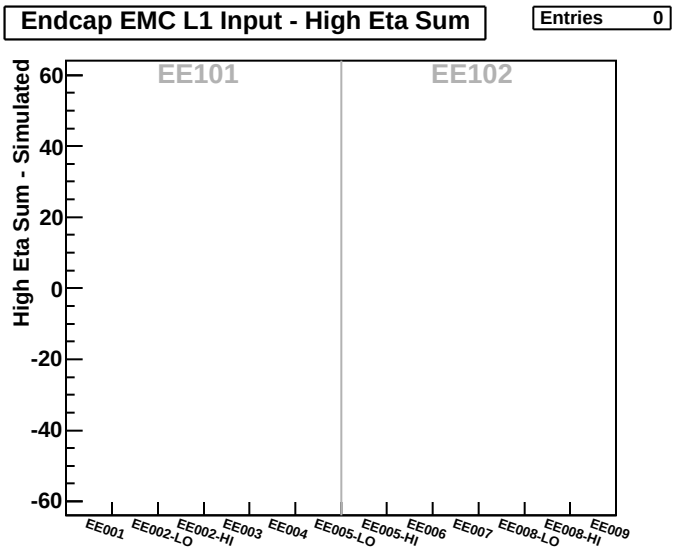

Barrel EMC L1 Input - High Tower Bits
Entries 1.387818e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

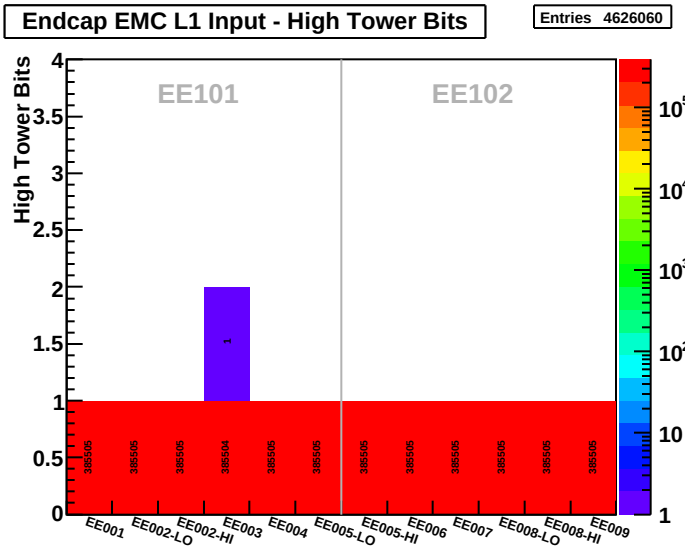

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

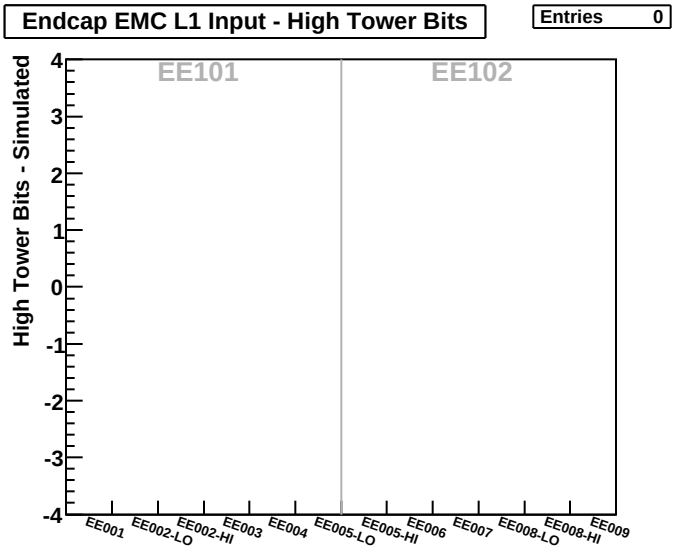

Endcap EMC L1 Input - Low Eta Sum
Entries 4626060
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

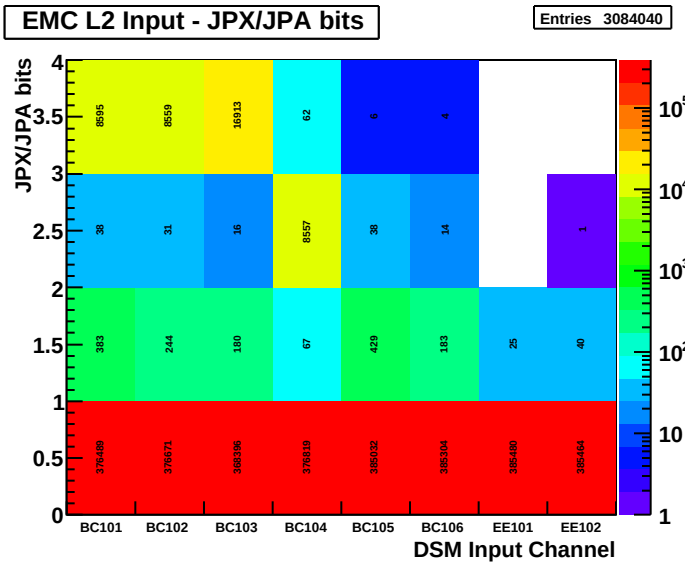

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

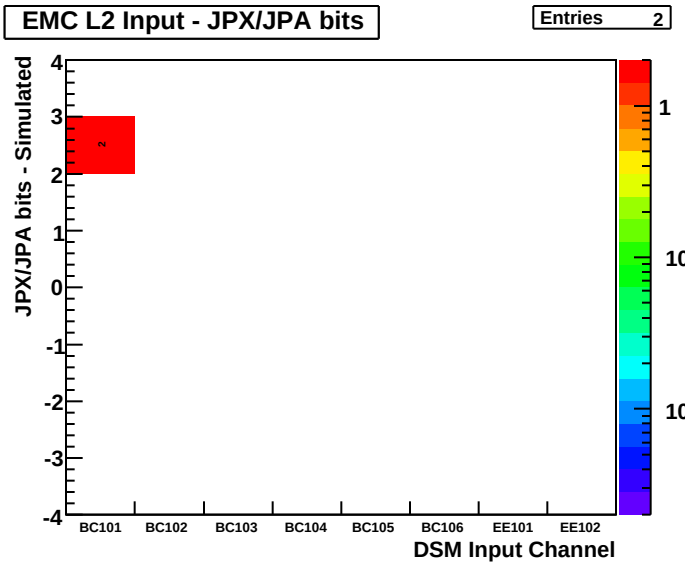

Endcap EMC L1 Input - High Eta Sum
Entries 4626060
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

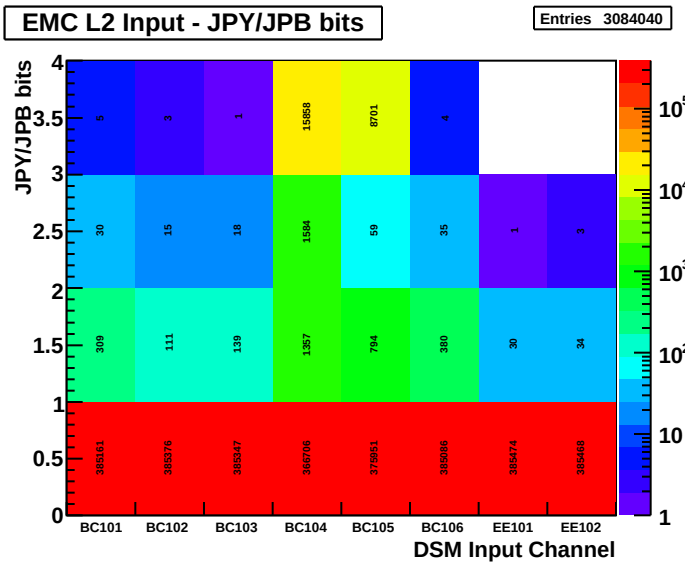

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

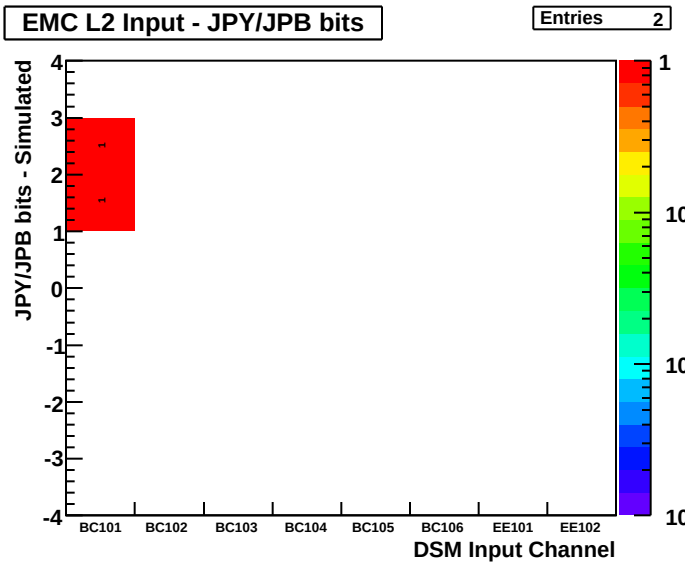

Endcap EMC L1 Input - High Tower Bits
Entries 4626060
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

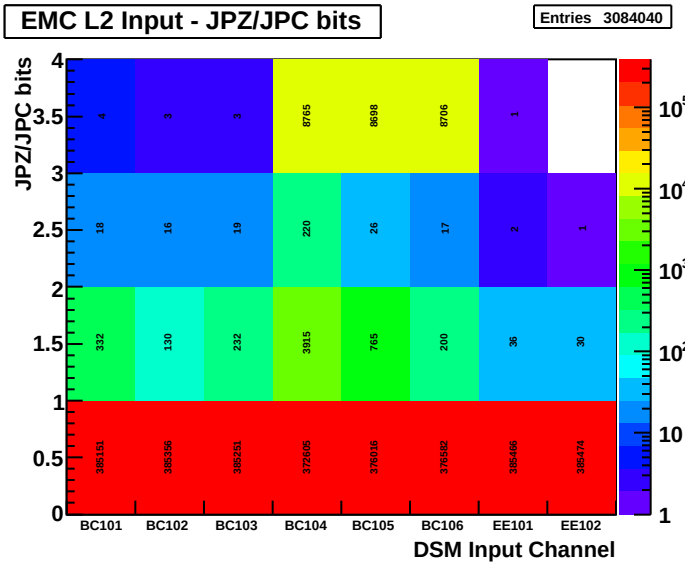

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

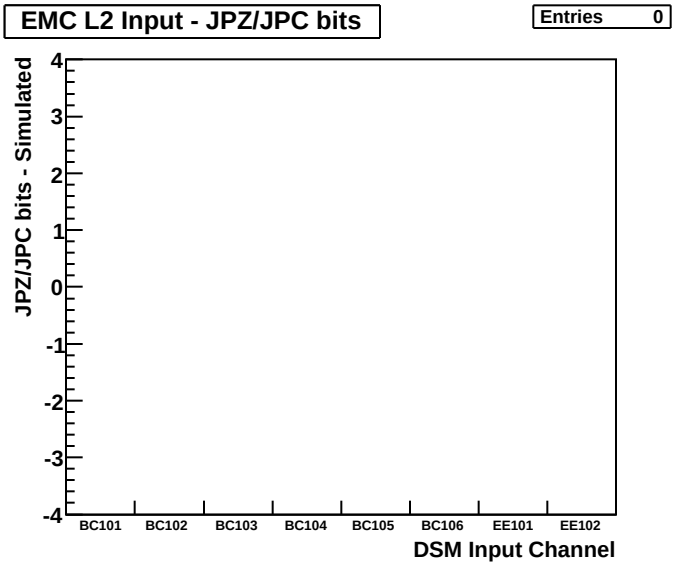

EMC L2 Input - JPX/JPA bits
Entries 3084040
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

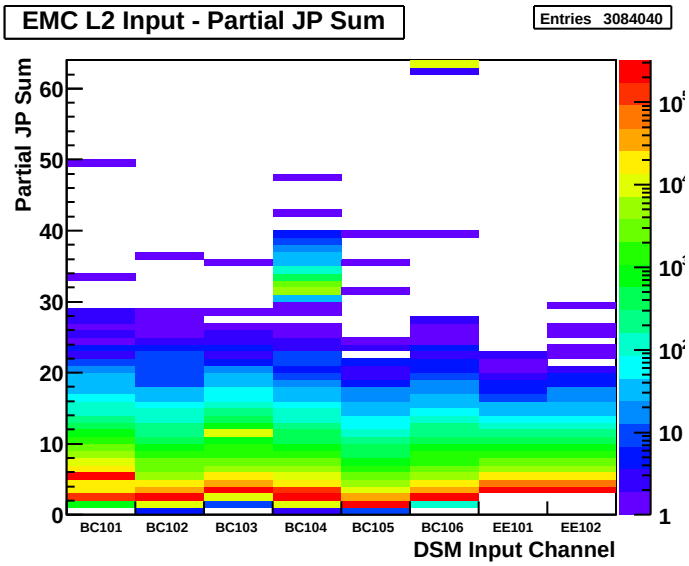

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

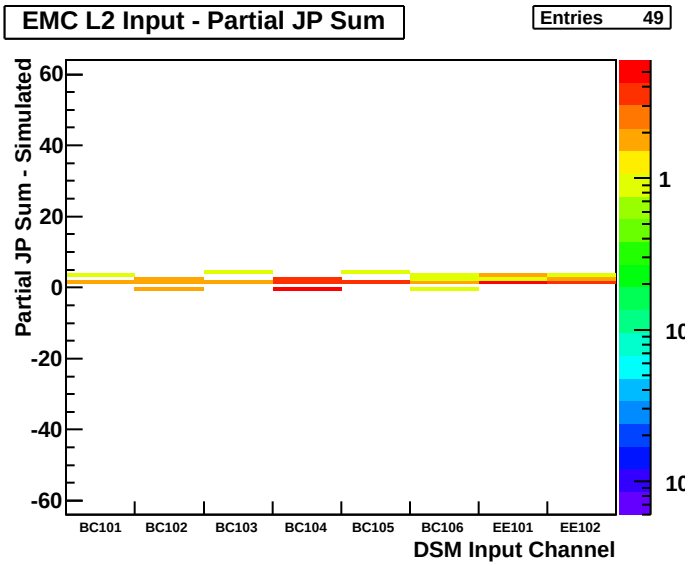

EMC L2 Input - JPY/JPB bits
Entries 3084040
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

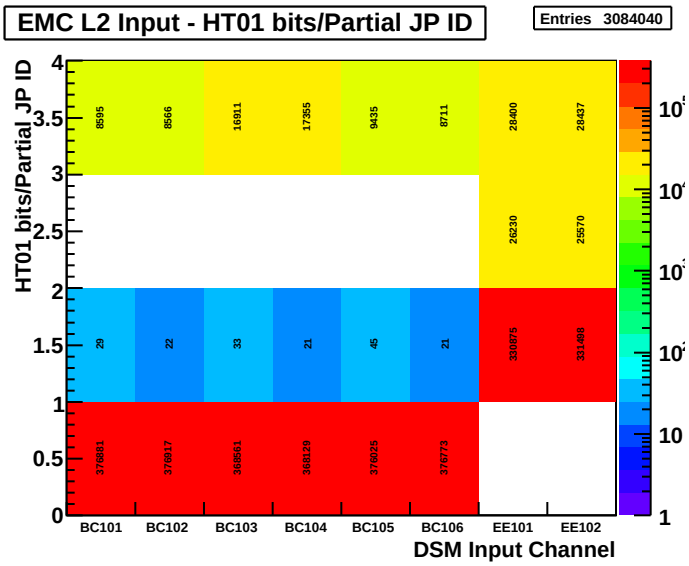

EMC L2 Input - JPY/JPB bits
Entries 2
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

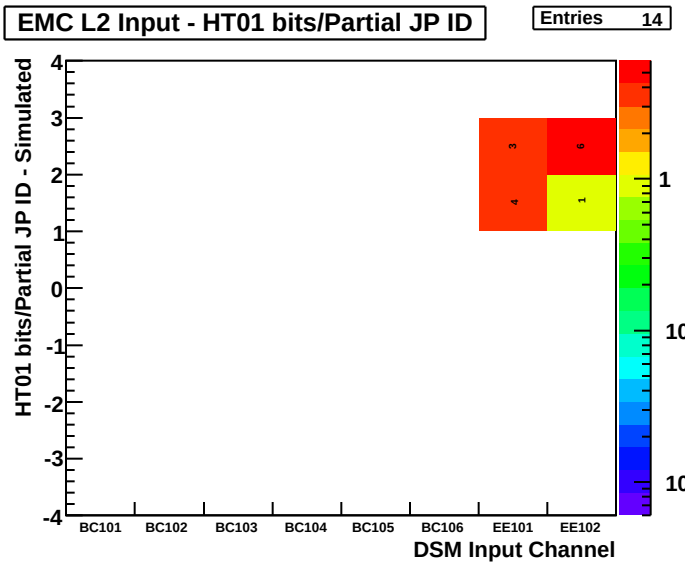

EMC L2 Input - JPZ/JPC bits
Entries 3084040
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

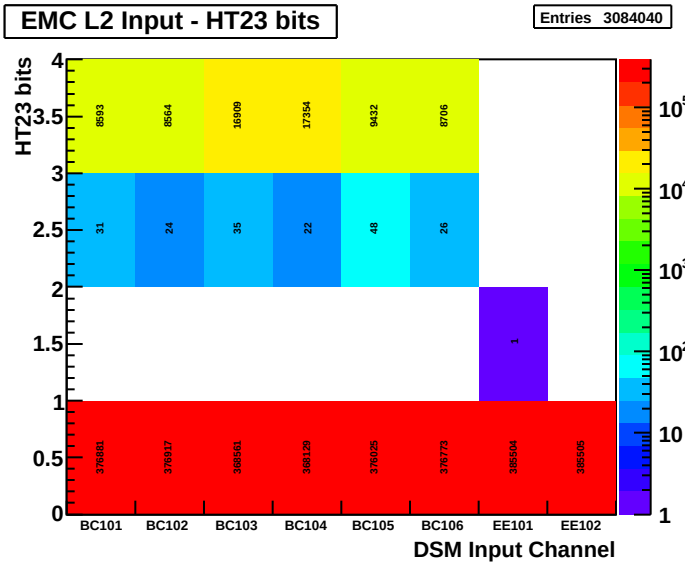

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

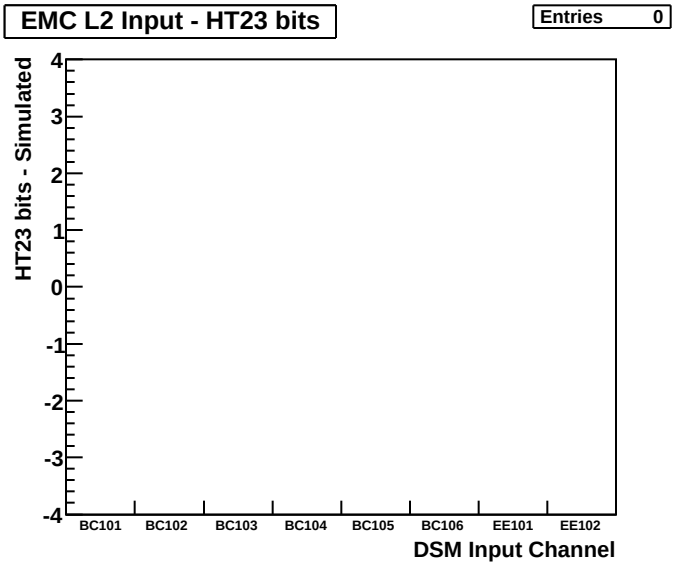

EMC L2 Input - Partial JP Sum
Entries 3084040
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

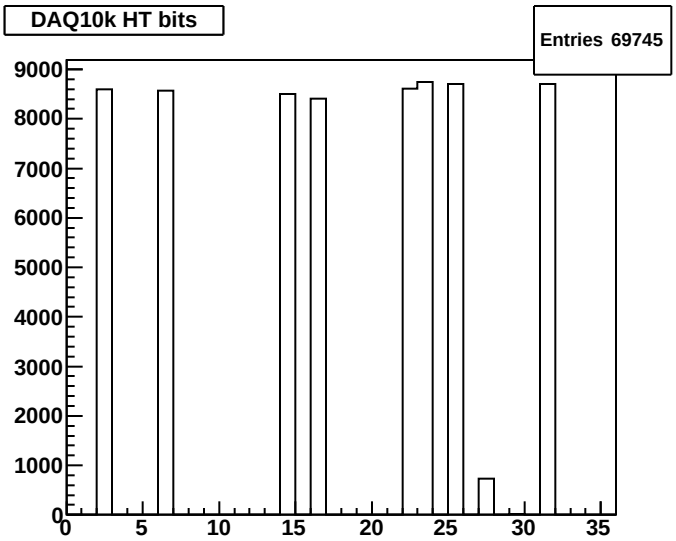

EMC L2 Input - Partial JP Sum
Entries 49
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

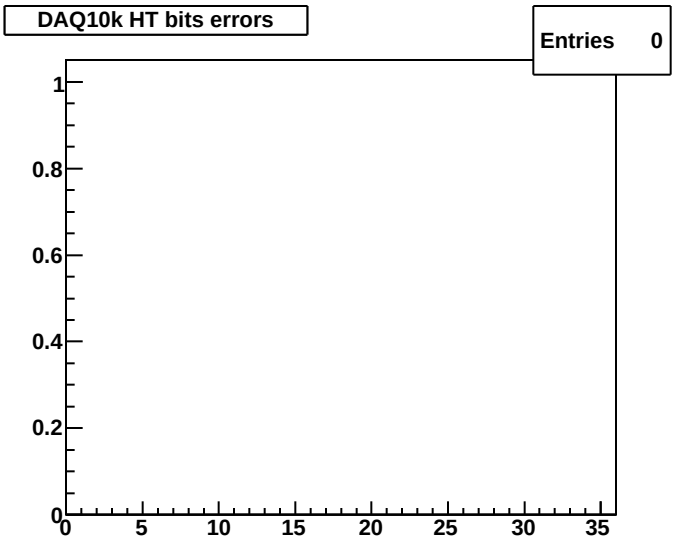

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3084040
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

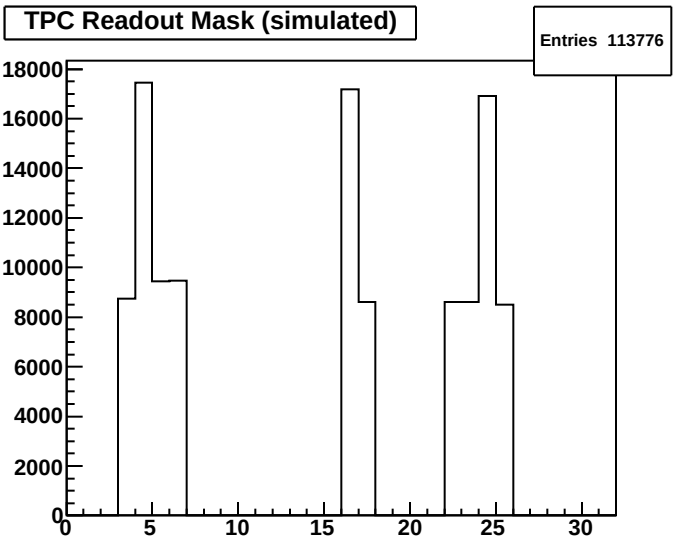

EMC L2 Input - HT01 bits/Partial JP ID
Entries 14
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

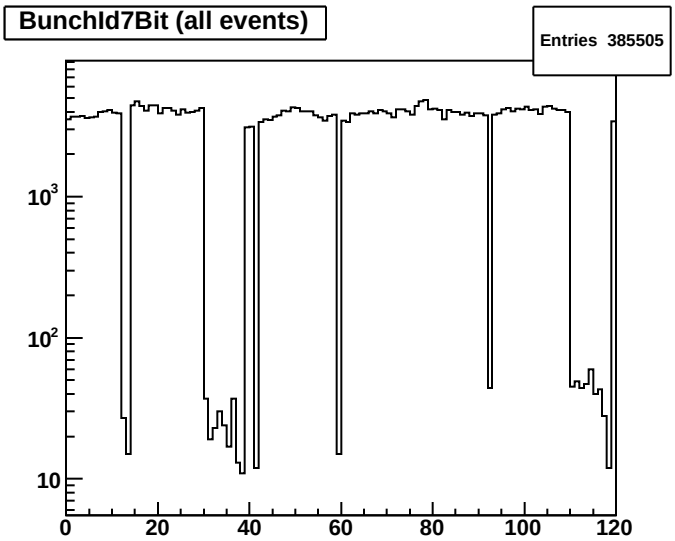

EMC L2 Input - HT23 bits
Entries 3084040
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

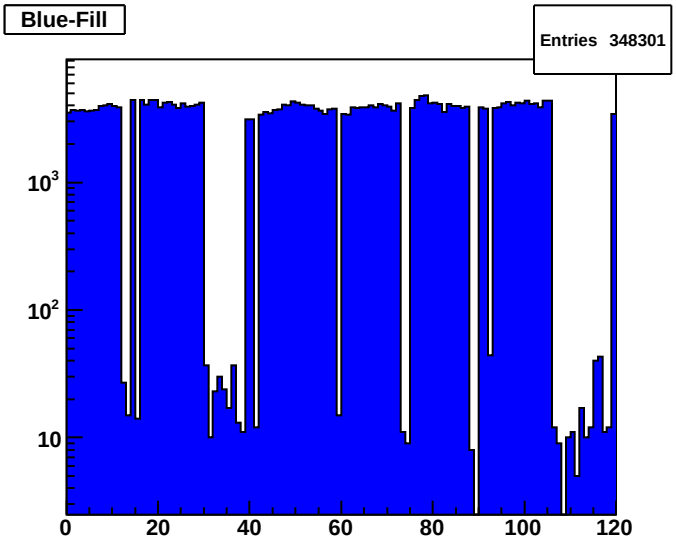

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

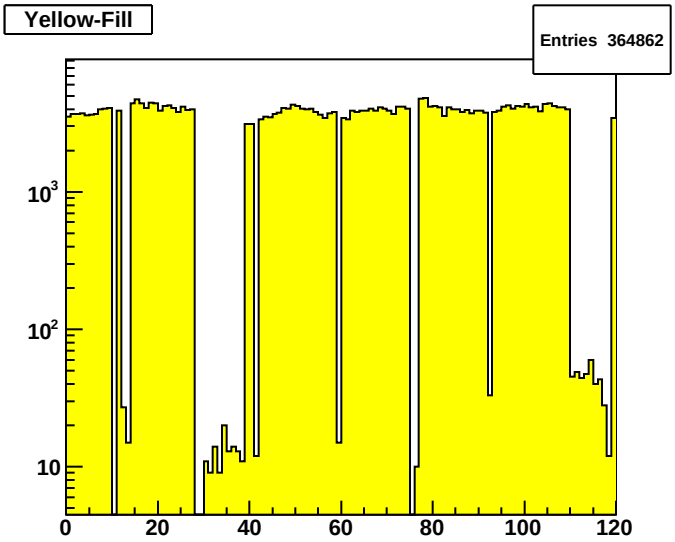

DAQ10k HT bits
Entries 69745

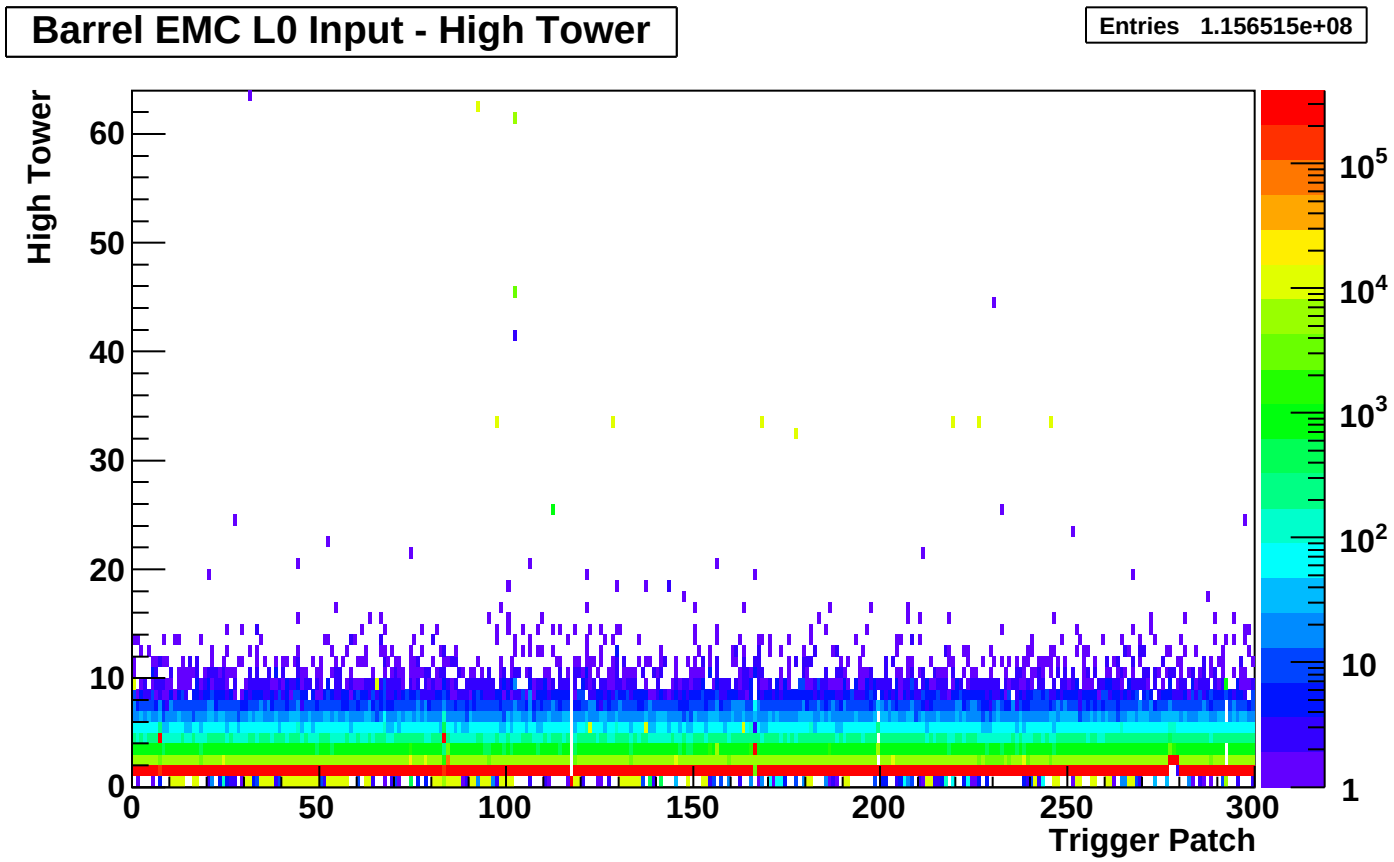

DAQ10k HT bits errors
Entries 0

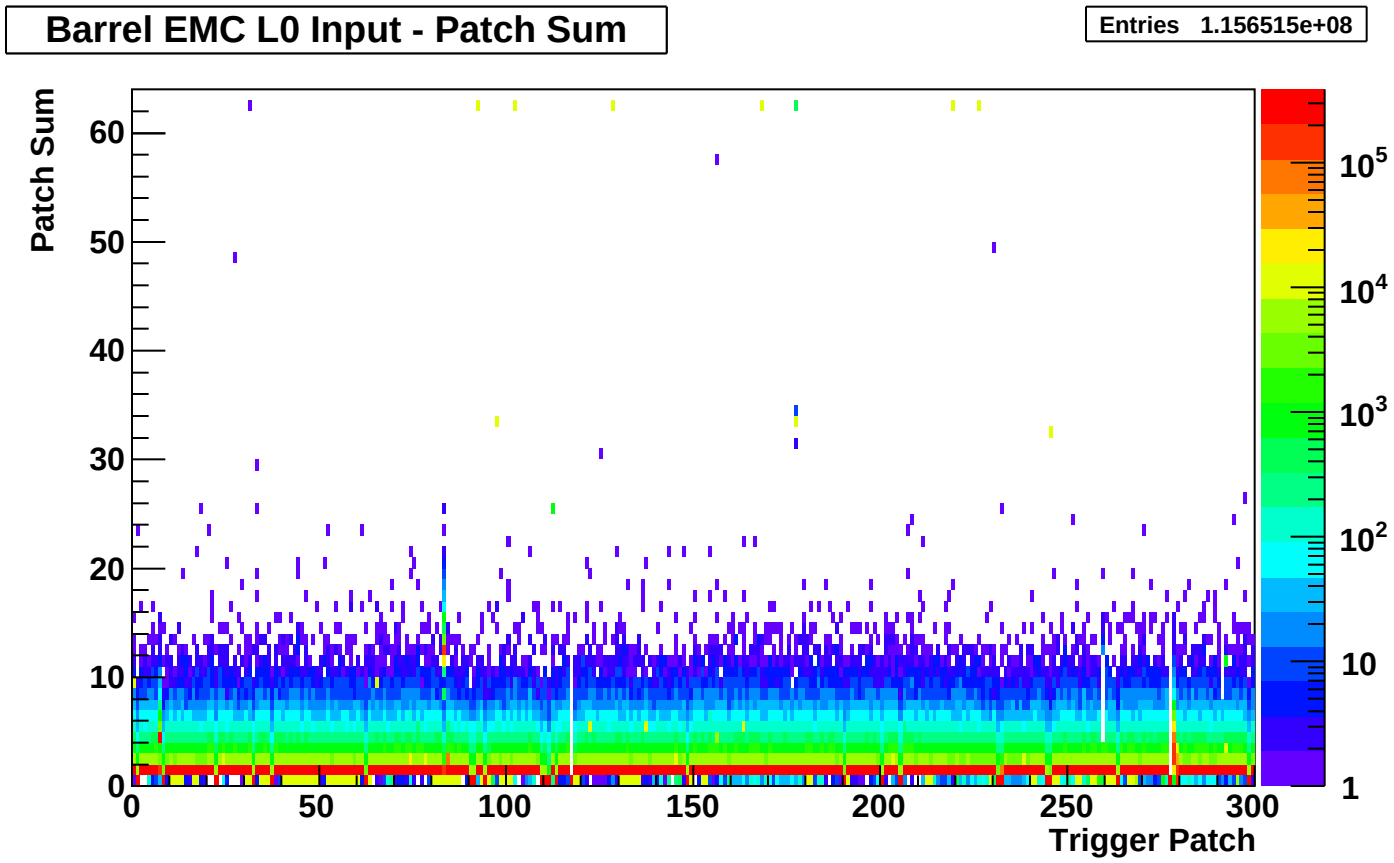

TPC Readout Mask (simulated)
Entries 113776

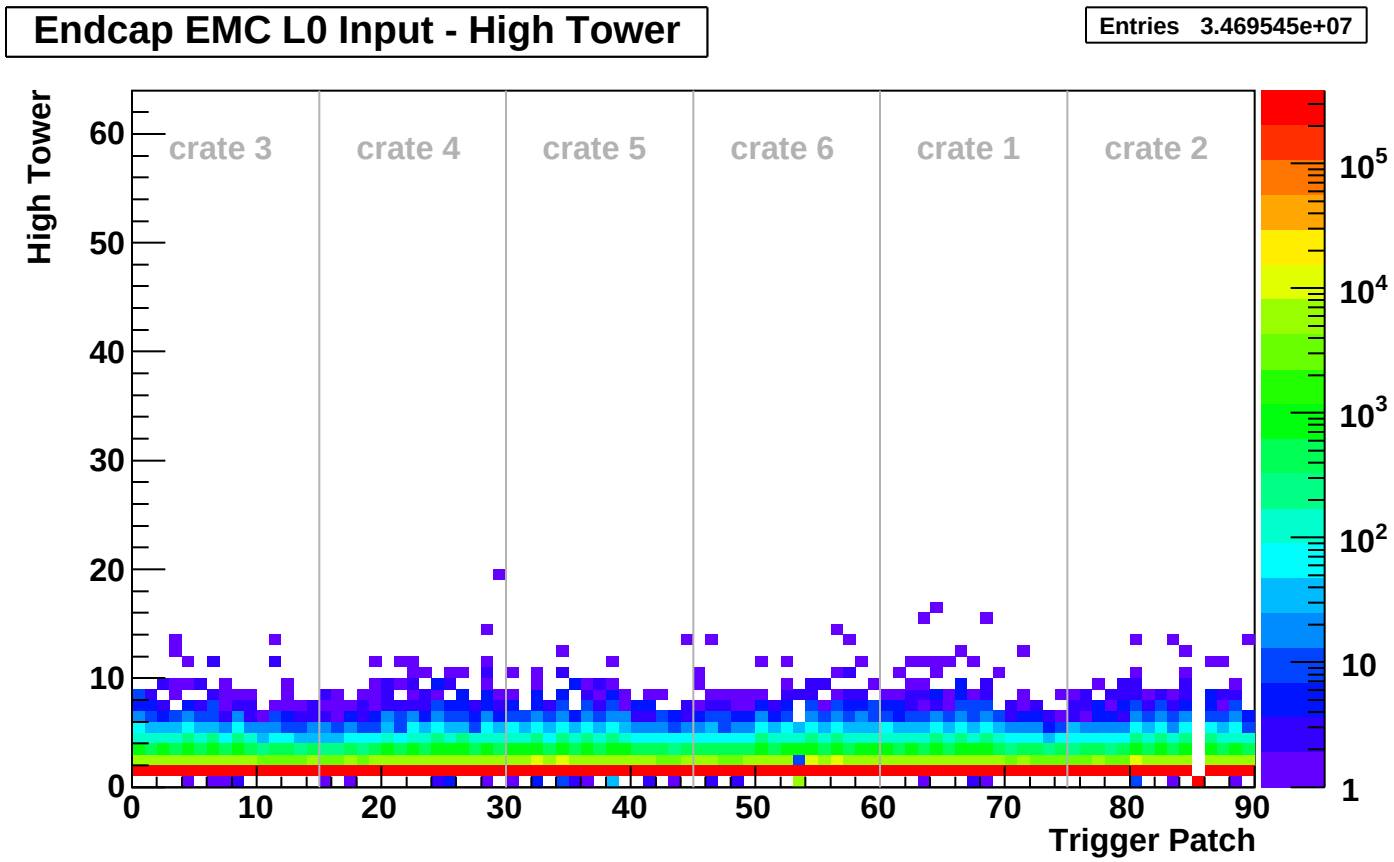

BunchId7Bit (all events)
Entries 385505

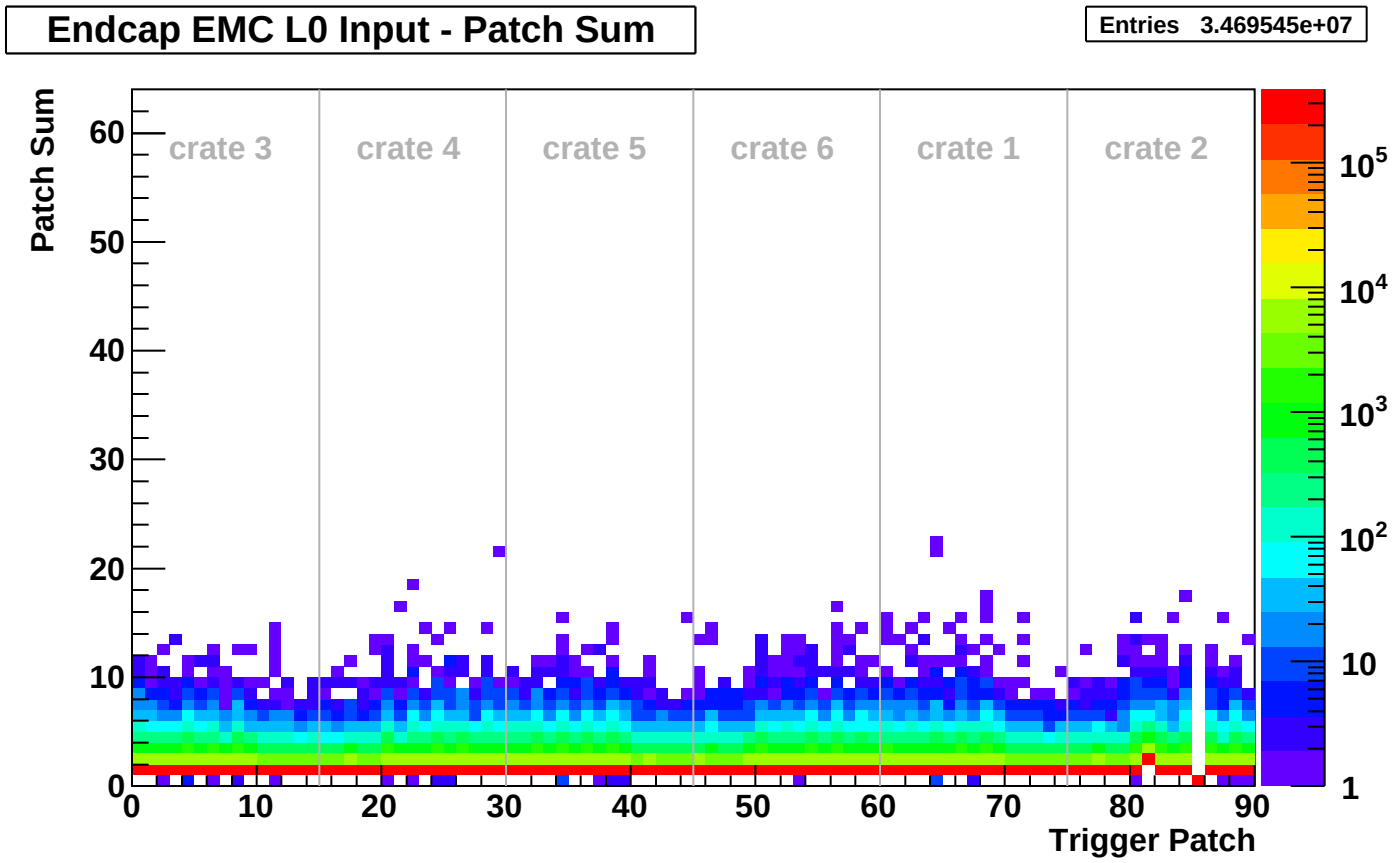

Blue-Fill
Entries 348301

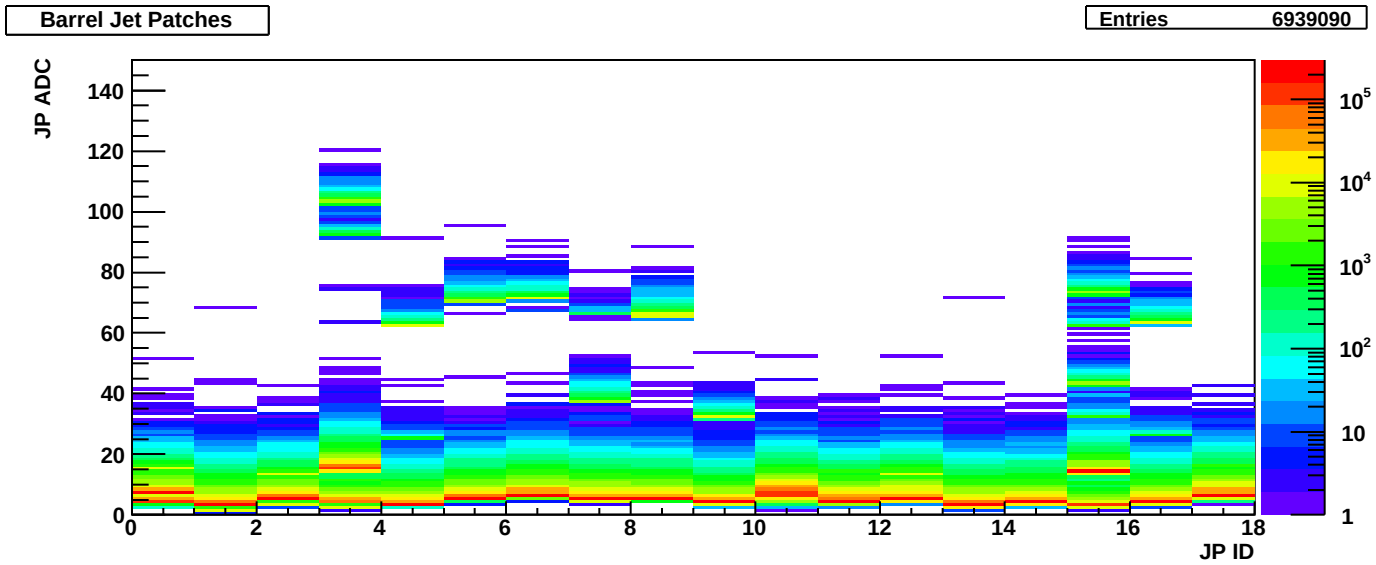

Yellow-Fill
Entries 364862

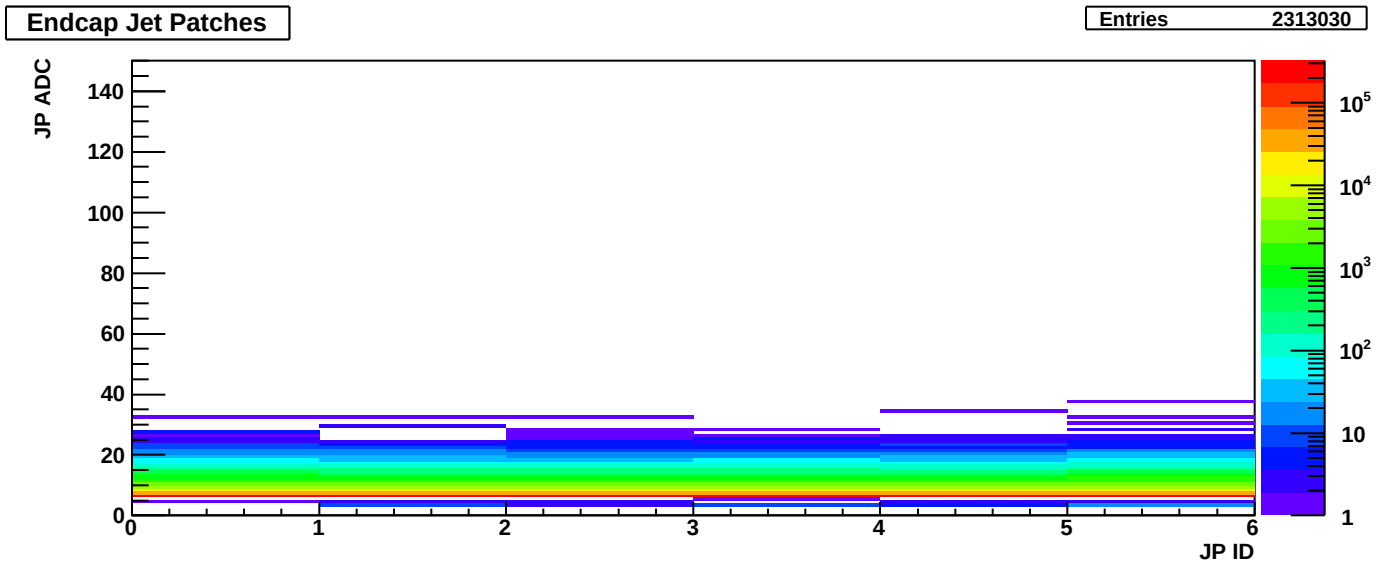

Barrel EMC L0 Input - High Tower
Entries 1.156515e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

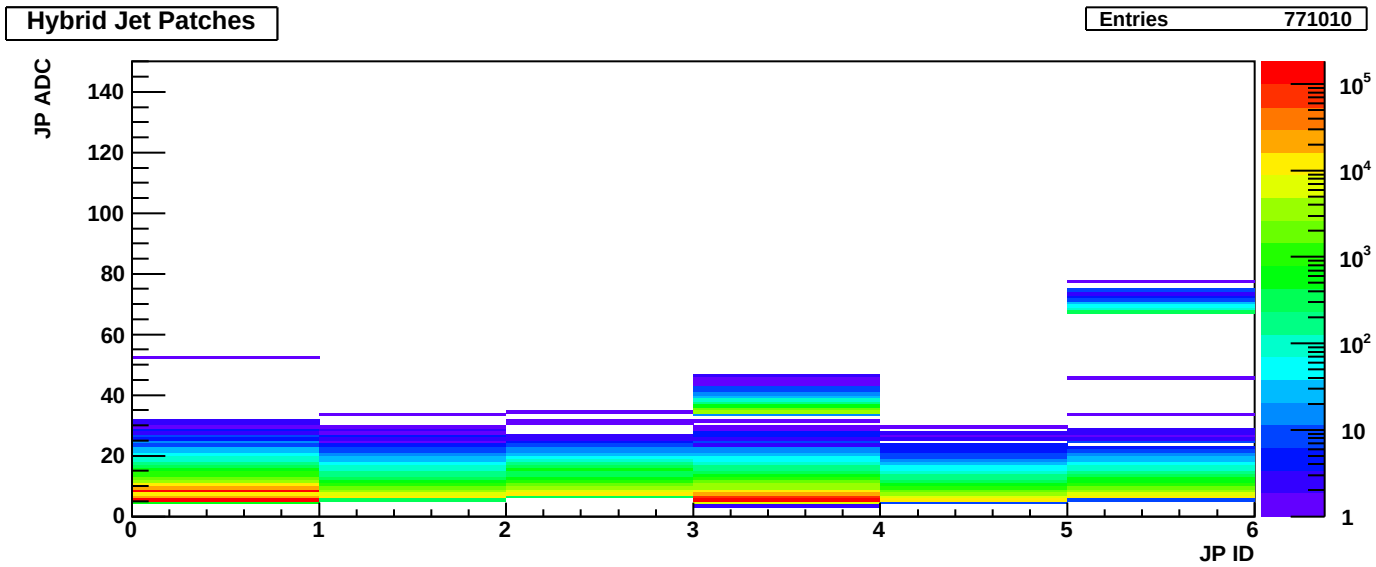

Barrel EMC L0 Input - Patch Sum
Entries 1.156515e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

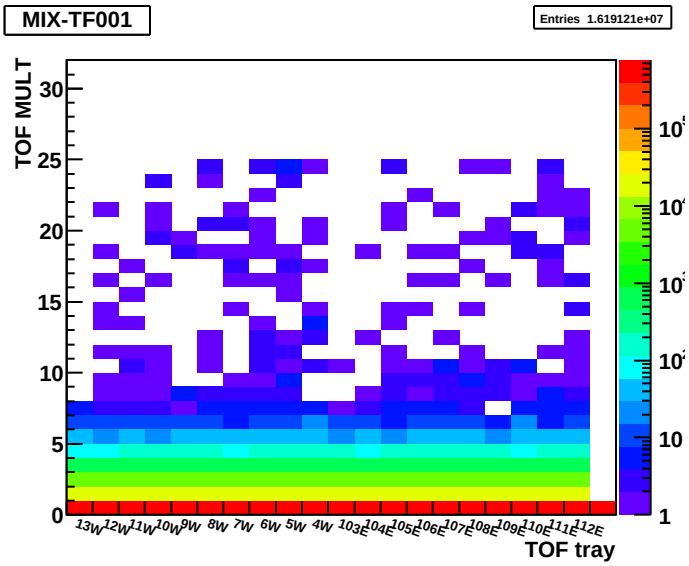

Endcap EMC L0 Input - High Tower
Entries 3.469545e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

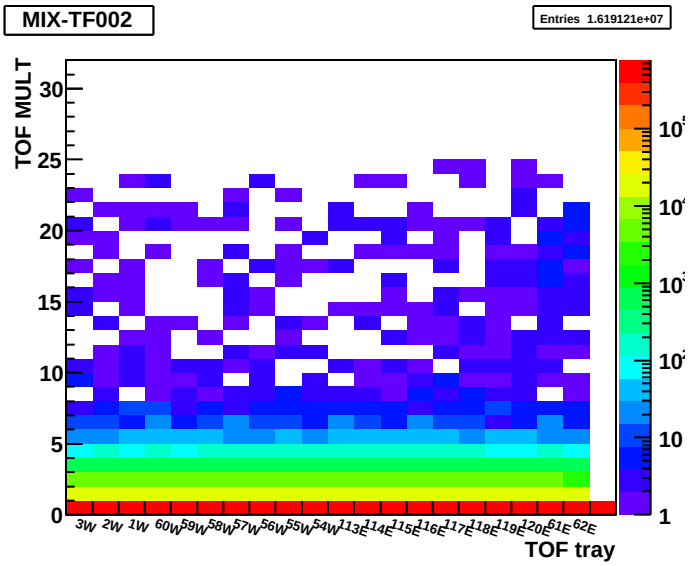

Endcap EMC L0 Input - Patch Sum
Entries 3.469545e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

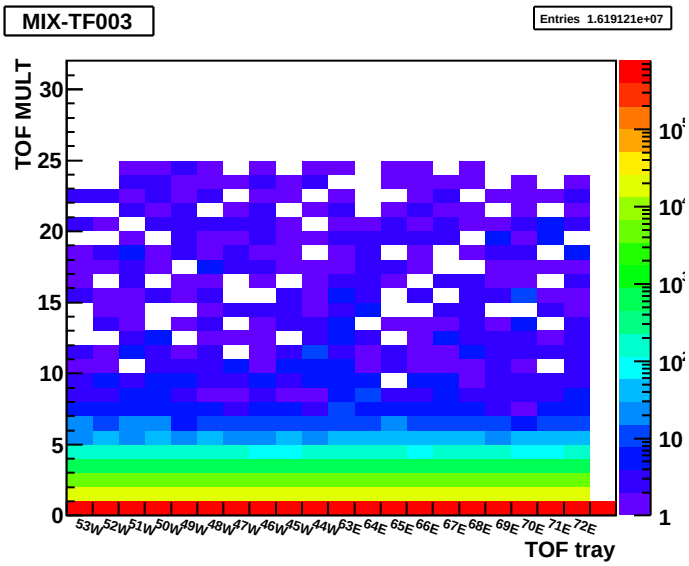

Barrel Jet Patches
Entries 6939090
JP ADC
JP ID

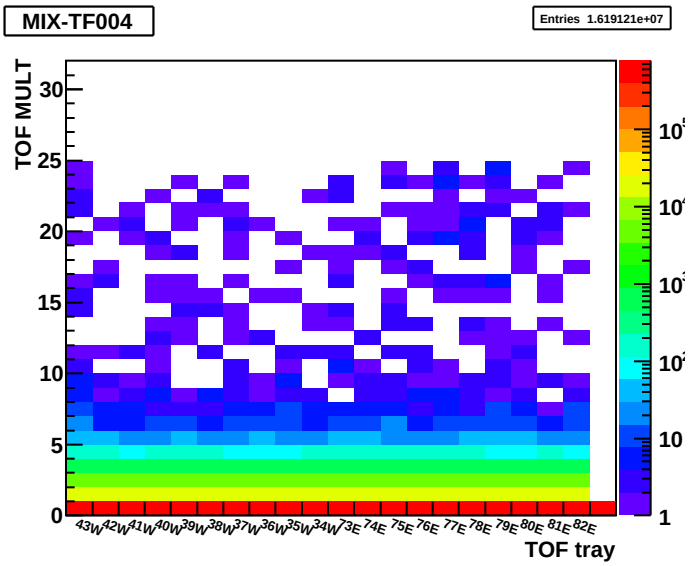

Endcap Jet Patches
Entries 2313030
JP ADC
JP ID

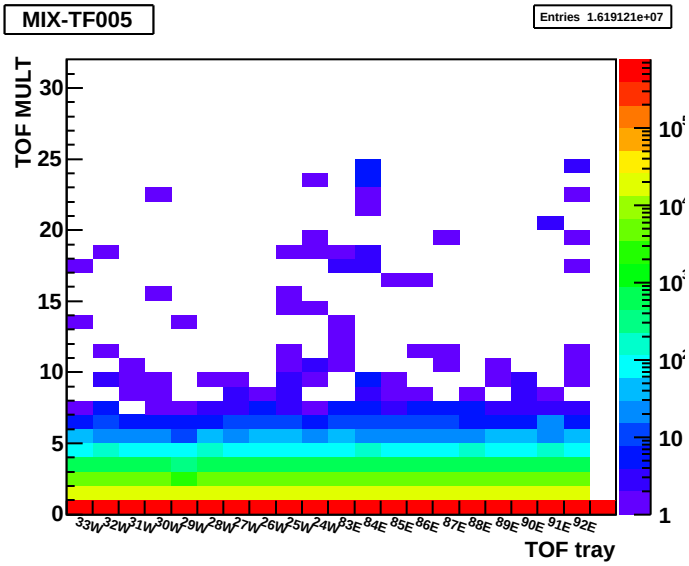

Hybrid Jet Patches
Entries 771010
JP ADC
JP ID

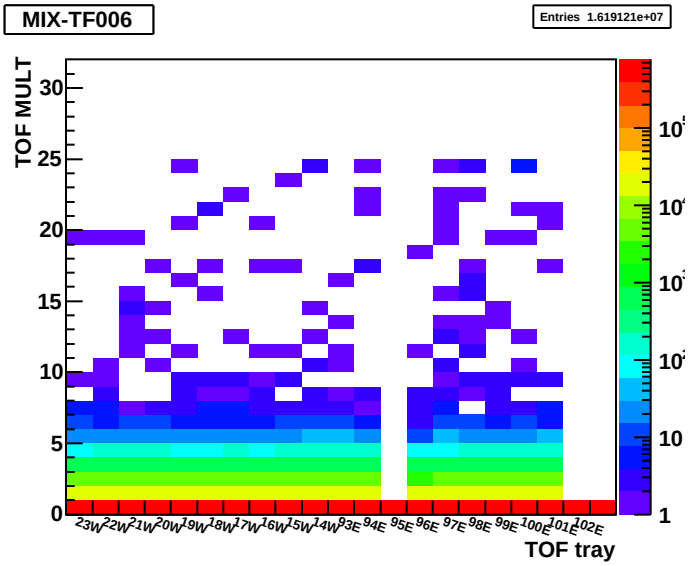

MIX-TF001
Entries 1.619121e+07
TOF MULT
TOF tray

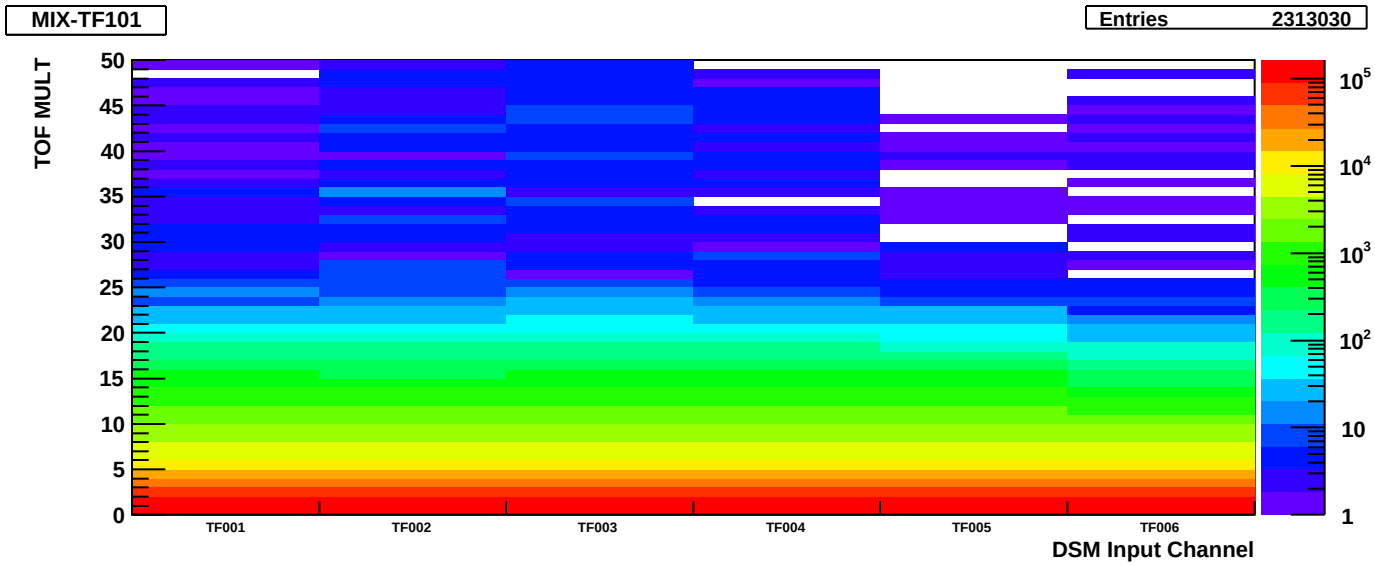

MIX-TF002
Entries 1.619121e+07
TOF MULT
TOF tray

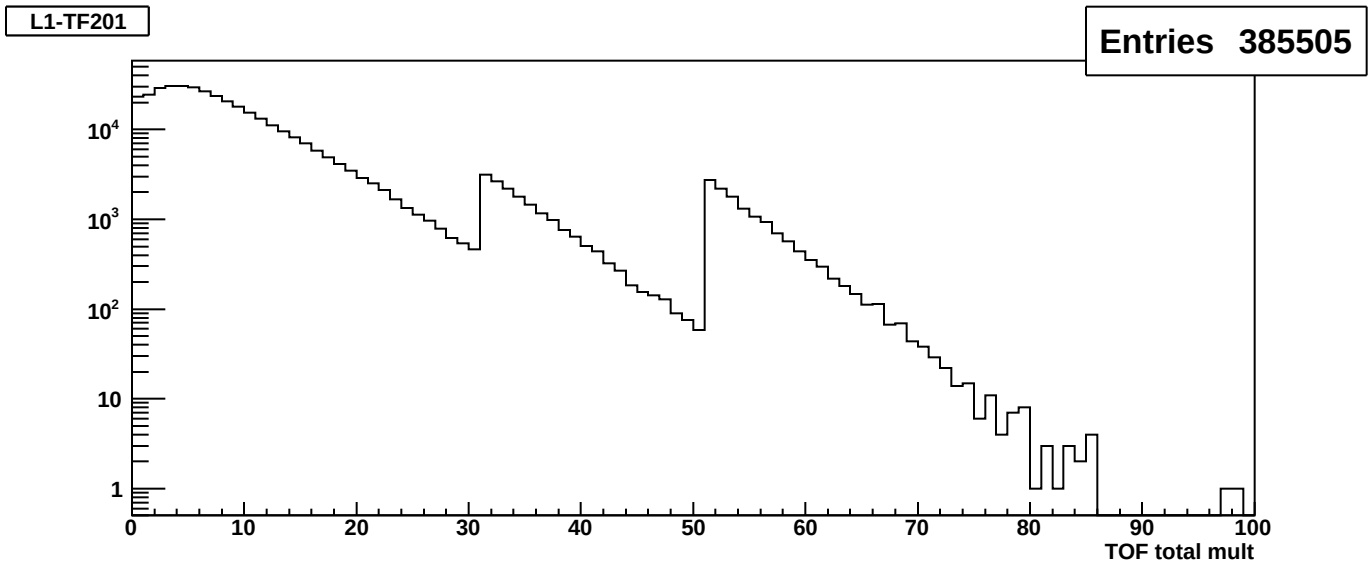

MIX-TF003
Entries 1.619121e+07
TOF MULT
TOF tray

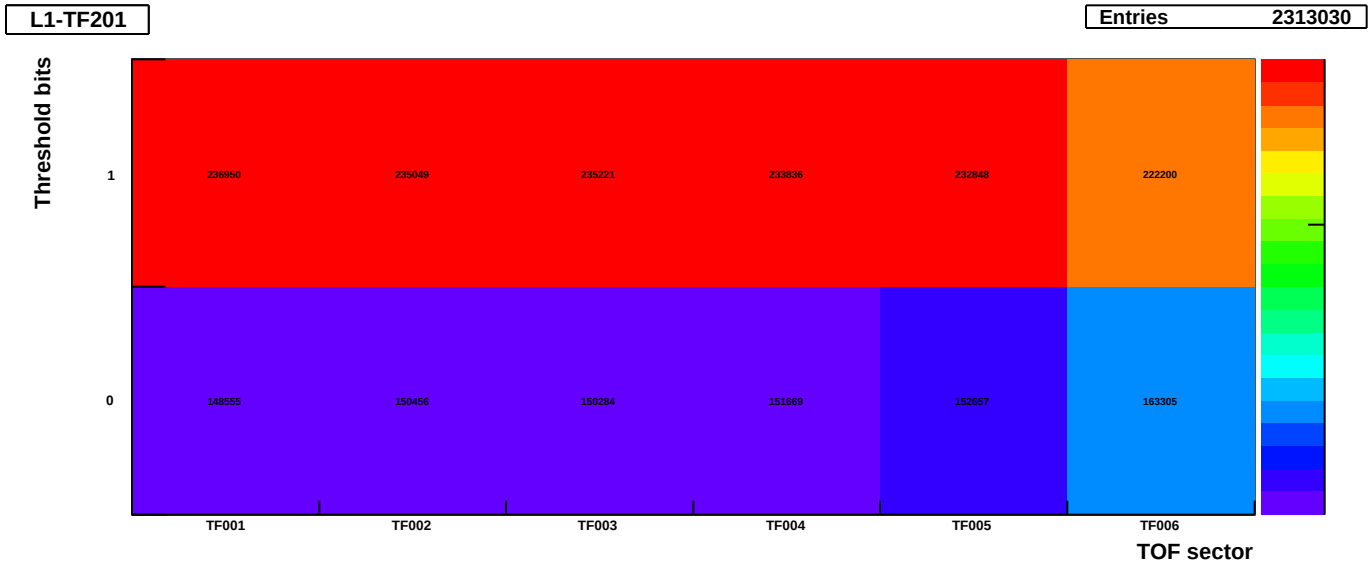

MIX-TF004
Entries 1.619121e+07
TOF MULT
TOF tray

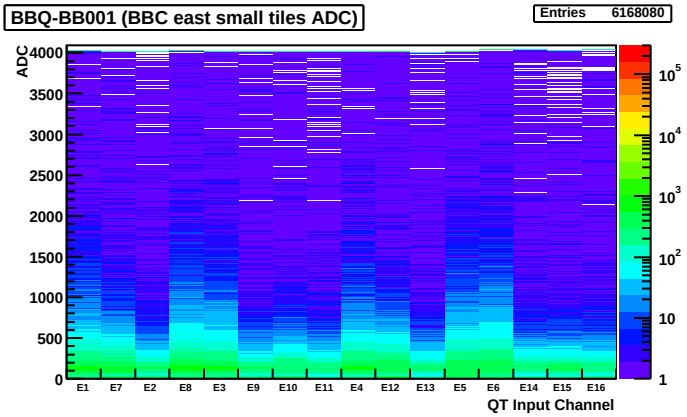

MIX-TF005
Entries 1.619121e+07
TOF MULT
TOF tray

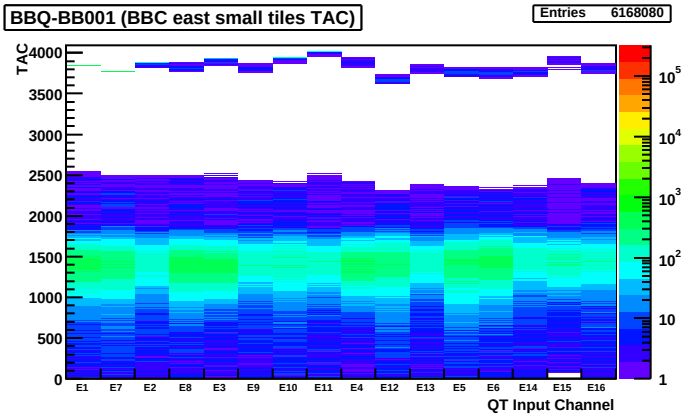

MIX-TF006
Entries 1.619121e+07
TOF MULT
TOF tray

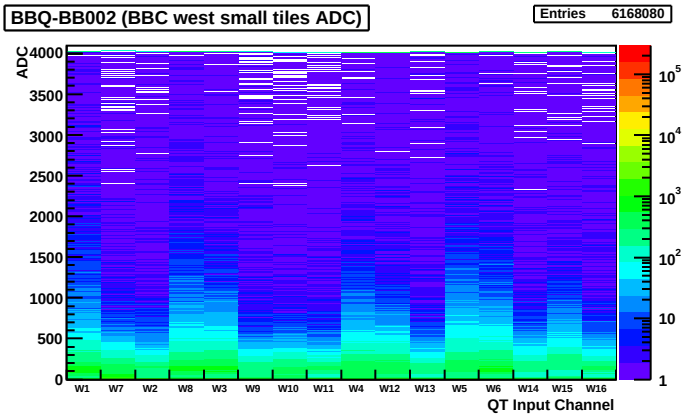

MIX-TF101
Entries 2313030
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

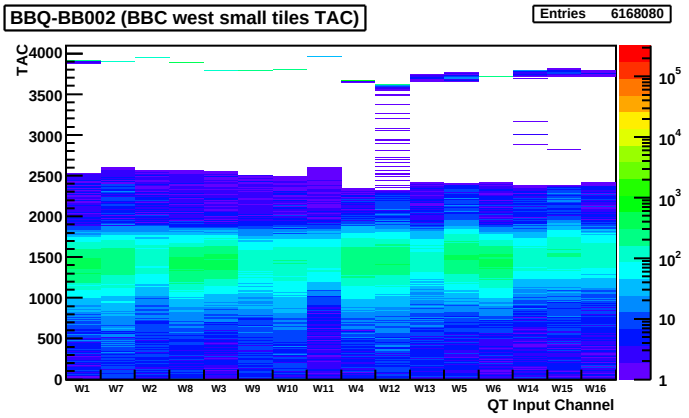

L1-TF201
Entries 385505
TOF total mult

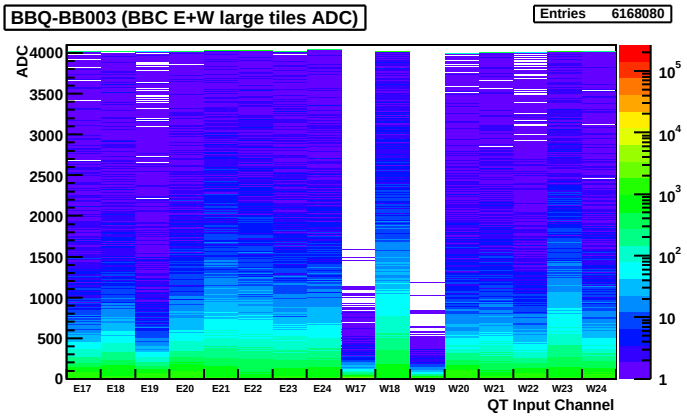

L1-TF201
Entries 2313030
Threshold bits
236950
235049
235221
233836
232848
222200
148555
150456
150284
151669
152657
163305
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

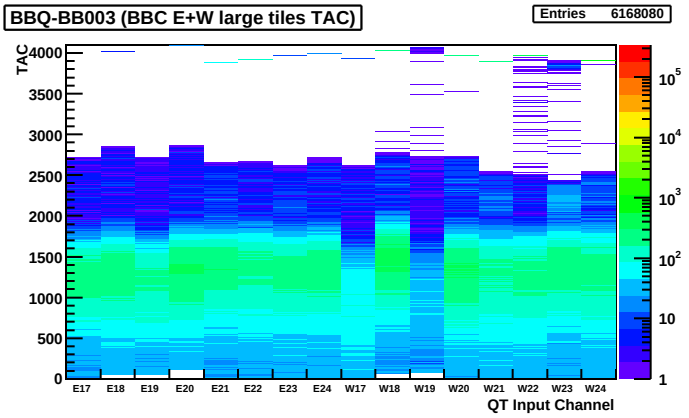

BBQ-BB001 (BBC east small tiles ADC)
Entries 6168080
ADC
QT Input Channel

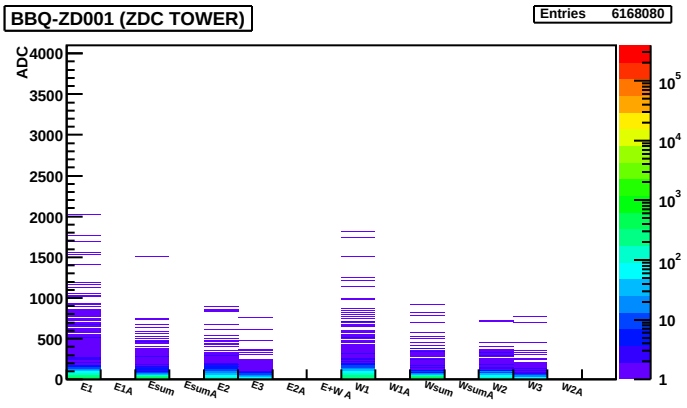

BBQ-BB001 (BBC east small tiles TAC)
Entries 6168080
TAC
QT Input Channel

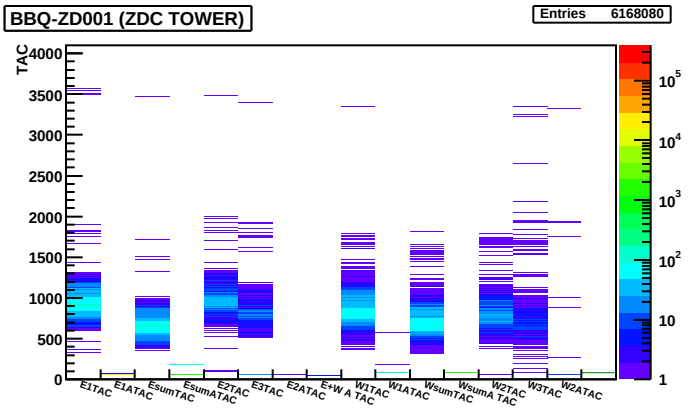

BBQ-BB002 (BBC west small tiles ADC)
Entries 6168080
ADC
QT Input Channel

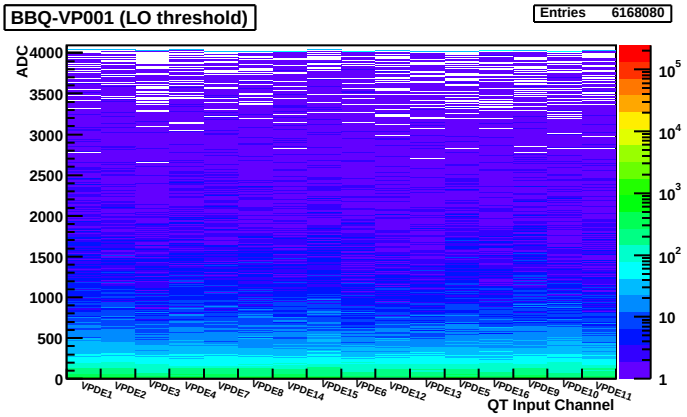

BBQ-BB002 (BBC west small tiles TAC)
Entries 6168080
TAC
QT Input Channel

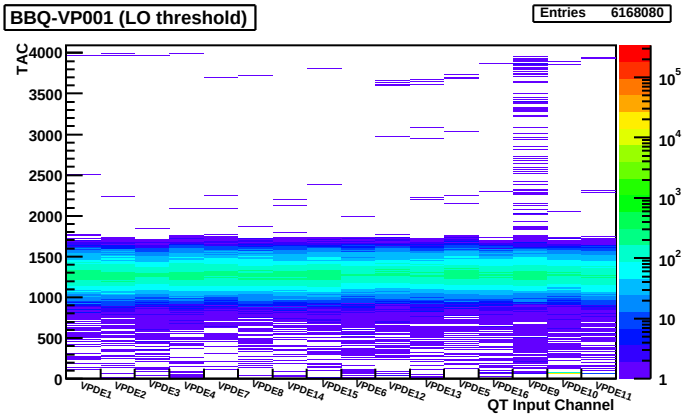

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6168080
ADC
QT Input Channel

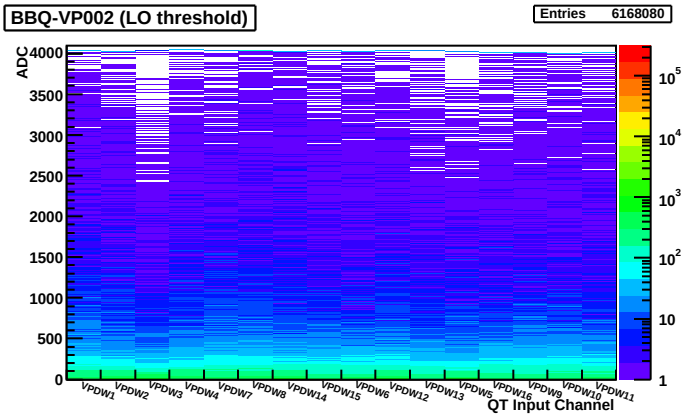

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6168080
TAC
QT Input Channel

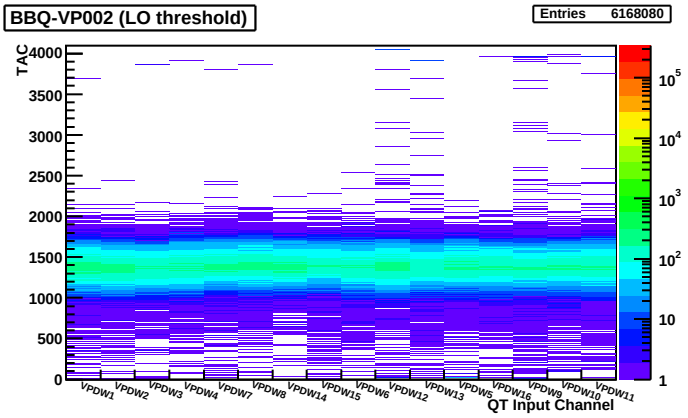

BBQ-ZD001 (ZDC TOWER)
Entries 6168080
ADC

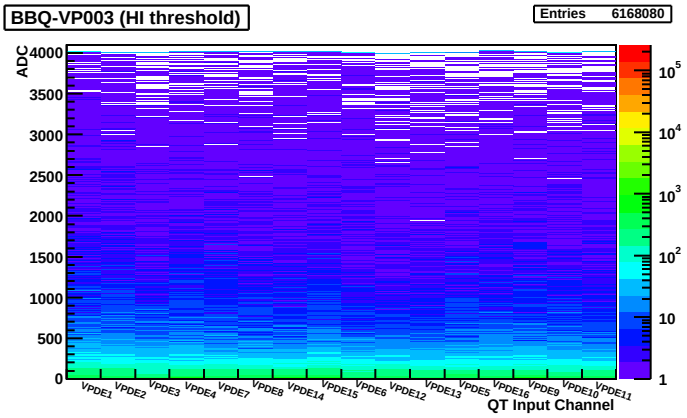

BBQ-ZD001 (ZDC TOWER)
Entries 6168080
TAC

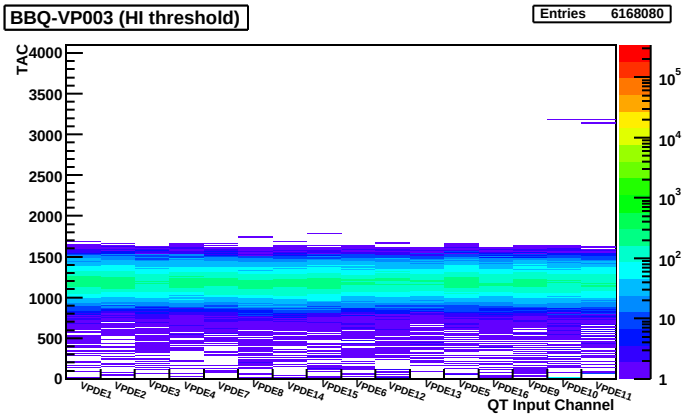

BBQ-VP001 (LO threshold)
Entries 6168080
ADC
QT Input Channel

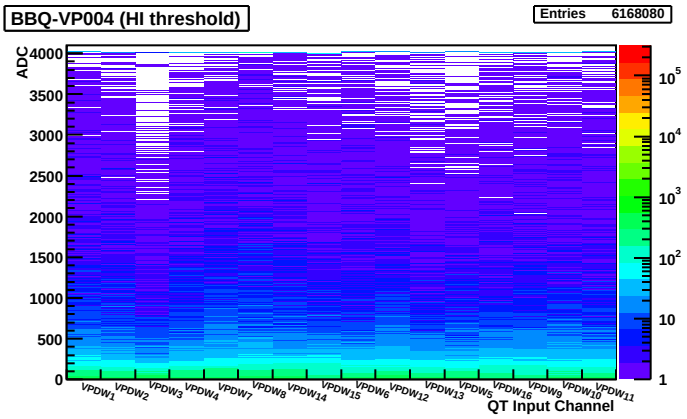

BBQ-VP001 (LO threshold)
Entries 6168080
TAC
QT Input Channel

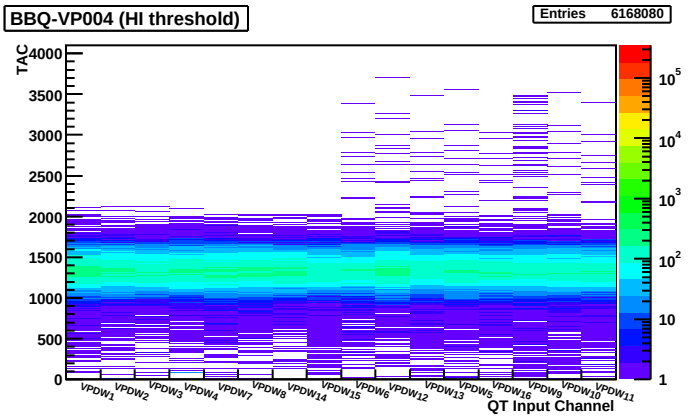

BBQ-VP002 (LO threshold)
Entries 6168080
ADC
QT Input Channel

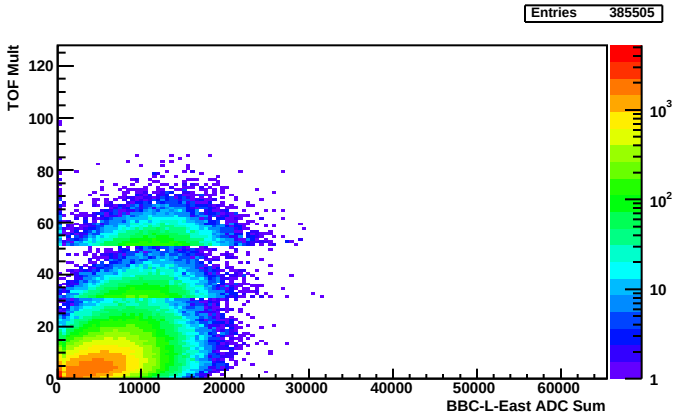

BBQ-VP002 (LO threshold)
Entries 6168080
TAC
QT Input Channel

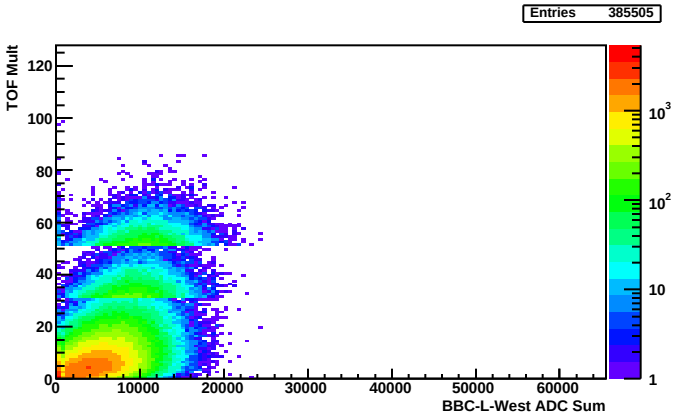

BBQ-VP003 (HI threshold)
Entries 6168080
ADC
QT Input Channel

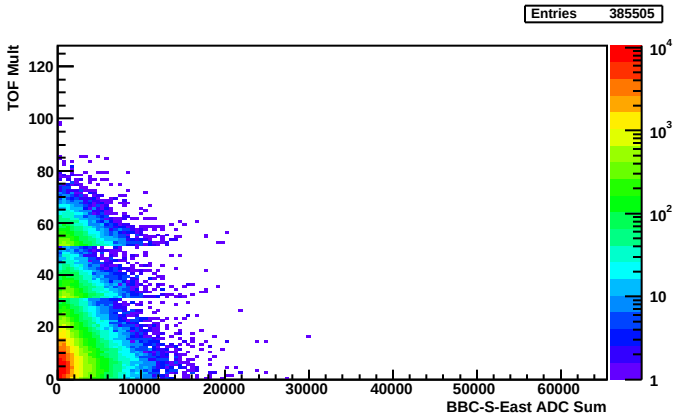

BBQ-VP003 (HI threshold)
Entries 6168080
TAC
QT Input Channel

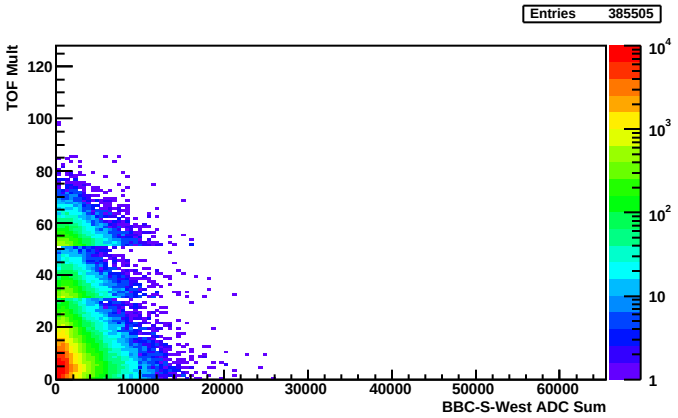

BBQ-VP004 (HI threshold)
Entries 6168080
ADC
QT Input Channel

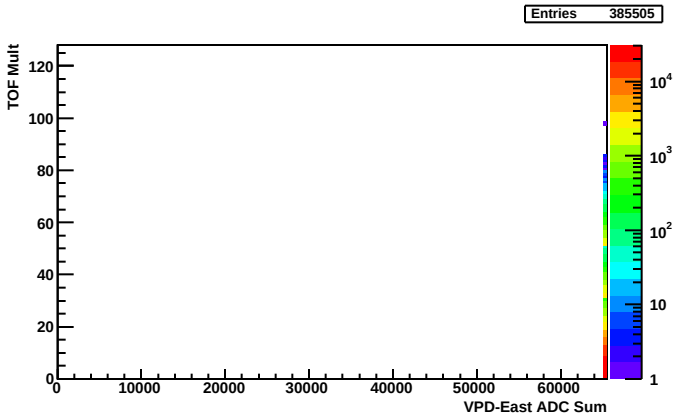

BBQ-VP004 (HI threshold)
Entries 6168080
TAC
QT Input Channel

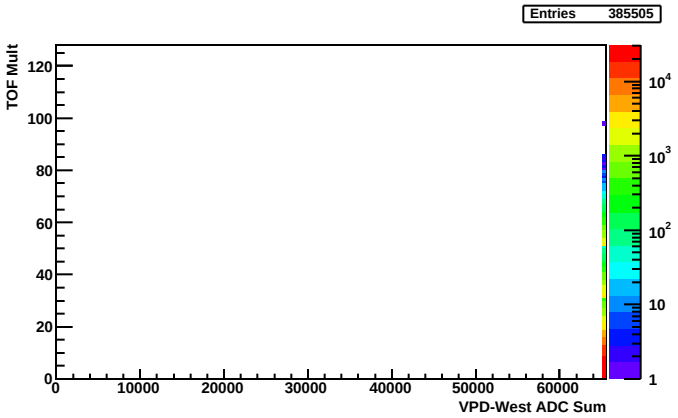

Entries 385505
TOF Mult
BBC-L-East ADC Sum

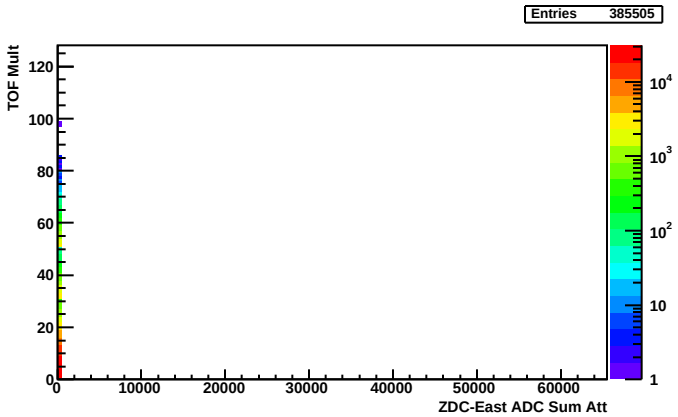

Entries 385505
TOF Mult
BBC-L-West ADC Sum

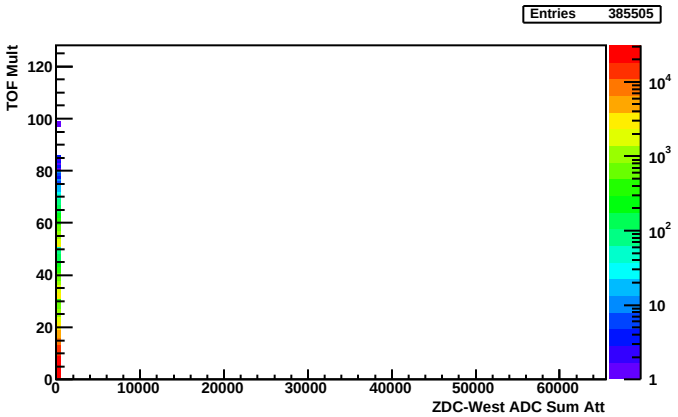

Entries 385505
TOF Mult
BBC-S-East ADC Sum

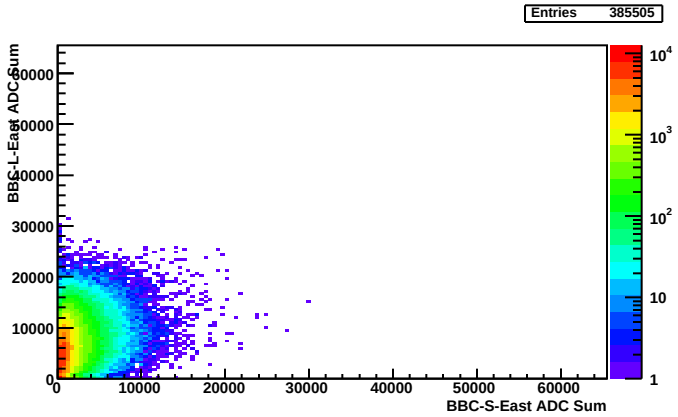

Entries 385505
TOF Mult
BBC-S-West ADC Sum

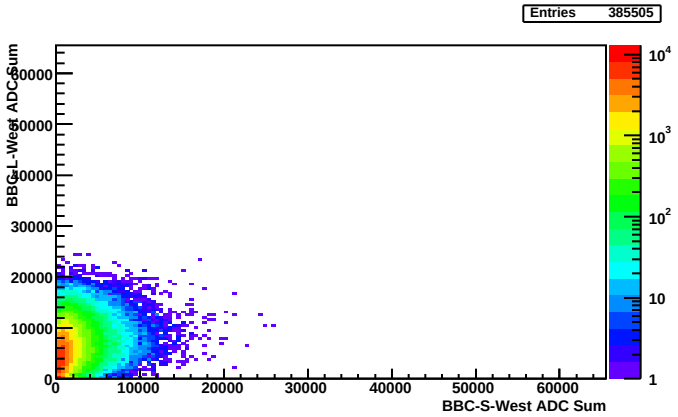

Entries 385505
TOF Mult
VPD-East ADC Sum

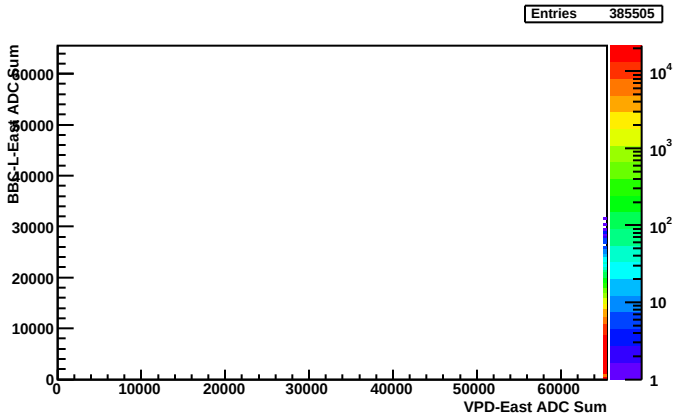

Entries 385505
TOF Mult
VPD-West ADC Sum

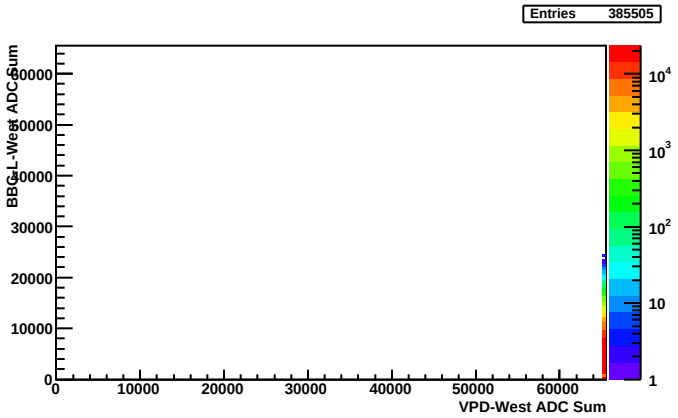

Entries 385505
TOF Mult
ZDC-East ADC Sum Att

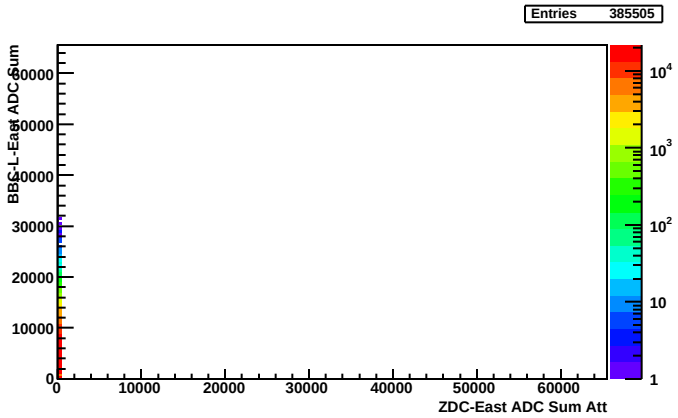

Entries 385505
TOF Mult
ZDC-West ADC Sum Att

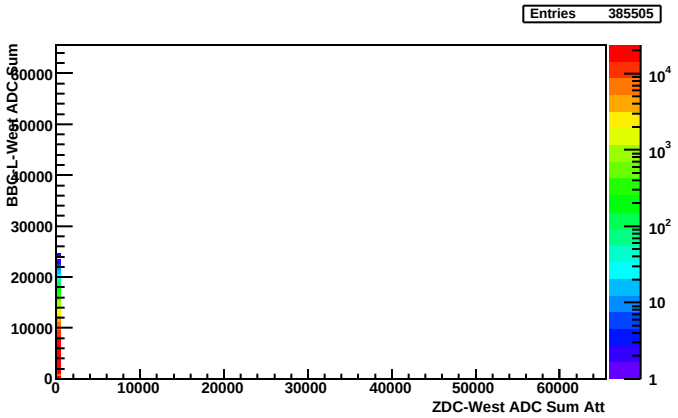

Entries 385505
BBC-L-East ADC Sum
BBC-S-East ADC Sum

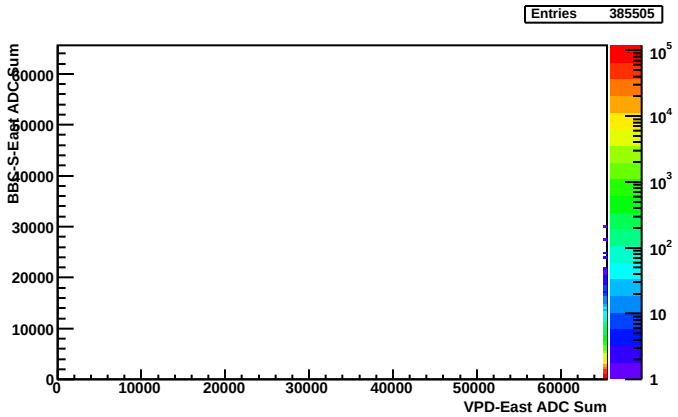

Entries 385505
BBC-L-West ADC Sum
BBC-S-West ADC Sum

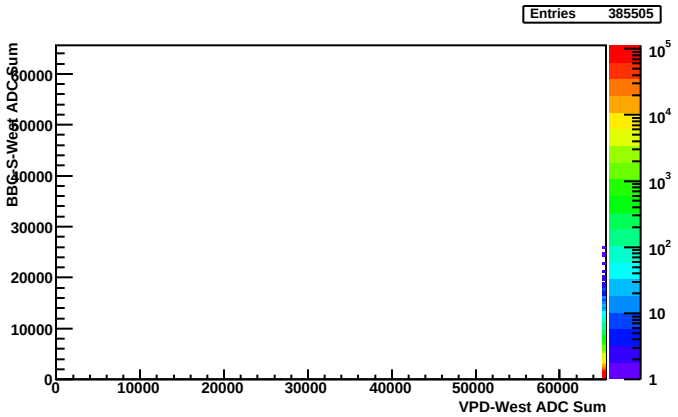

Entries 385505
BBC-L-East ADC Sum
VPD-East ADC Sum

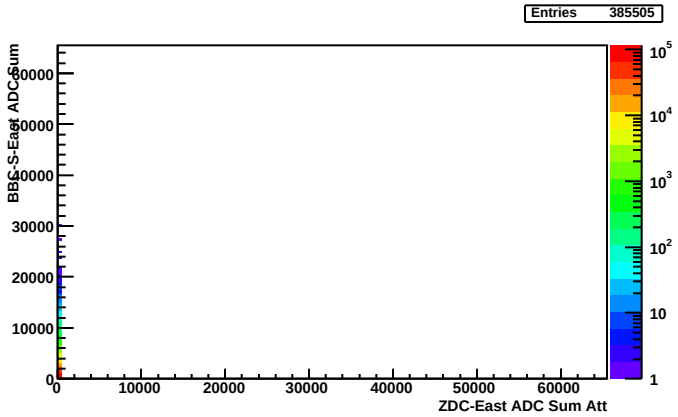

Entries 385505
BBC-L-West ADC Sum
VPD-West ADC Sum

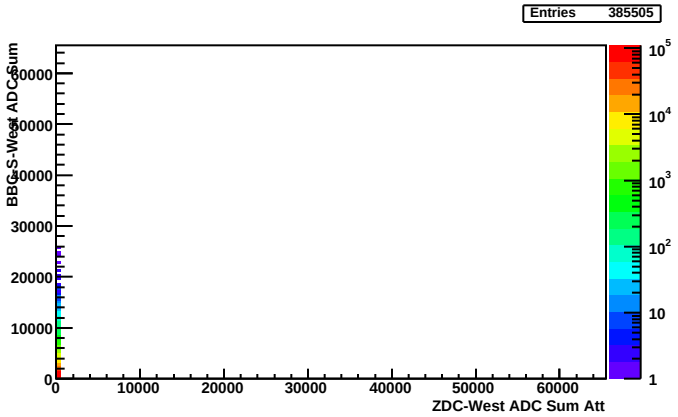

Entries 385505
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

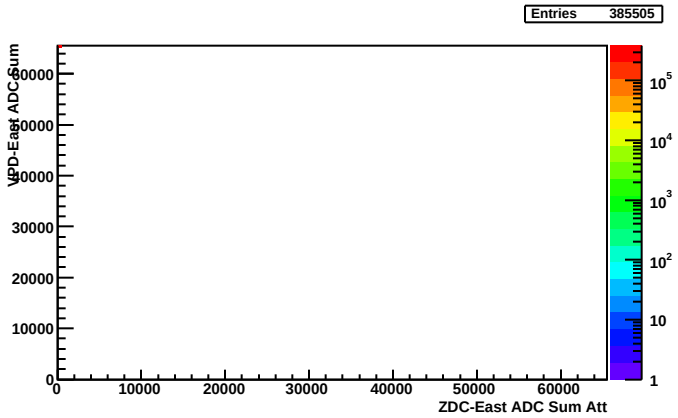

Entries 385505
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

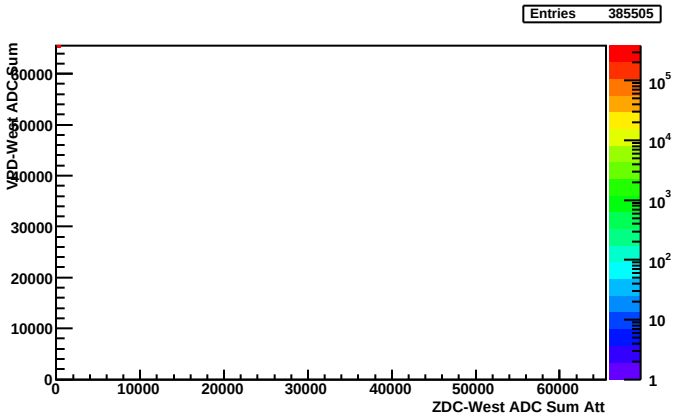

Entries 385505
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 385505
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 385505
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 385505
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 385505
VPD-East ADC Sum
ZDC-East ADC Sum Att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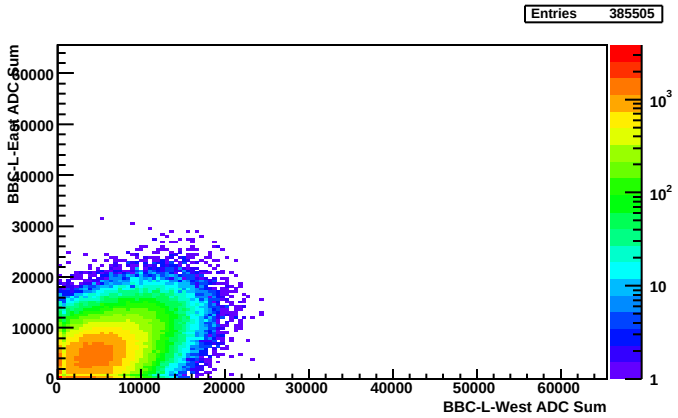
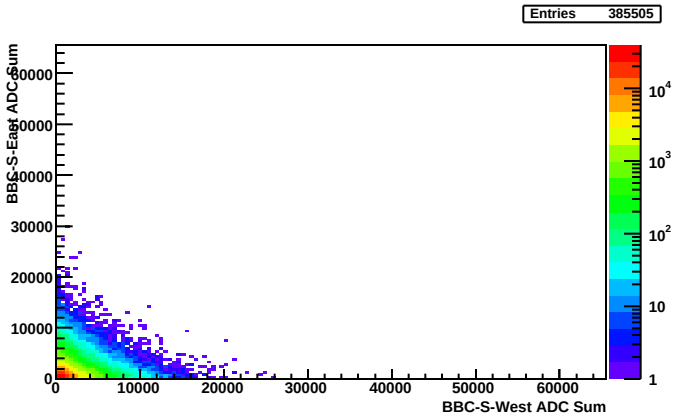
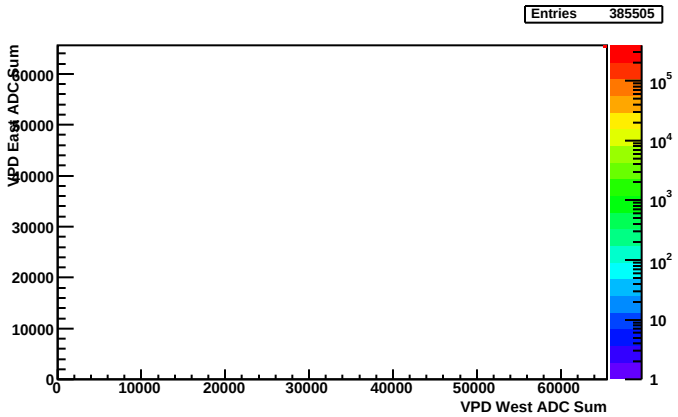
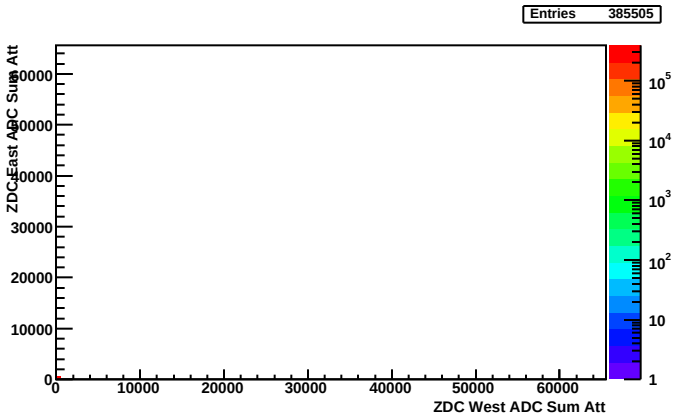
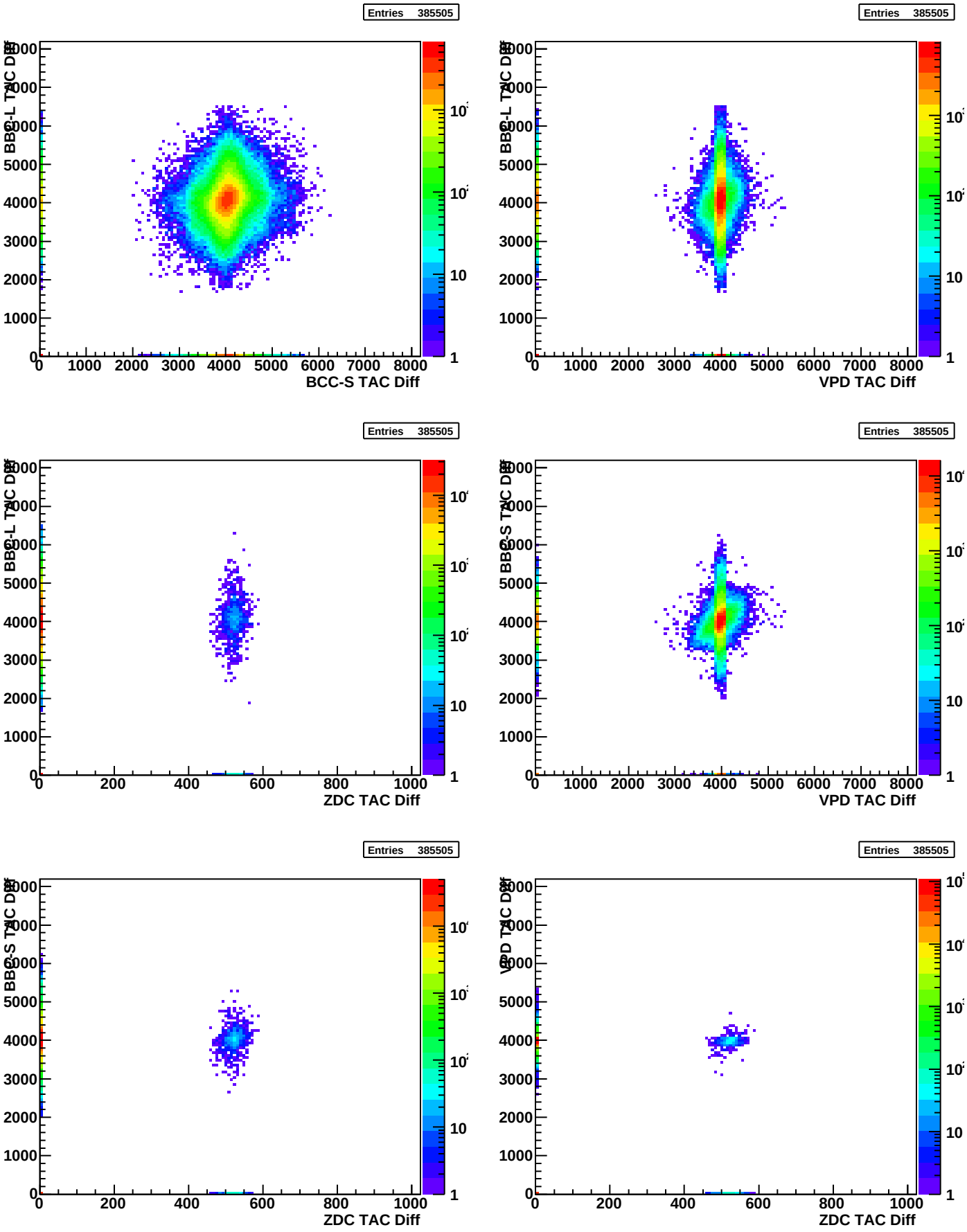
VPD-West ADC Sum
ZDC-West ADC Sum Att

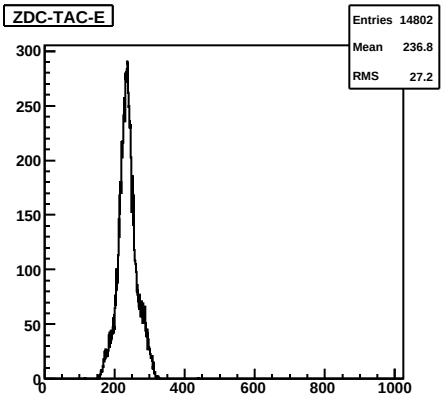

Entries 385505
BBC-L-East ADC Sum
BBC-L-West ADC Sum

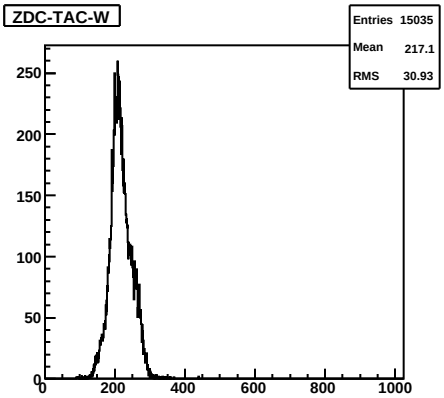

Entries 385505
BBC-S-East ADC Sum
BBC-S-West ADC Sum

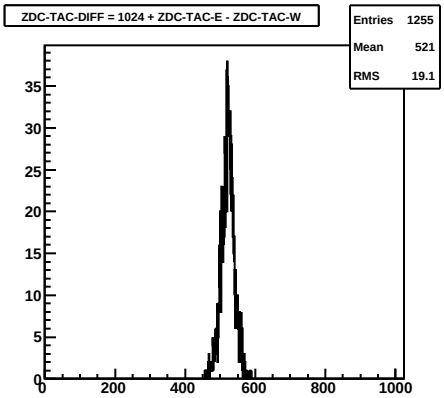

Entries 385505
VPD East ADC Sum
VPD West ADC Sum

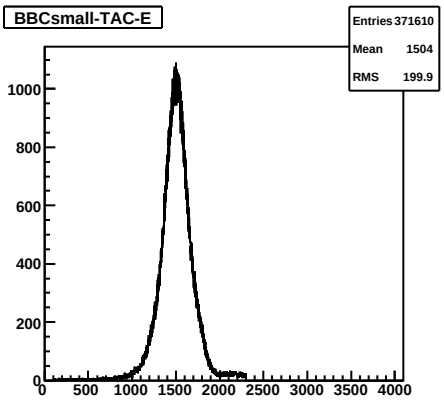

Entries 385505
ZDC East ADC Sum Att
ZDC West ADC Sum Att

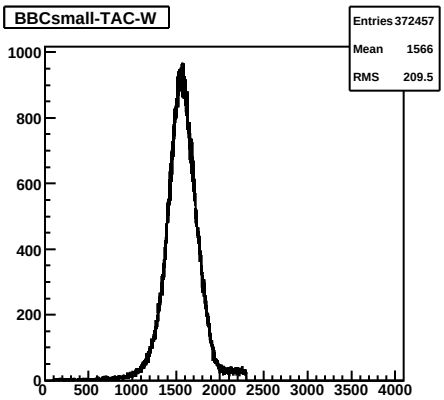

Entries 385505
BBC-L TAC Diff
BCC-S TAC Diff
Entries 385505
BBC-L TAC Diff
VPD TAC Diff
Entries 385505
BBC-L TAC Diff
ZDC TAC Diff
Entries 385505
BBC-S TAC Diff
VPD TAC Diff
Entries 385505
BBC-S TAC Diff
ZDC TAC Diff
Entries 385505
VPD TAC Diff
ZDC TAC Diff

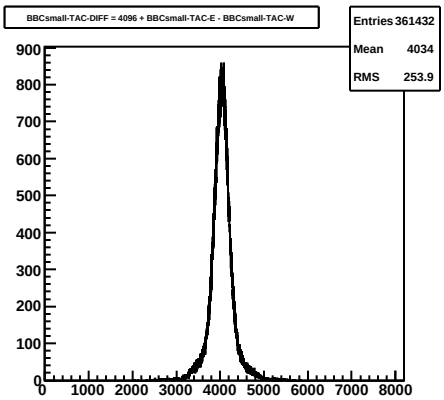

ZDC-TAC-E
Entries 14802
Mean 236.8
RMS 27.2

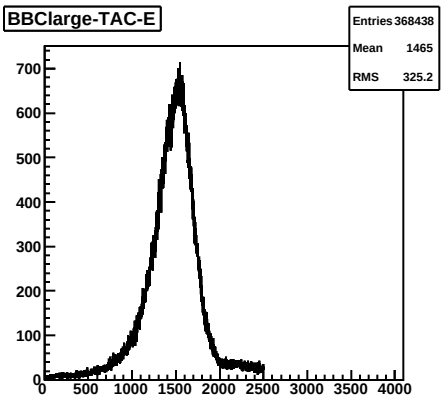

ZDC-TAC-W
Entries 15035
Mean 217.1
RMS 30.93

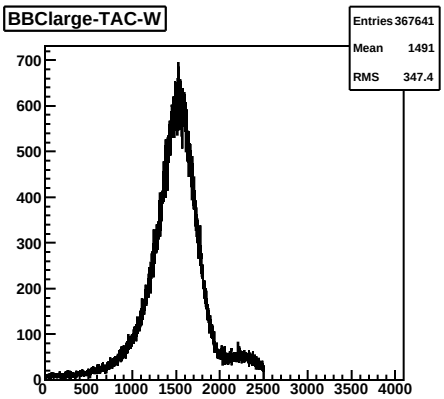

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1255
Mean 521
RMS 19.1

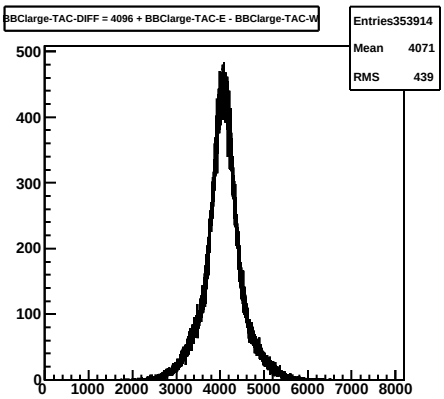

BBCsmall-TAC-E
Entries 371610
Mean 1504
RMS 199.9

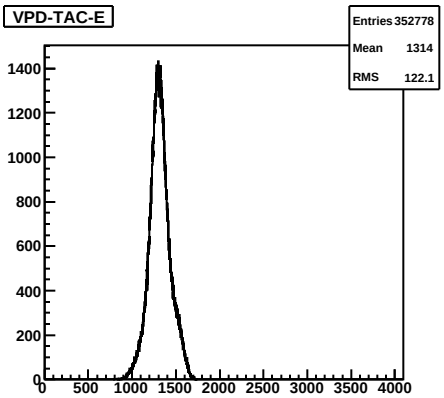

BBCsmall-TAC-W
Entries 372457
Mean 1566
RMS 209.5

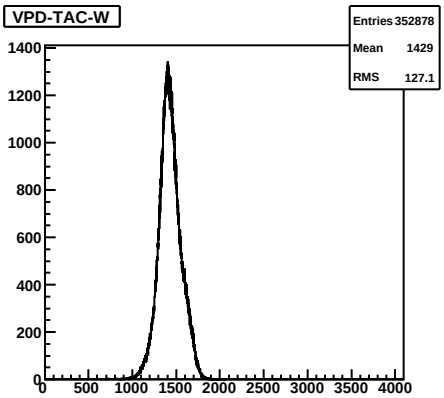

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 361432
Mean 4034
RMS 253.9

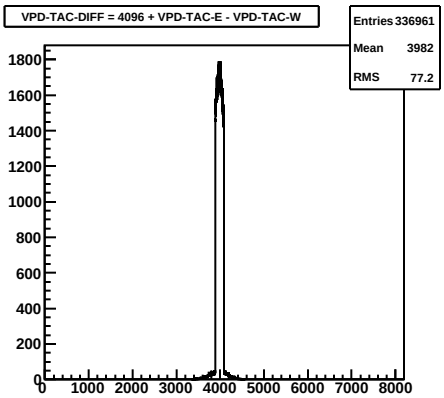

BBClarge-TAC-E
Entries 368438
Mean 1465
RMS 325.2

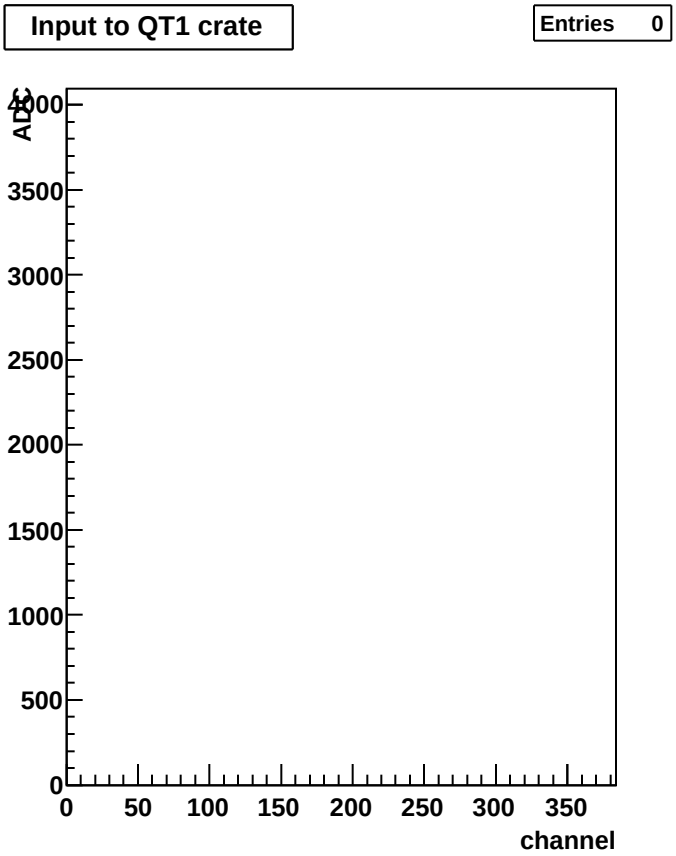

BBClarge-TAC-W
Entries 367641
Mean 1491
RMS 347.4

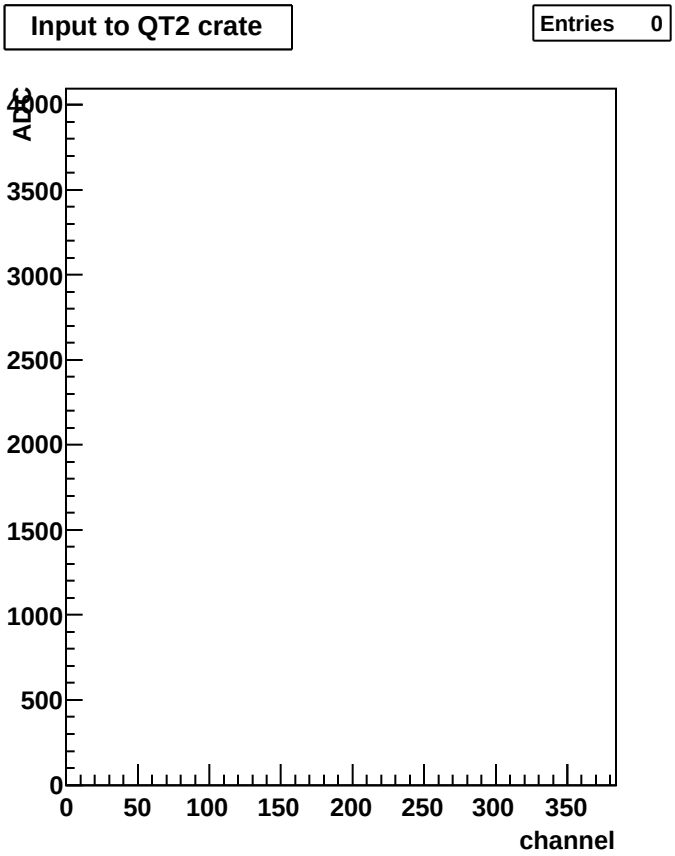

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 353914
Mean 4071
RMS 439

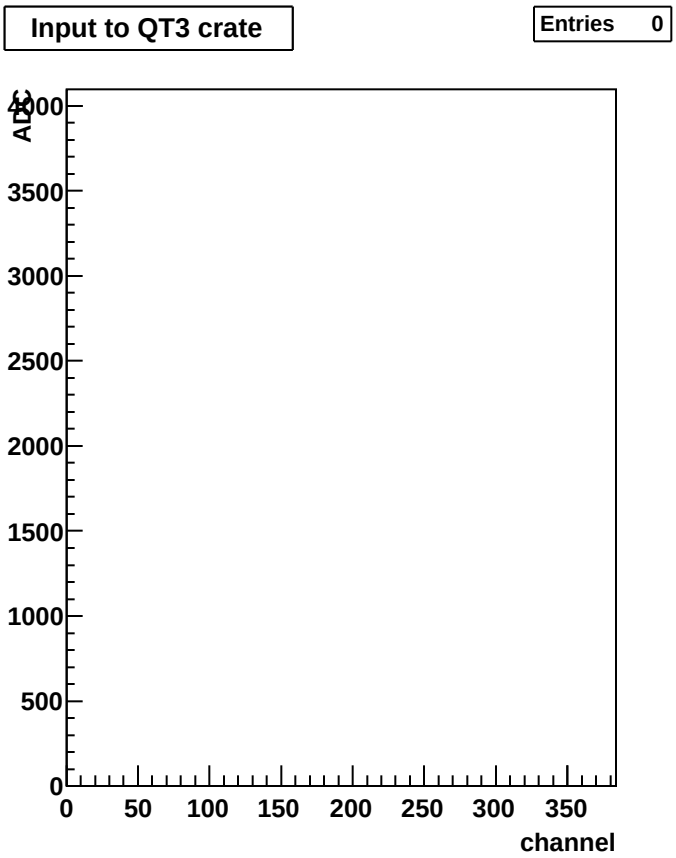

VPD-TAC-E
Entries 352778
Mean 1314
RMS 122.1

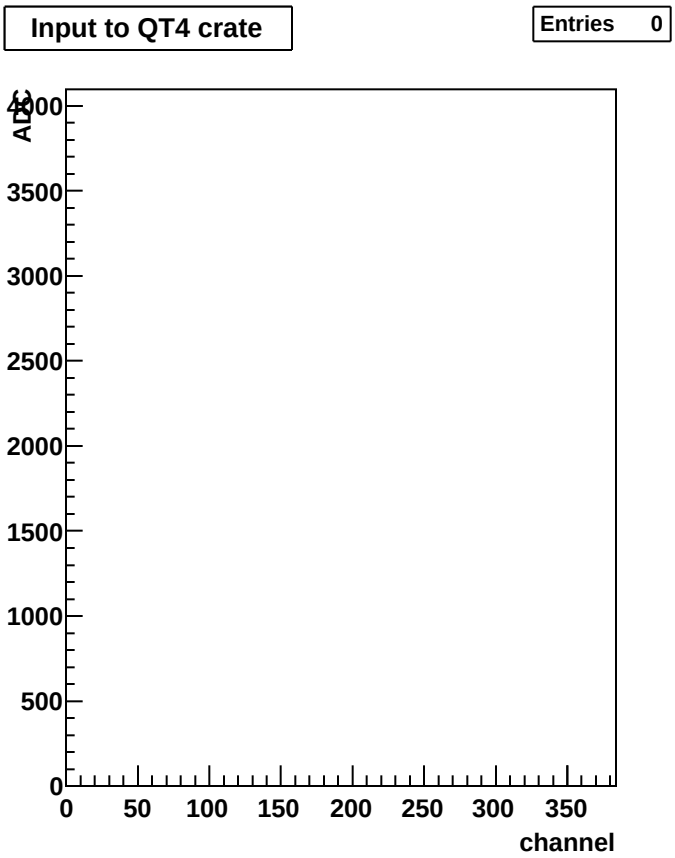

VPD-TAC-W
Entries 352878
Mean 1429
RMS 127.1

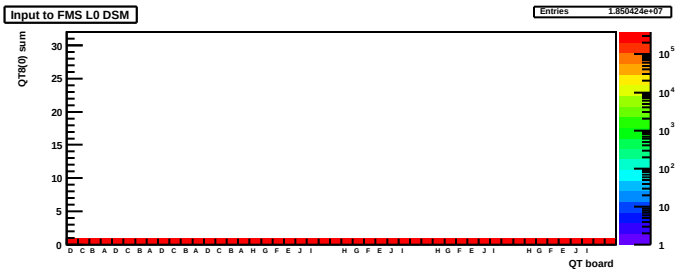

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 336961
Mean 3982
RMS 77.2

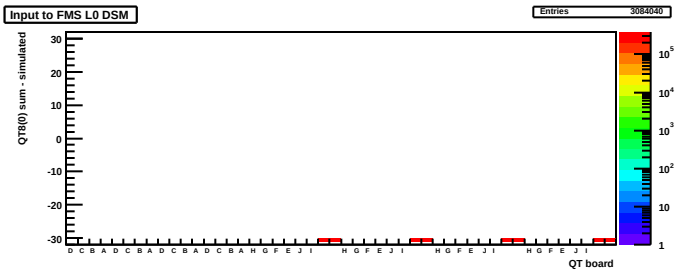

Input to QT1 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

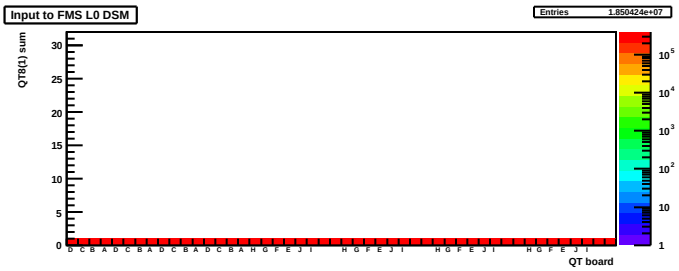

Input to QT2 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

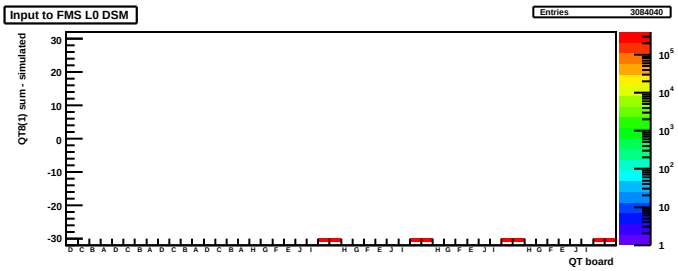

Input to QT3 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

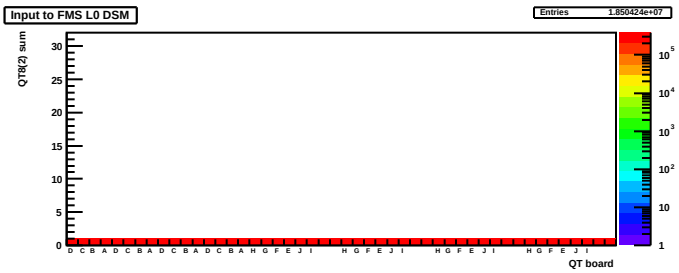

Input to QT4 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

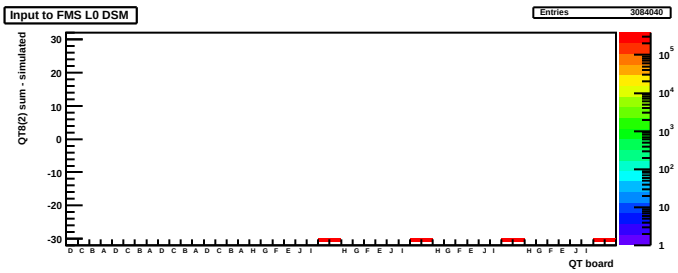

Input to FMS L0 DSM
Entries 1.850424e+07
QT8(0) sum
QT board

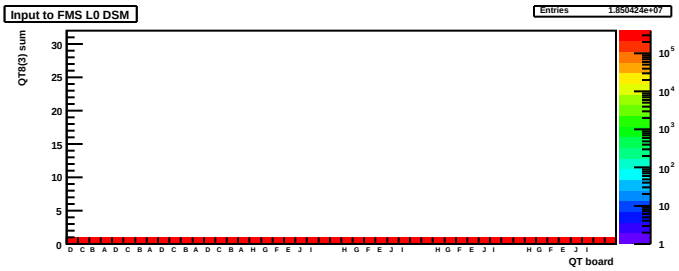

Input to FMS L0 DSM
Entries 3084040
QT8(0) sum - simulated
QT board

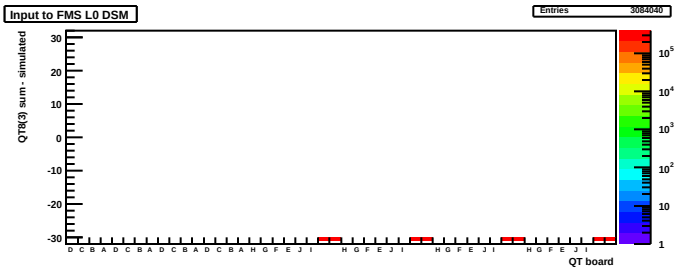

Input to FMS L0 DSM
Entries 1.850424e+07
QT8(1) sum
QT board

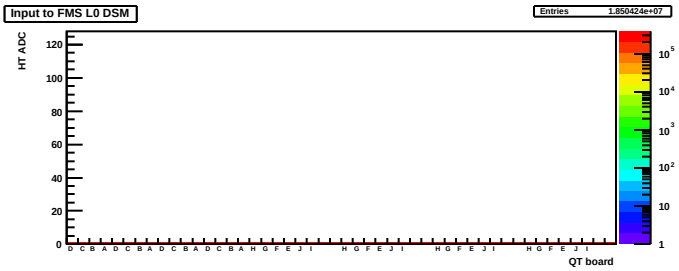

Input to FMS L0 DSM
Entries 3084040
QT8(1) sum - simulated
QT board

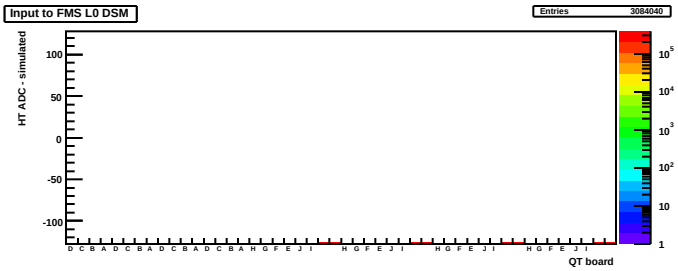

Input to FMS L0 DSM
Entries 1.850424e+07
QT8(2) sum
QT board

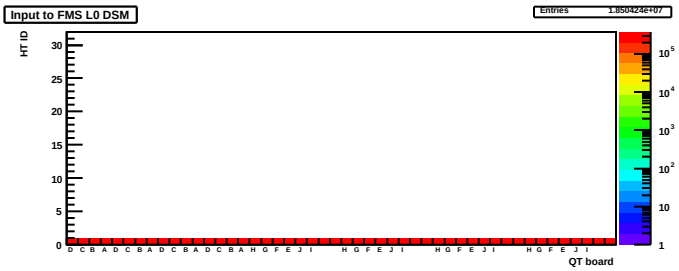

Input to FMS L0 DSM
Entries 3084040
QT8(2) sum - simulated
QT board

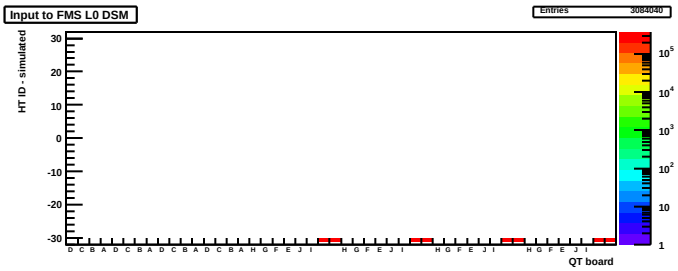

Input to FMS L0 DSM
Entries 1.850424e+07
QT8(3) sum
QT board

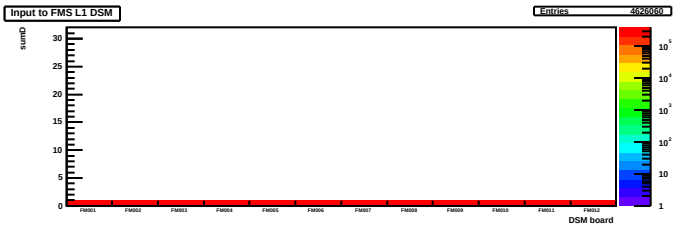

Input to FMS L0 DSM
Entries 3084040
QT8(3) sum - simulated
QT board

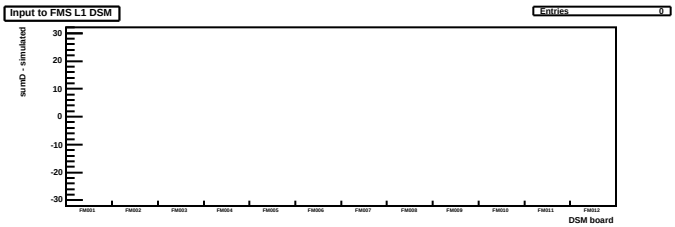

Input to FMS L0 DSM
Entries 1.850424e+07
HT ADC
QT board

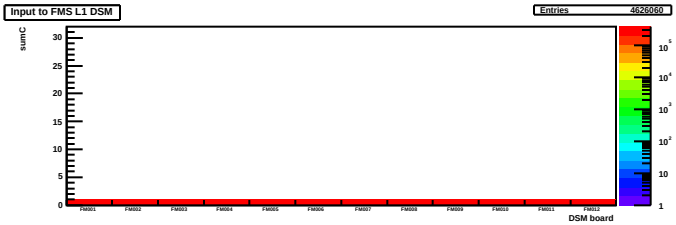

Input to FMS L0 DSM
Entries 3084040
HT ADC - simulated
QT board

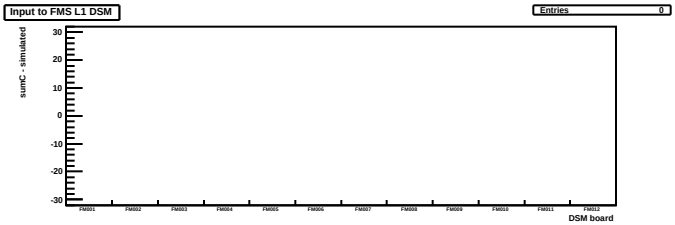

Input to FMS L0 DSM
Entries 1.850424e+07
HT ID
QT board

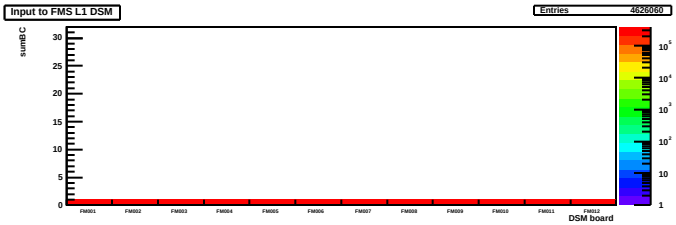

Input to FMS L0 DSM
Entries 3084040
HT ID - simulated
QT board

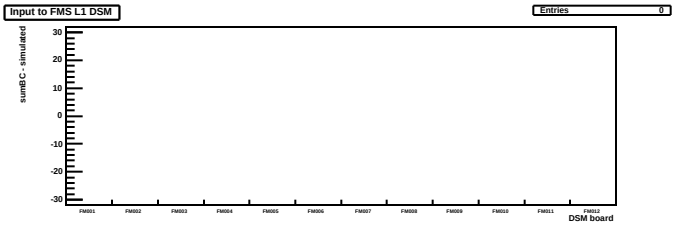

Input to FMS L1 DSM
Entries 4626060
sumD
DSM board

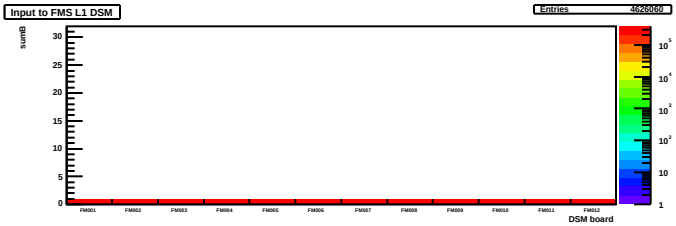

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

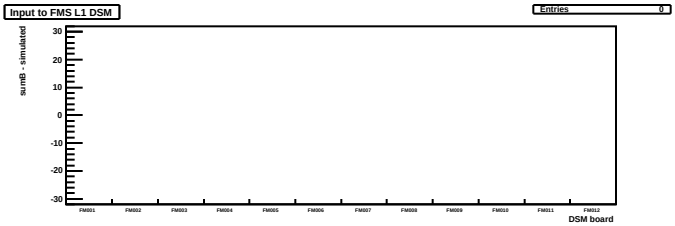

Input to FMS L1 DSM
Entries 4626060
sumC
DSM board

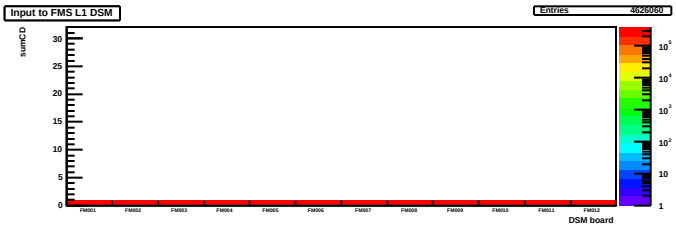

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

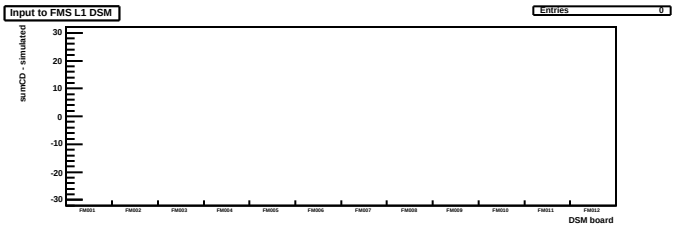

Input to FMS L1 DSM
Entries 4626060
sumBC
DSM board

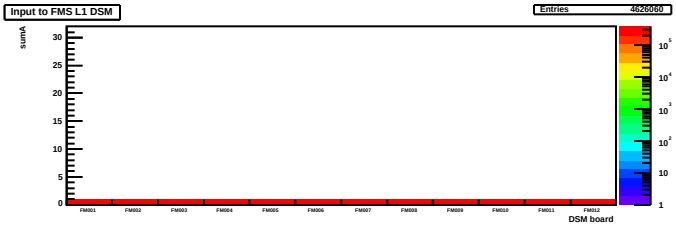

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

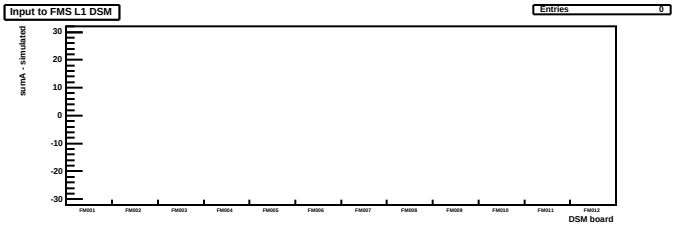

Input to FMS L1 DSM
Entries 4626060
sumB
DSM board

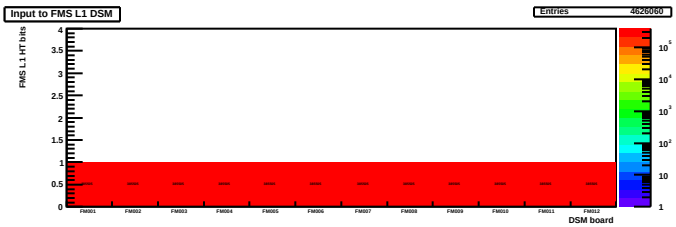

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

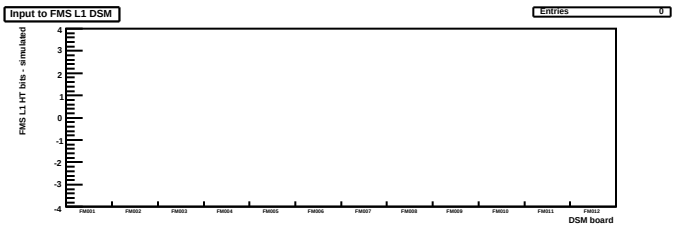

Input to FMS L1 DSM
Entries 4626060
sumCD
DSM board

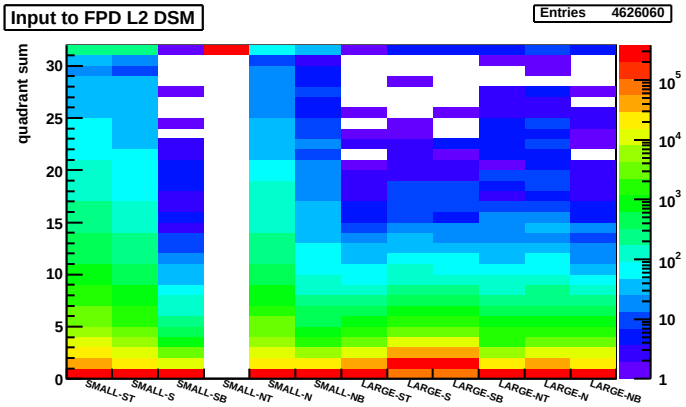

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

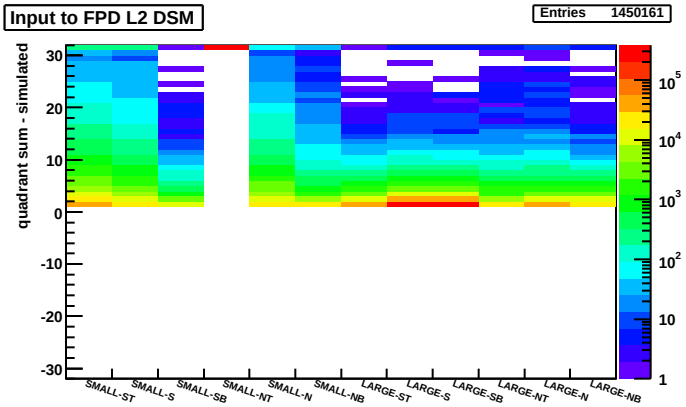

Input to FMS L1 DSM
Entries 4626060
sumA
DSM board

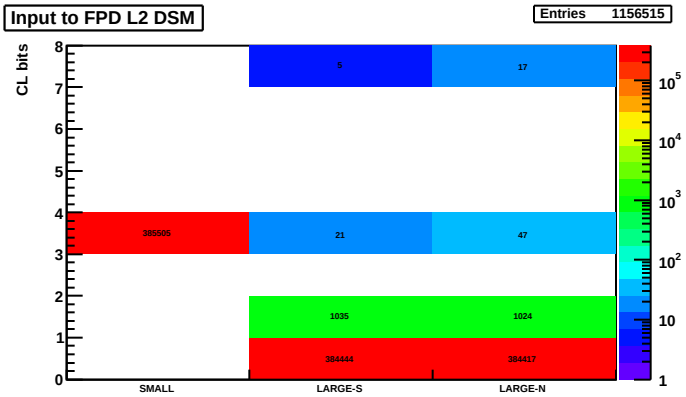

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

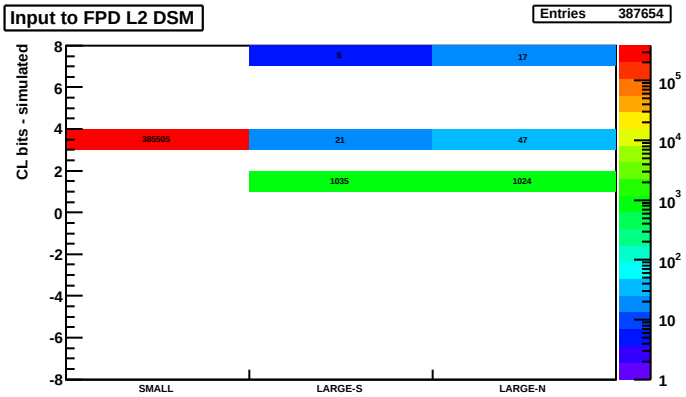

Input to FMS L1 DSM
Entries 4626060
FMS L1 HT bits
DSM board

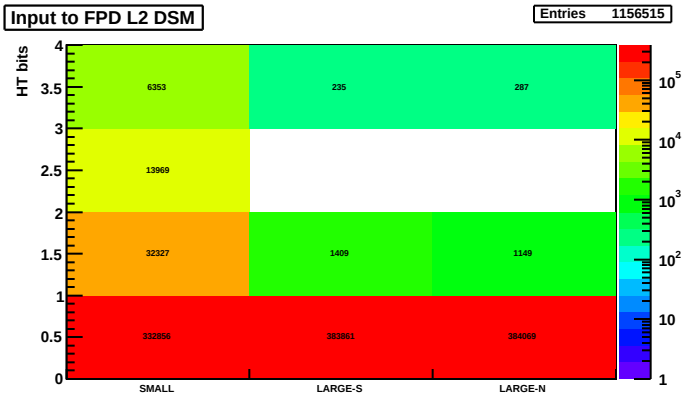

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

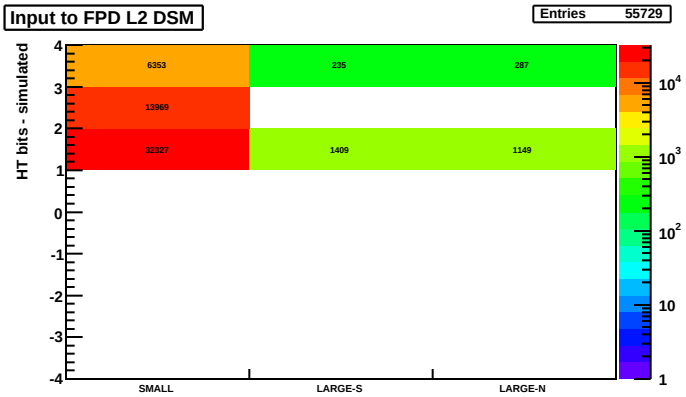

Input to FPD L2 DSM
Entries 4626060
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

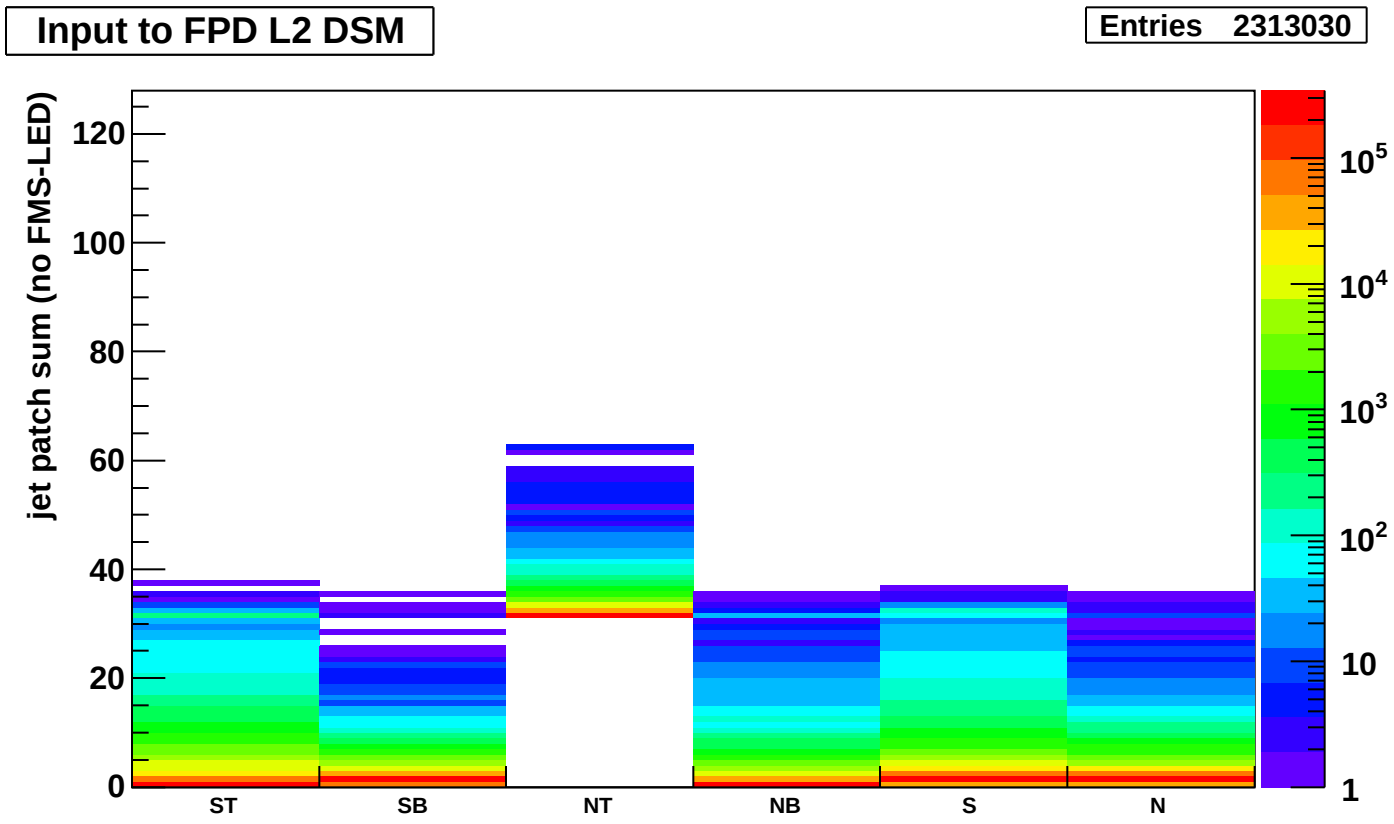

Input to FPD L2 DSM
Entries 1450161
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

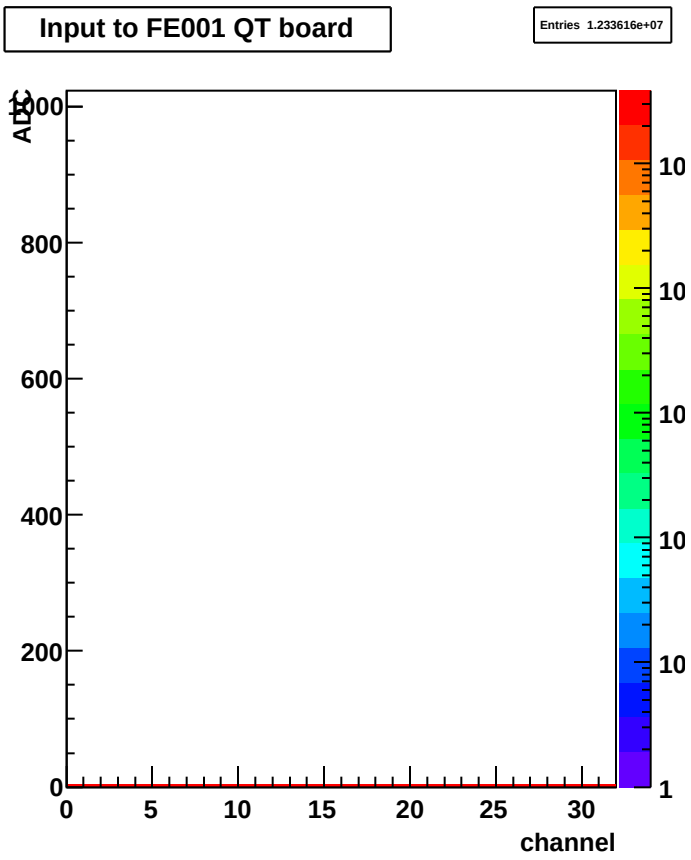

Input to FPD L2 DSM
Entries 1156515
CL bits
5
17
385505
21
47
1035
1024
384444
384417
SMALL
LARGE-S
LARGE-N

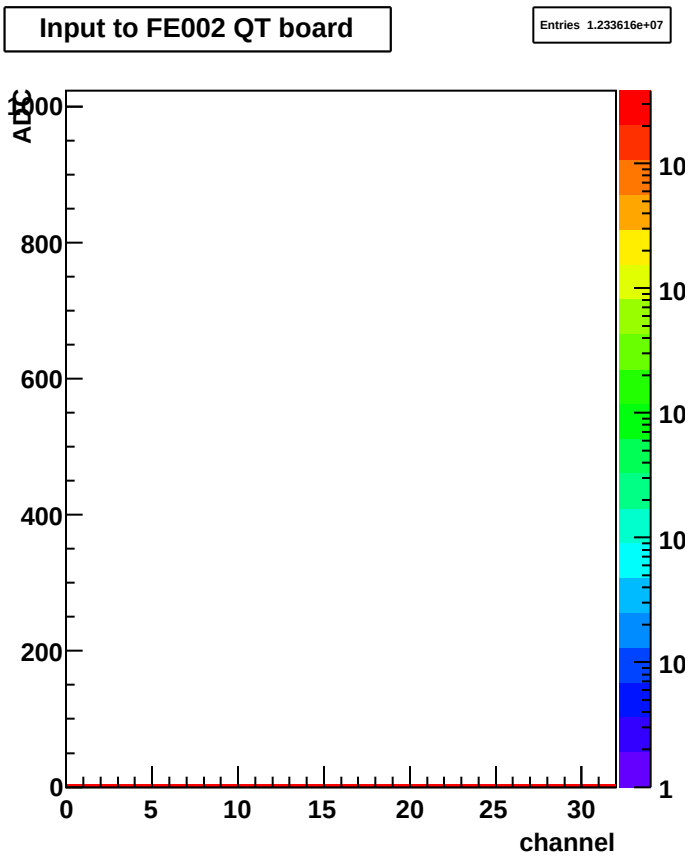

Input to FPD L2 DSM
Entries 387654
CL bits - simulated
5
17
385505
21
47
1035
1024
SMALL
LARGE-S
LARGE-N

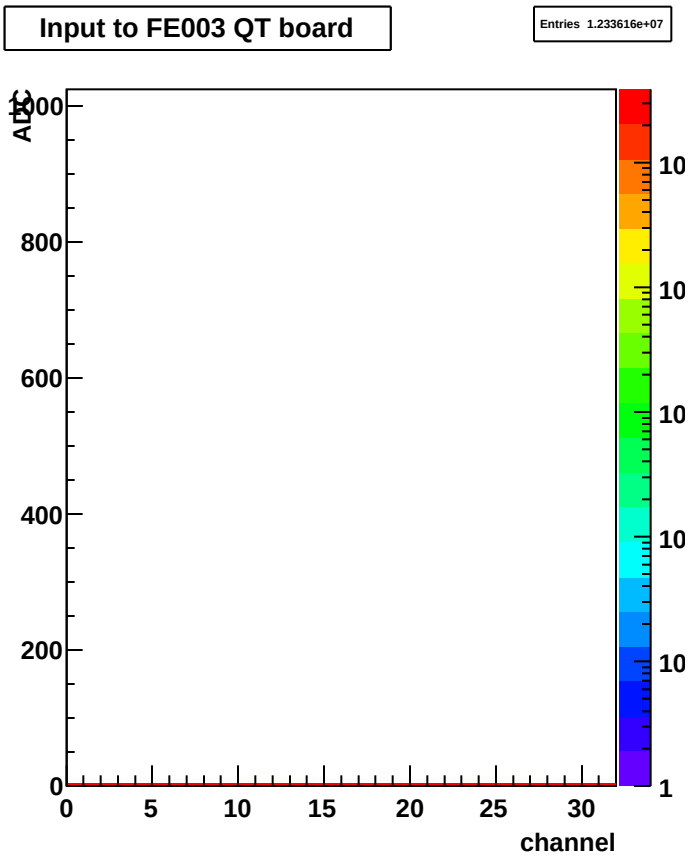

Input to FPD L2 DSM
Entries 1156515
HT bits
6353
235
287
13969
32327
1409
1149
332856
383861
384069
SMALL
LARGE-S
LARGE-N

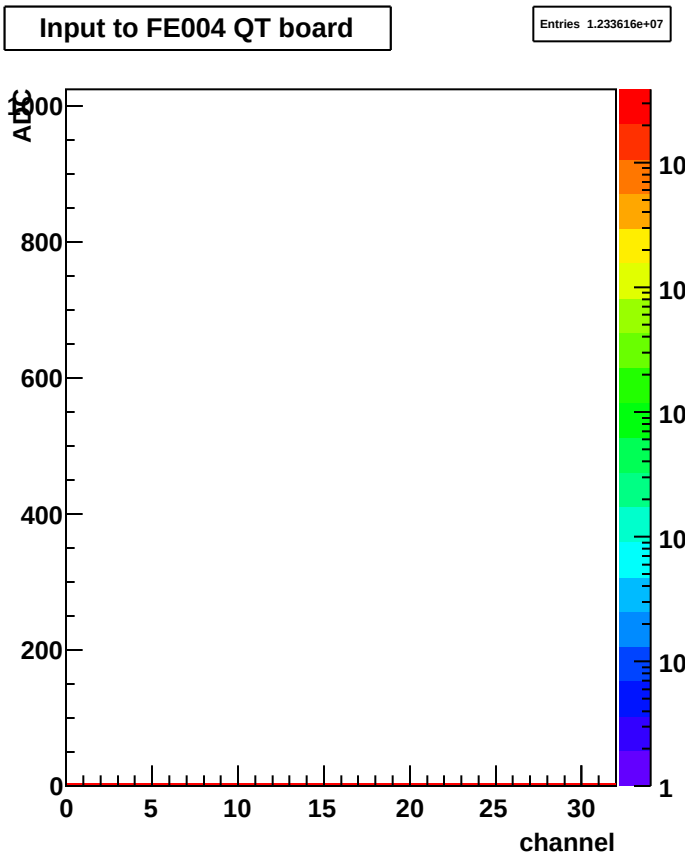

Input to FPD L2 DSM
Entries 55729
HT bits - simulated
6353
235
287
13969
32327
1409
1149
SMALL
LARGE-S
LARGE-N

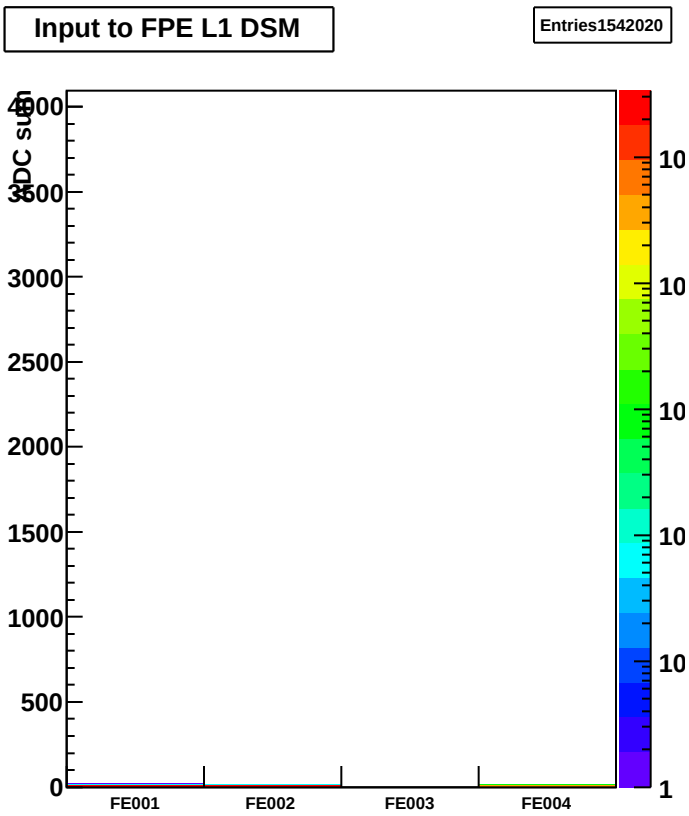

Input to FPD L2 DSM
Entries 2313030
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

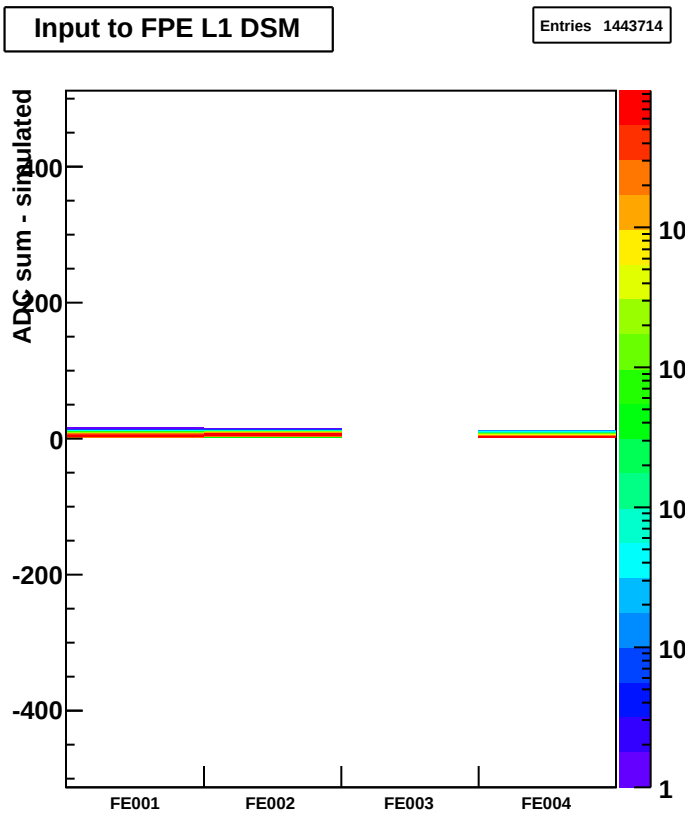

Input to FE001 QT board
Entries 1.233616e+07
ADC
channel


Input to FE002 QT board
Entries 1.233616e+07
ADC
channel

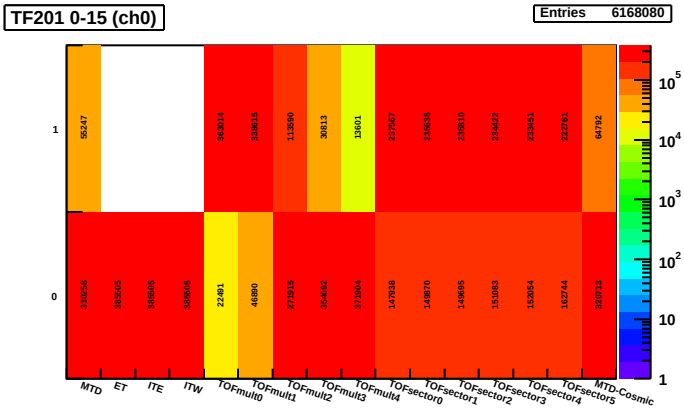

Input to FE003 QT board
Entries 1.233616e+07
ADC
channel


Input to FE004 QT board
Entries 1.233616e+07
ADC
channel

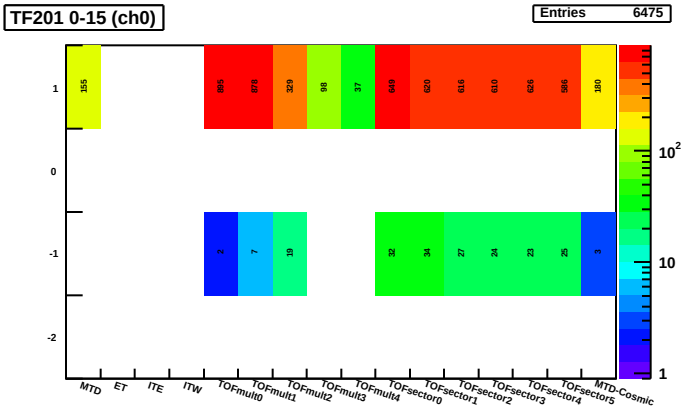

Input to FPE L1 DSM
Entries1542020
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 1443714
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

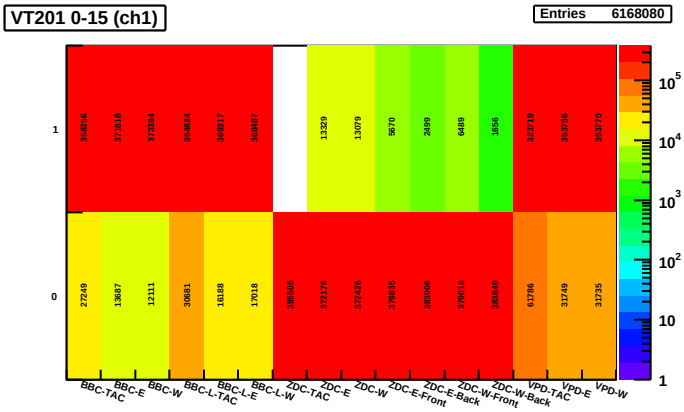
TF201 0-15 (ch0)



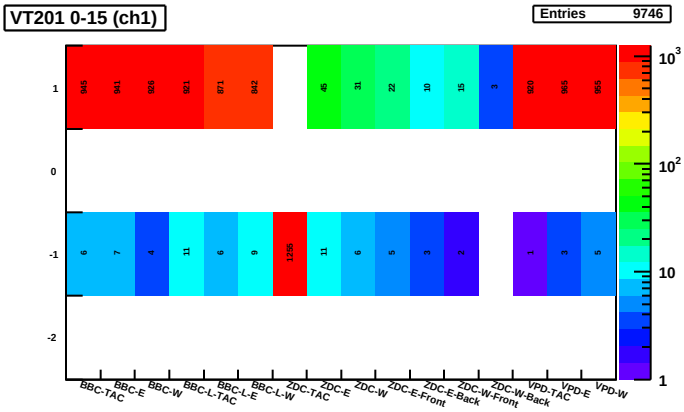
TF201 0-15 (ch0)



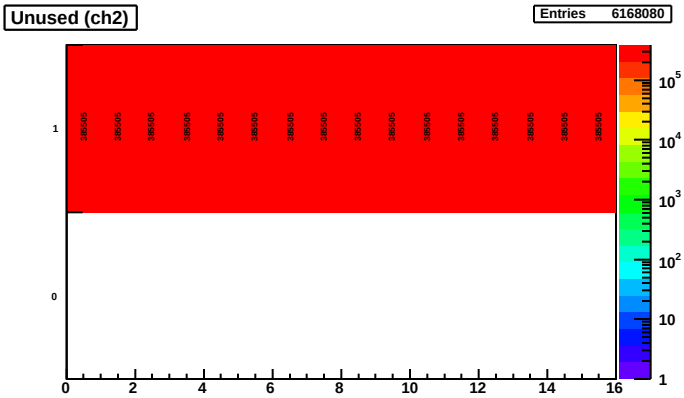
VT201 0-15 (ch1)

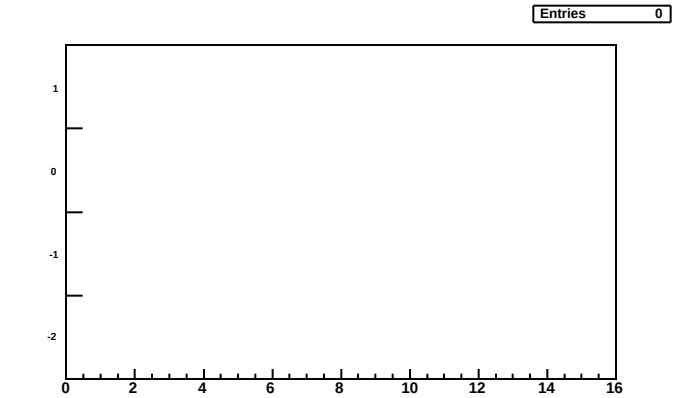


VT201 0-15 (ch1)

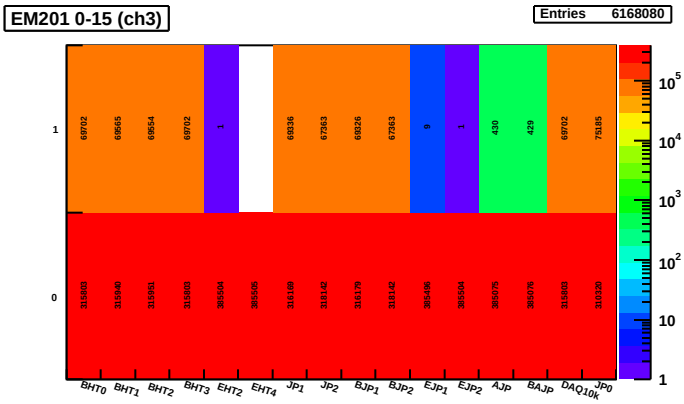


Unused (ch2)





EM201 0-15 (ch3)



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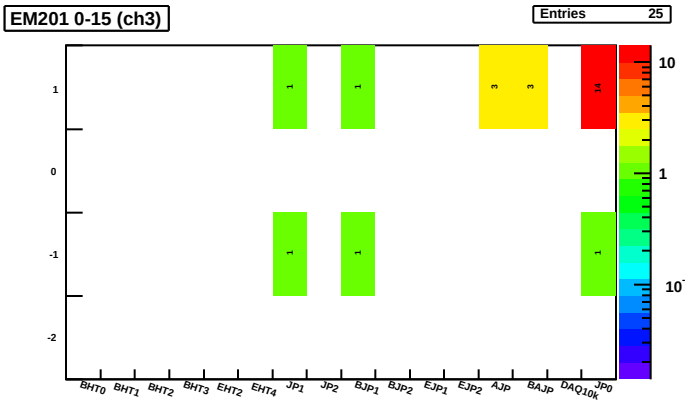
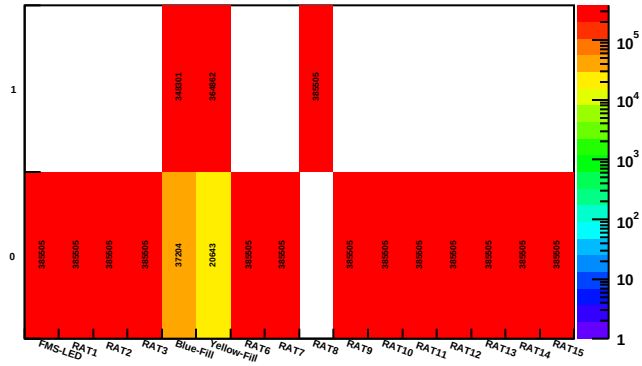
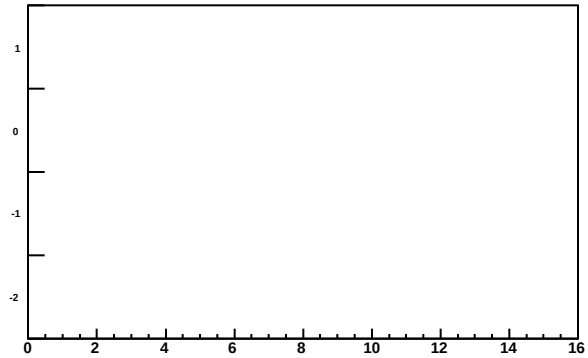


RAT board (ch4)





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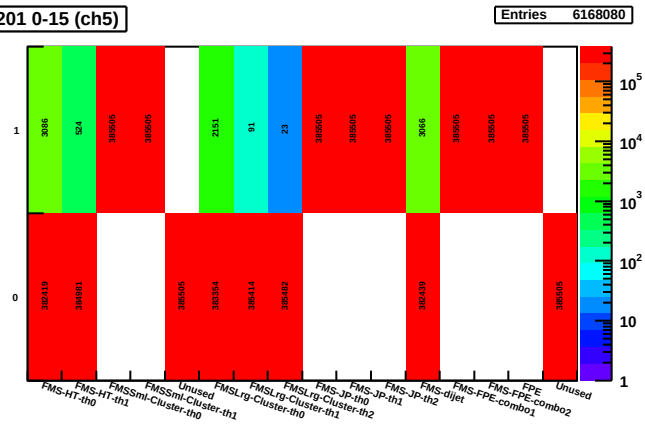

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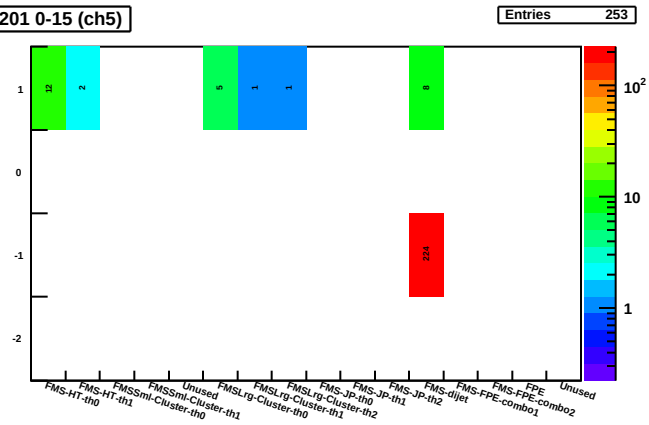

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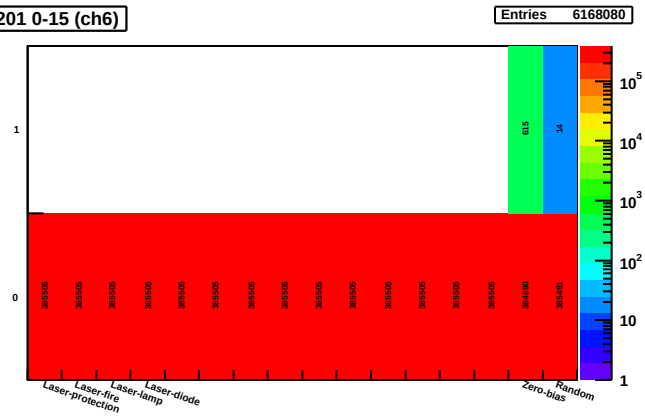

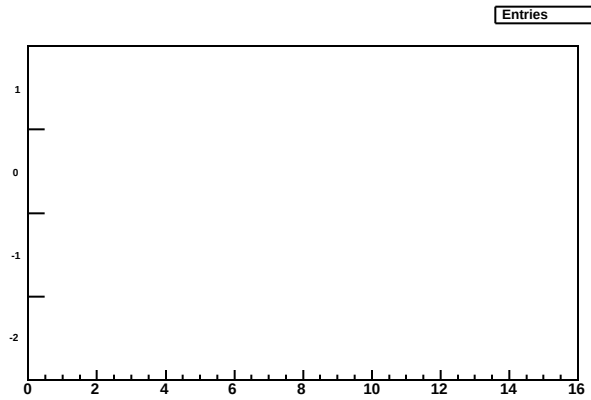
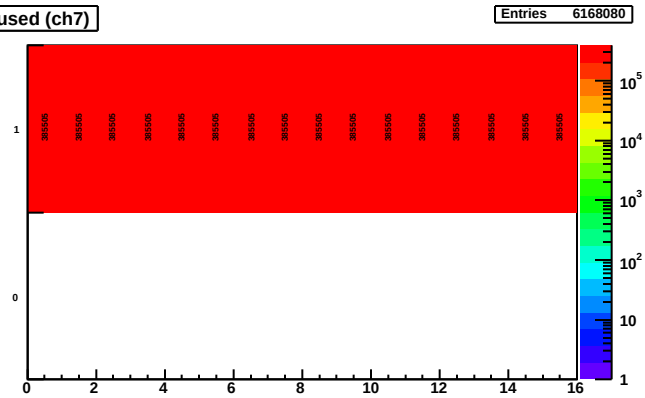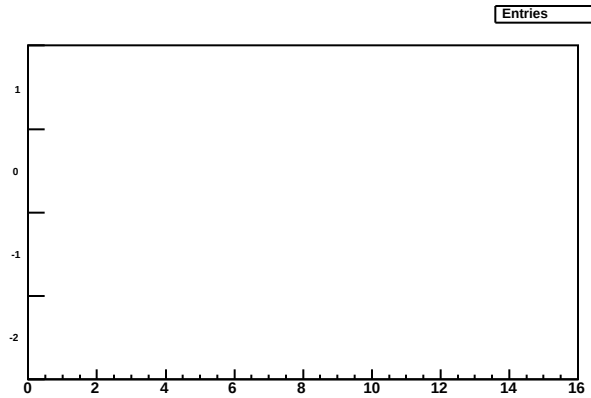
Unused (ch7)