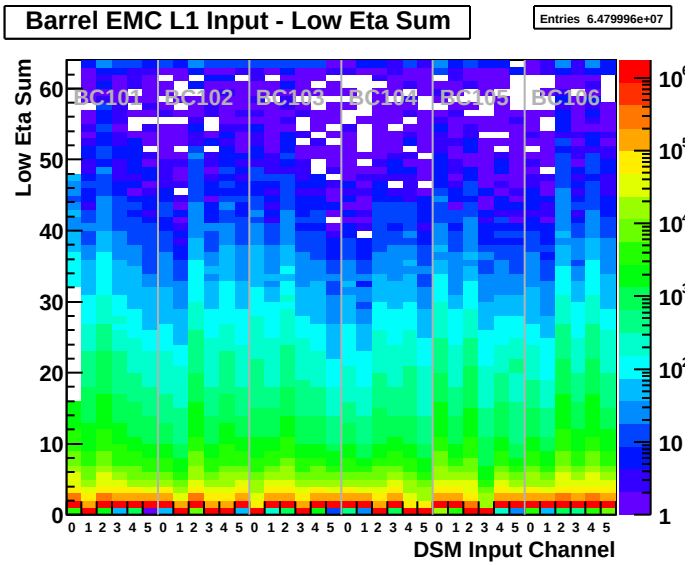
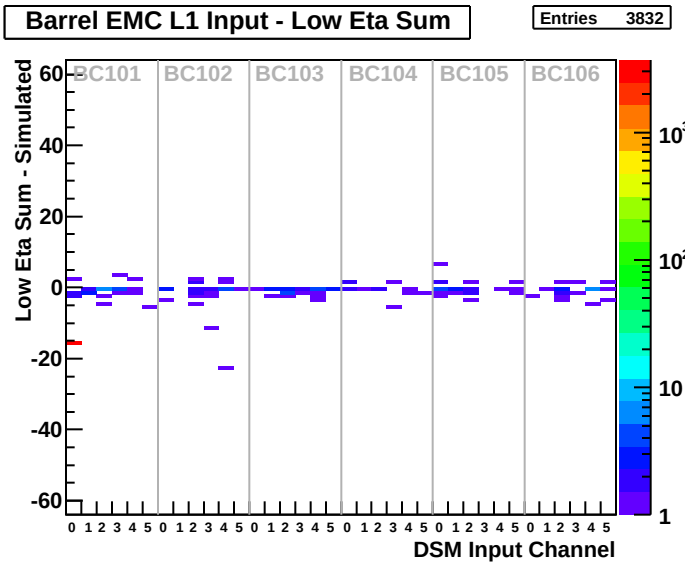
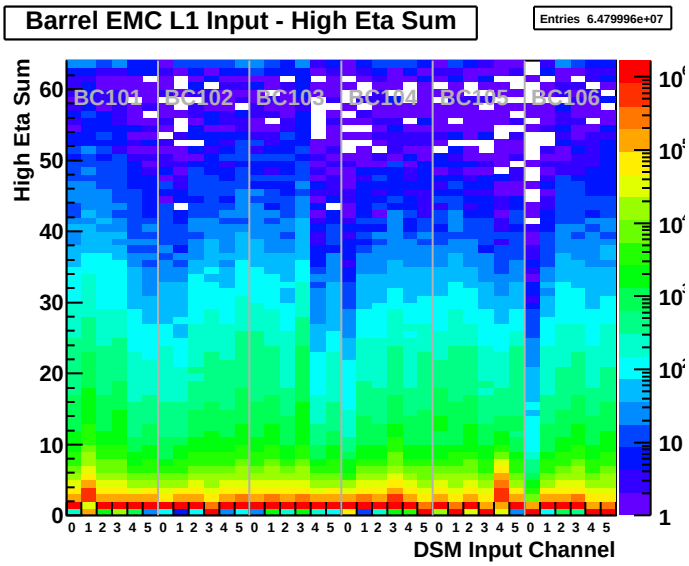
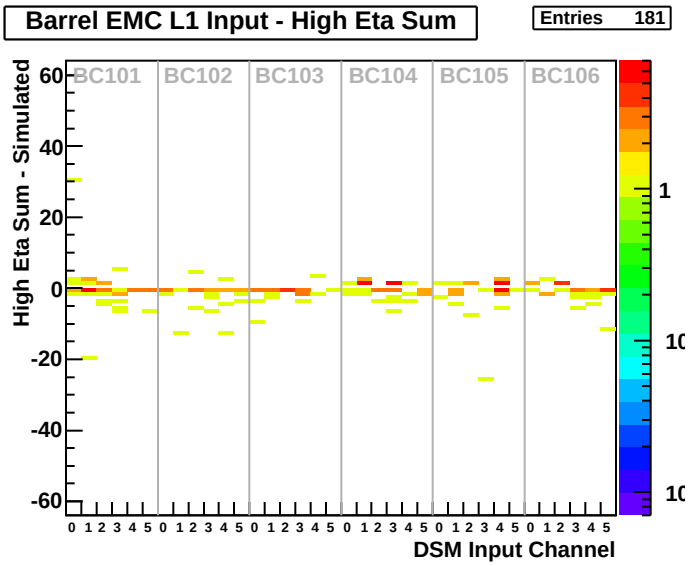
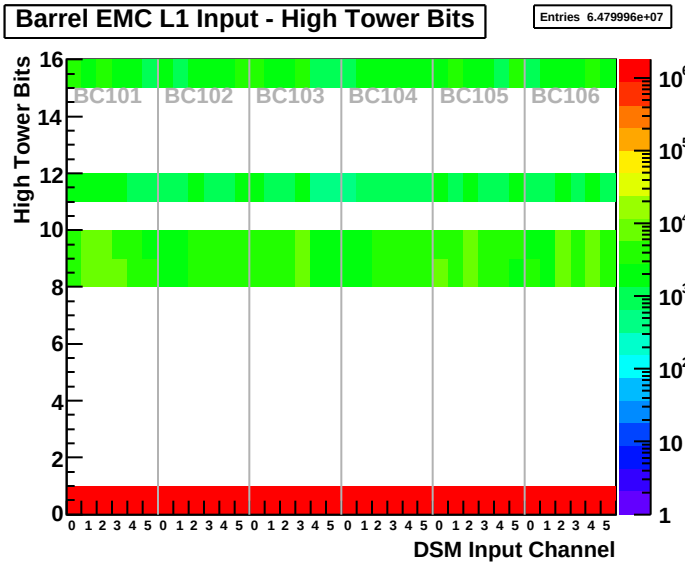
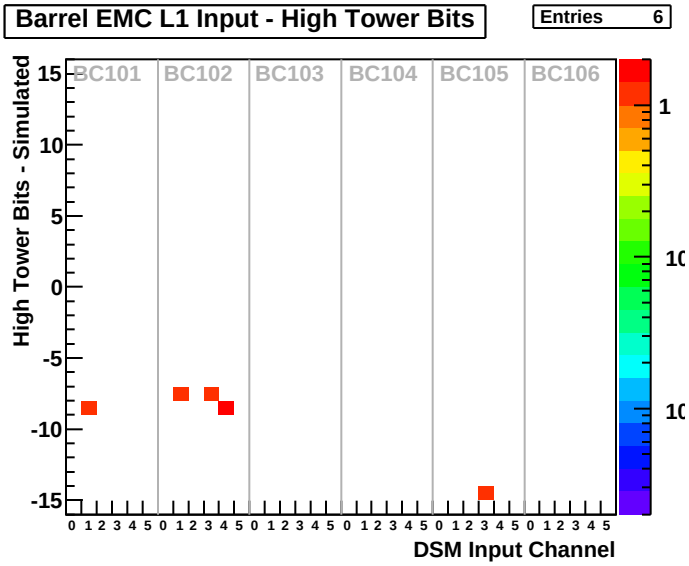

Barrel EMC L1 Input - Low Eta Sum
Entries 6.479996e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

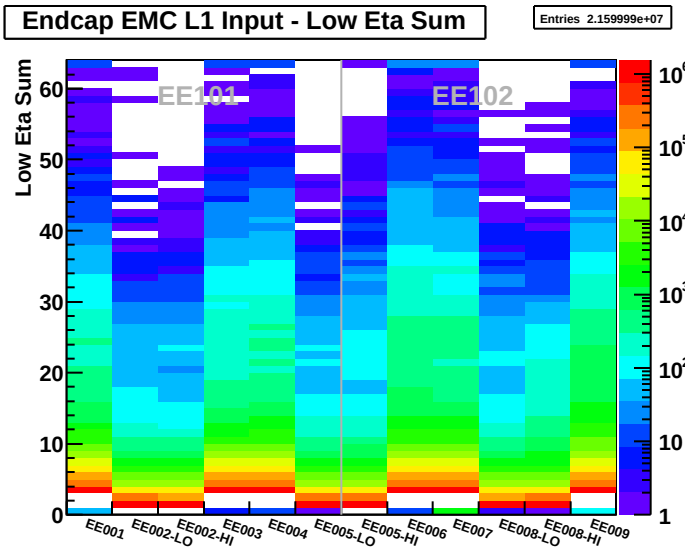

Barrel EMC L1 Input - Low Eta Sum
Entries 3832
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

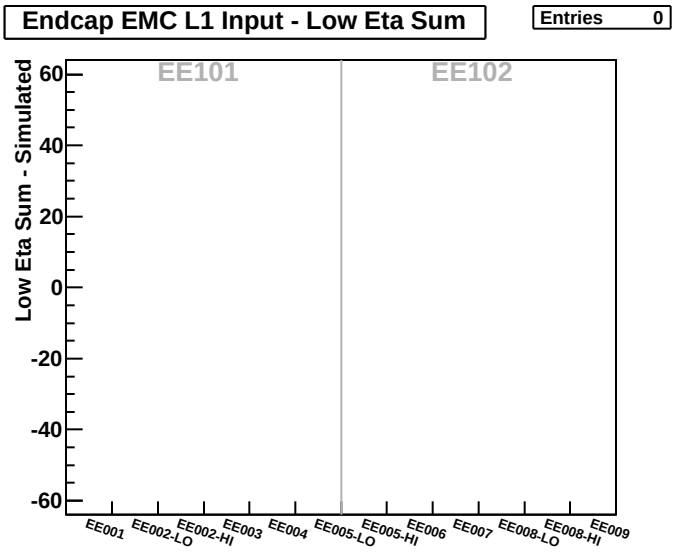

Barrel EMC L1 Input - High Eta Sum
Entries 6.479996e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

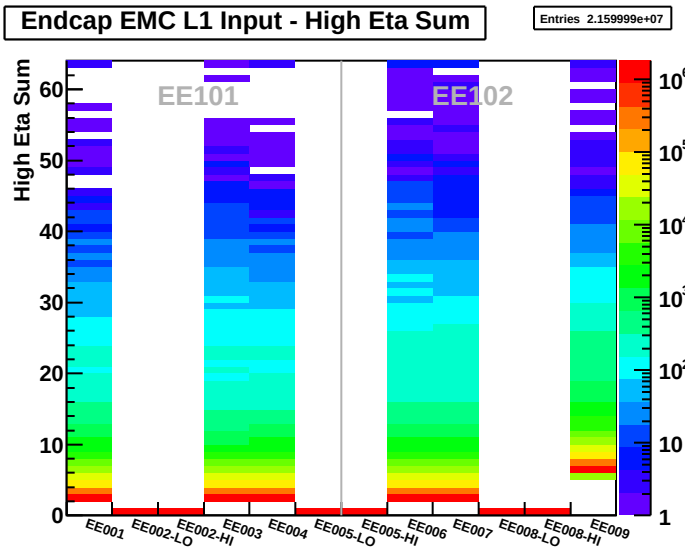

Barrel EMC L1 Input - High Eta Sum
Entries 181
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

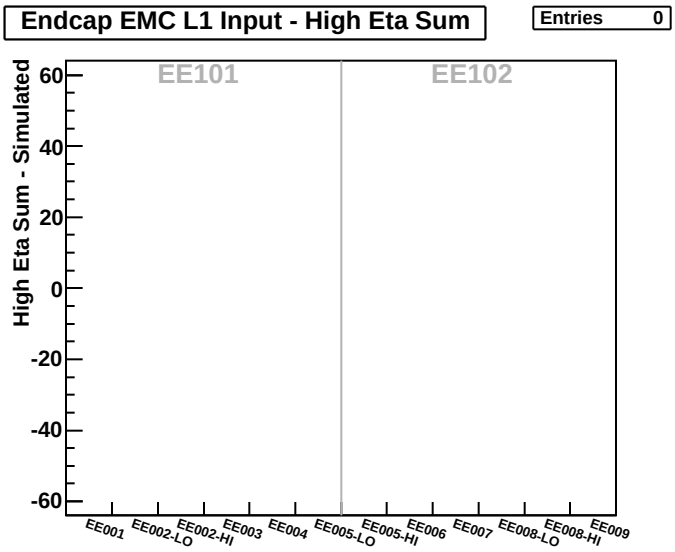

Barrel EMC L1 Input - High Tower Bits
Entries 6.479996e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

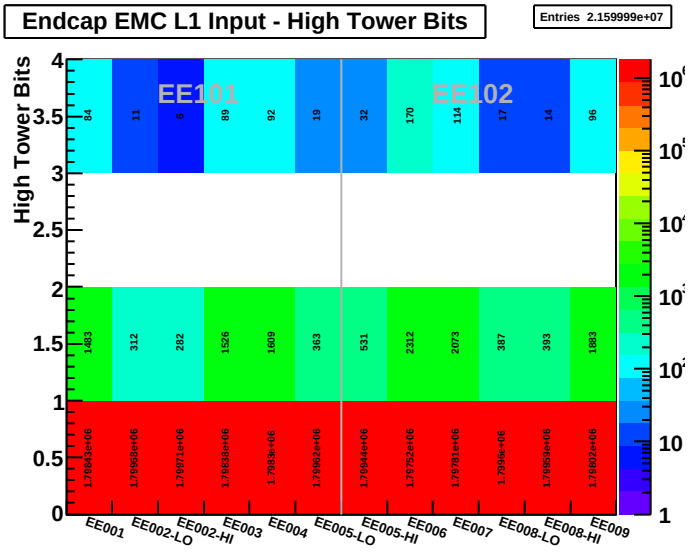

Barrel EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

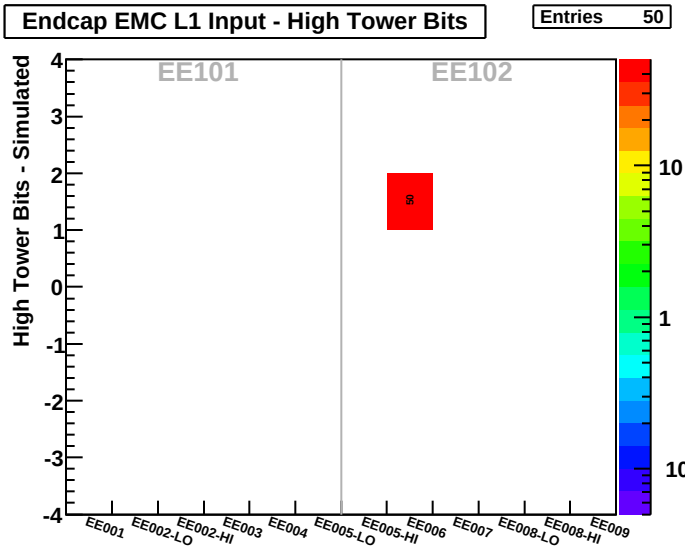

Endcap EMC L1 Input - Low Eta Sum
Entries 2.159999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

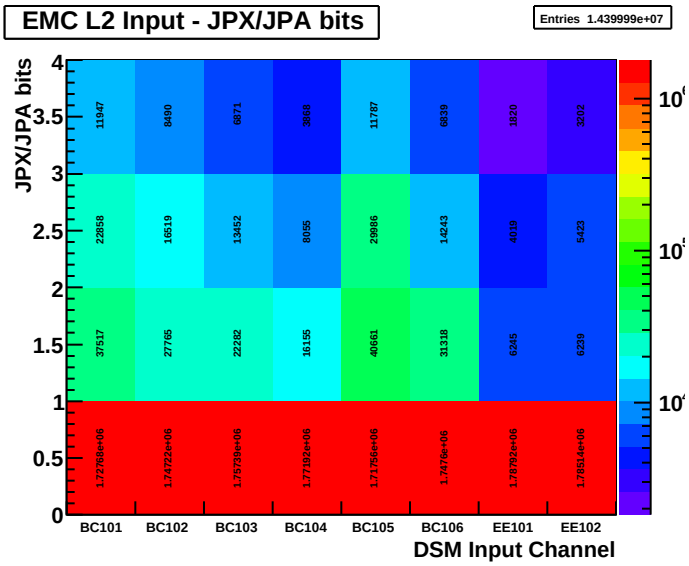

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

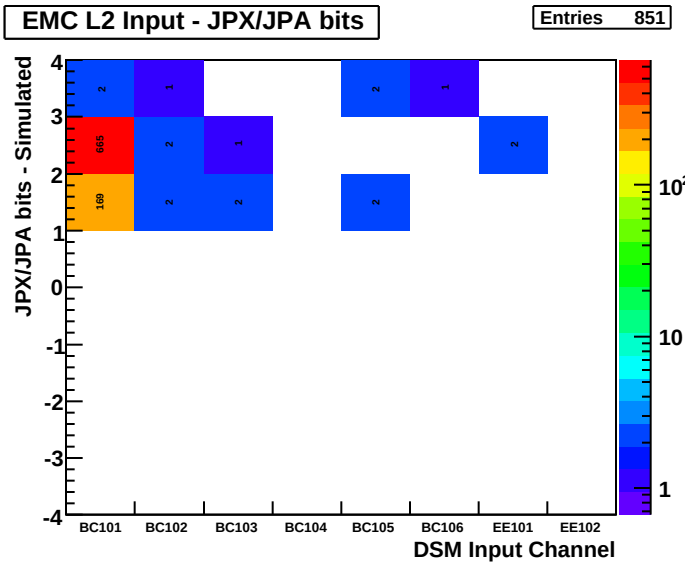

Endcap EMC L1 Input - High Eta Sum
Entries 2.159999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

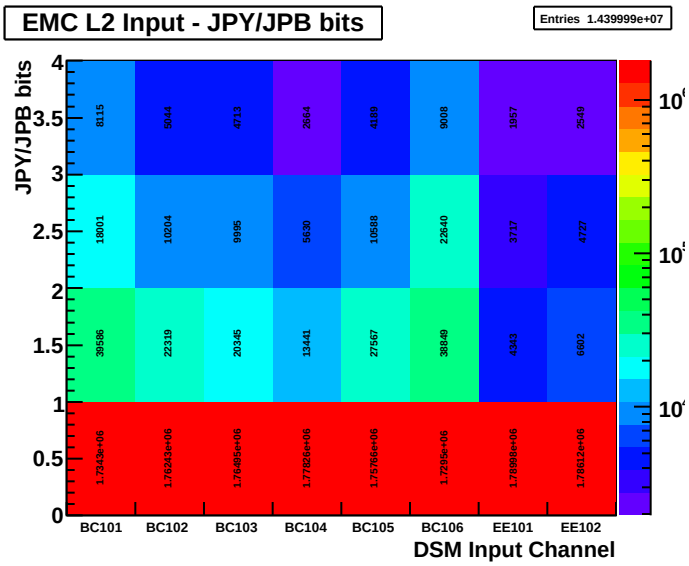

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

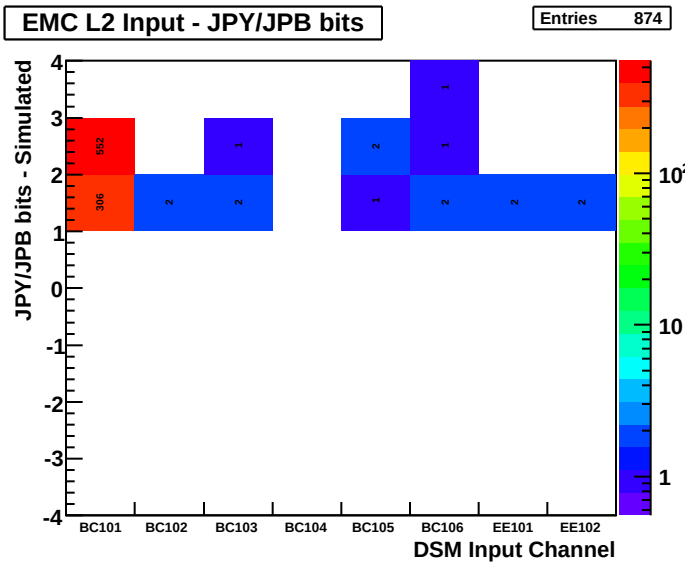

Endcap EMC L1 Input - High Tower Bits
Entries 2.159999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

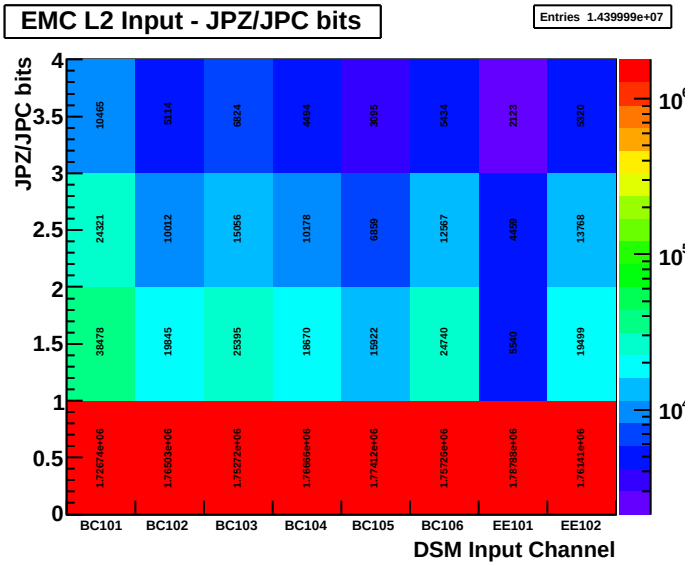

Endcap EMC L1 Input - High Tower Bits
Entries 50
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

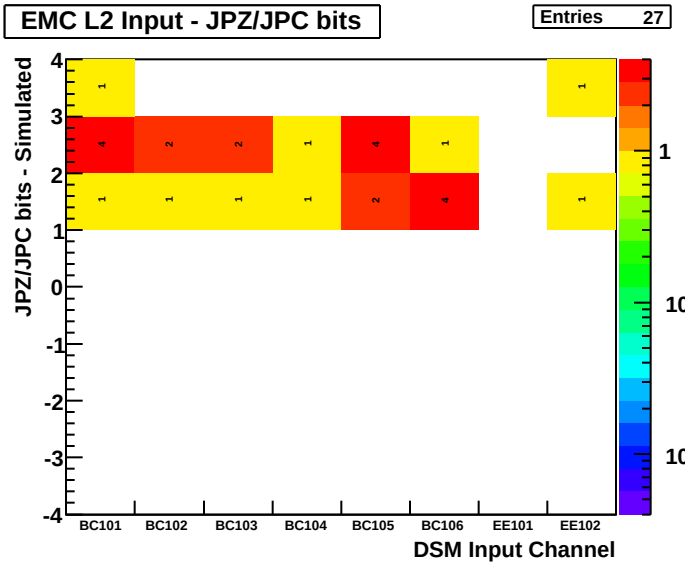

EMC L2 Input - JPX/JPA bits
Entries 1.439999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
11947 8490 6871 3868 11787 6839 1820 3202
22858 16519 13452 8055 29986 14243 4019 5423
37517 27765 22282 16195 40661 31318 6245 6239
1.72768e+06 1.74722e+06 1.75739e+06 1.77192e+06 1.71756e+06 1.7476e+06 1.78792e+06 1.78514e+06

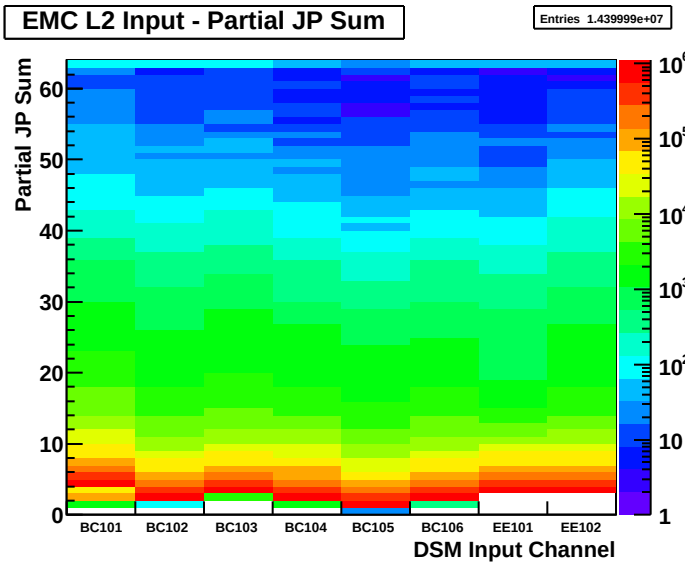

EMC L2 Input - JPX/JPA bits
Entries 851
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 2 1
665 2 1 2
169 2 2 2

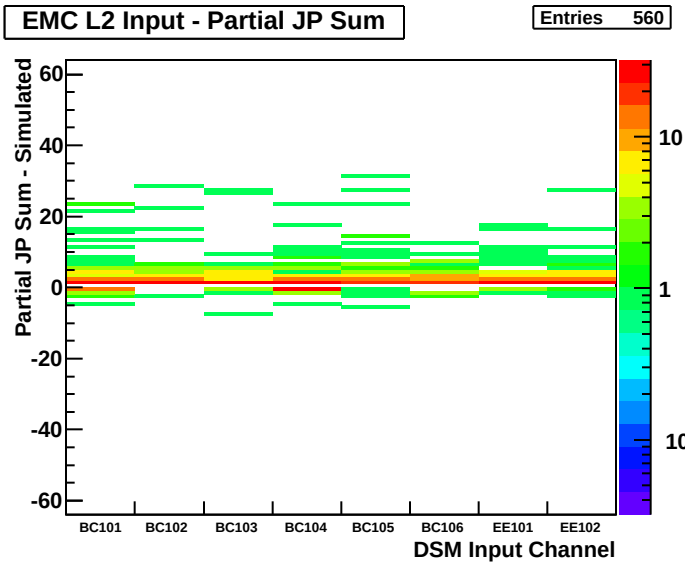

EMC L2 Input - JPY/JPB bits
Entries 1.439999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8115 5044 4713 2664 4189 9008 1957 2549
18001 10204 9995 5630 10588 22640 3717 4727
39586 22319 20345 13441 27567 38849 4343 6602
1.7343e+06 1.76243e+06 1.76485e+06 1.77826e+06 1.75786e+06 1.7295e+06 1.78998e+06 1.78612e+06

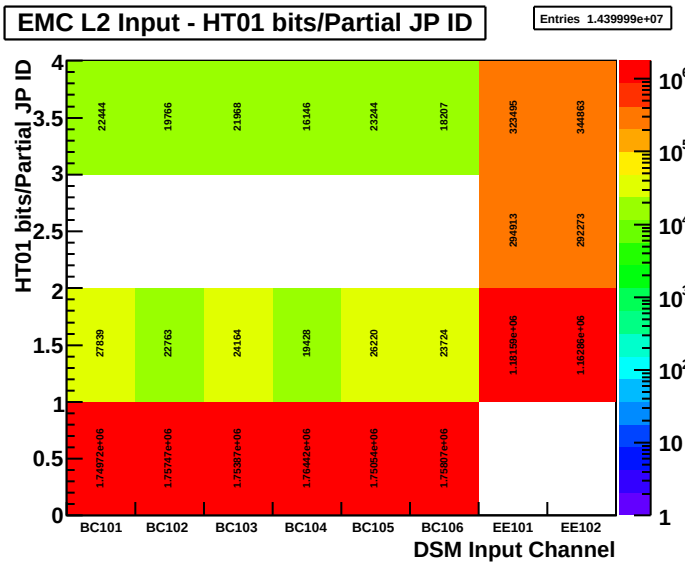

EMC L2 Input - JPY/JPB bits
Entries 874
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
552 1 2 1
306 2 2 1 2 2 2

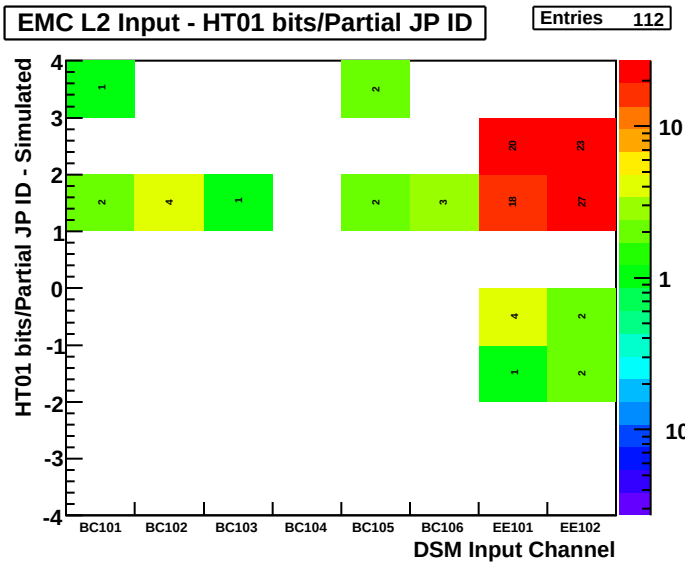

EMC L2 Input - JPZ/JPC bits
Entries 1.439999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10465 5114 6824 4494 3095 5434 2123 5320
24321 10012 15056 10178 6859 12567 4459 13768
38478 19845 25395 18670 15922 24740 5540 19499
1.72674e+06 1.76503e+06 1.75272e+06 1.76665e+06 1.77412e+06 1.75726e+06 1.78783e+06 1.76141e+06

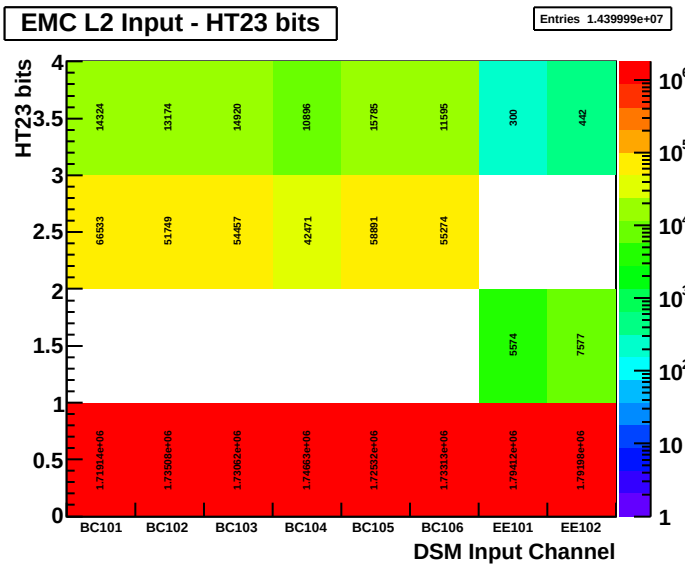

EMC L2 Input - JPZ/JPC bits
Entries 27
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1
4 2 2 1 4 1
1 1 1 1 2 4 1

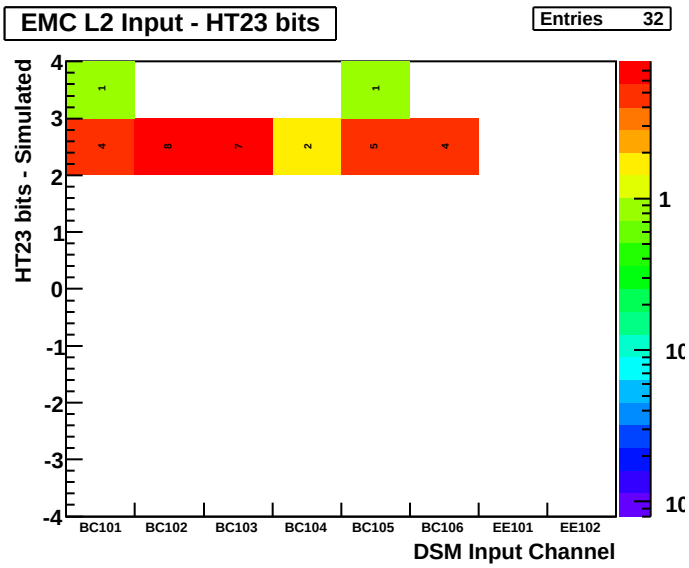

EMC L2 Input - Partial JP Sum
Entries 1.439999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

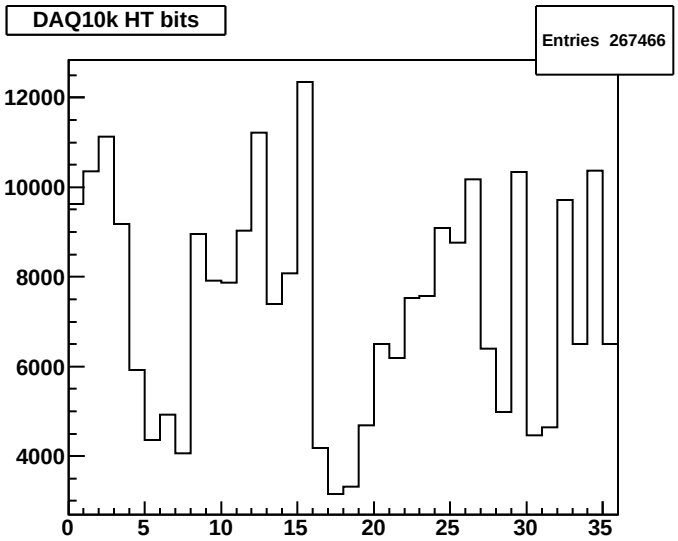

EMC L2 Input - Partial JP Sum
Entries 560
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

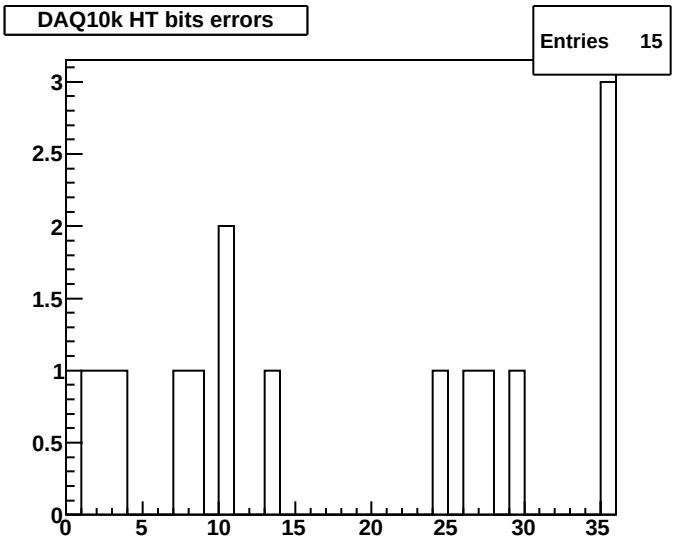

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.439999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

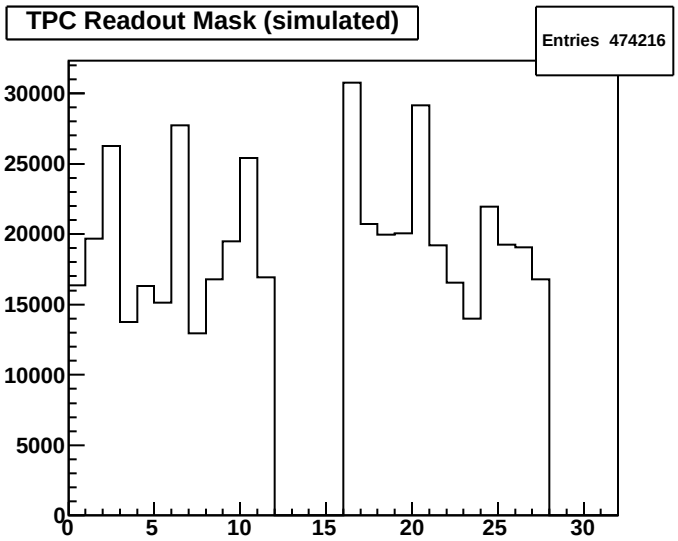

EMC L2 Input - HT01 bits/Partial JP ID
Entries 112
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

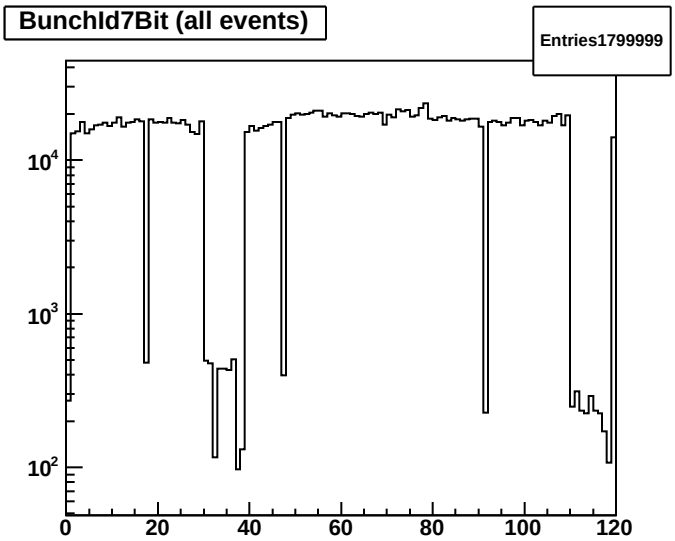

EMC L2 Input - HT23 bits
Entries 1.439999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

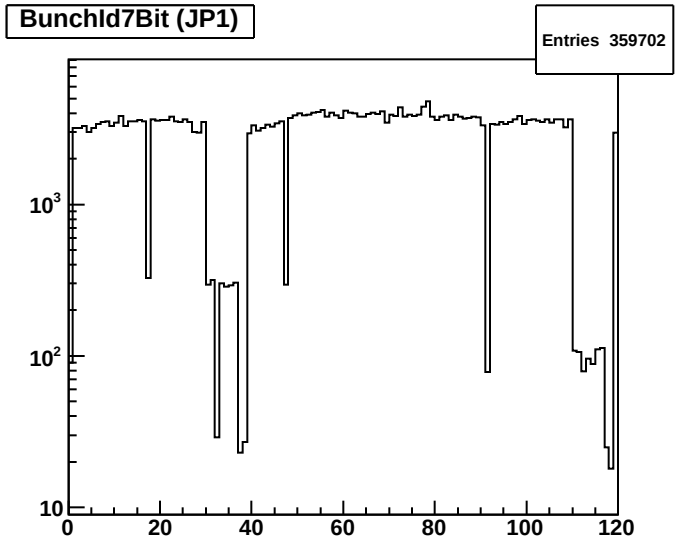

EMC L2 Input - HT23 bits
Entries 32
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

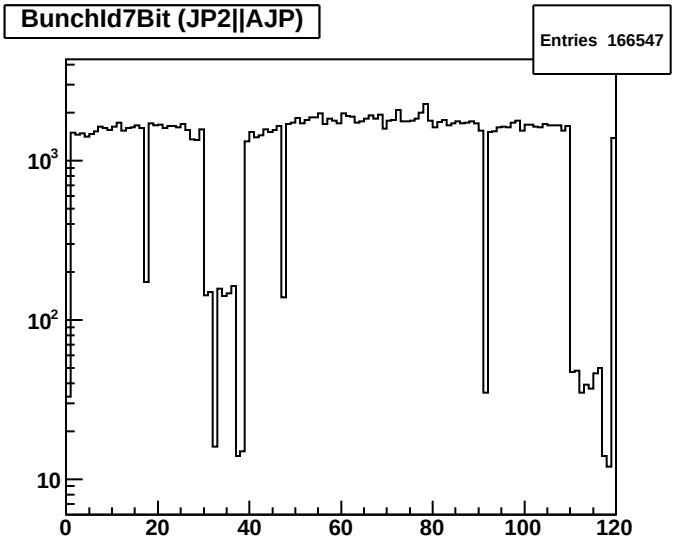

DAQ10k HT bits
Entries 267466

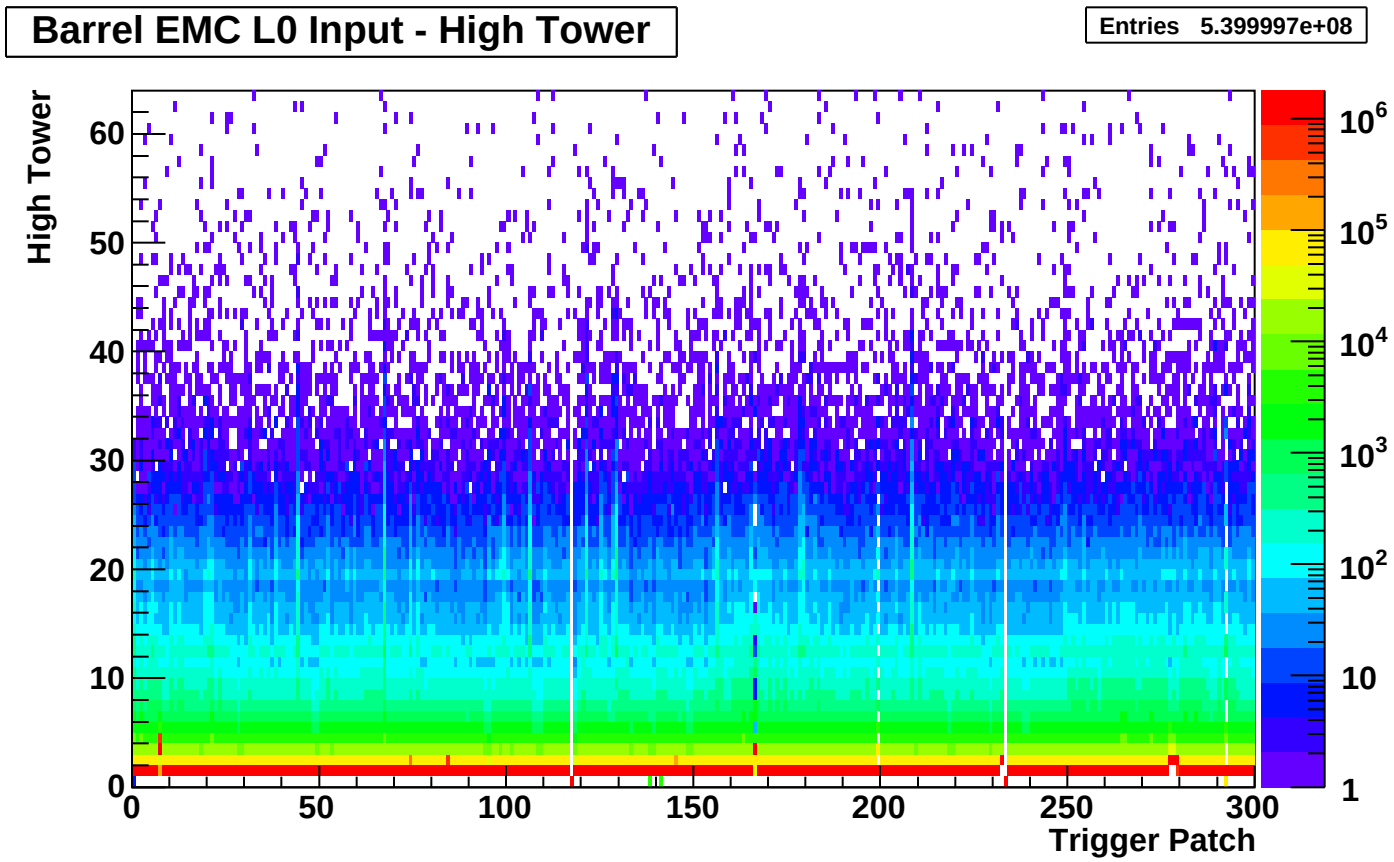

DAQ10k HT bits errors
Entries 15

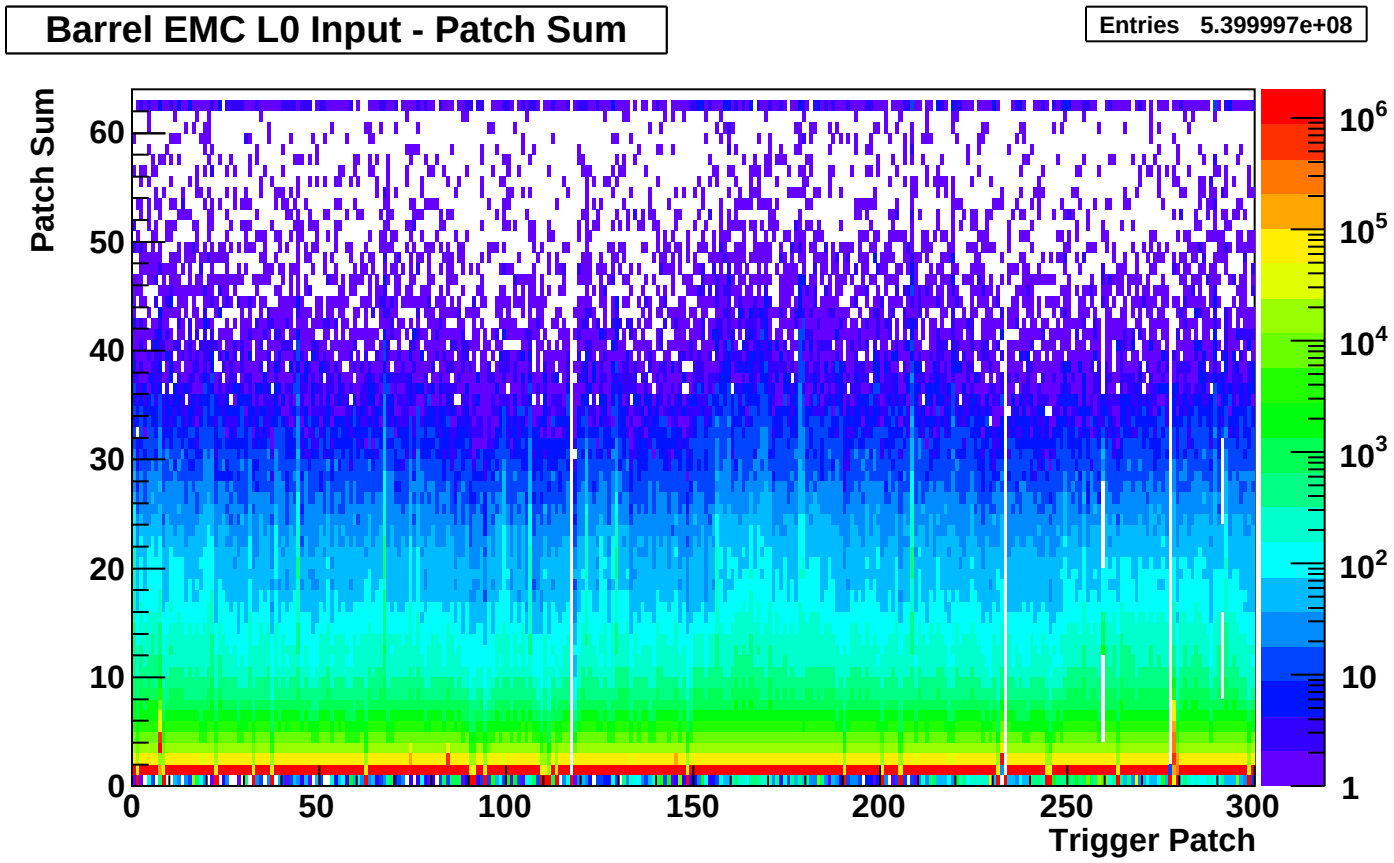

TPC Readout Mask (simulated)
Entries 474216

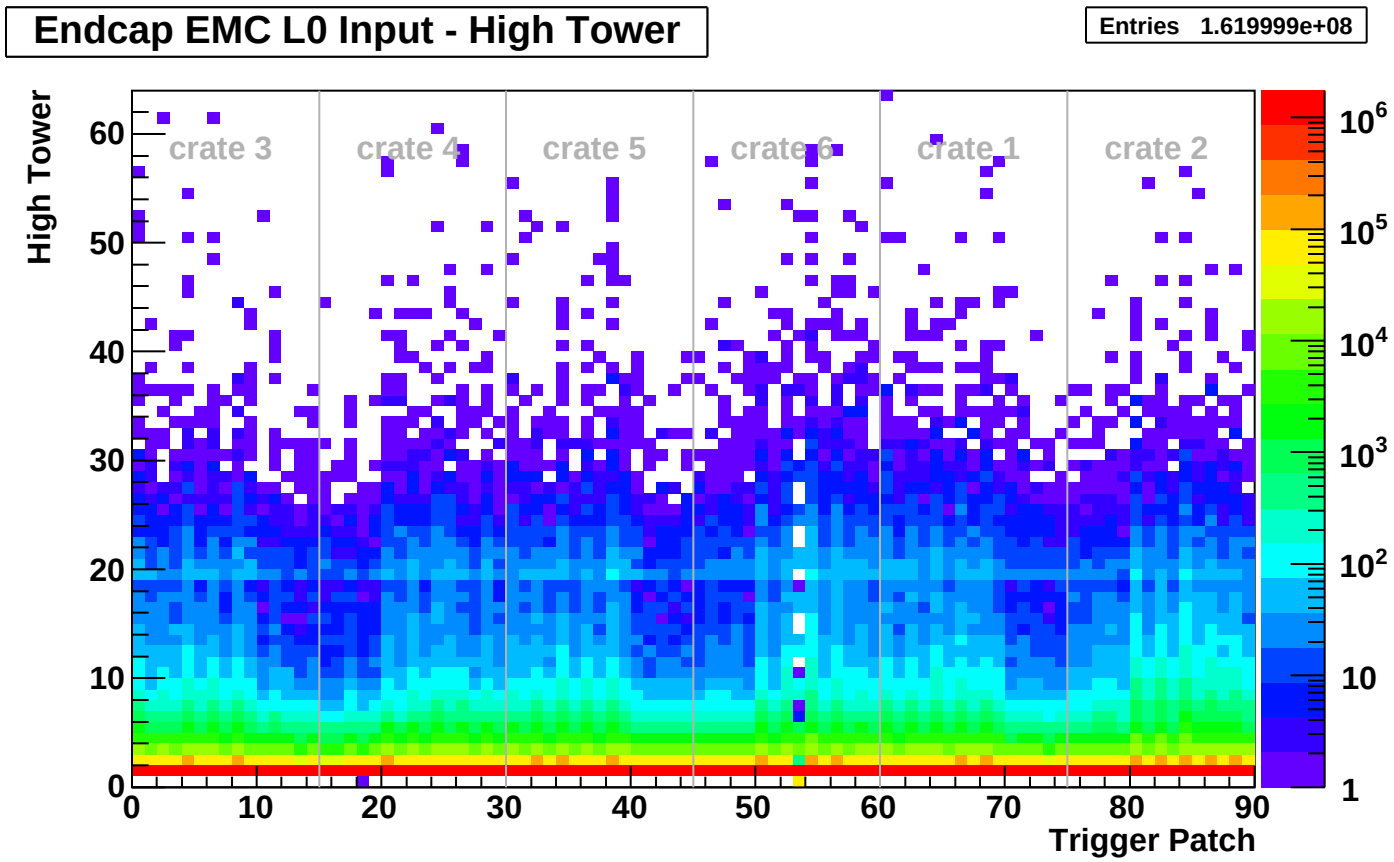

BunchId7Bit (all events)
Entries1799999

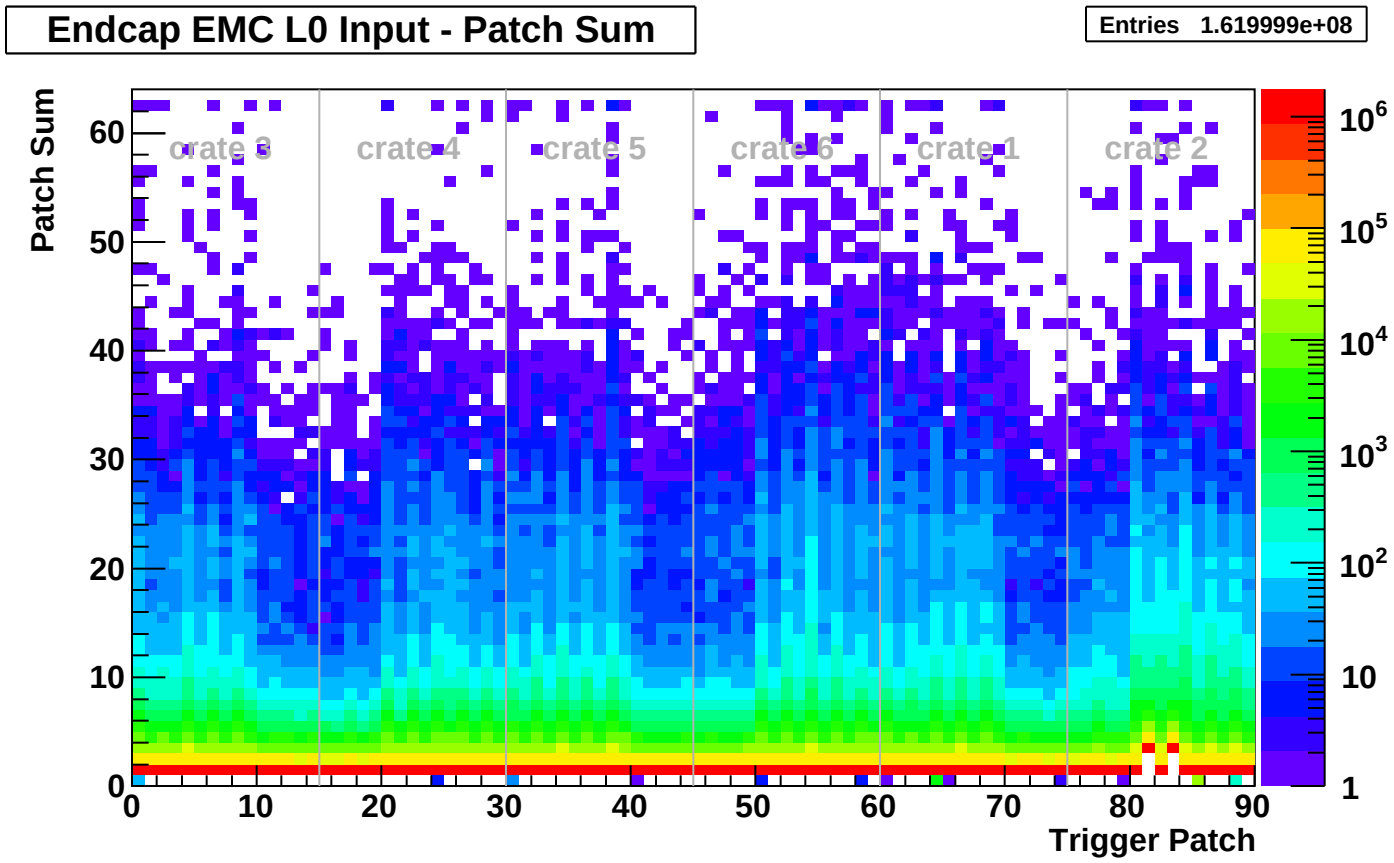

BunchId7Bit (JP1)
Entries 359702

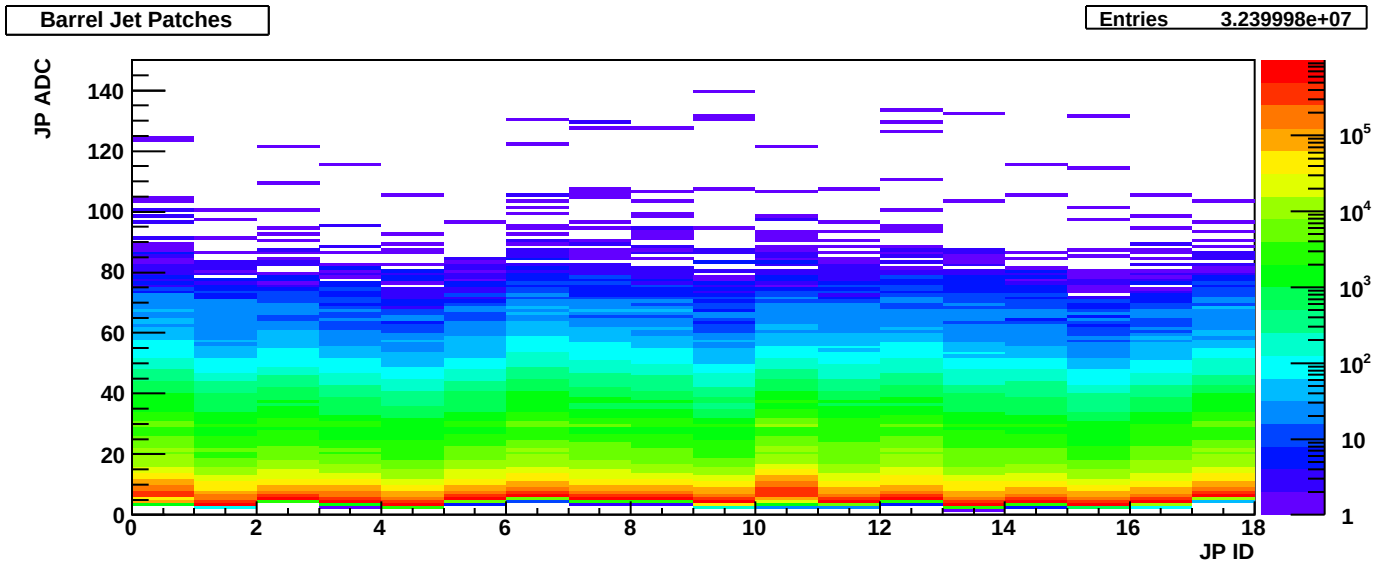

BunchId7Bit (JP2||AJP)
Entries 166547

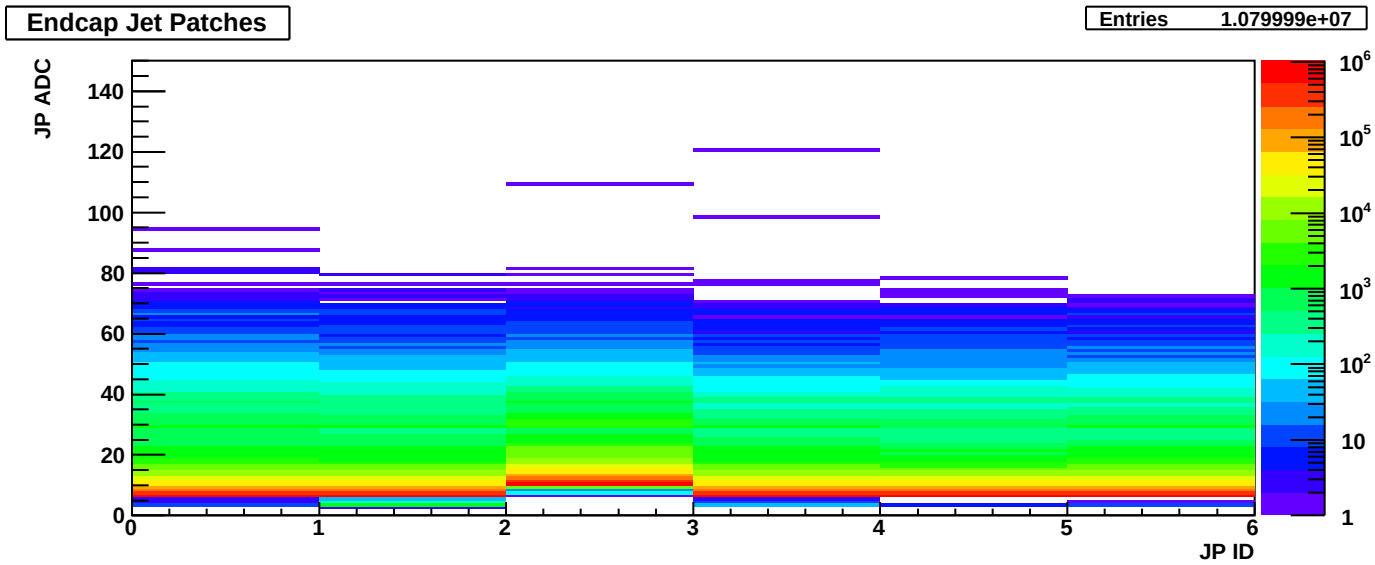

Barrel EMC L0 Input - High Tower
Entries 5.399997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

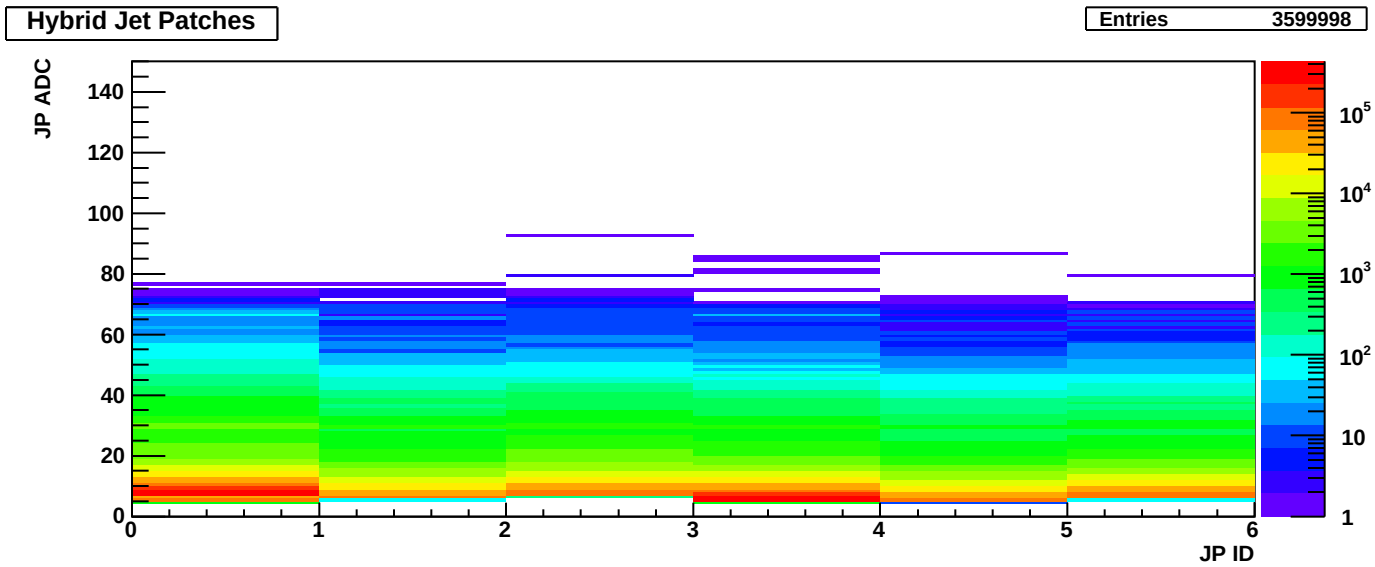

Barrel EMC L0 Input - Patch Sum
Entries 5.399997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

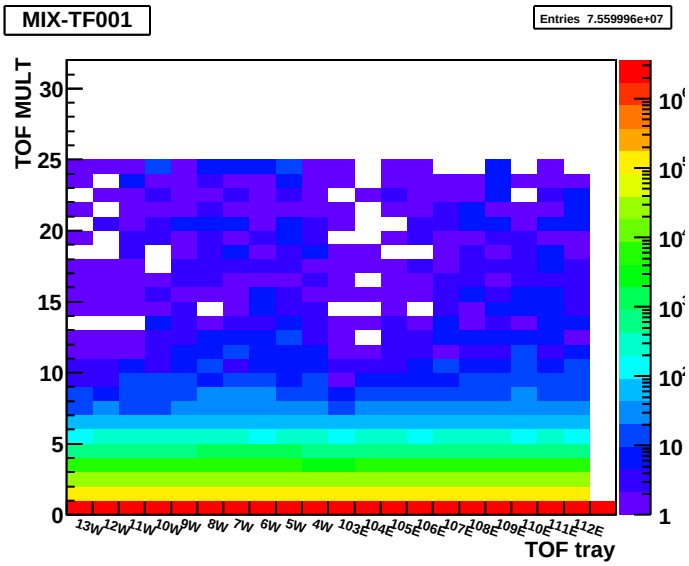

Endcap EMC L0 Input - High Tower
Entries 1.619999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

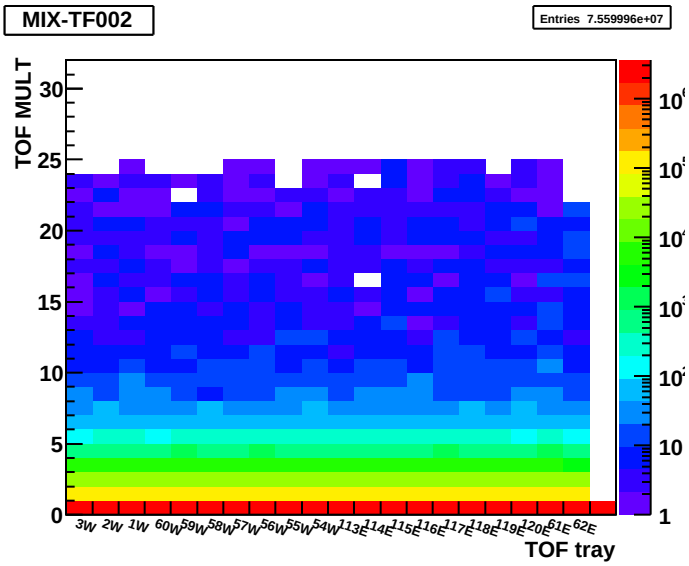

Endcap EMC L0 Input - Patch Sum
Entries 1.619999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

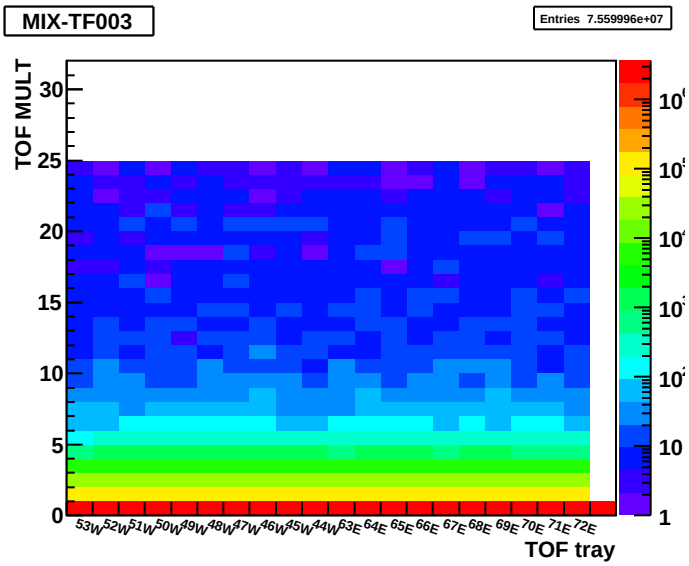

Barrel Jet Patches
Entries 3.239998e+07
JP ADC
JP ID

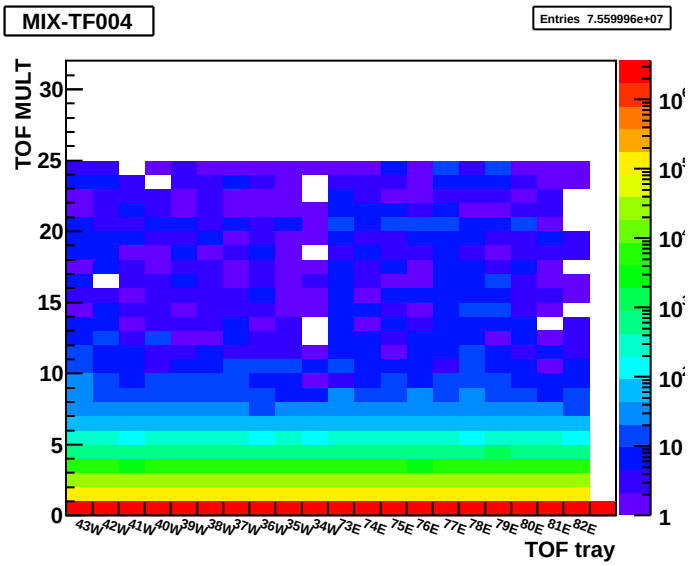

Endcap Jet Patches
Entries 1.079999e+07
JP ADC
JP ID

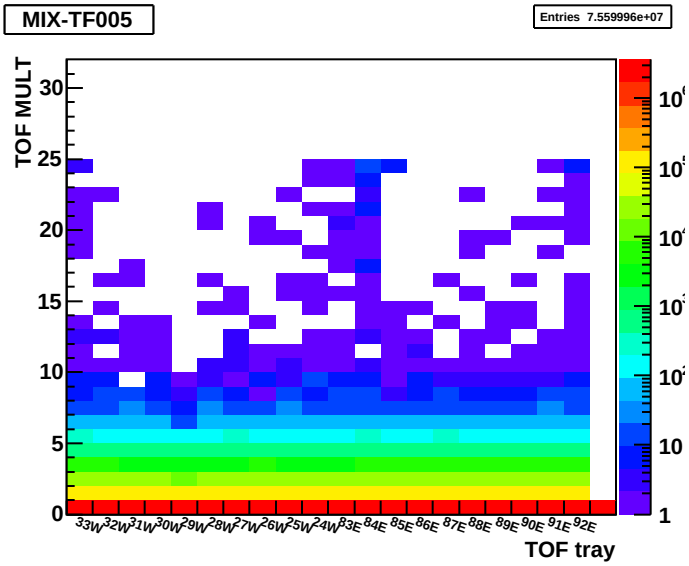

Hybrid Jet Patches
Entries 3599998
JP ADC
JP ID

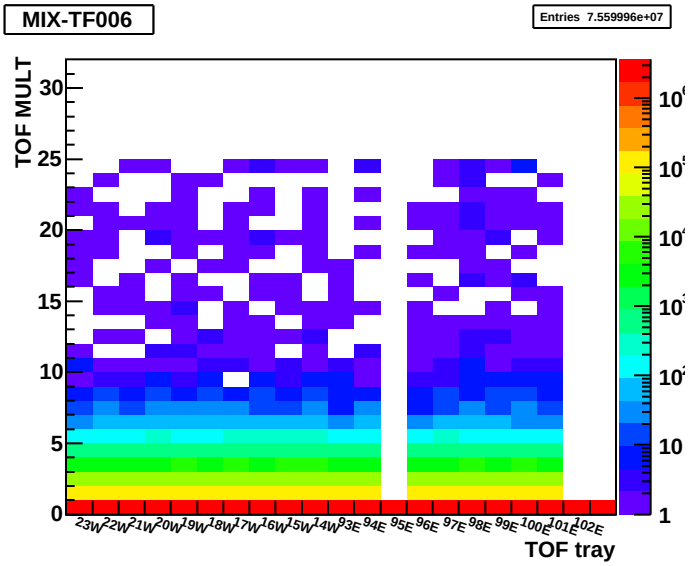

MIX-TF001
Entries 7.559996e+07
TOF MULT
TOF tray

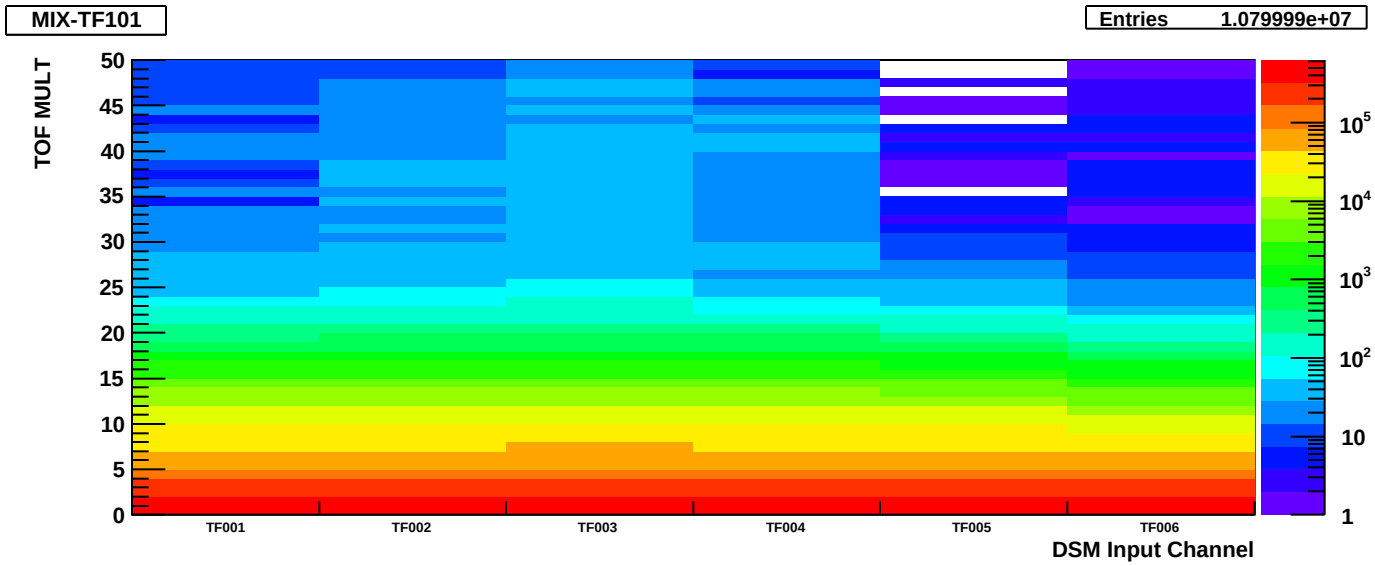

MIX-TF002
Entries 7.559996e+07
TOF MULT
TOF tray

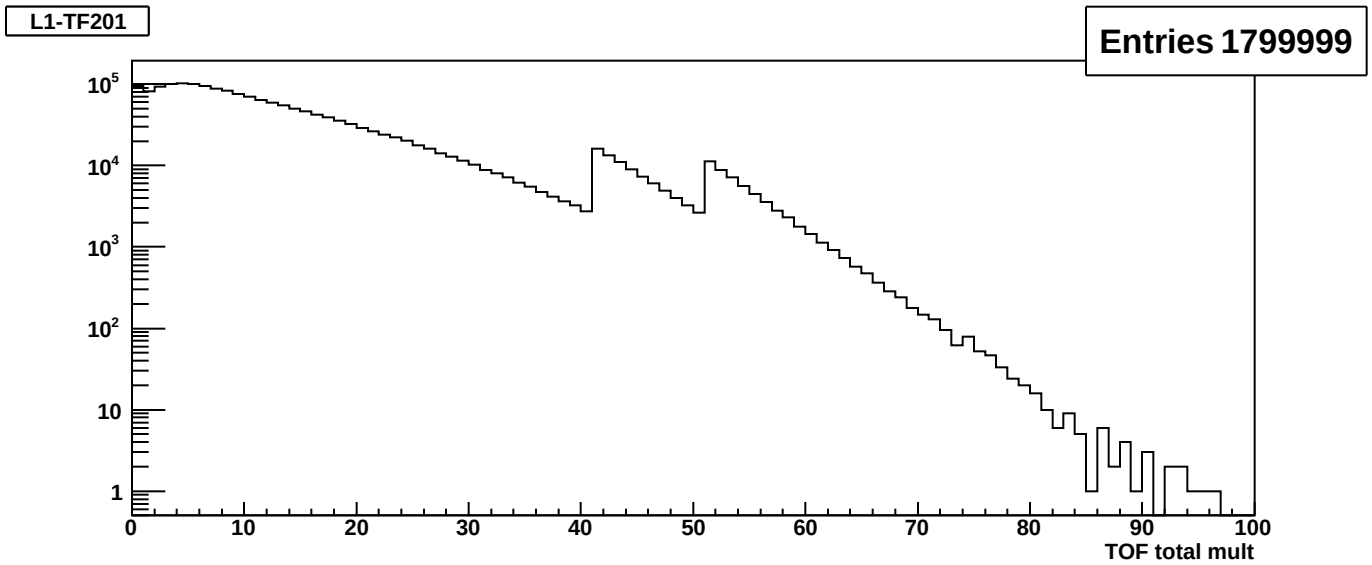

MIX-TF003
Entries 7.559996e+07
TOF MULT
TOF tray

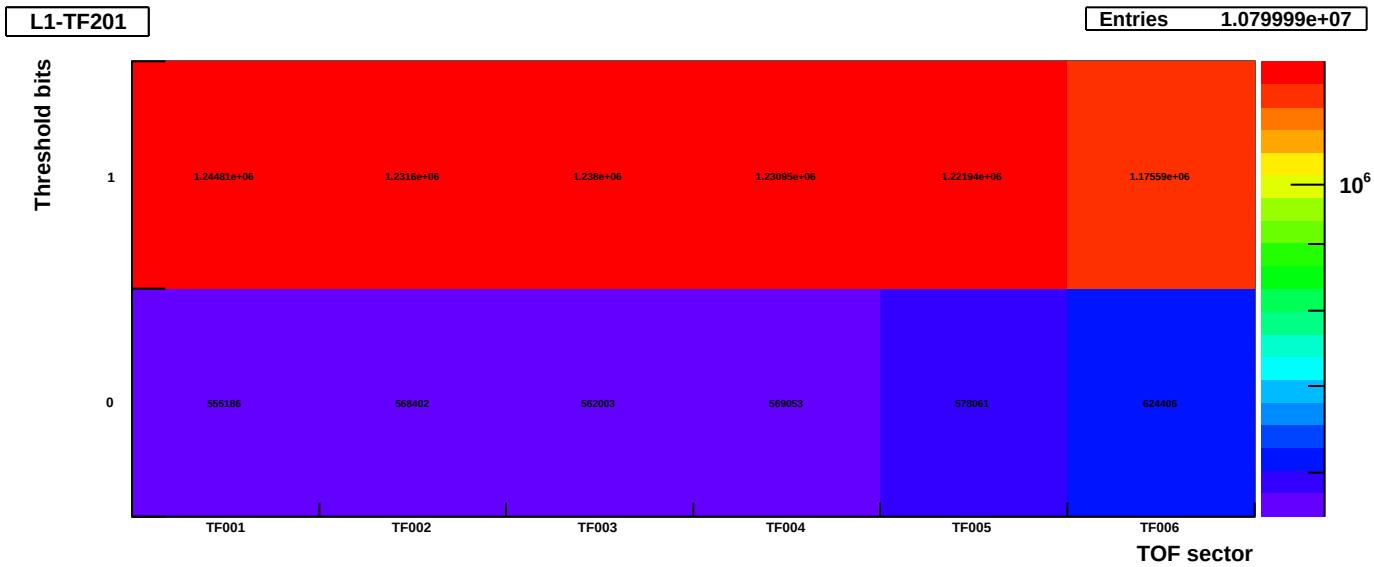

MIX-TF004
Entries 7.559996e+07
TOF MULT
TOF tray

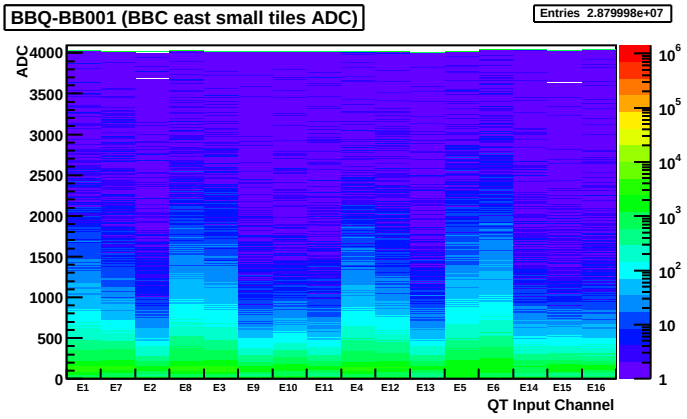

MIX-TF005
Entries 7.559996e+07
TOF MULT
TOF tray

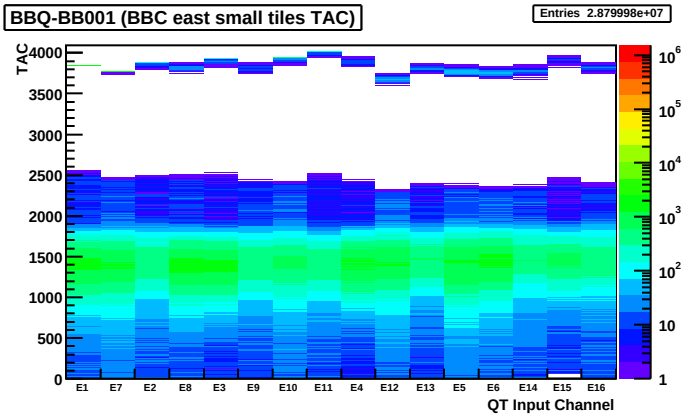

MIX-TF006
Entries 7.559996e+07
TOF MULT
TOF tray

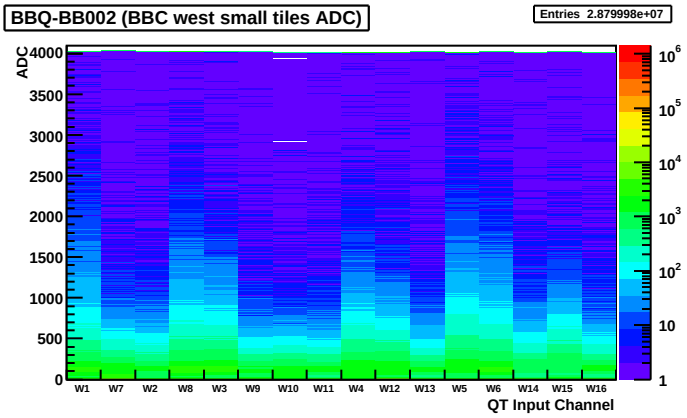

MIX-TF101
Entries 1.079999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

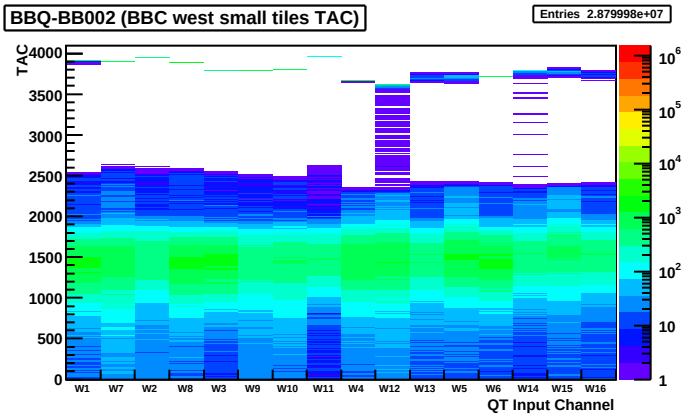

L1-TF201
Entries 1799999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

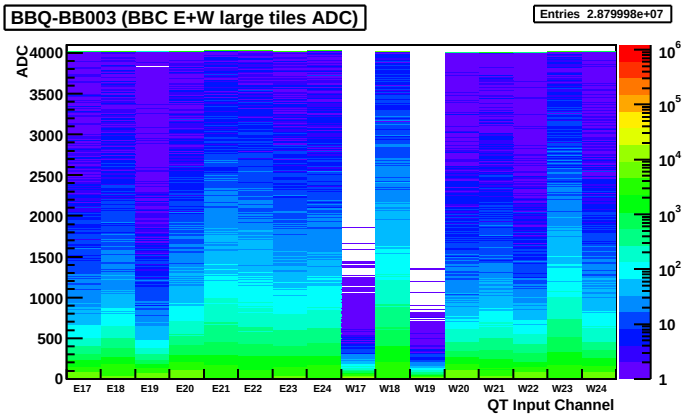

L1-TF201
Entries 1.079999e+07
Threshold bits
1
0
1.24481e+06
1.2316e+06
1.238e+06
1.23095e+06
1.22194e+06
1.17559e+06
555186
568402
562003
569053
578061
624406
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

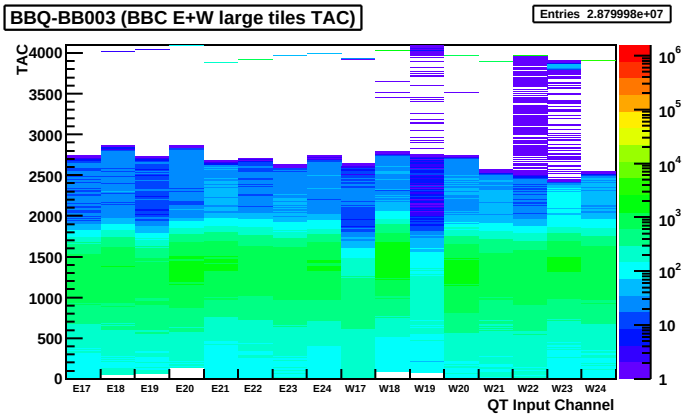

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.879998e+07
ADC
QT Input Channel

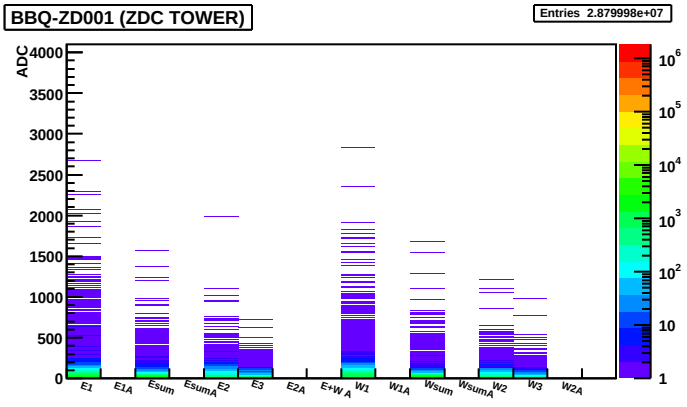

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.879998e+07
TAC
QT Input Channel

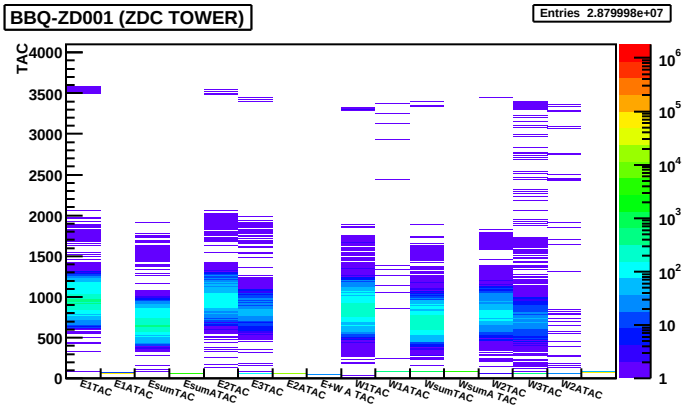

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.879998e+07
ADC
QT Input Channel

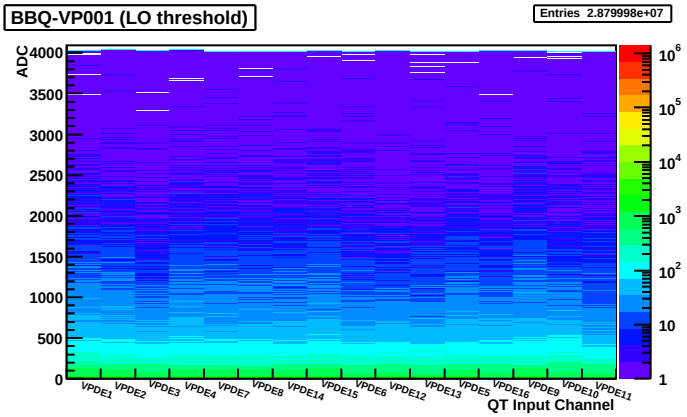

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.879998e+07
TAC
QT Input Channel

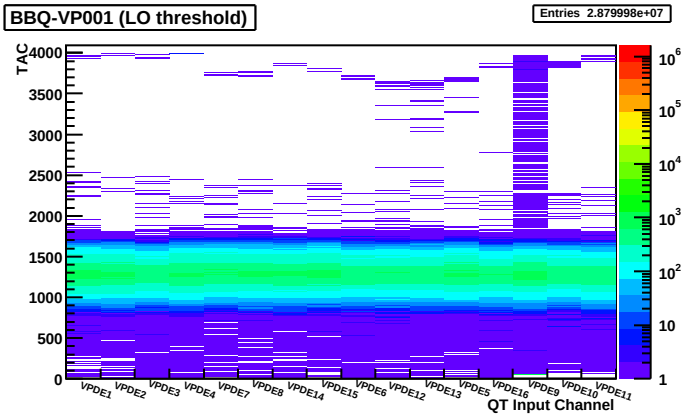

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.879998e+07
ADC
QT Input Channel

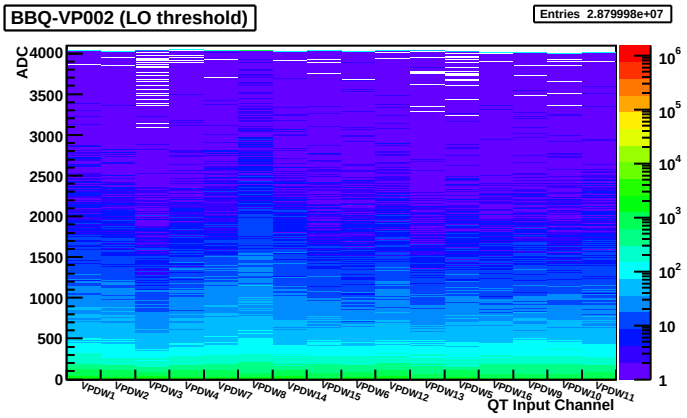

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.879998e+07
TAC
QT Input Channel

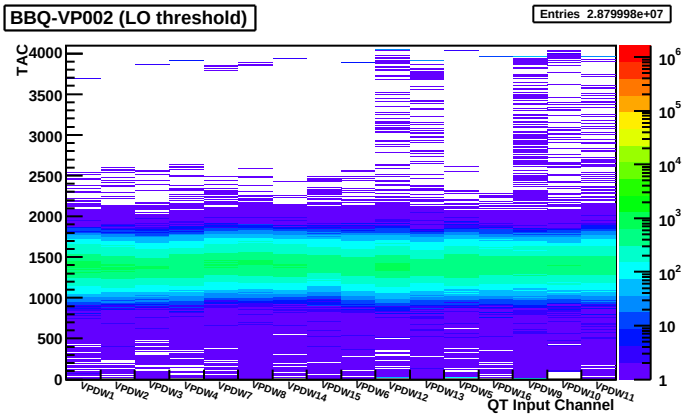

BBQ-ZD001 (ZDC TOWER)
Entries 2.879998e+07
ADC

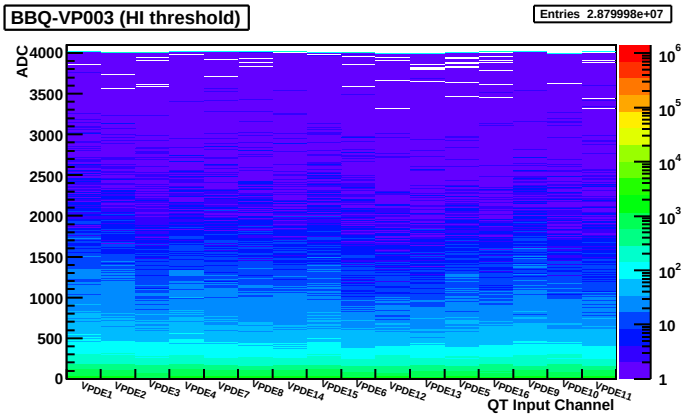

BBQ-ZD001 (ZDC TOWER)
Entries 2.879998e+07
TAC

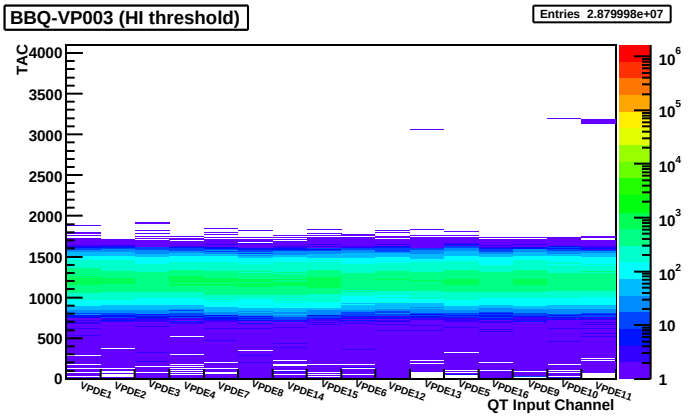

BBQ-VP001 (LO threshold)
Entries 2.879998e+07
ADC
QT Input Channel

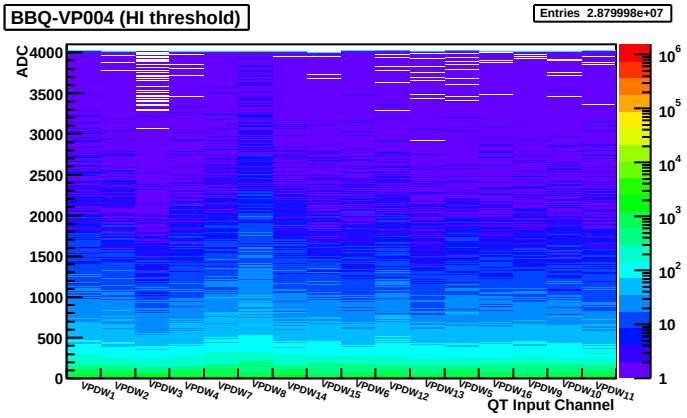

BBQ-VP001 (LO threshold)
Entries 2.879998e+07
TAC
QT Input Channel

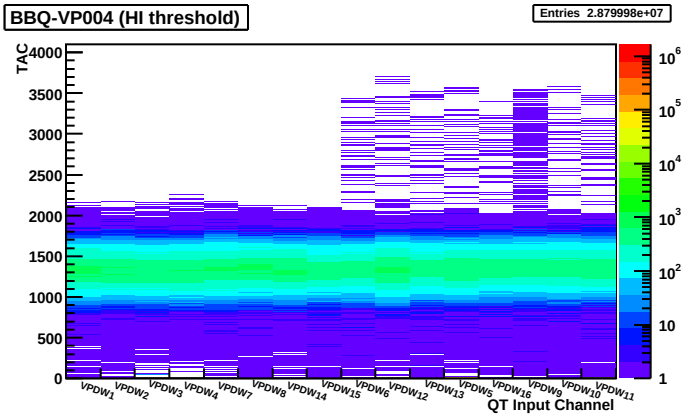

BBQ-VP002 (LO threshold)
Entries 2.879998e+07
ADC
QT Input Channel

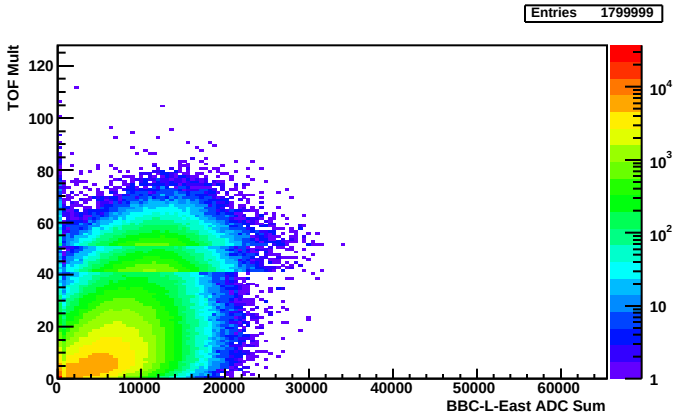

BBQ-VP002 (LO threshold)
Entries 2.879998e+07
TAC
QT Input Channel

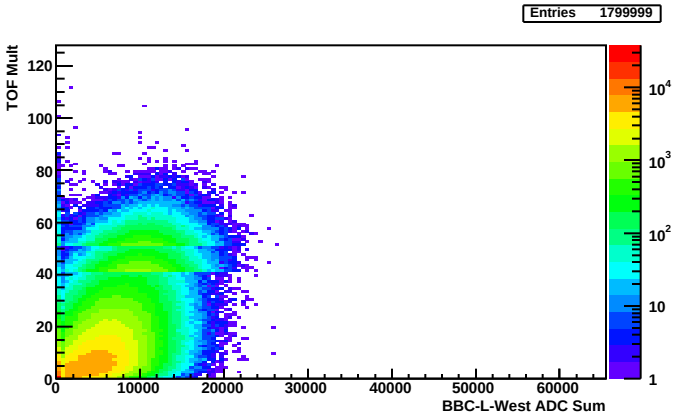

BBQ-VP003 (HI threshold)
Entries 2.879998e+07
ADC
QT Input Channel

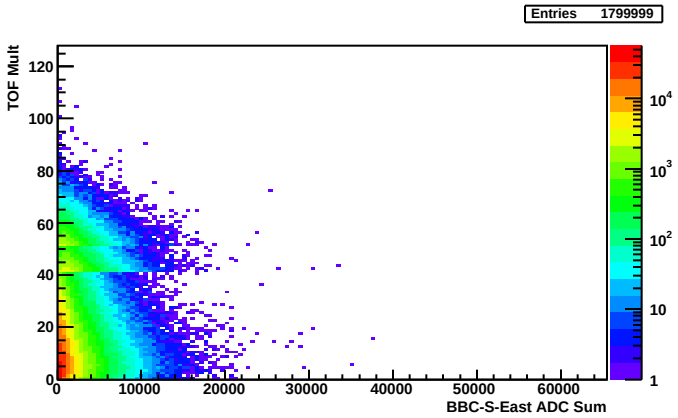

BBQ-VP003 (HI threshold)
Entries 2.879998e+07
TAC
QT Input Channel

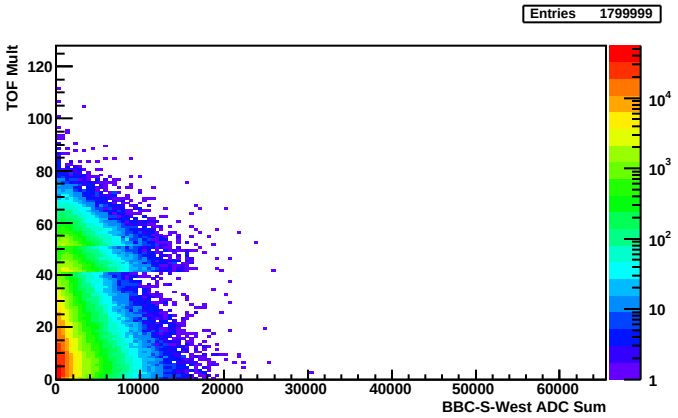

BBQ-VP004 (HI threshold)
Entries 2.879998e+07
ADC
QT Input Channel

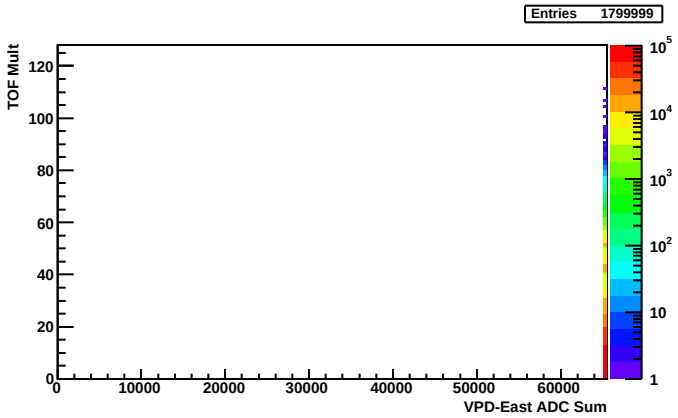

BBQ-VP004 (HI threshold)
Entries 2.879998e+07
TAC
QT Input Channel

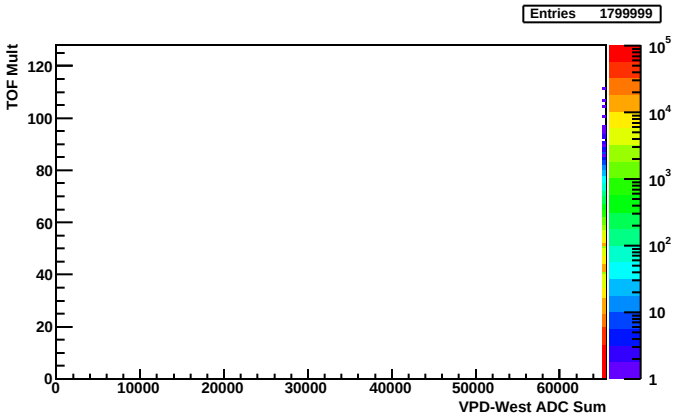

Entries 1799999
TOF Mult
BBC-L-East ADC Sum

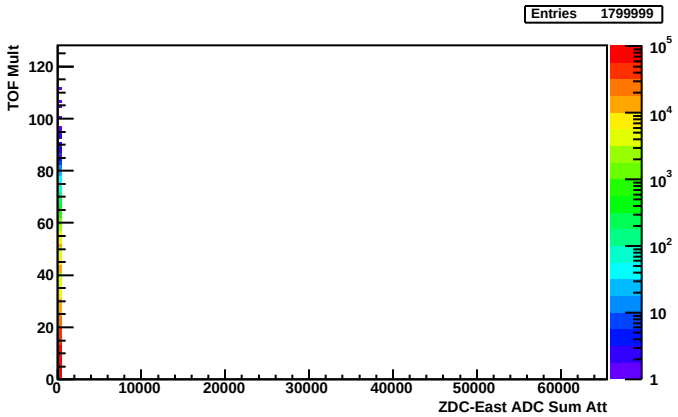

Entries 1799999
TOF Mult
BBC-L-West ADC Sum

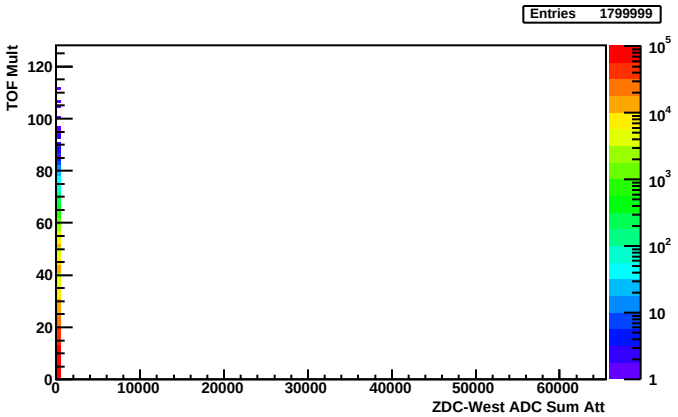

Entries 1799999
TOF Mult
BBC-S-East ADC Sum

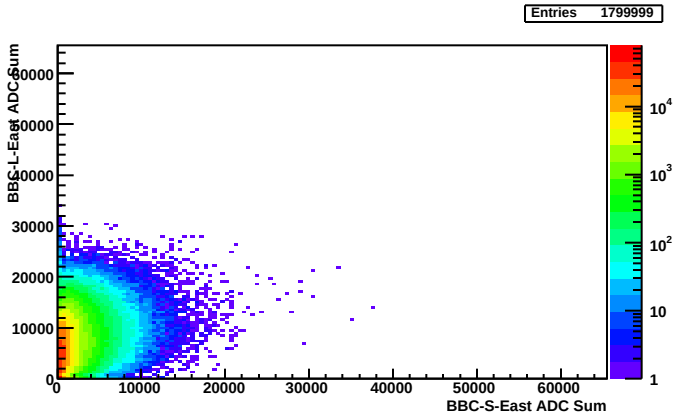

Entries 1799999
TOF Mult
BBC-S-West ADC Sum

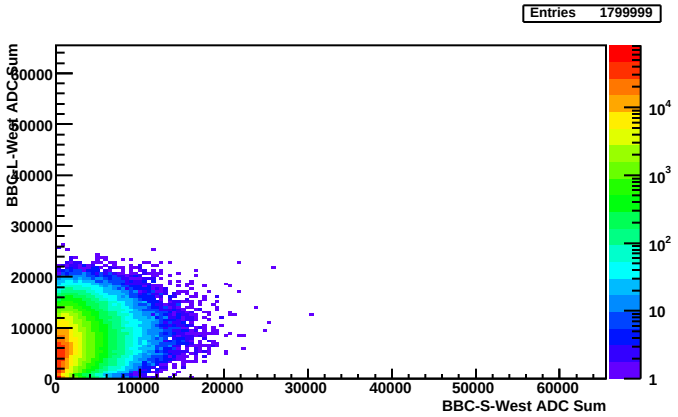

Entries 1799999
TOF Mult
VPD-East ADC Sum

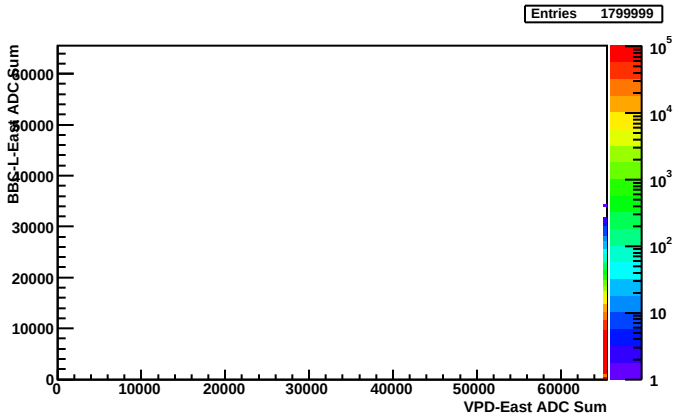

Entries 1799999
TOF Mult
VPD-West ADC Sum

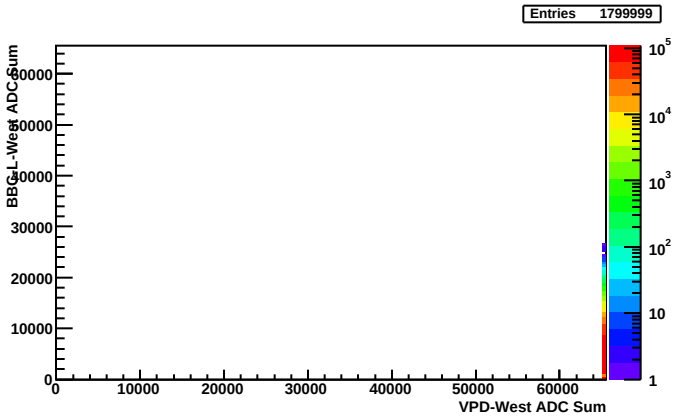

Entries 1799999
TOF Mult
ZDC-East ADC Sum Att

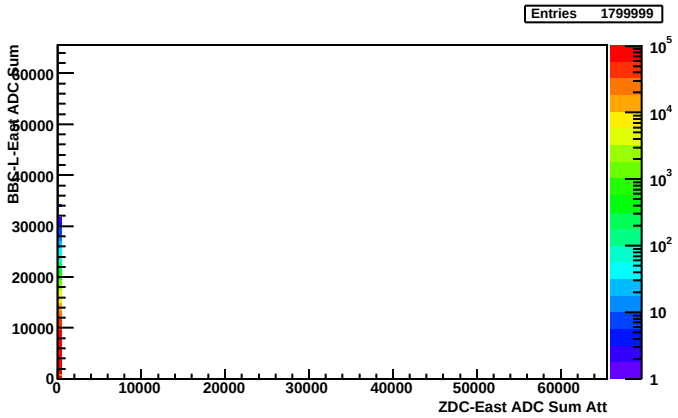

Entries 1799999
TOF Mult
ZDC-West ADC Sum Att

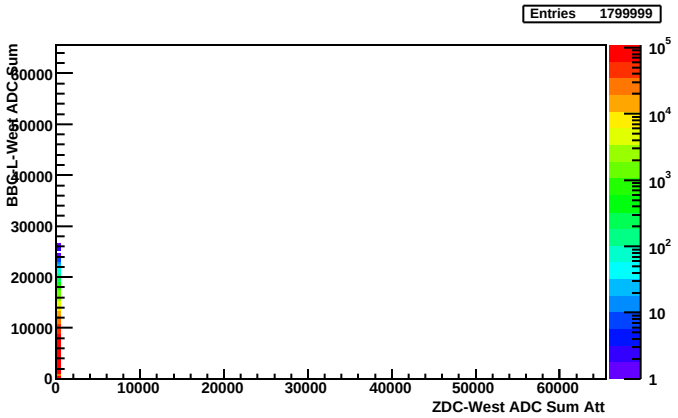

Entries 1799999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

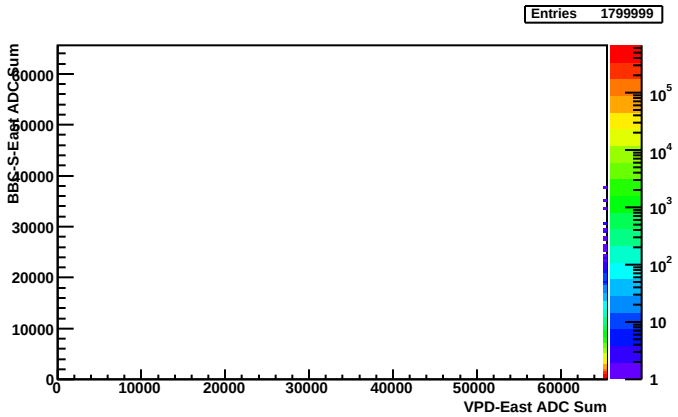

Entries 1799999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

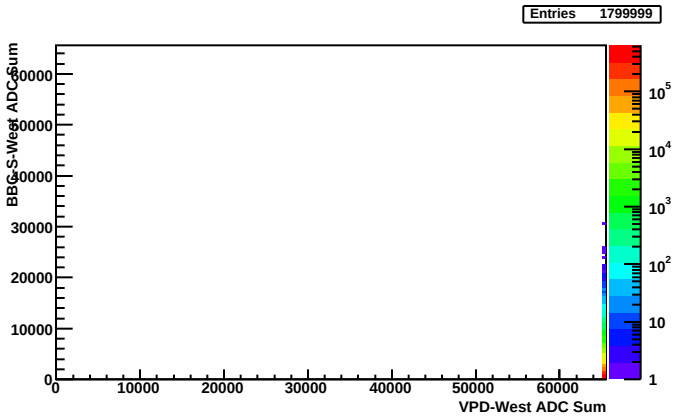

Entries 1799999
BBC-L-East ADC Sum
VPD-East ADC Sum

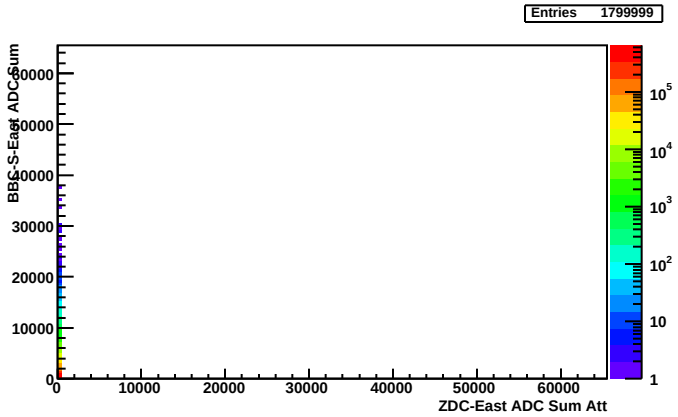

Entries 1799999
BBC-L-West ADC Sum
VPD-West ADC Sum

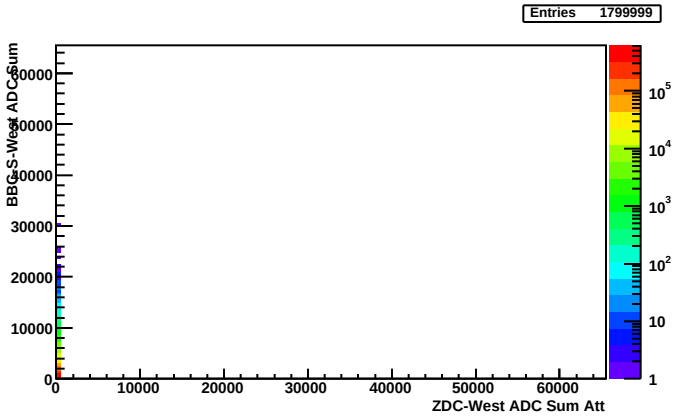

Entries 1799999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

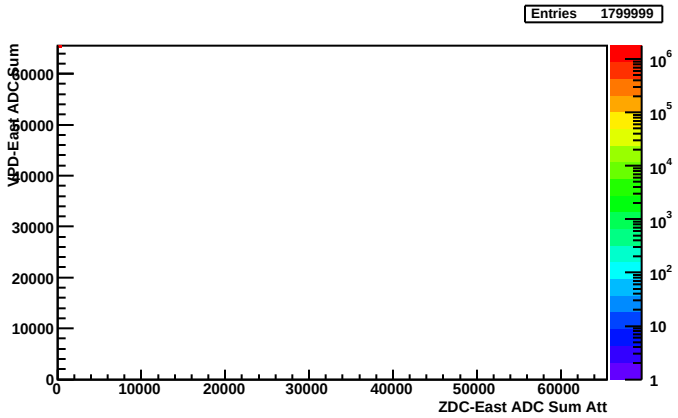

Entries 1799999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

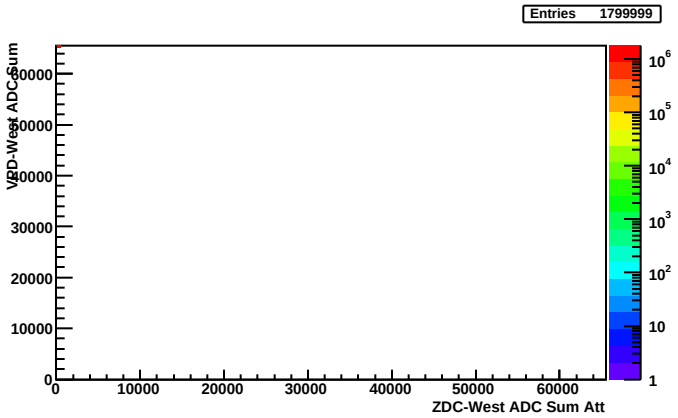

Entries 1799999
BBC-S-East ADC Sum
VPD-East ADC Sum

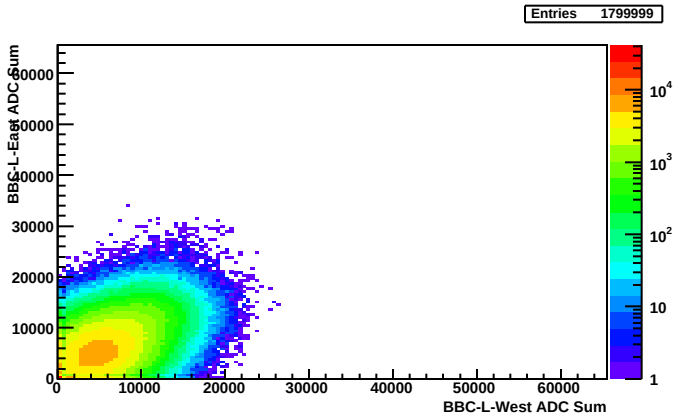

Entries 1799999
BBC-S-West ADC Sum
VPD-West ADC Sum

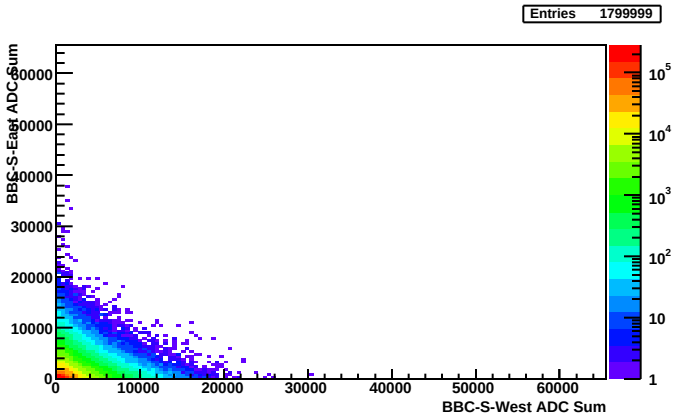

Entries 1799999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

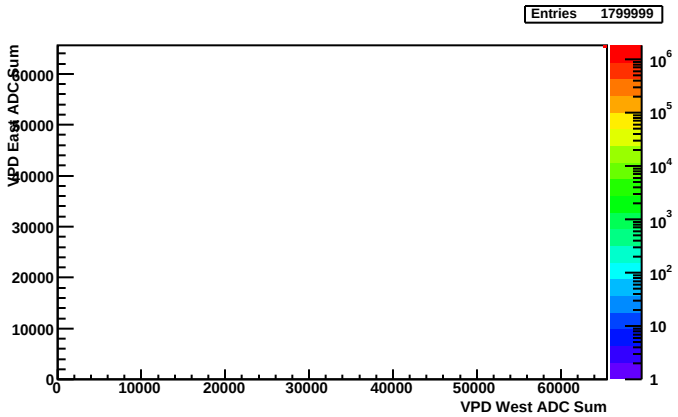

Entries 1799999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

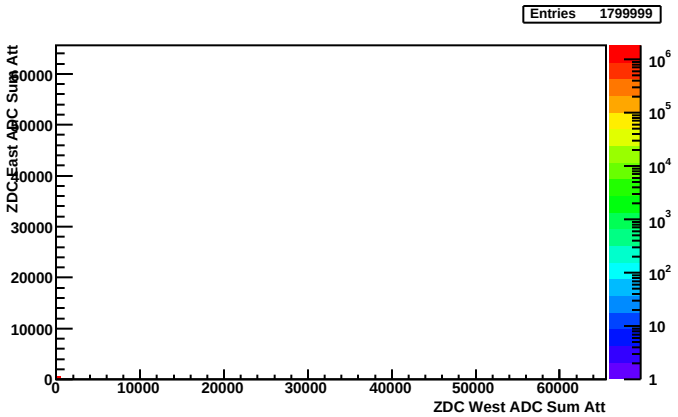

Entries 1799999
VPD-East ADC Sum
ZDC-East ADC Sum Att

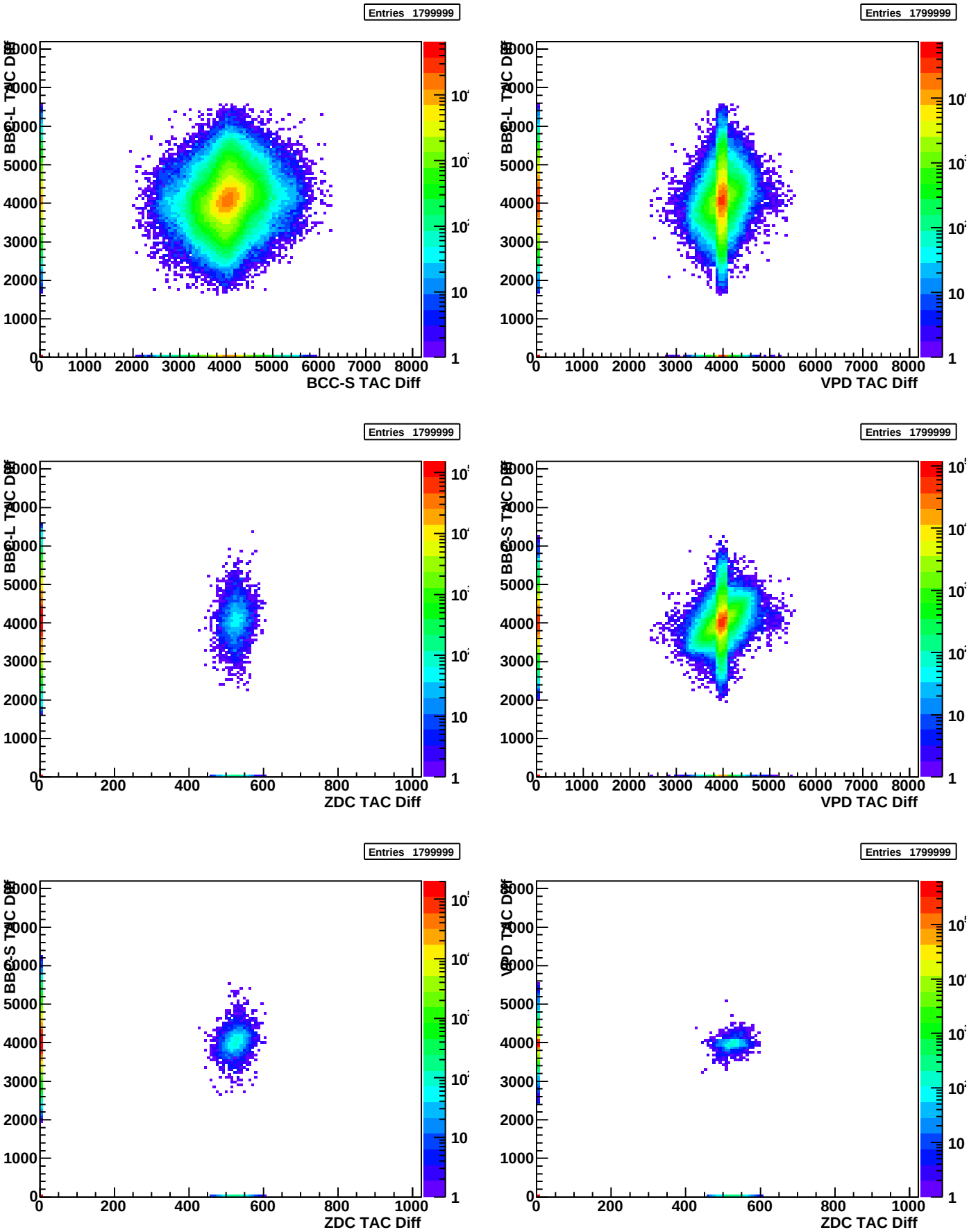

Entries 1799999
VPD-West ADC Sum
ZDC-West ADC Sum Att

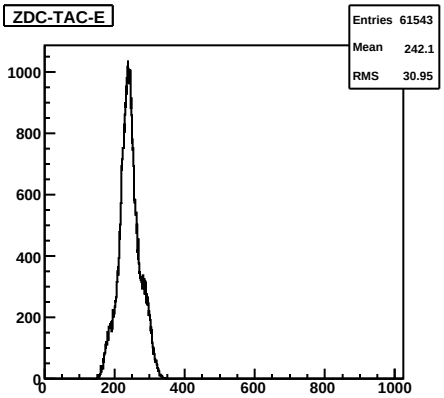

Entries 1799999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

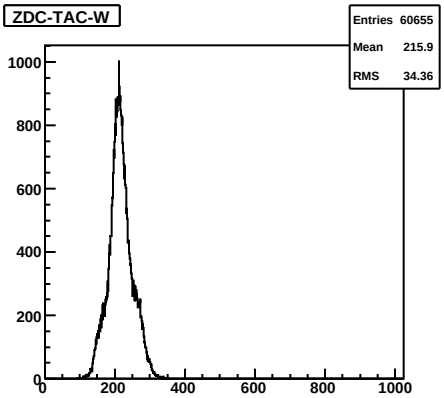

Entries 1799999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

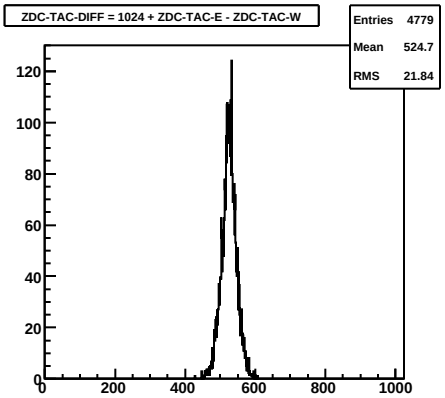

Entries 1799999
VPD East ADC Sum
VPD West ADC Sum

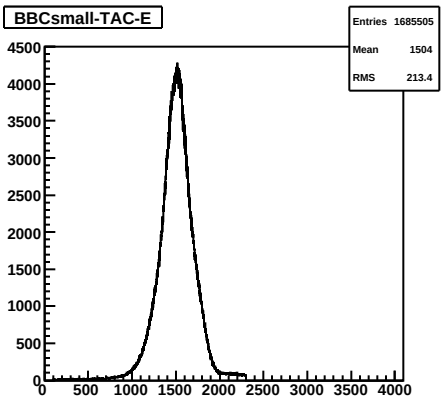

Entries 1799999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

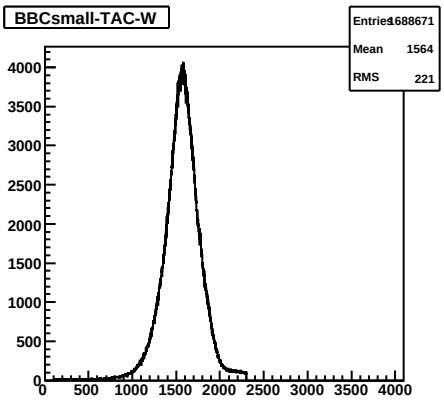

Entries 1799999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1799999
BBC-L TAC Diff
VPD TAC Diff
Entries 1799999
BBC-L TAC Diff
ZDC TAC Diff
Entries 1799999
BBC-S TAC Diff
VPD TAC Diff
Entries 1799999
BBC-S TAC Diff
ZDC TAC Diff
Entries 1799999
VPD TAC Diff
ZDC TAC Diff

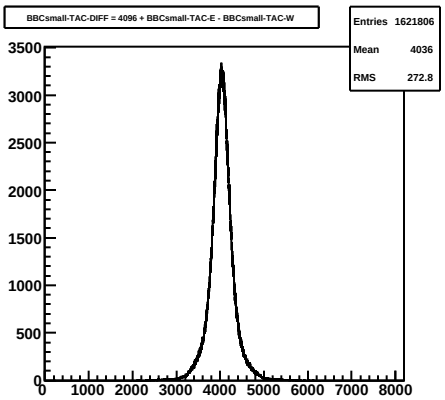

ZDC-TAC-E
Entries 61543
Mean 242.1
RMS 30.95

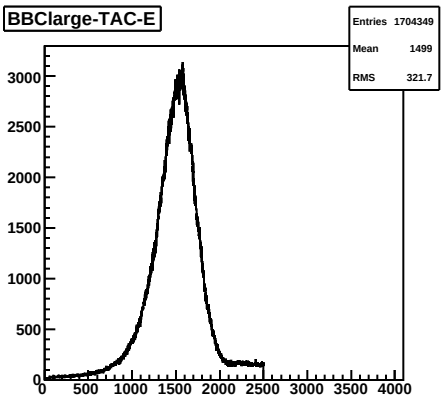

ZDC-TAC-W
Entries 60655
Mean 215.9
RMS 34.36

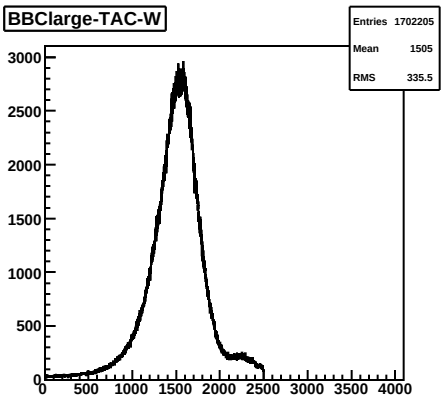

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4779
Mean 524.7
RMS 21.84

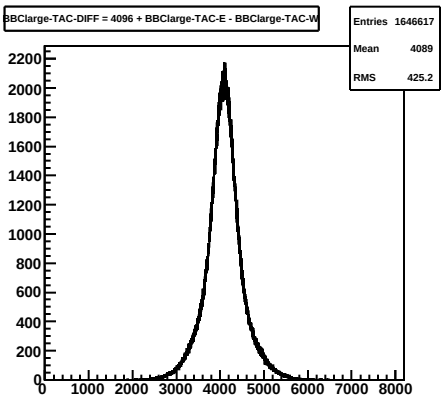

BBCsmall-TAC-E
Entries 1685505
Mean 1504
RMS 213.4

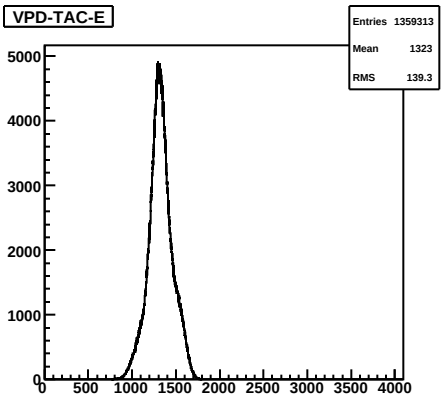

BBCsmall-TAC-W
Entries 1688671
Mean 1564
RMS 221

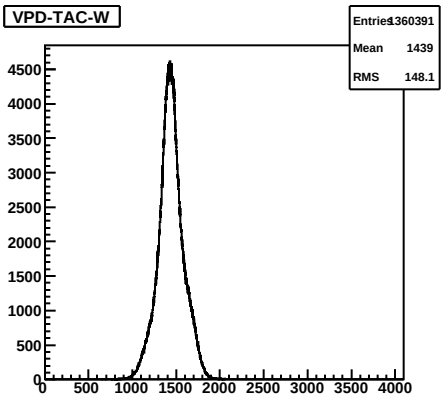

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1621806
Mean 4036
RMS 272.8

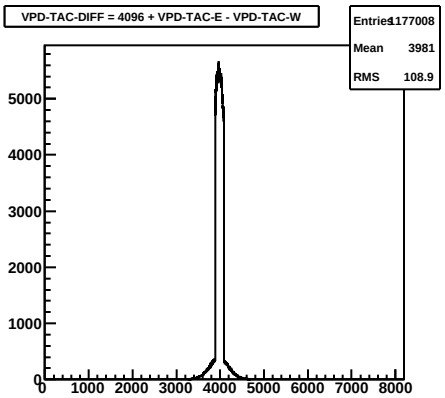

BBClarge-TAC-E
Entries 1704349
Mean 1499
RMS 321.7

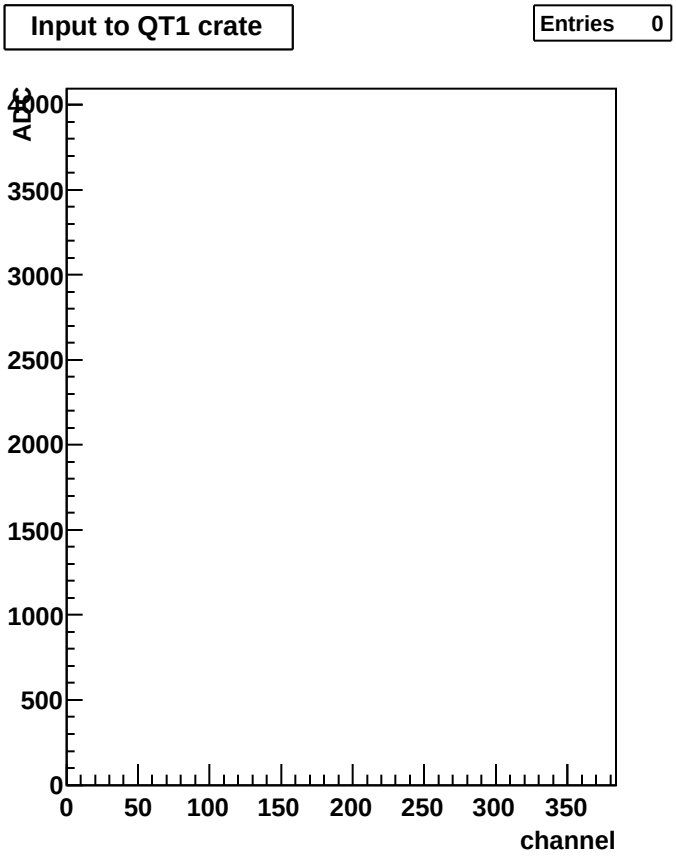

BBClarge-TAC-W
Entries 1702205
Mean 1505
RMS 335.5

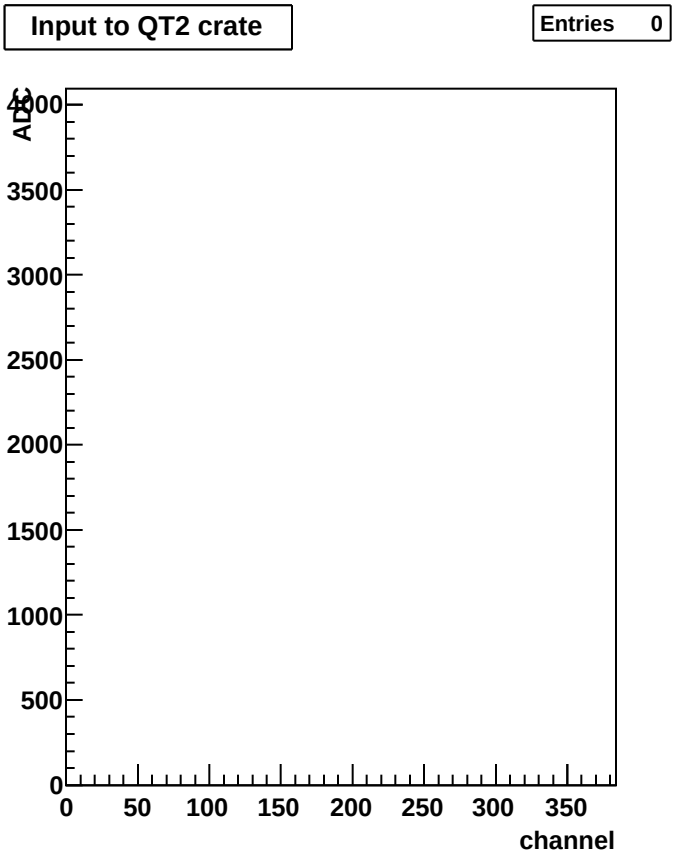

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1646617
Mean 4089
RMS 425.2

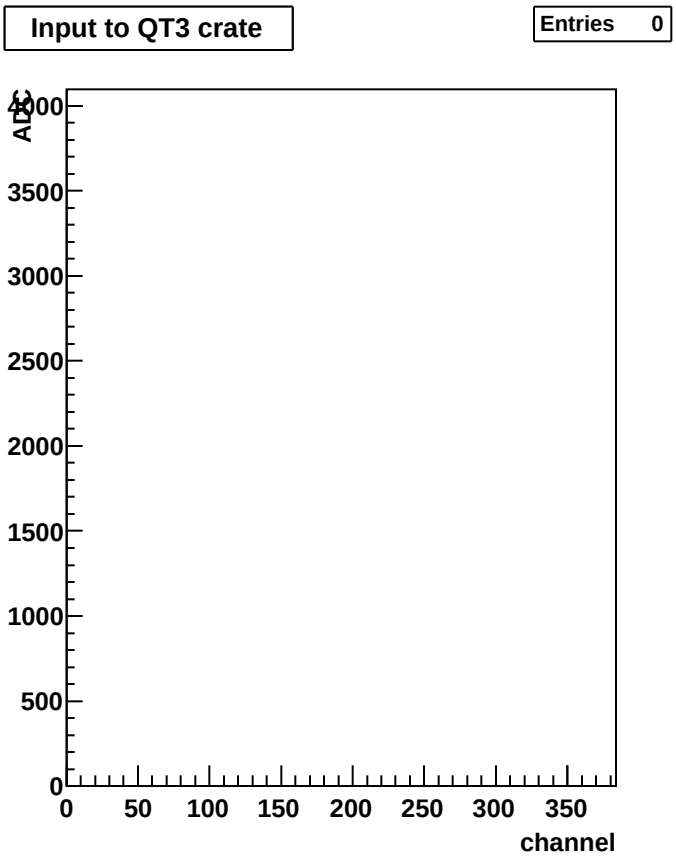

VPD-TAC-E
Entries 1359313
Mean 1323
RMS 139.3

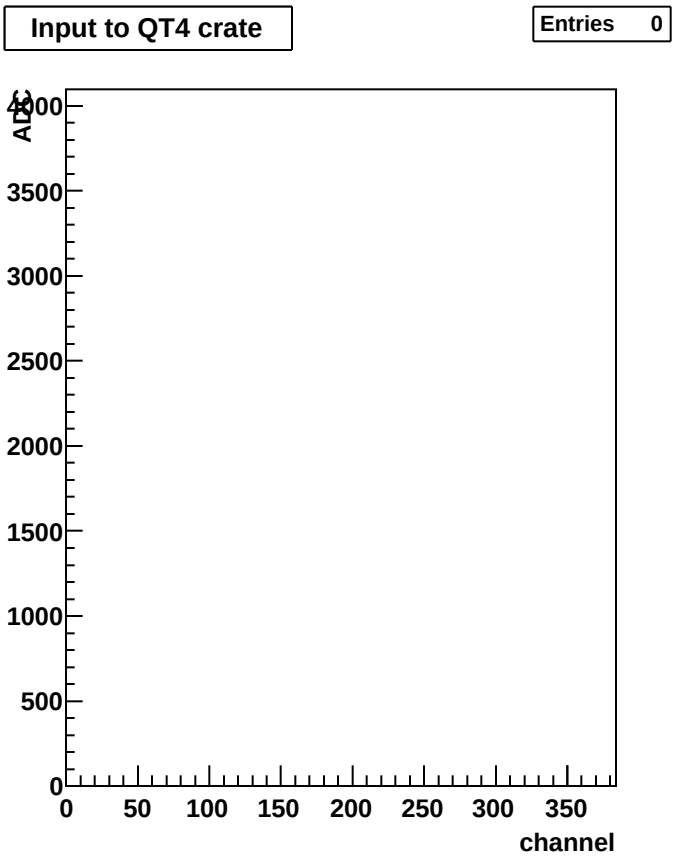

VPD-TAC-W
Entries 1360391
Mean 1439
RMS 148.1

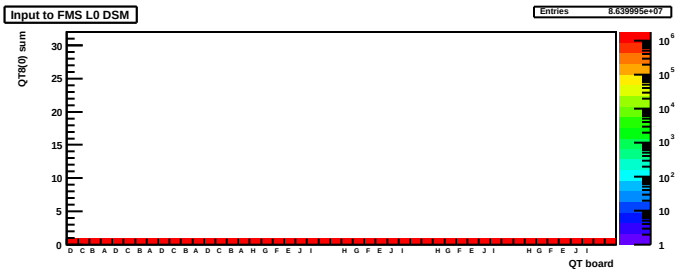

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1177008
Mean 3981
RMS 108.9

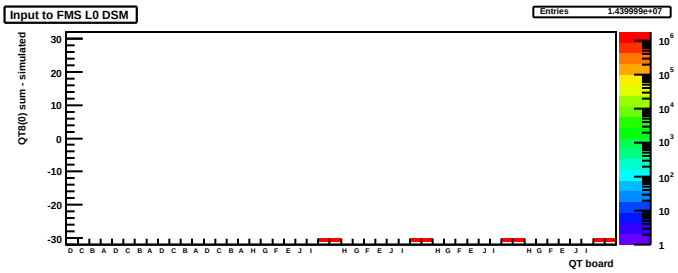

Input to QT1 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

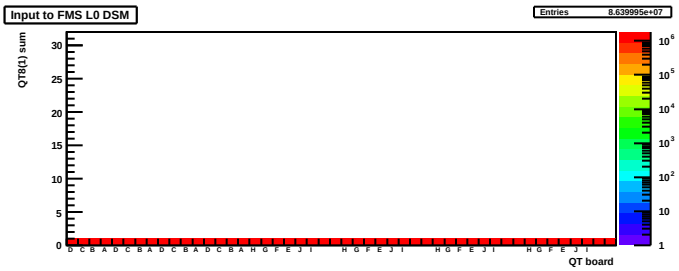

Input to QT2 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

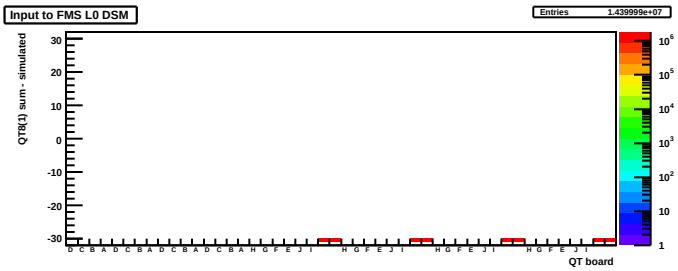

Input to QT3 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

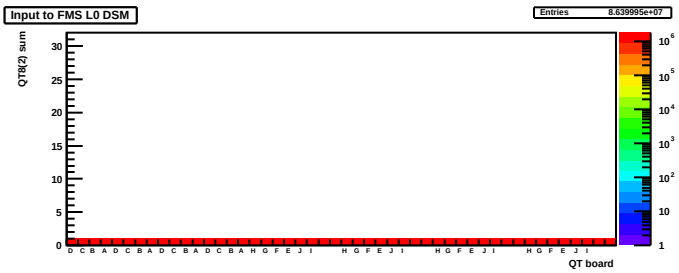

Input to QT4 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

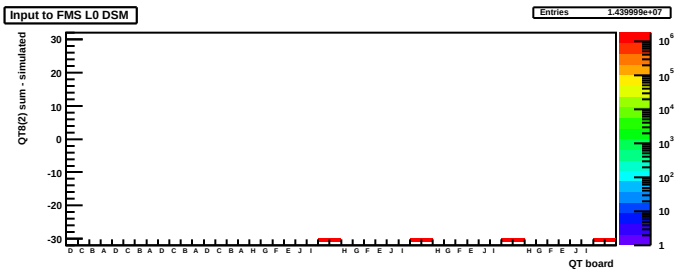

Input to FMS L0 DSM
Entries 8.639995e+07
QT8(0) sum
QT board

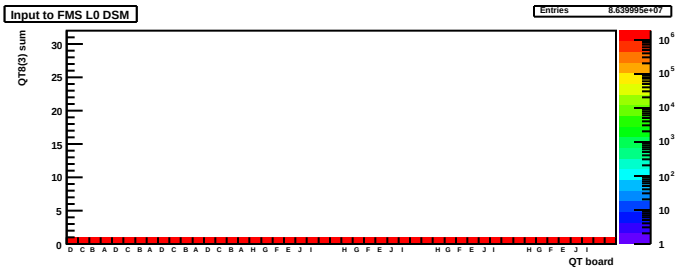

Input to FMS L0 DSM
Entries 1.439999e+07
QT8(0) sum - simulated
QT board

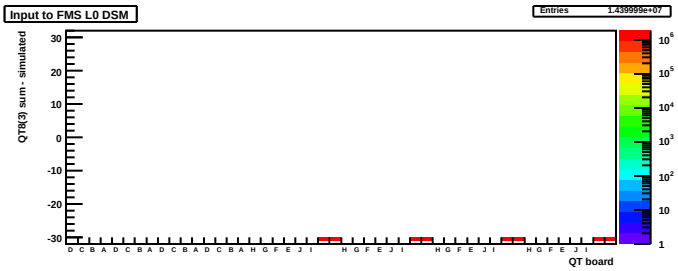

Input to FMS L0 DSM
Entries 8.639995e+07
QT8(1) sum
QT board

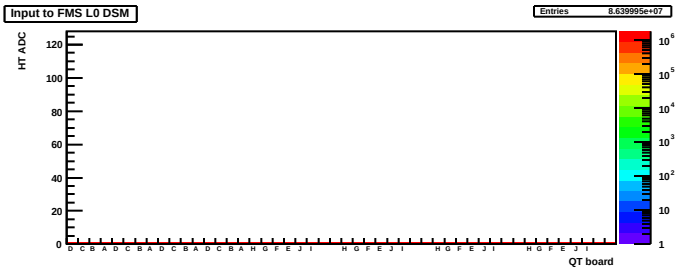

Input to FMS L0 DSM
Entries 1.439999e+07
QT8(1) sum - simulated
QT board

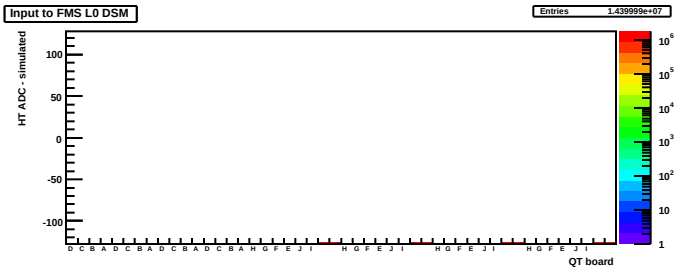

Input to FMS L0 DSM
Entries 8.639995e+07
QT8(2) sum
QT board

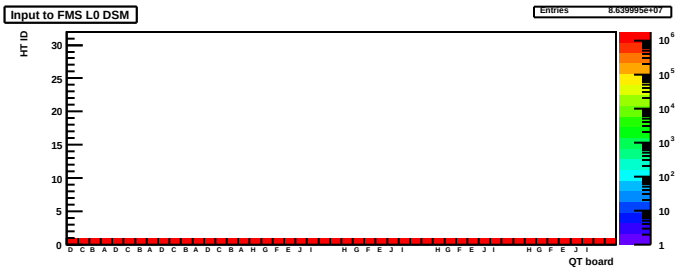

Input to FMS L0 DSM
Entries 1.439999e+07
QT8(2) sum - simulated
QT board

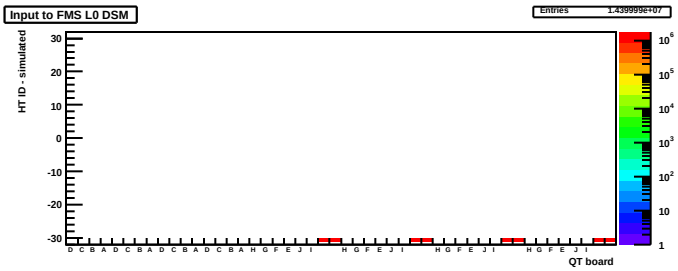

Input to FMS L0 DSM
Entries 8.639995e+07
QT8(3) sum
QT board

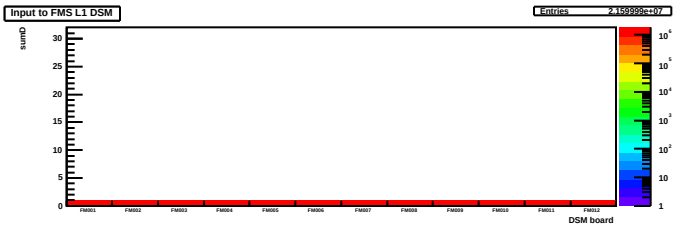

Input to FMS L0 DSM
Entries 1.439999e+07
QT8(3) sum - simulated
QT board

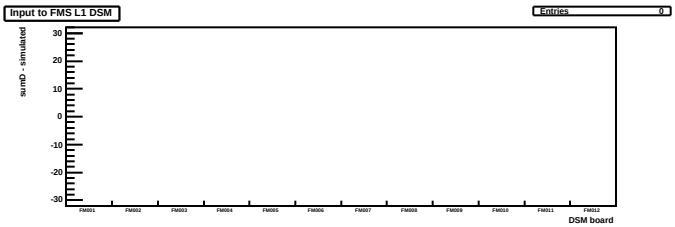

Input to FMS L0 DSM
Entries 8.639995e+07
HT ADC
QT board

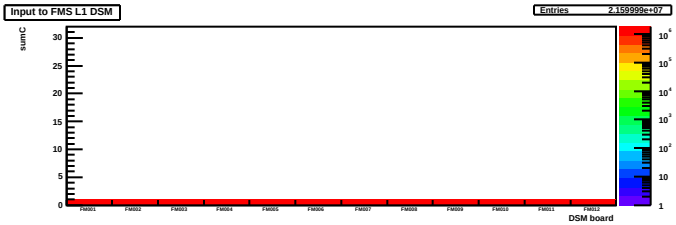

Input to FMS L0 DSM
Entries 1.439999e+07
HT ADC - simulated
QT board

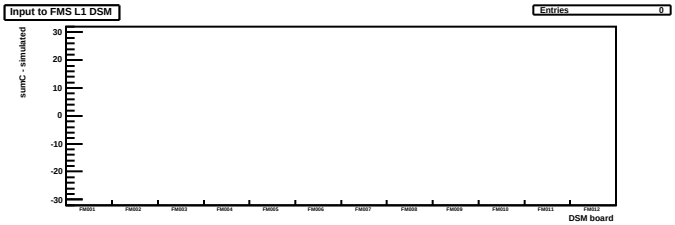

Input to FMS L0 DSM
Entries 8.639995e+07
HT ID
QT board

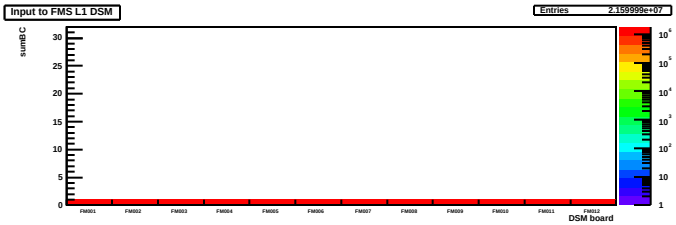

Input to FMS L0 DSM
Entries 1.439999e+07
HT ID - simulated
QT board

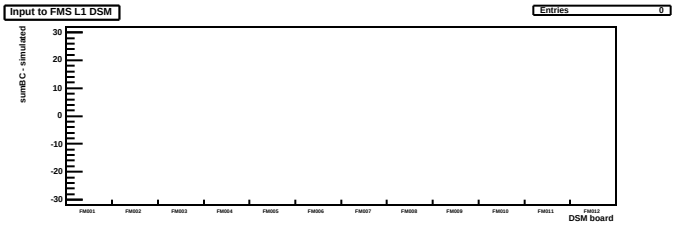

Input to FMS L1 DSM
Entries 2.159999e+07
sumD
DSM board

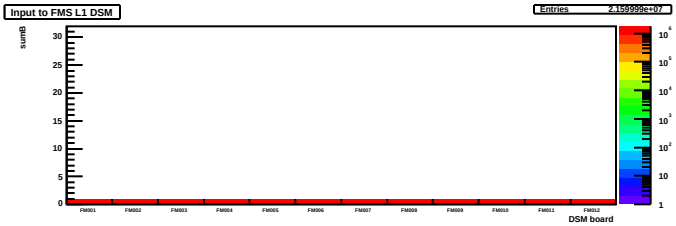

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

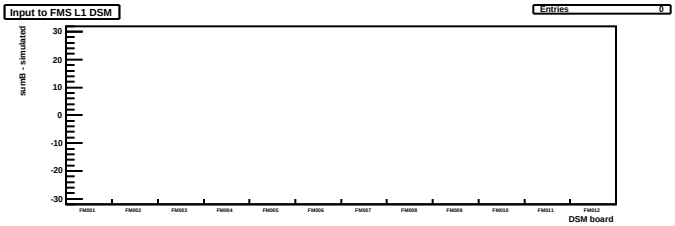

Input to FMS L1 DSM
Entries 2.159999e+07
sumC
DSM board

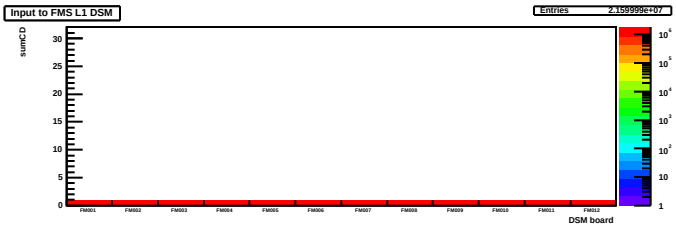

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

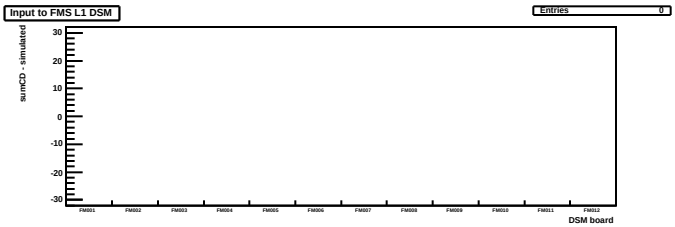

Input to FMS L1 DSM
Entries 2.159999e+07
sumBC
DSM board

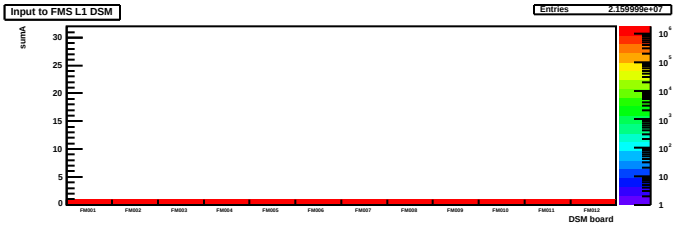

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

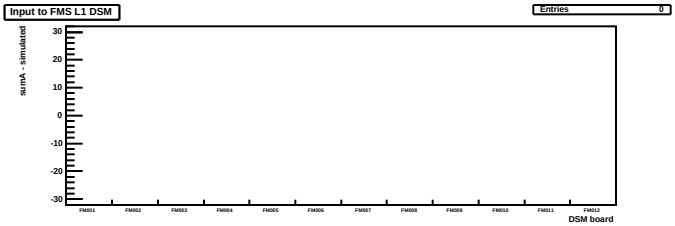

Input to FMS L1 DSM
Entries 2.159999e+07
sumB
DSM board

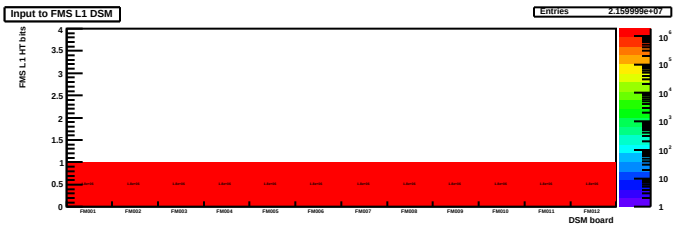

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

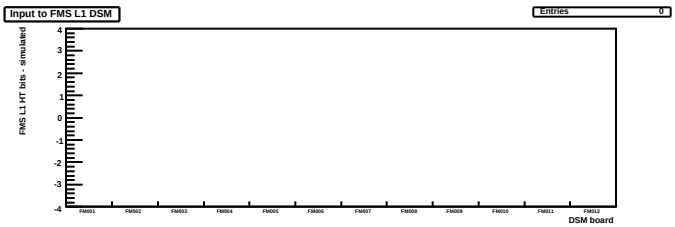

Input to FMS L1 DSM
Entries 2.159999e+07
sumCD
DSM board

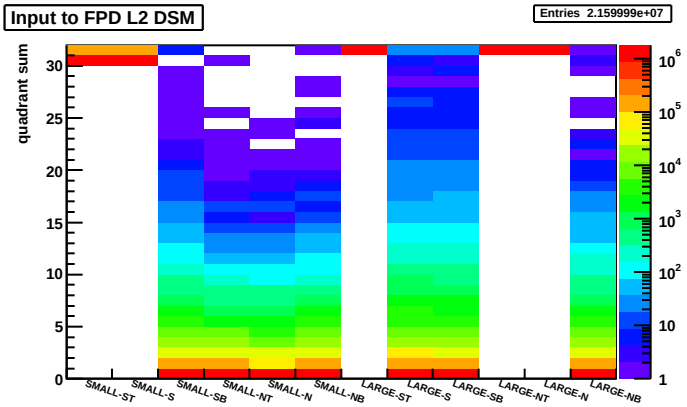

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

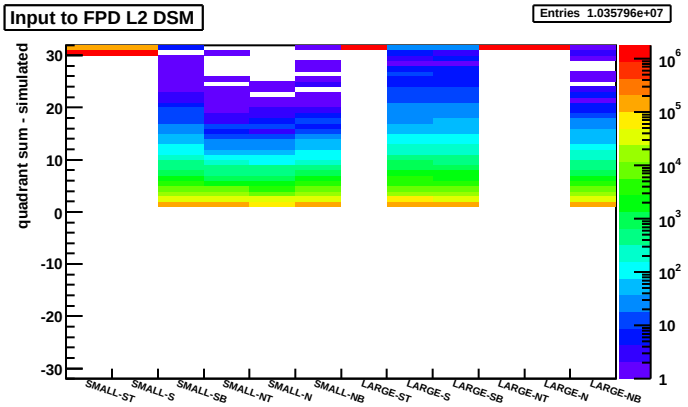

Input to FMS L1 DSM
Entries 2.159999e+07
sumA
DSM board

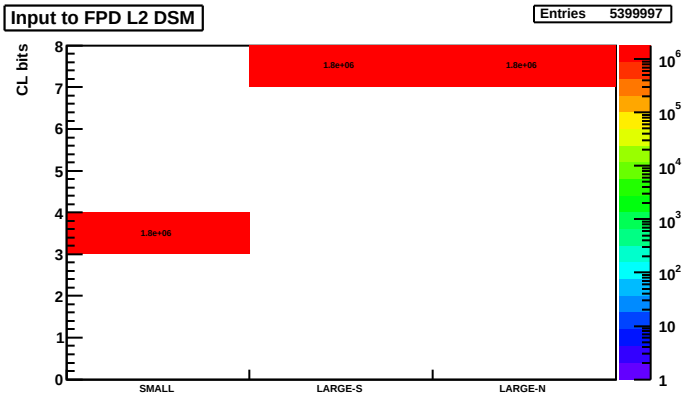

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

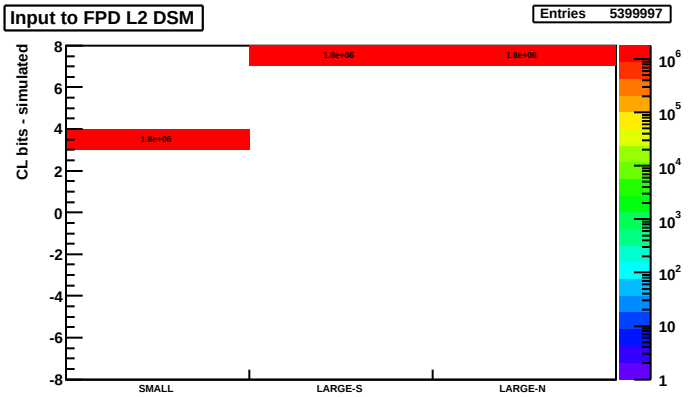

Input to FMS L1 DSM
Entries 2.159999e+07
FMS L1 HT bits
DSM board

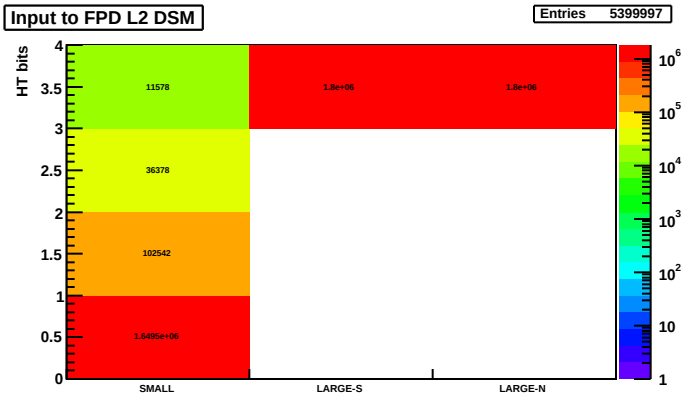

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

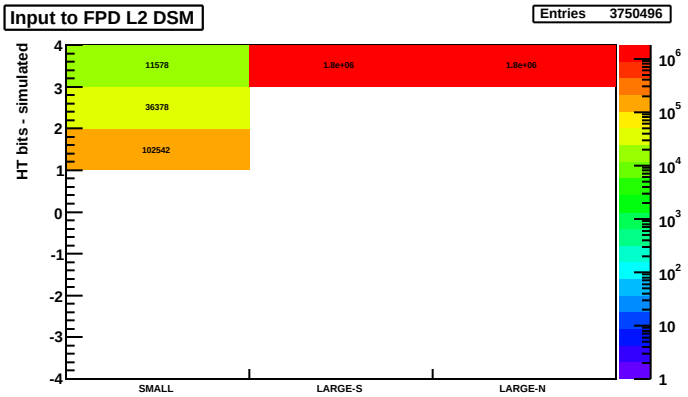

Input to FPD L2 DSM
Entries 2.159999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

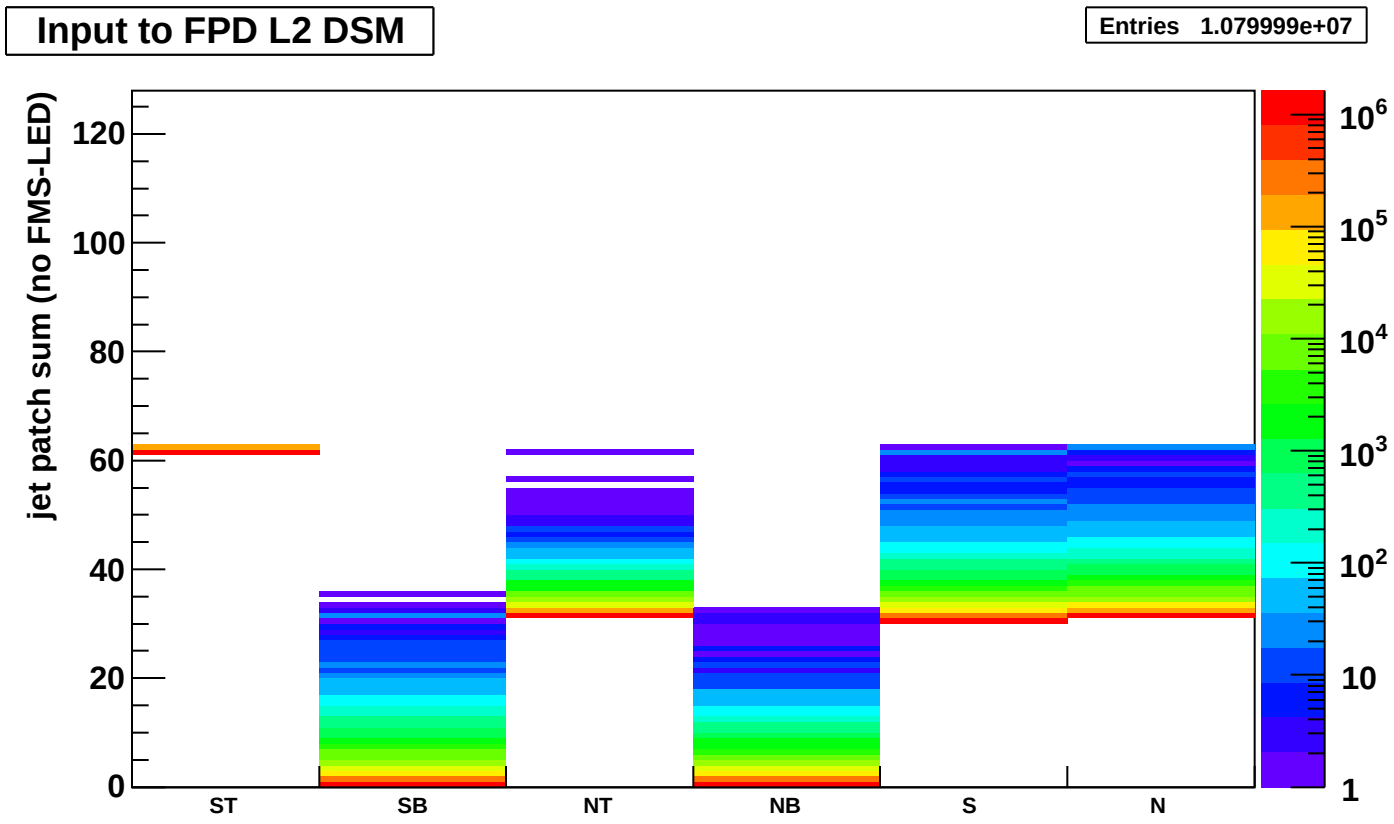

Input to FPD L2 DSM
Entries 1.035796e+07
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

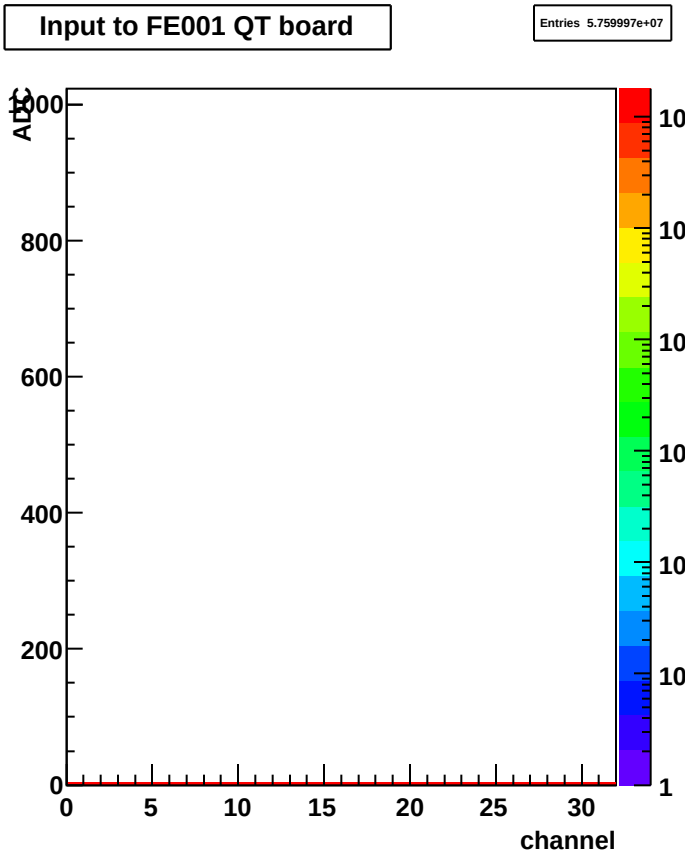

Input to FPD L2 DSM
Entries 5399997
CL bits
1.8e+06
1.8e+06
1.8e+06
SMALL
LARGE-S
LARGE-N

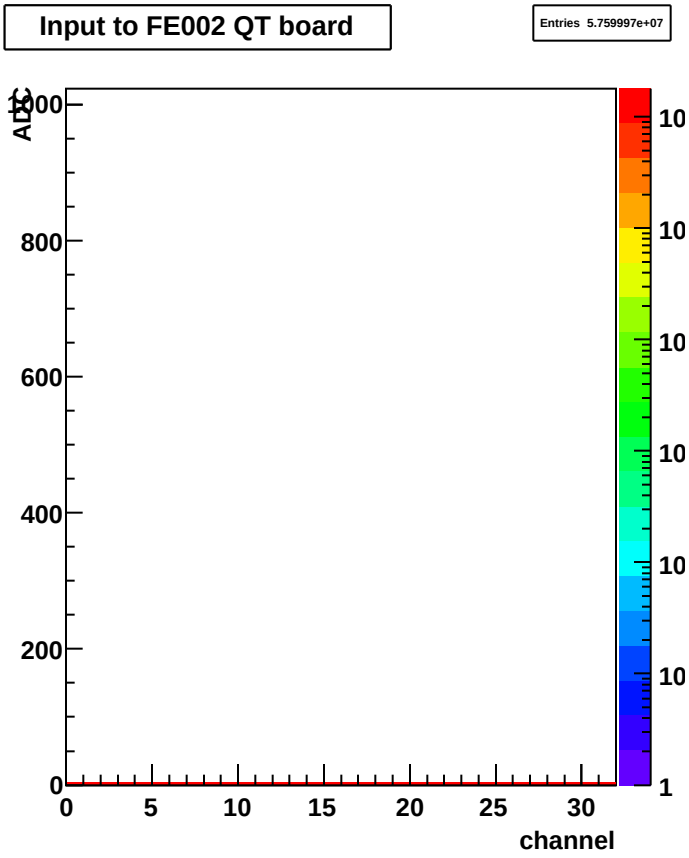

Input to FPD L2 DSM
Entries 5399997
CL bits - simulated
1.8e+06
1.8e+06
1.8e+06
SMALL
LARGE-S
LARGE-N

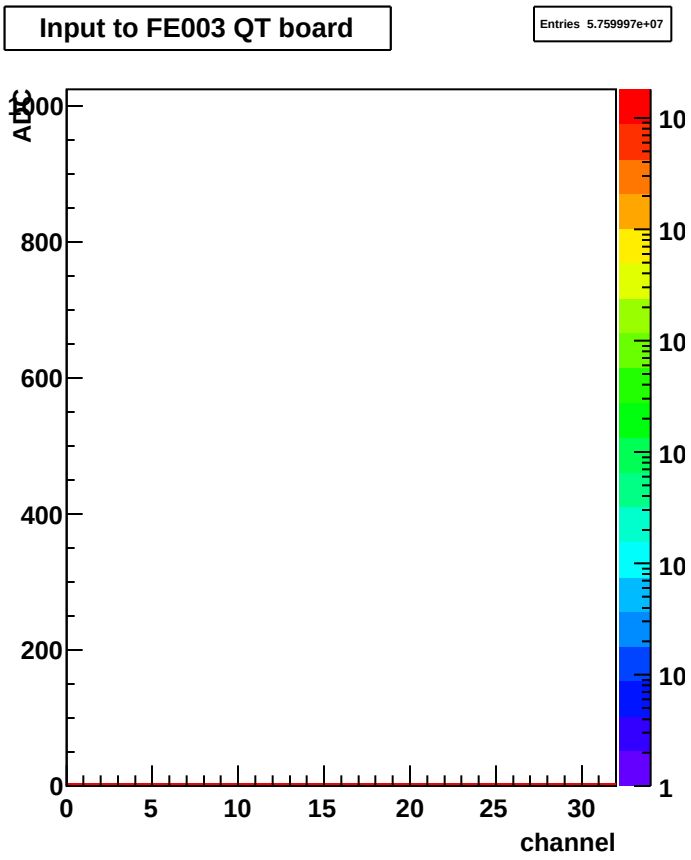

Input to FPD L2 DSM
Entries 5399997
HT bits
11578
36378
102542
1.6495e+06
1.8e+06
1.8e+06
SMALL
LARGE-S
LARGE-N

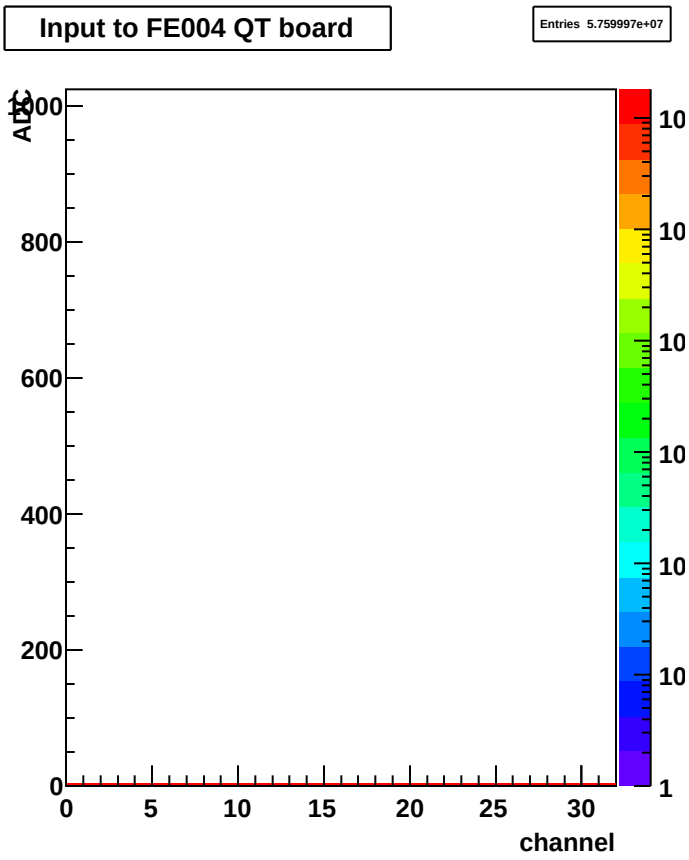

Input to FPD L2 DSM
Entries 3750496
HT bits - simulated
11578
36378
102542
1.8e+06
1.8e+06
SMALL
LARGE-S
LARGE-N

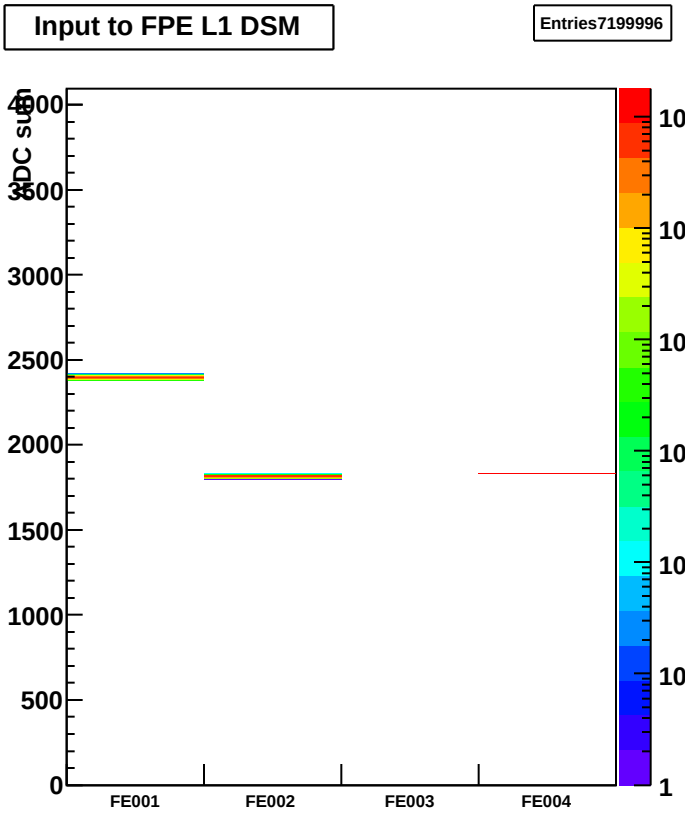

Input to FPD L2 DSM
Entries 1.079999e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 5.759997e+07
ADC
channel

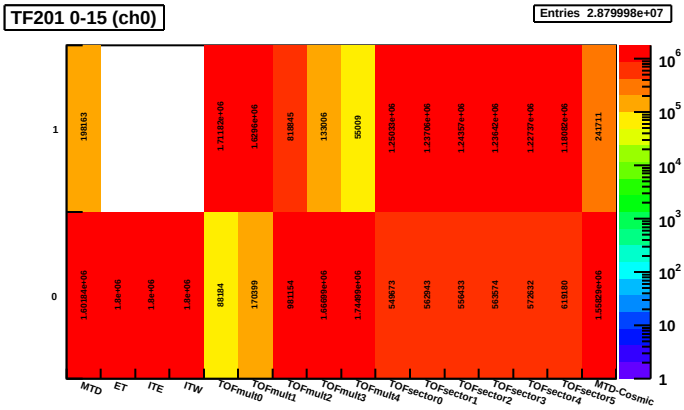

Input to FE002 QT board
Entries 5.759997e+07
ADC
channel


Input to FE003 QT board
Entries 5.759997e+07
ADC
channel

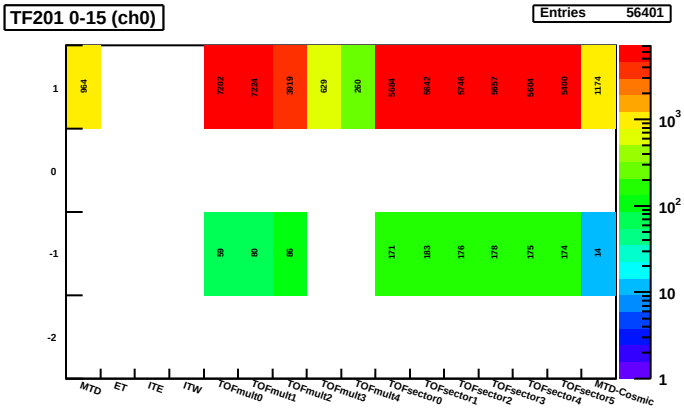

Input to FE004 QT board
Entries 5.759997e+07
ADC
channel


Input to FPE L1 DSM
Entries7199996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

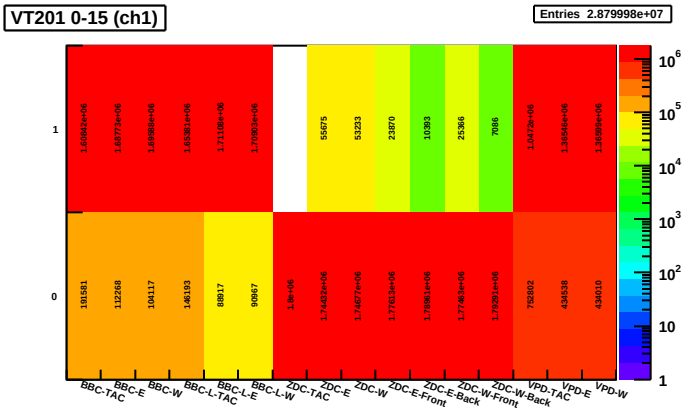
TF201 0-15 (ch0)



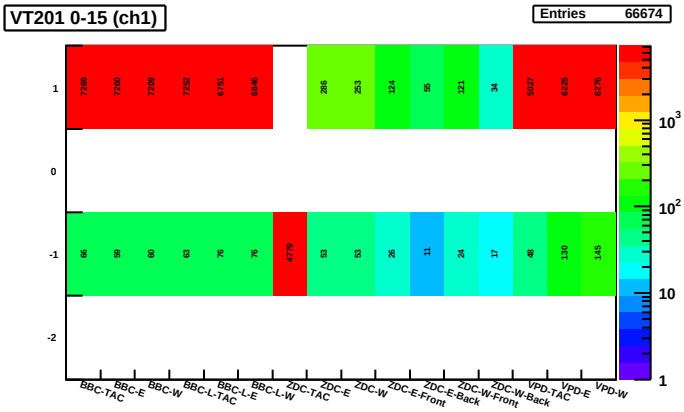
TF201 0-15 (ch0)



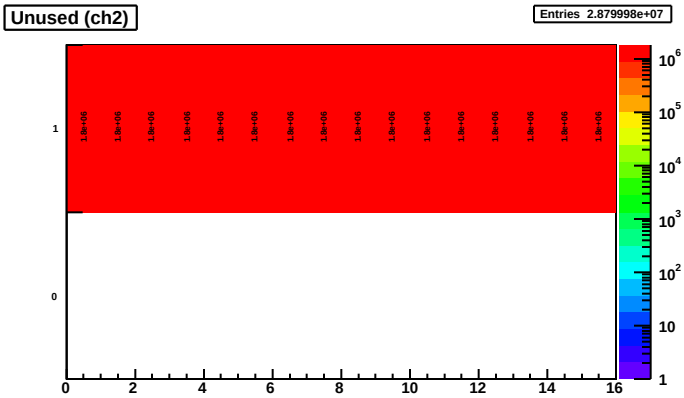
VT201 0-15 (ch1)

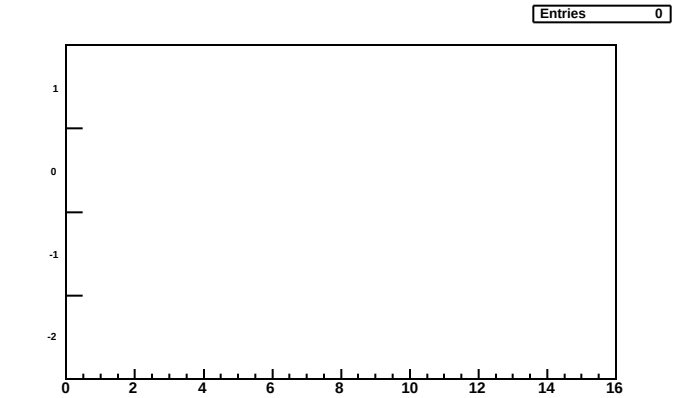


VT201 0-15 (ch1)

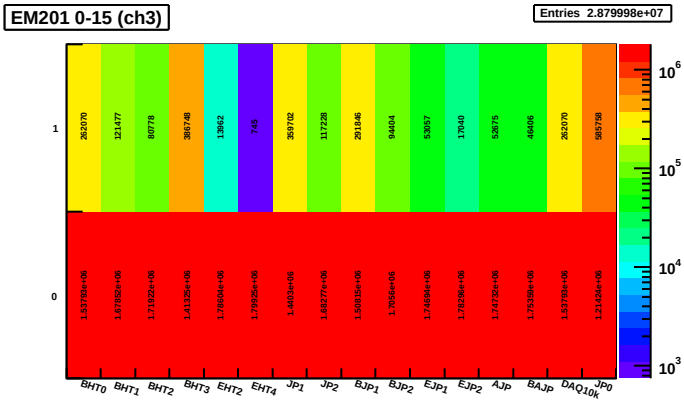


Unused (ch2)

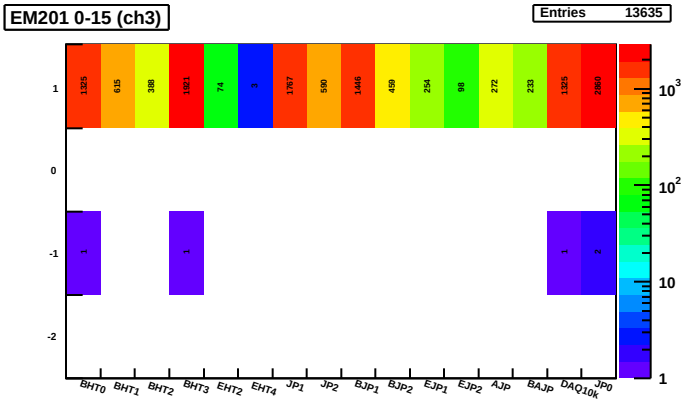




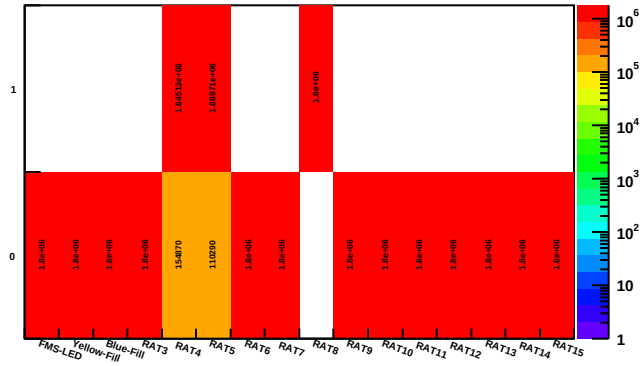
EM201 0-15 (ch3)

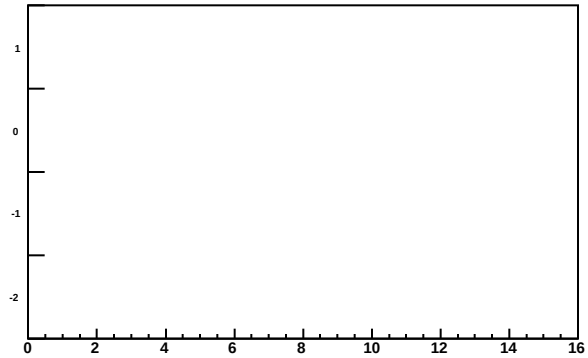


EM201 0-15 (ch3)

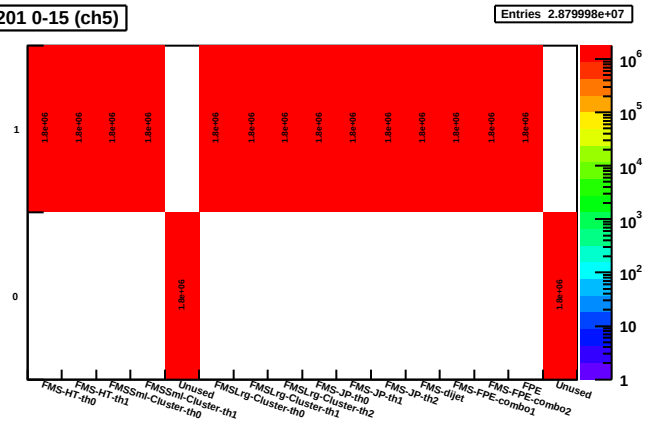


RAT board (ch4)

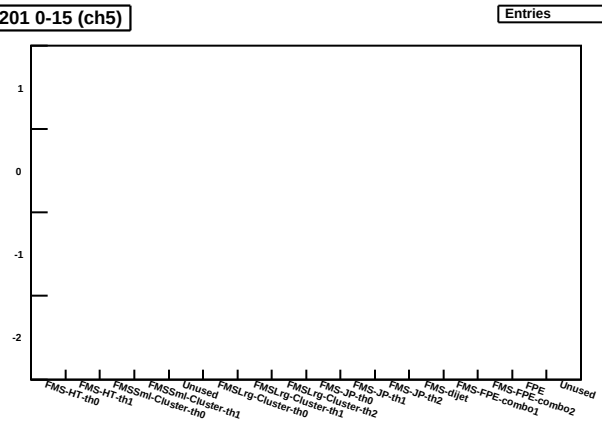




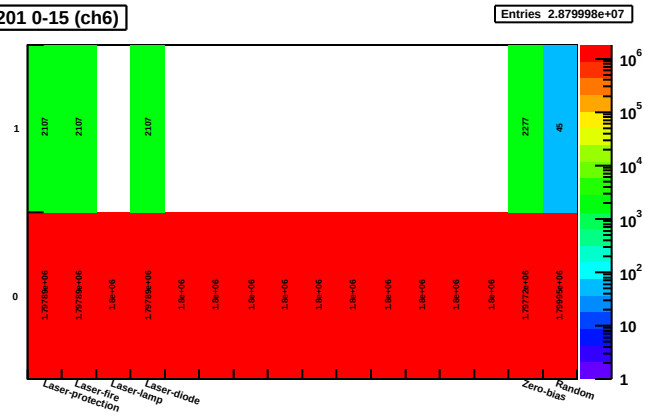
FP201 0-15 (ch5)

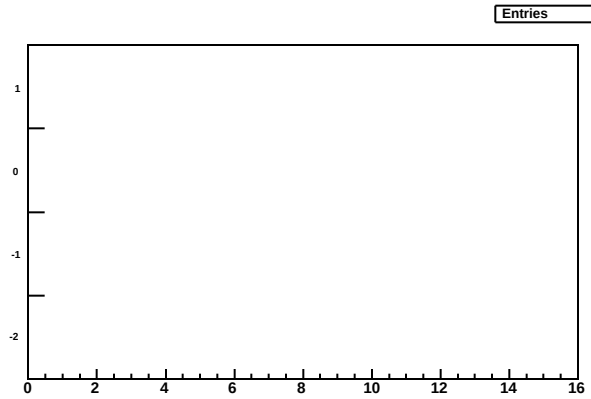
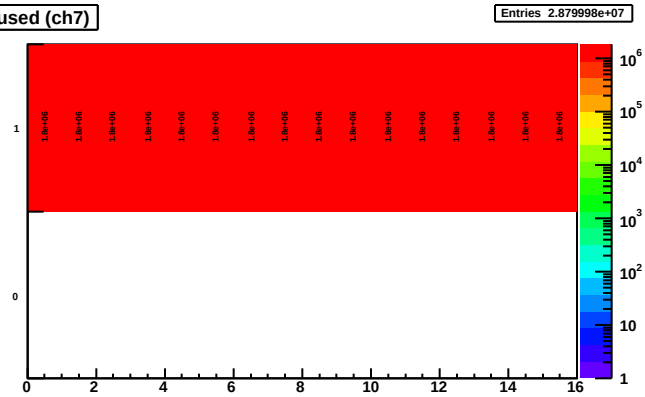
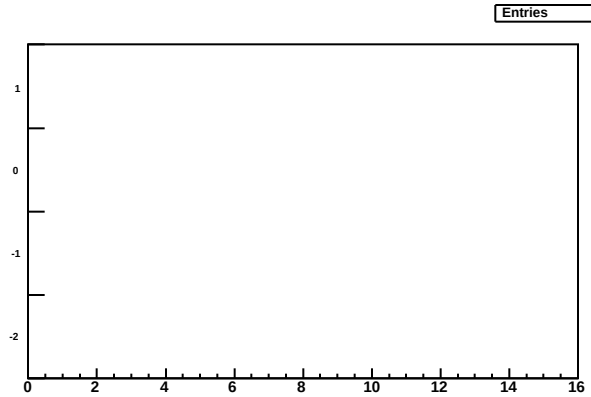


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)