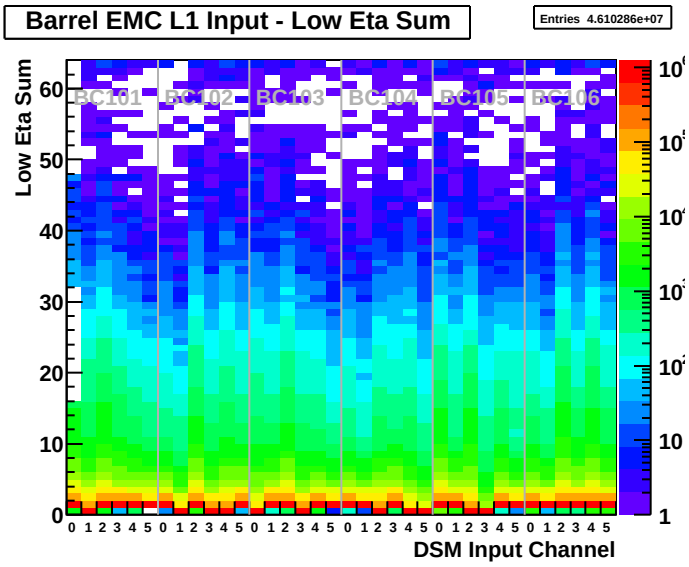
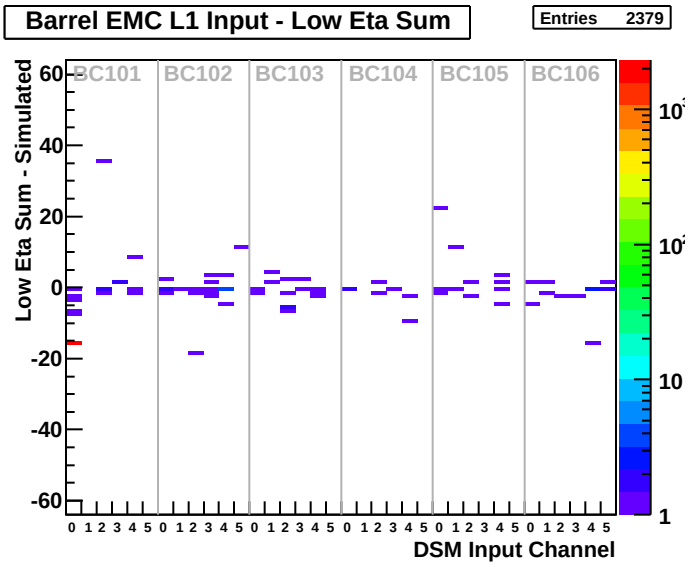
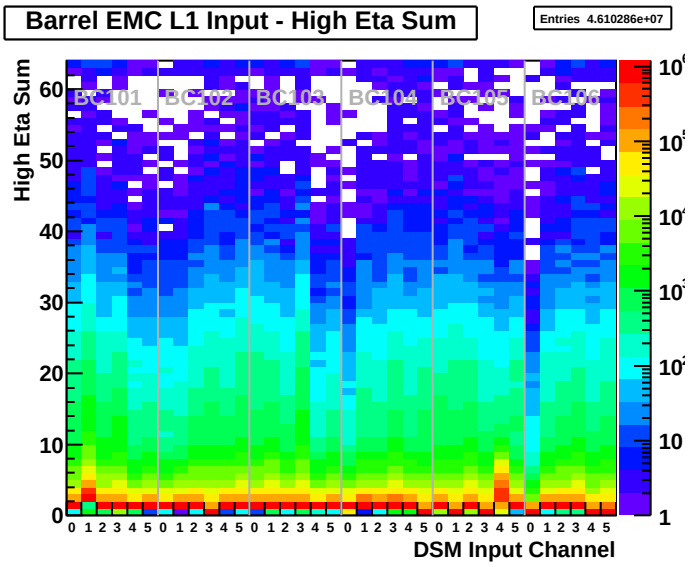
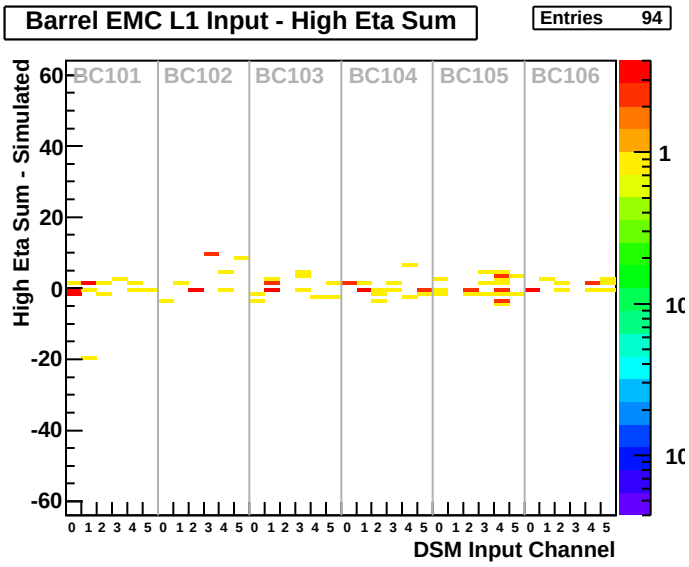
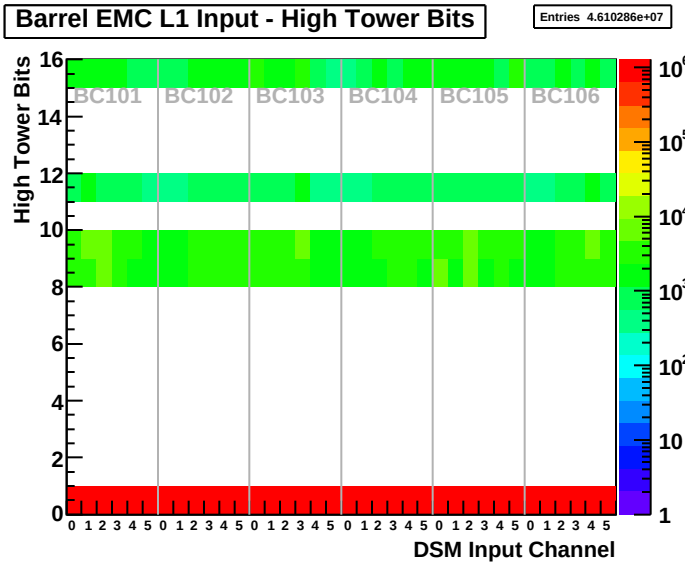
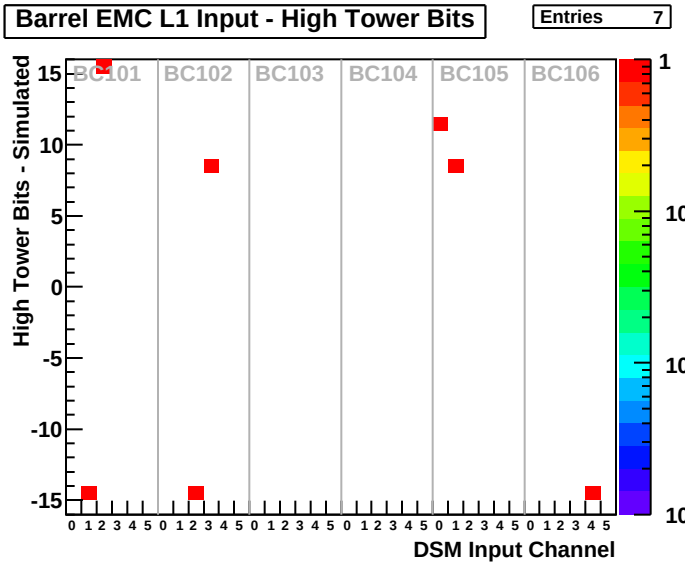

Barrel EMC L1 Input - Low Eta Sum
Entries 4.610286e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

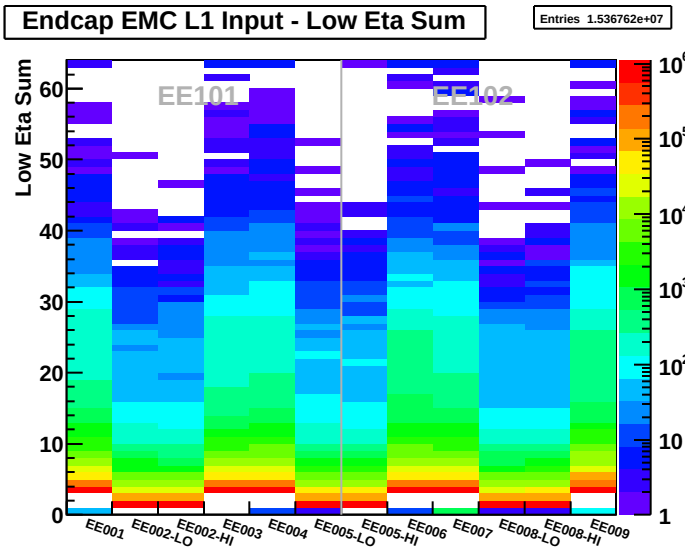

Barrel EMC L1 Input - Low Eta Sum
Entries 2379
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

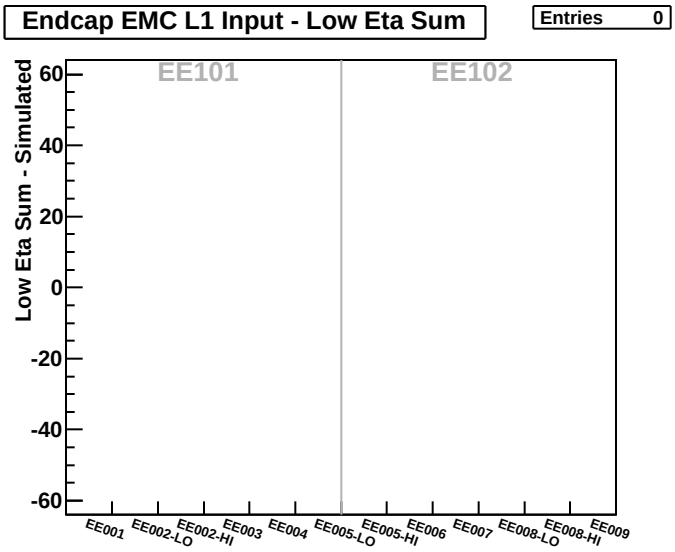

Barrel EMC L1 Input - High Eta Sum
Entries 4.610286e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

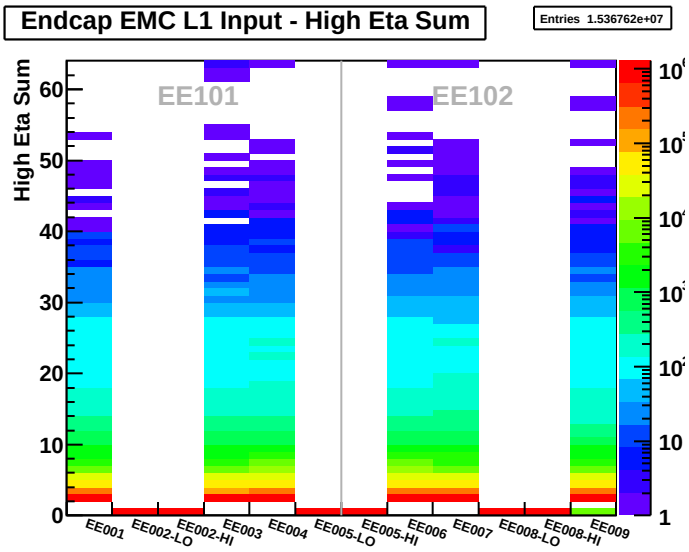

Barrel EMC L1 Input - High Eta Sum
Entries 94
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

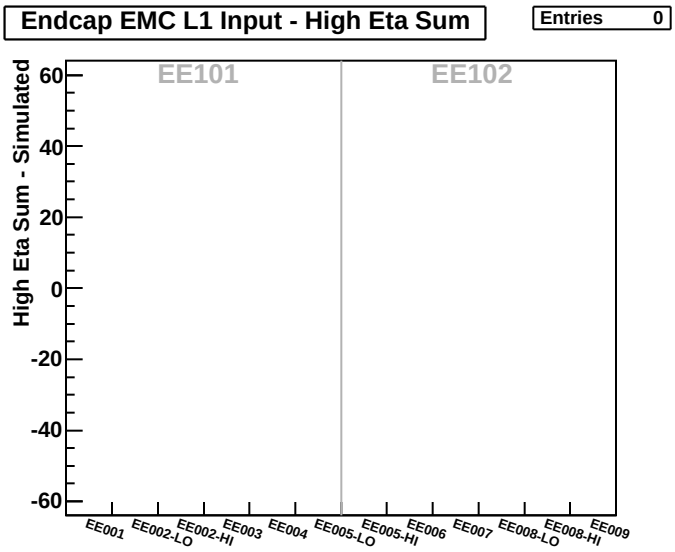

Barrel EMC L1 Input - High Tower Bits
Entries 4.610286e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

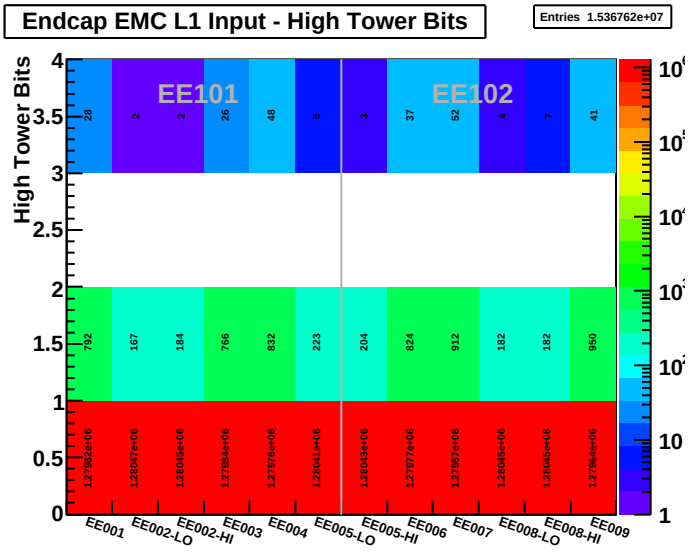

Barrel EMC L1 Input - High Tower Bits
Entries 7
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

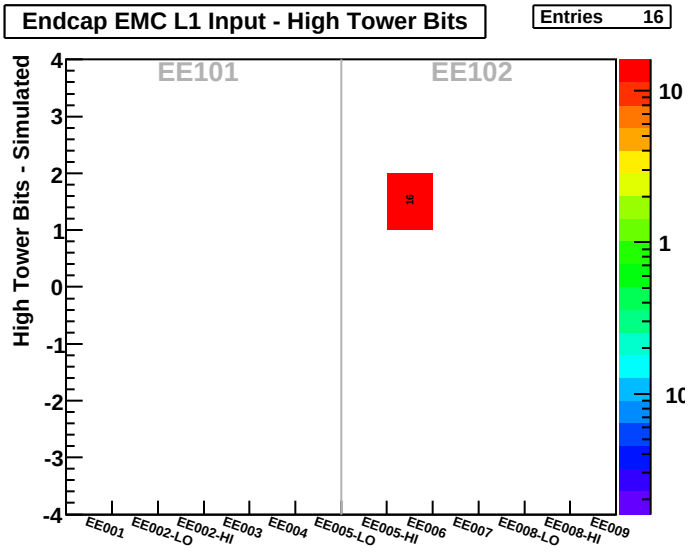

Endcap EMC L1 Input - Low Eta Sum
Entries 1.536762e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

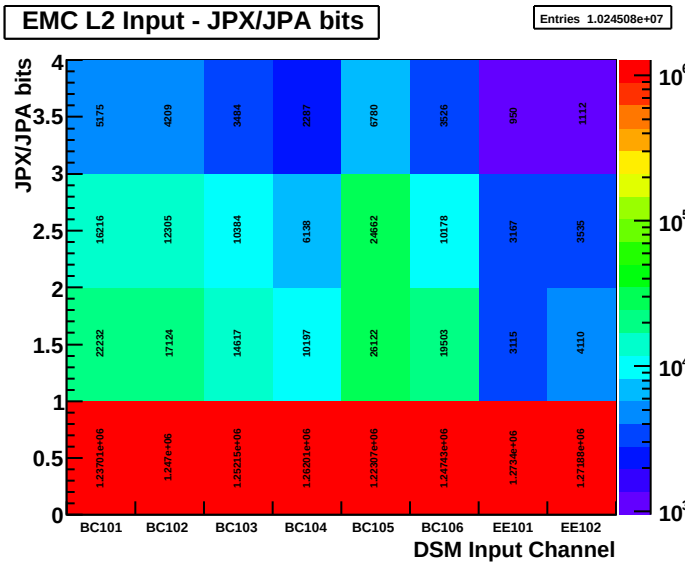

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

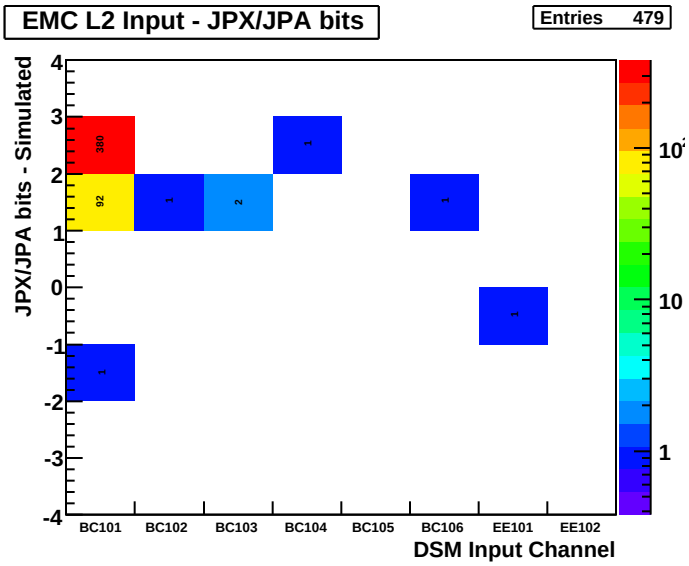

Endcap EMC L1 Input - High Eta Sum
Entries 1.536762e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

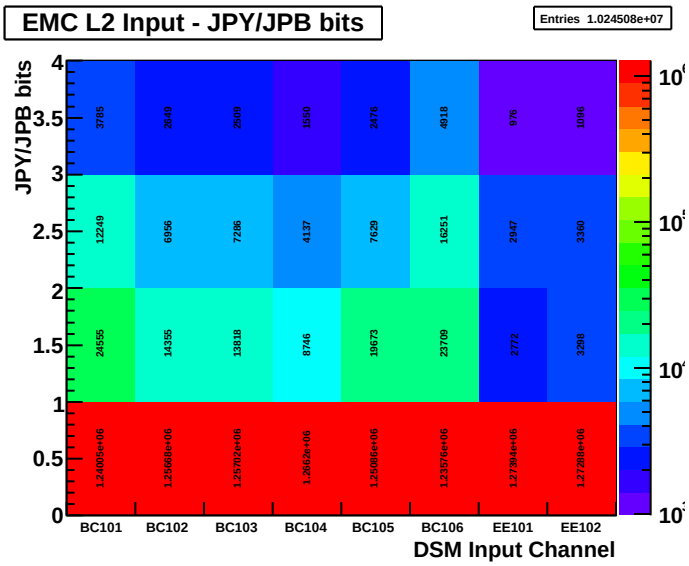

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

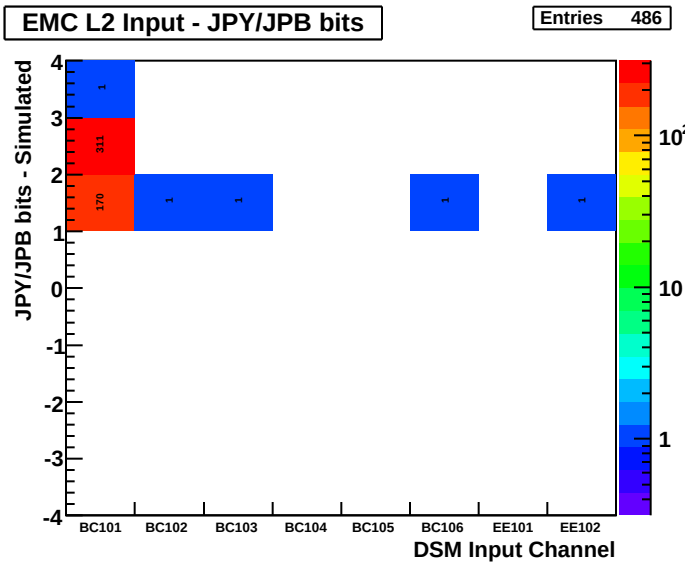

Endcap EMC L1 Input - High Tower Bits
Entries 1.536762e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

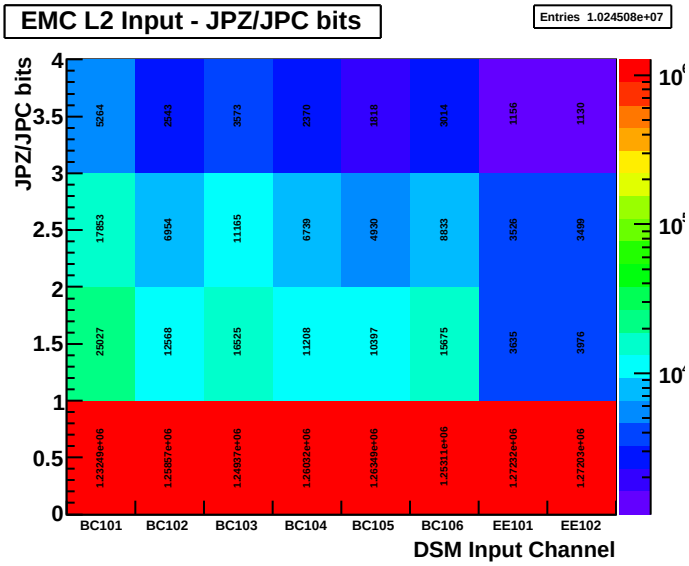

Endcap EMC L1 Input - High Tower Bits
Entries 16
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

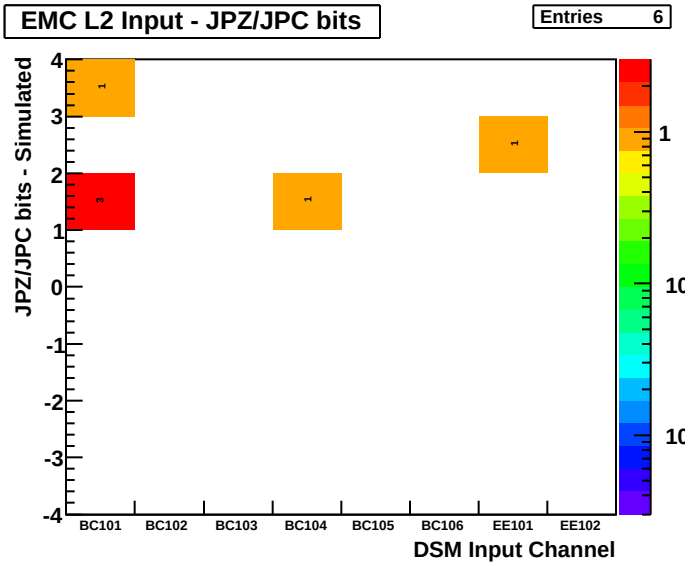

EMC L2 Input - JPX/JPA bits
Entries 1.024508e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5175 4209 3484 2287 6780 3526 950 1112
16216 12305 10384 6138 24662 10178 3167 3535
22232 17124 14617 10197 26122 19503 3115 4110
1.23701e+06 1.247e+06 1.25215e+06 1.26201e+06 1.22307e+06 1.24743e+06 1.2734e+06 1.27188e+06

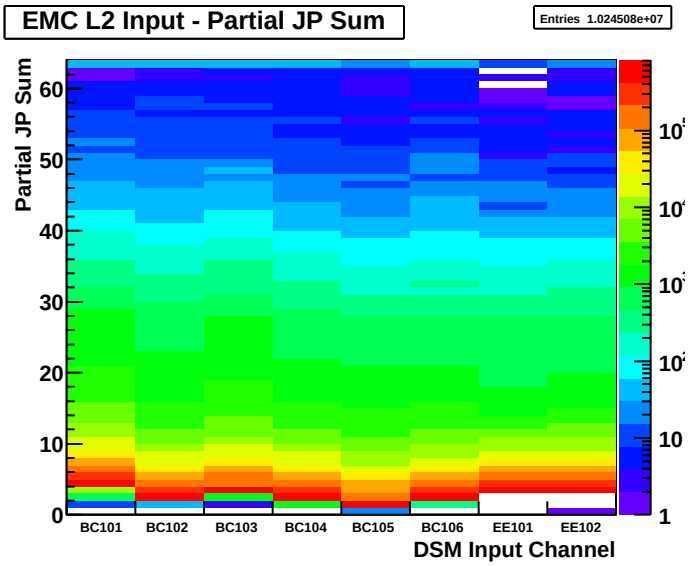

EMC L2 Input - JPX/JPA bits
Entries 479
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
380 1 92 1 2 1 1 1

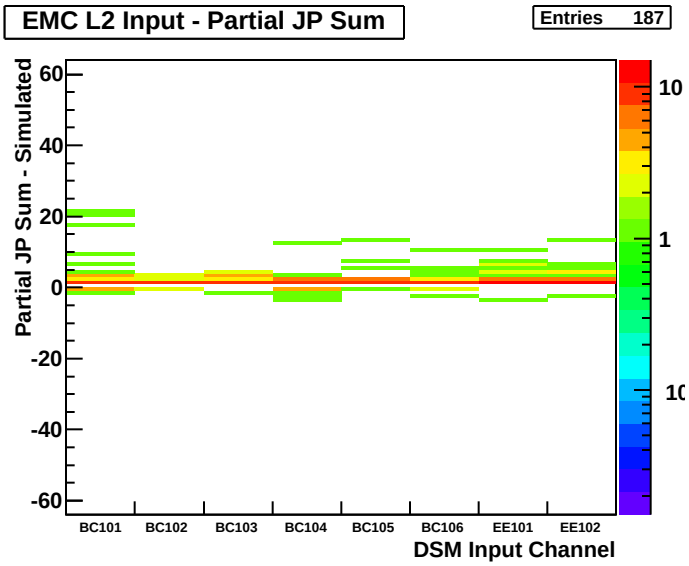

EMC L2 Input - JPY/JPB bits
Entries 1.024508e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3785 2649 2509 1550 2476 4918 976 1096
12249 6956 7286 4137 7629 16251 2947 3360
24555 14355 13818 8746 19673 23709 2772 3298
1.24005e+06 1.25668e+06 1.25702e+06 1.2662e+06 1.25086e+06 1.23576e+06 1.27394e+06 1.27288e+06

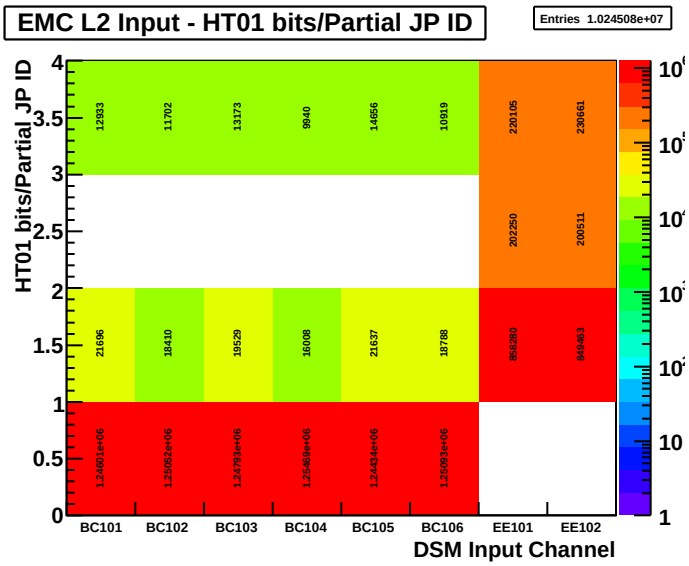

EMC L2 Input - JPY/JPB bits
Entries 486
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 311 170 1 1 1 1

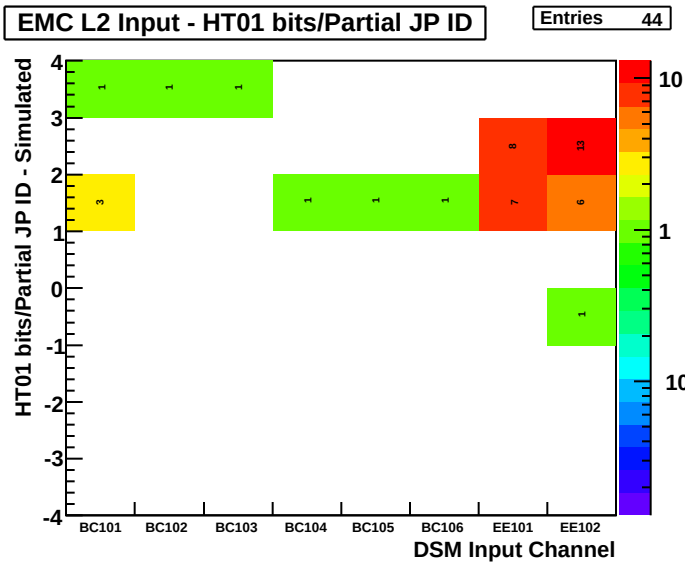

EMC L2 Input - JPZ/JPC bits
Entries 1.024508e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5264 2543 3573 2370 1518 3014 1156 1130
17853 6954 11165 6739 4930 8833 3528 3499
25027 12568 16525 11208 10397 15675 3635 3976
1.23249e+06 1.25857e+06 1.24937e+06 1.26032e+06 1.26349e+06 1.25311e+06 1.27232e+06 1.27203e+06

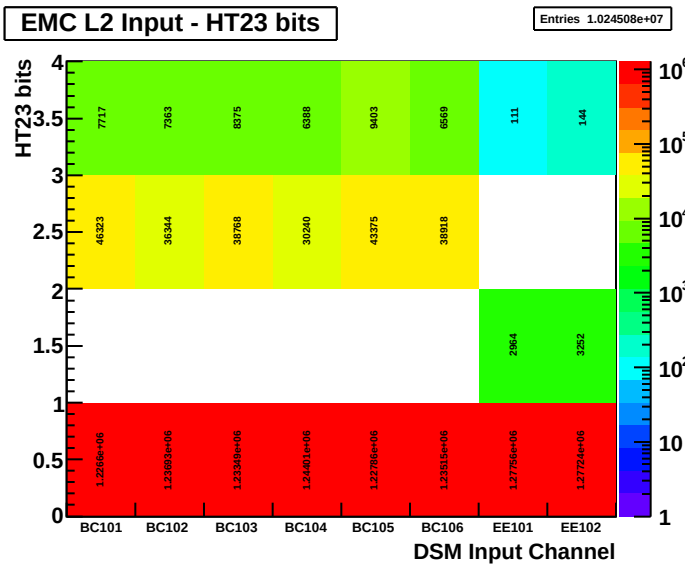

EMC L2 Input - JPZ/JPC bits
Entries 6
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 3 1 1

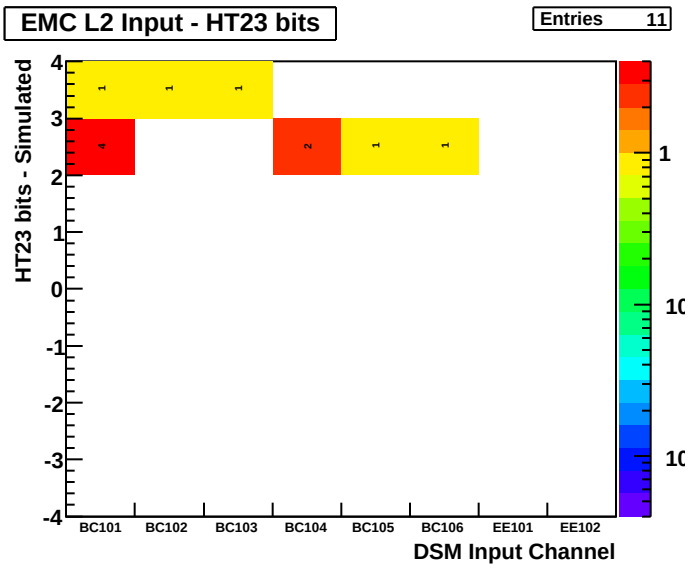

EMC L2 Input - Partial JP Sum
Entries 1.024508e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

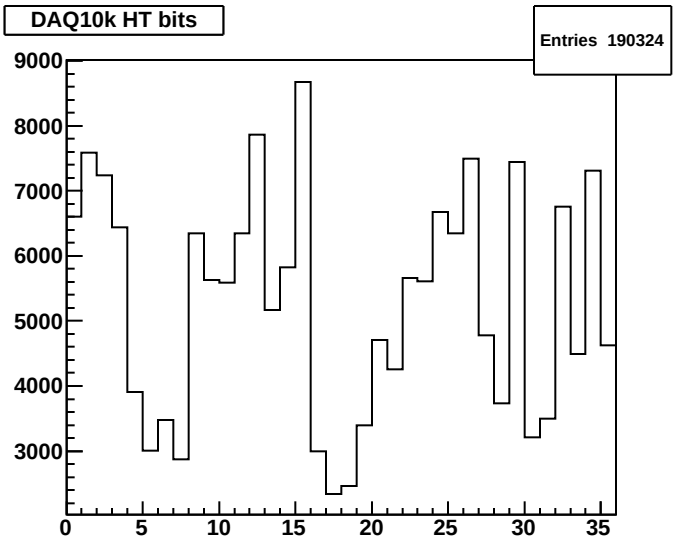

EMC L2 Input - Partial JP Sum
Entries 187
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

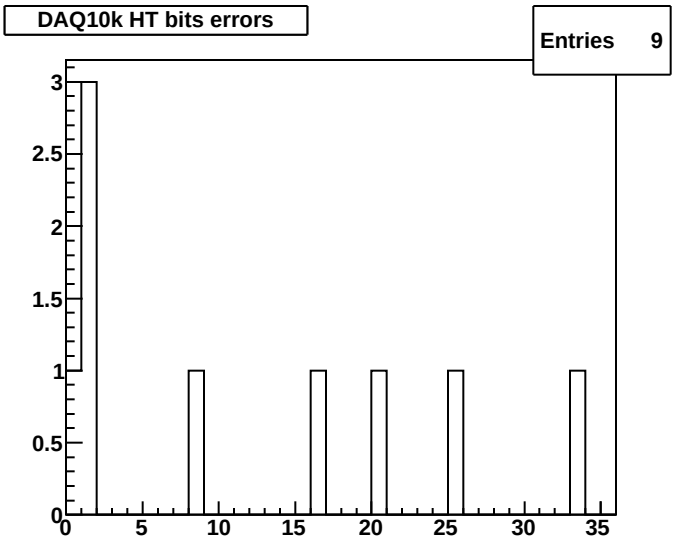

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.024508e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

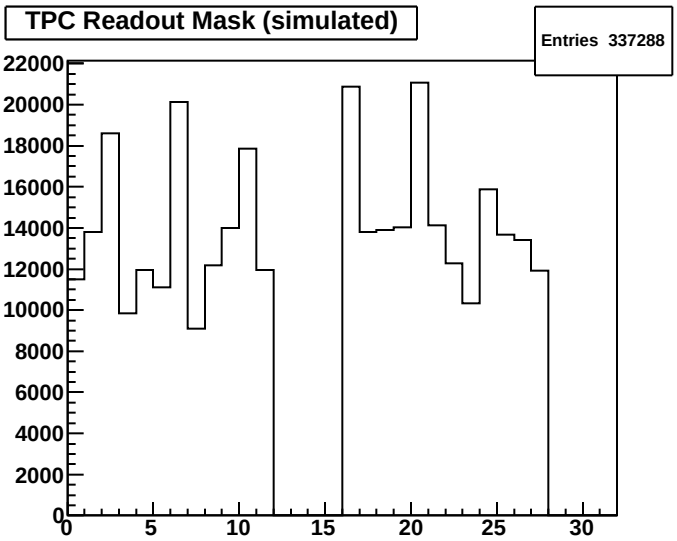

EMC L2 Input - HT01 bits/Partial JP ID
Entries 44
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

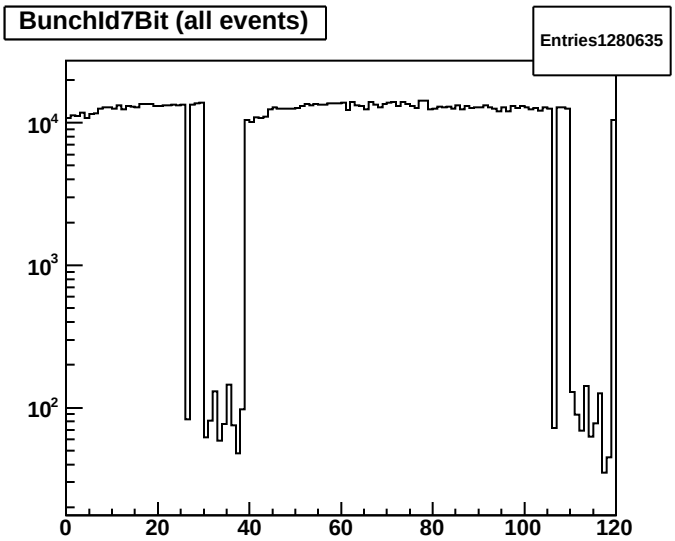

EMC L2 Input - HT23 bits
Entries 1.024508e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

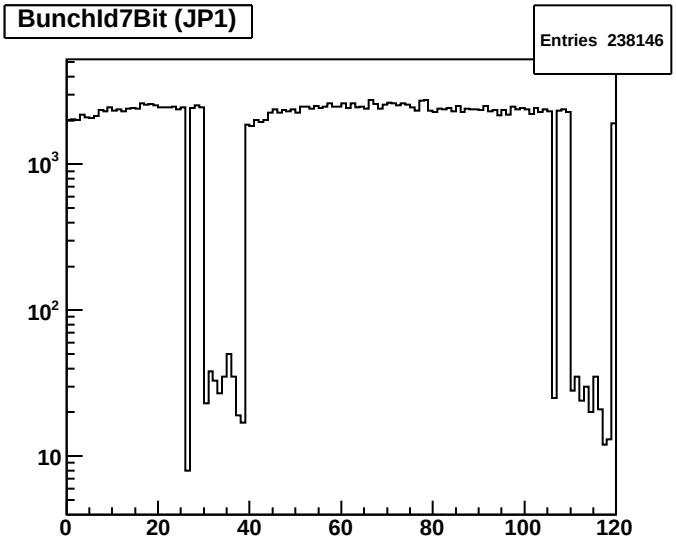

EMC L2 Input - HT23 bits
Entries 11
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

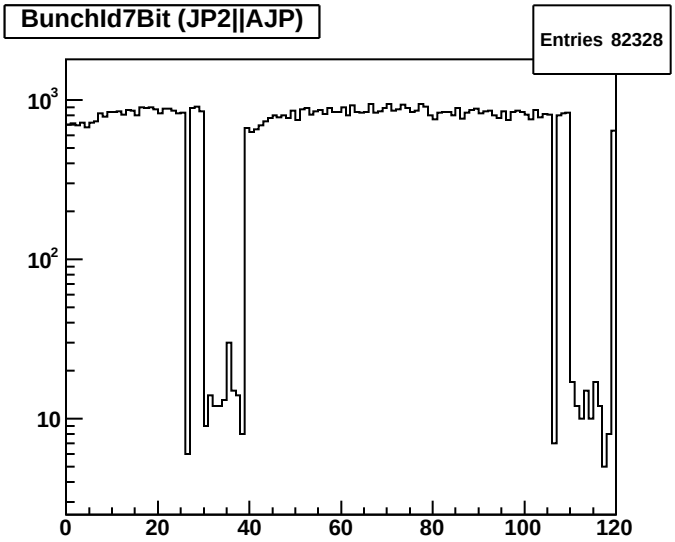

DAQ10k HT bits
Entries 190324

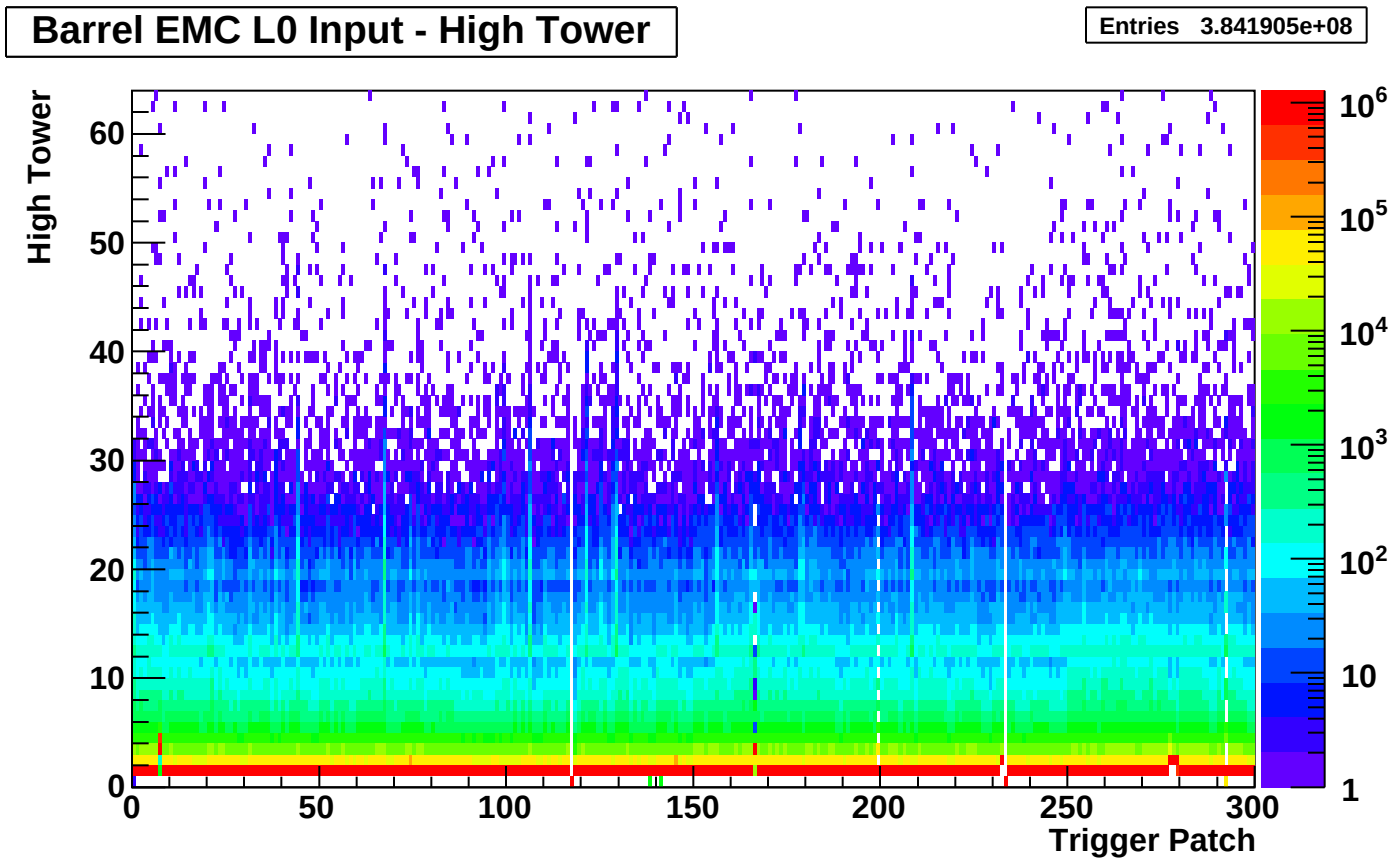

DAQ10k HT bits errors
Entries 9

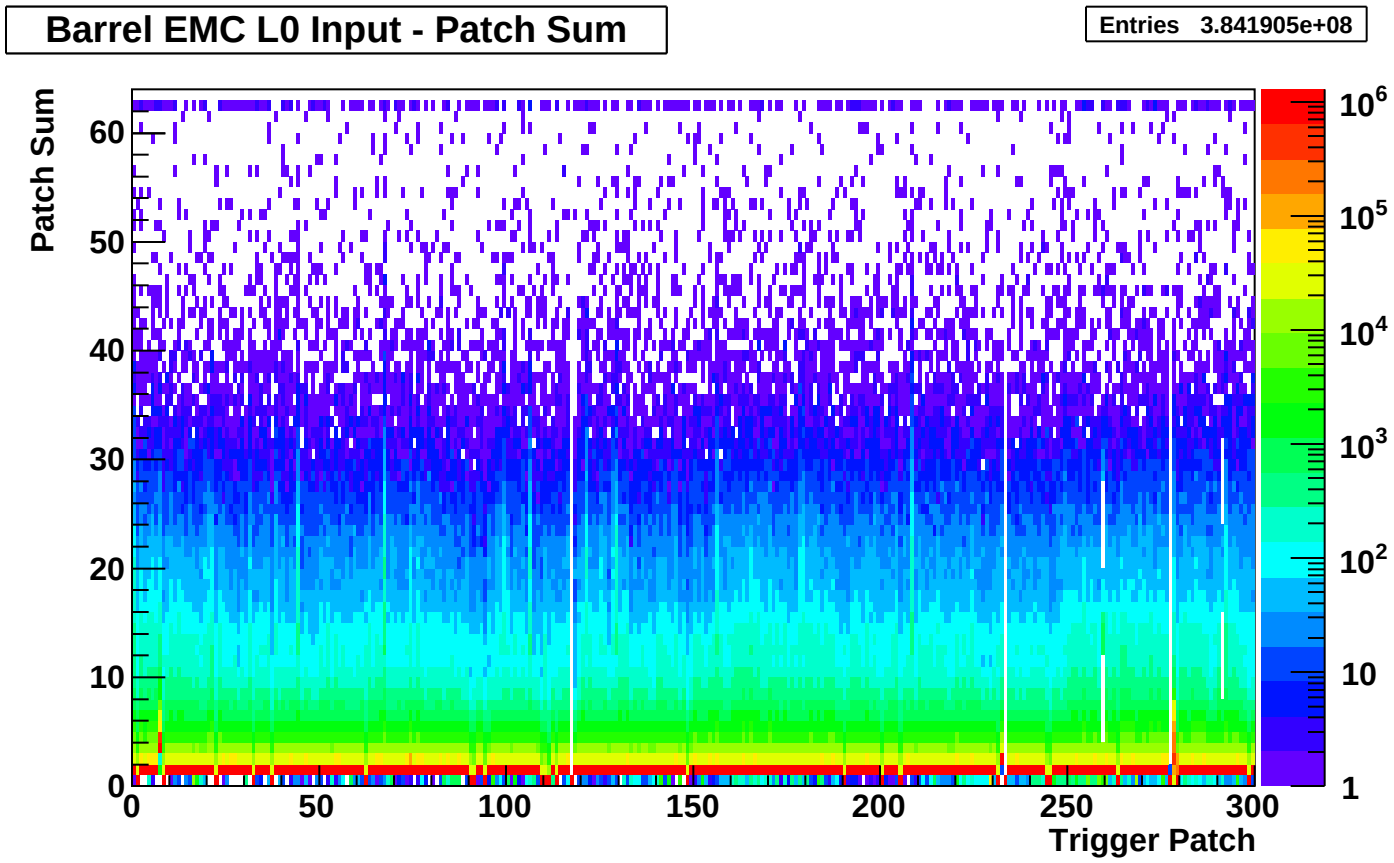

TPC Readout Mask (simulated)
Entries 337288

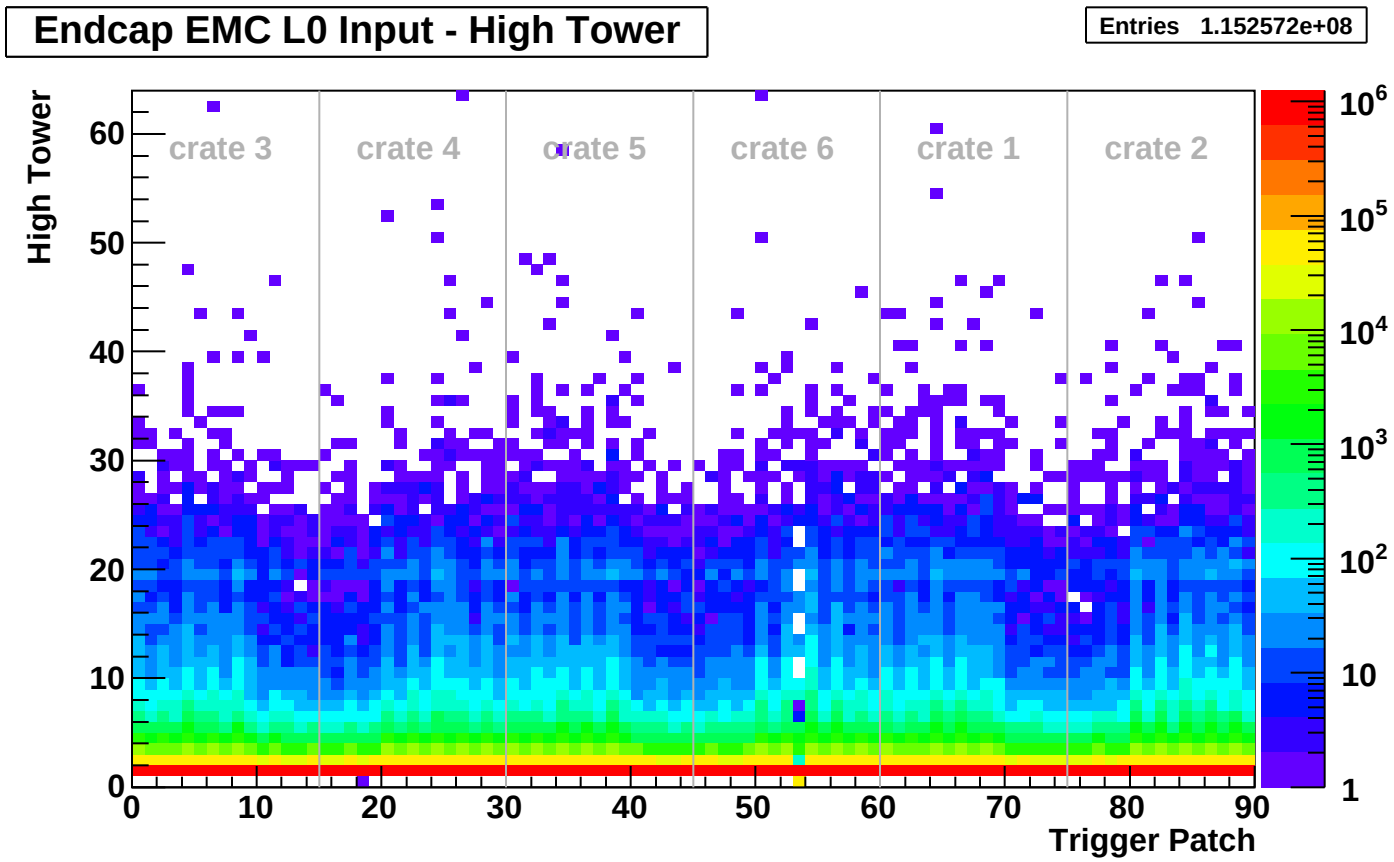

BunchId7Bit (all events)
Entries1280635

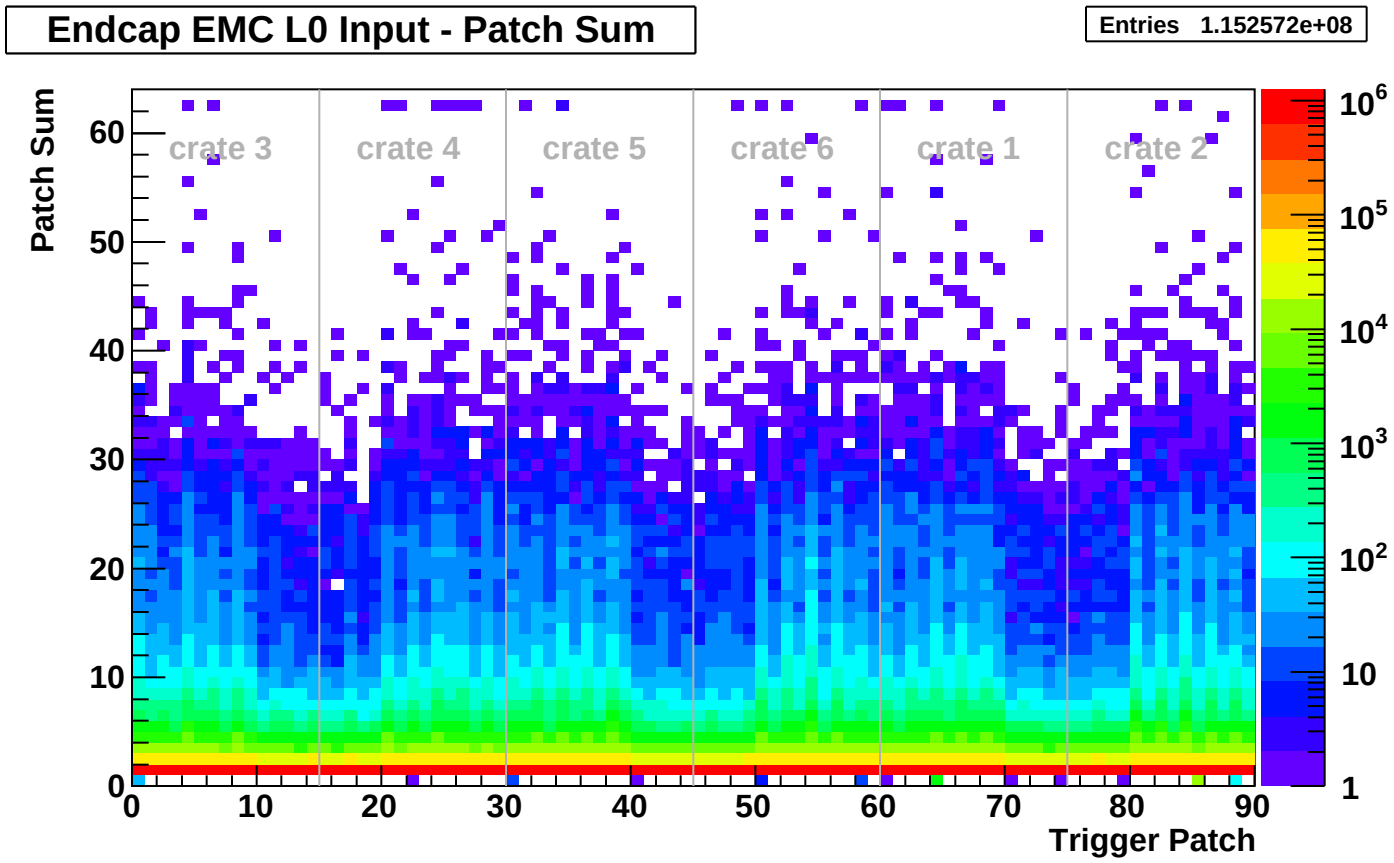

BunchId7Bit (JP1)
Entries 238146

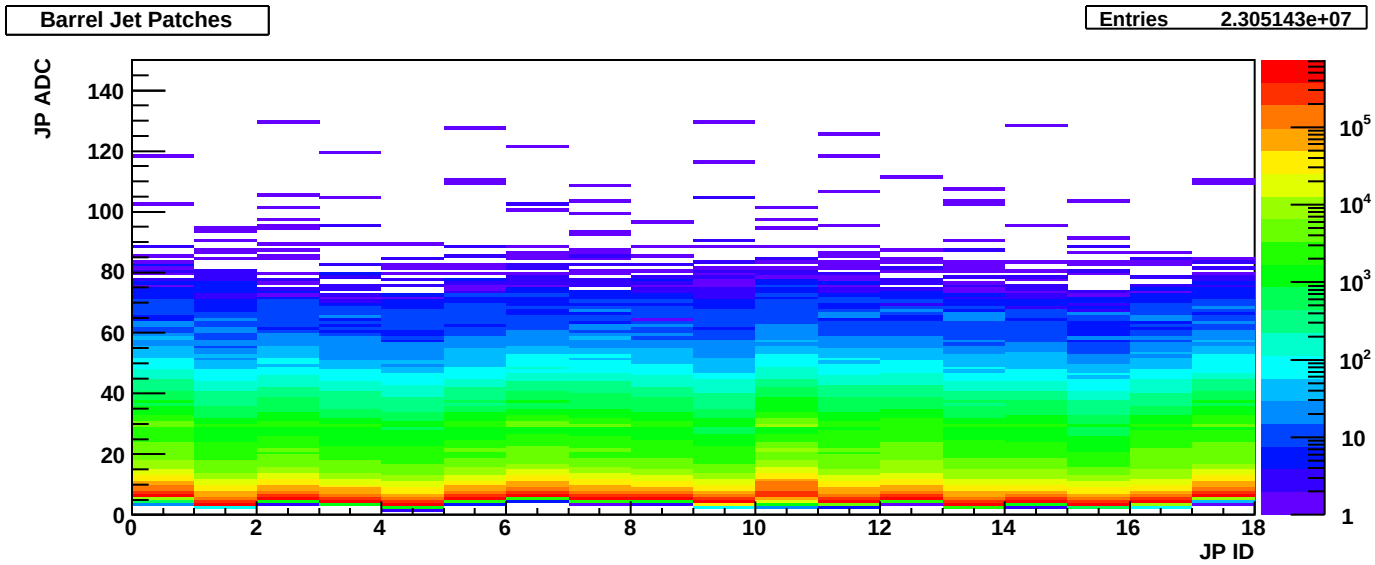

BunchId7Bit (JP2||AJP)
Entries 82328

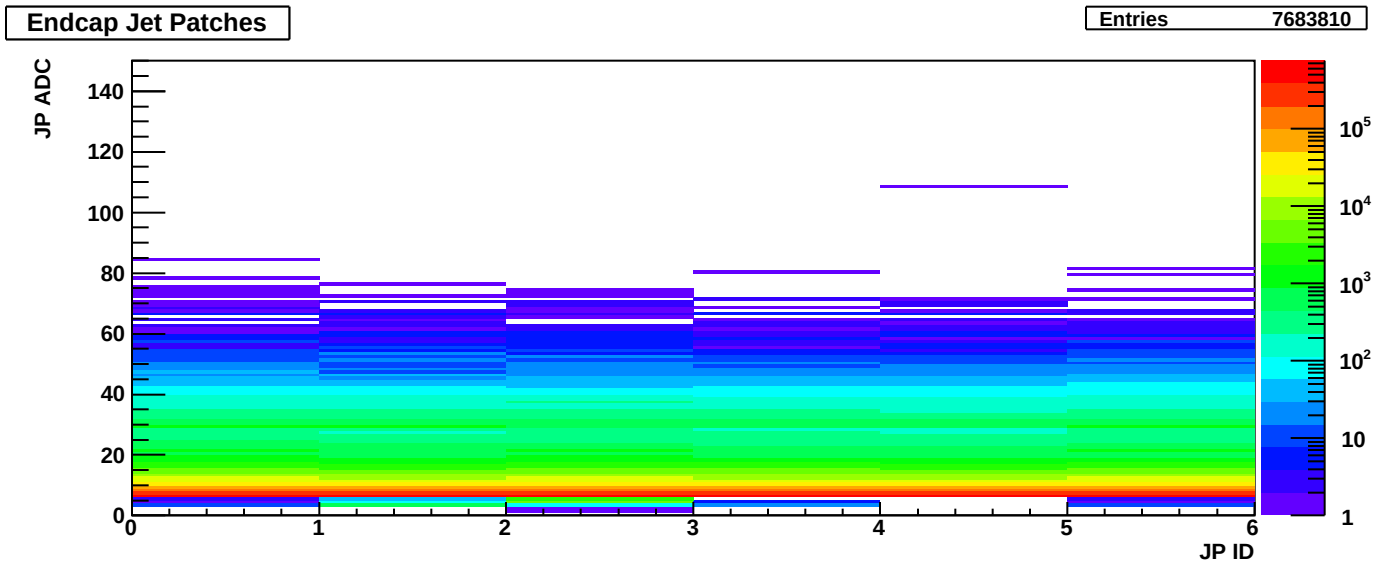

Barrel EMC L0 Input - High Tower
Entries 3.841905e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

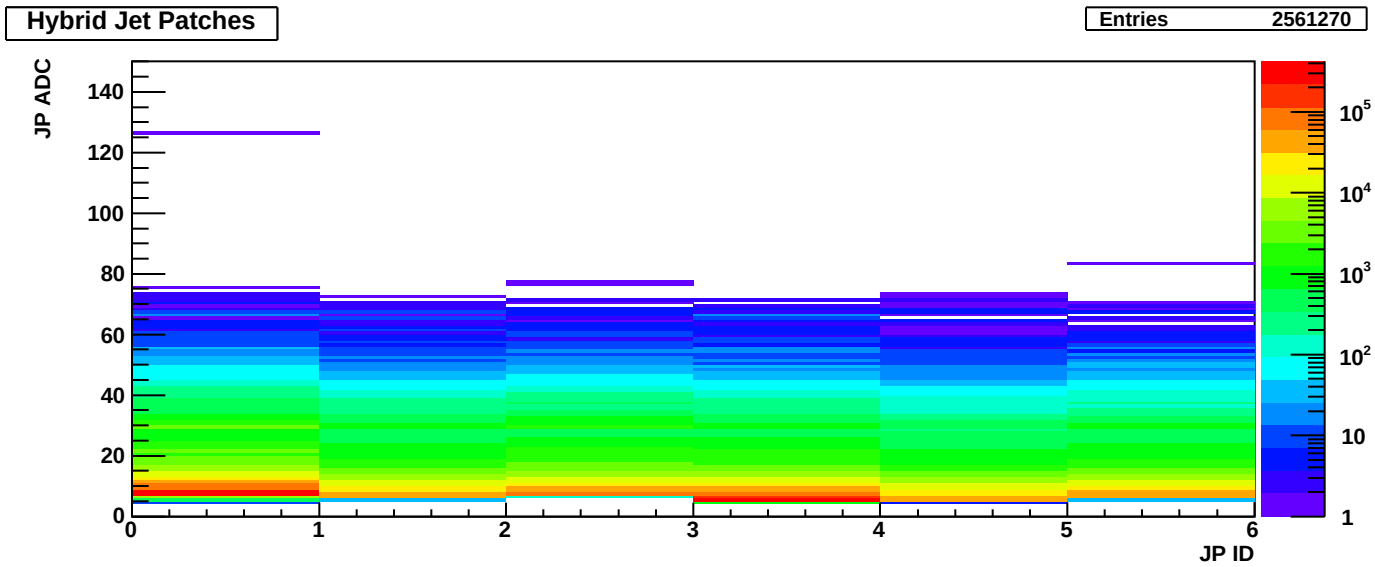

Barrel EMC L0 Input - Patch Sum
Entries 3.841905e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

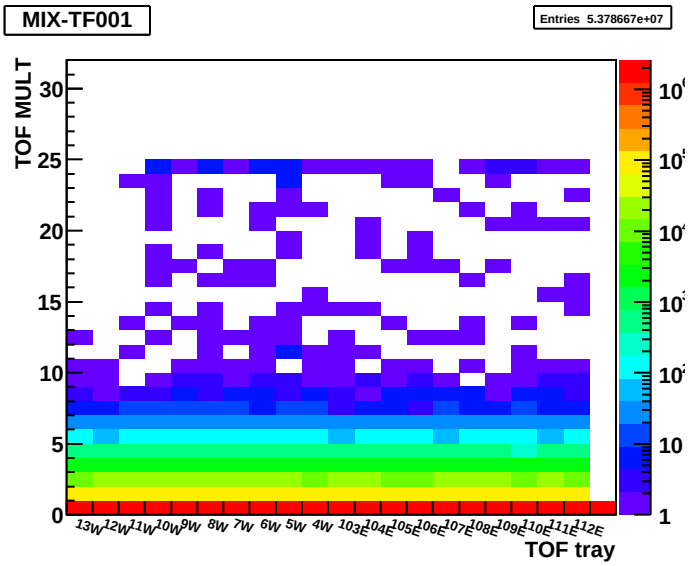

Endcap EMC L0 Input - High Tower
Entries 1.152572e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

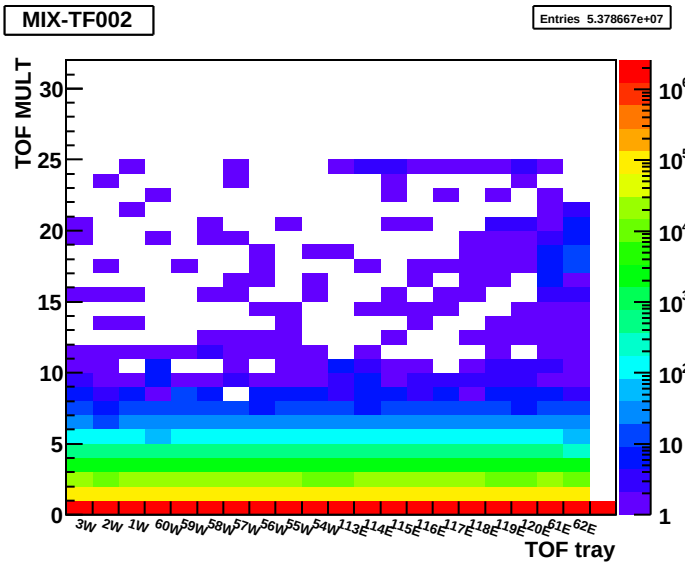

Endcap EMC L0 Input - Patch Sum
Entries 1.152572e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

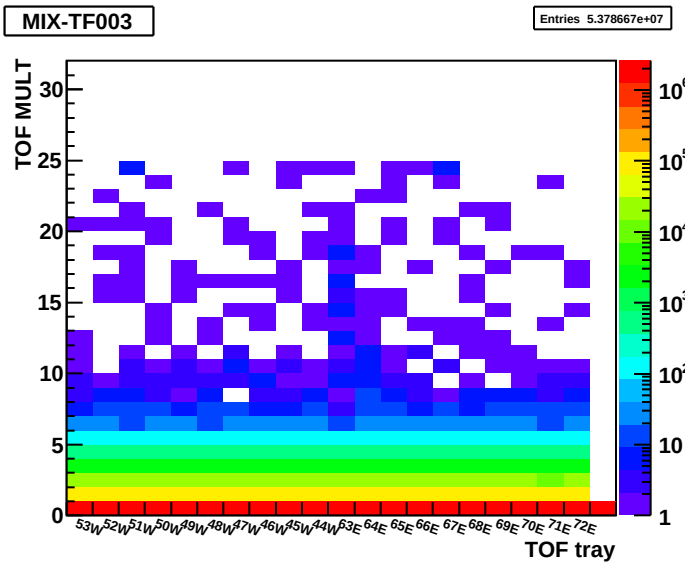

Barrel Jet Patches
Entries 2.305143e+07
JP ADC
JP ID

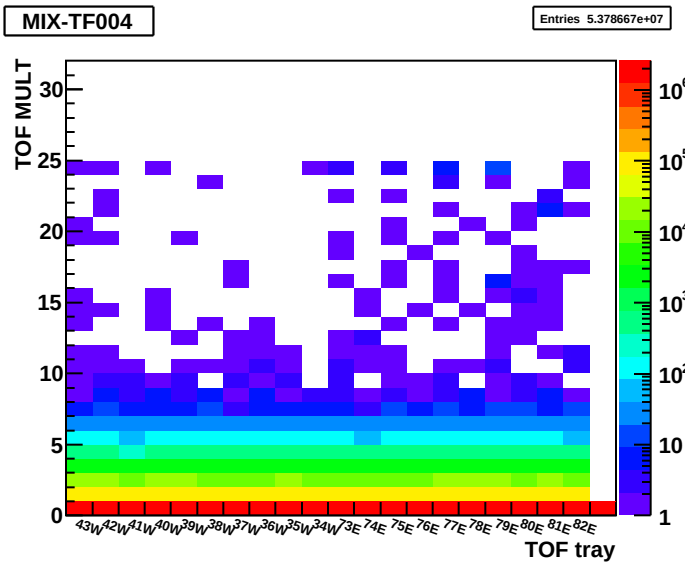

Endcap Jet Patches
Entries 7683810
JP ADC
JP ID

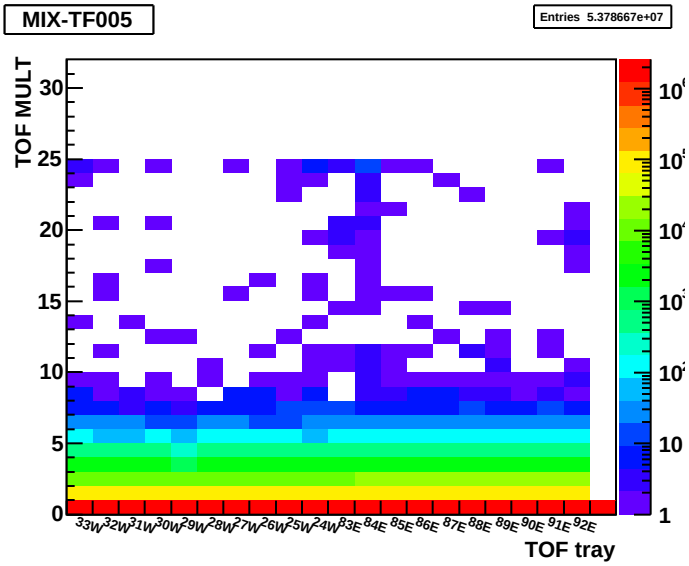

Hybrid Jet Patches
Entries 2561270
JP ADC
JP ID

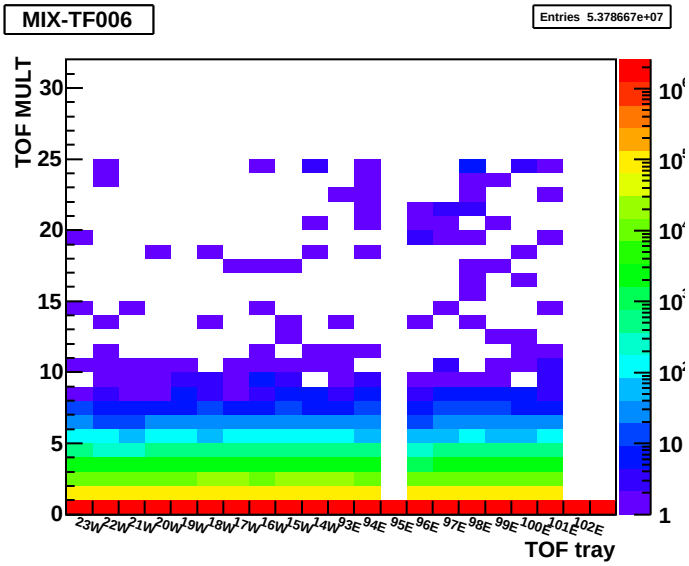

MIX-TF001
Entries 5.378667e+07
TOF MULT
TOF tray

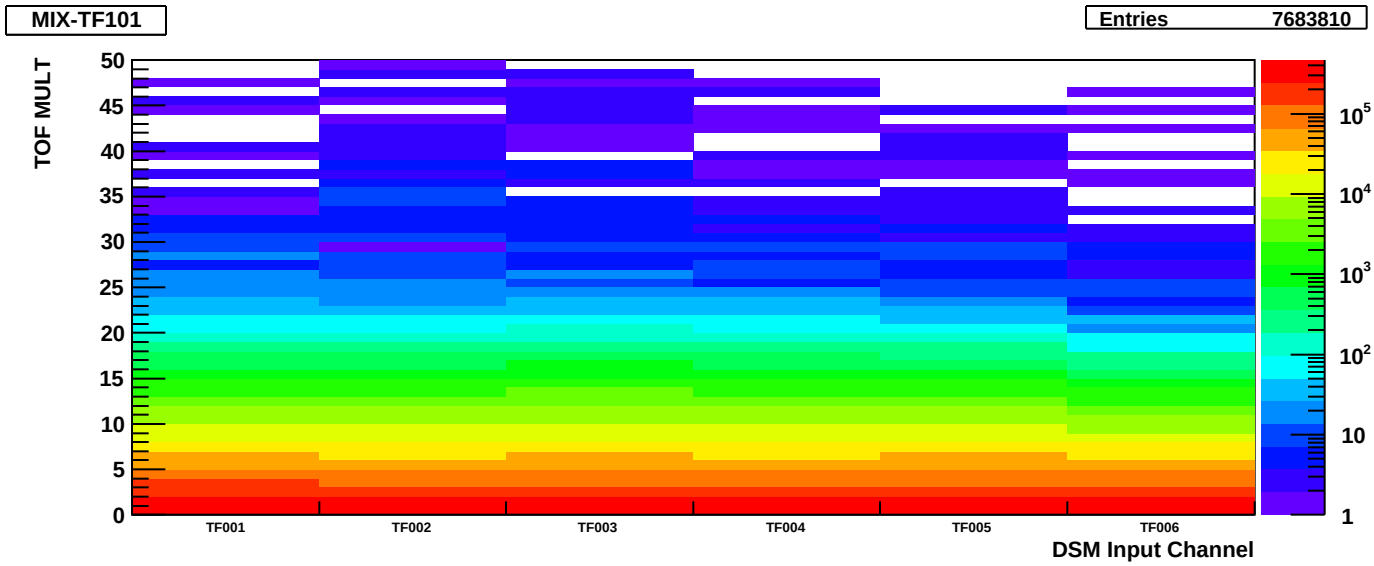

MIX-TF002
Entries 5.378667e+07
TOF MULT
TOF tray

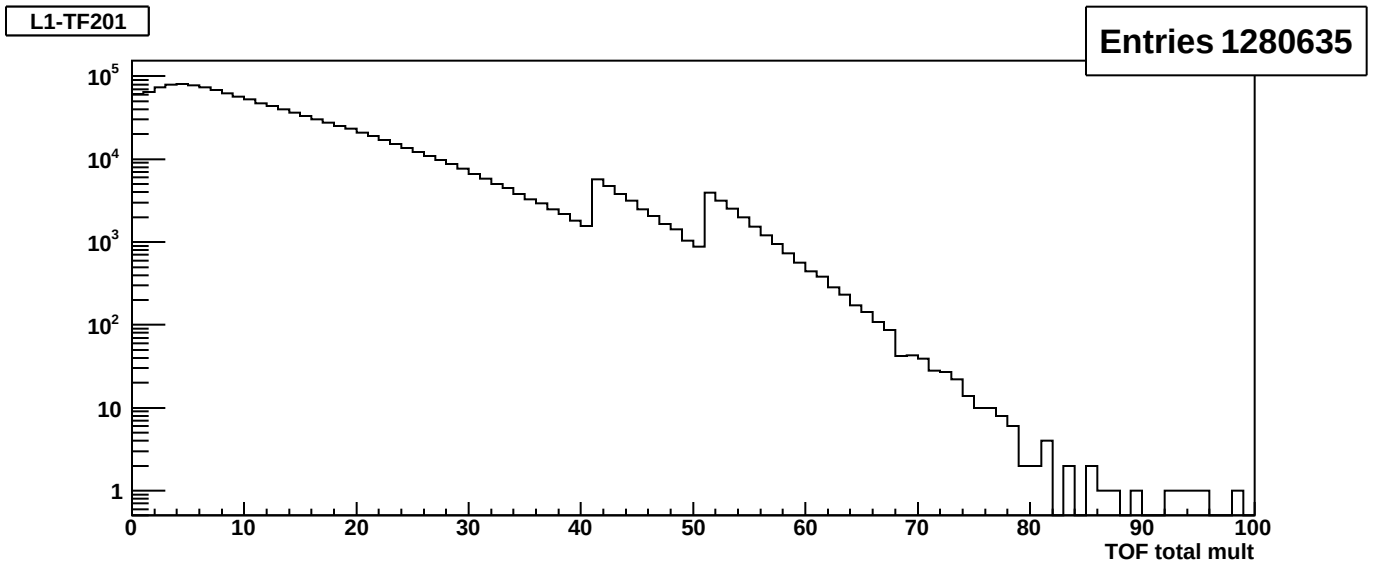

MIX-TF003
Entries 5.378667e+07
TOF MULT
TOF tray

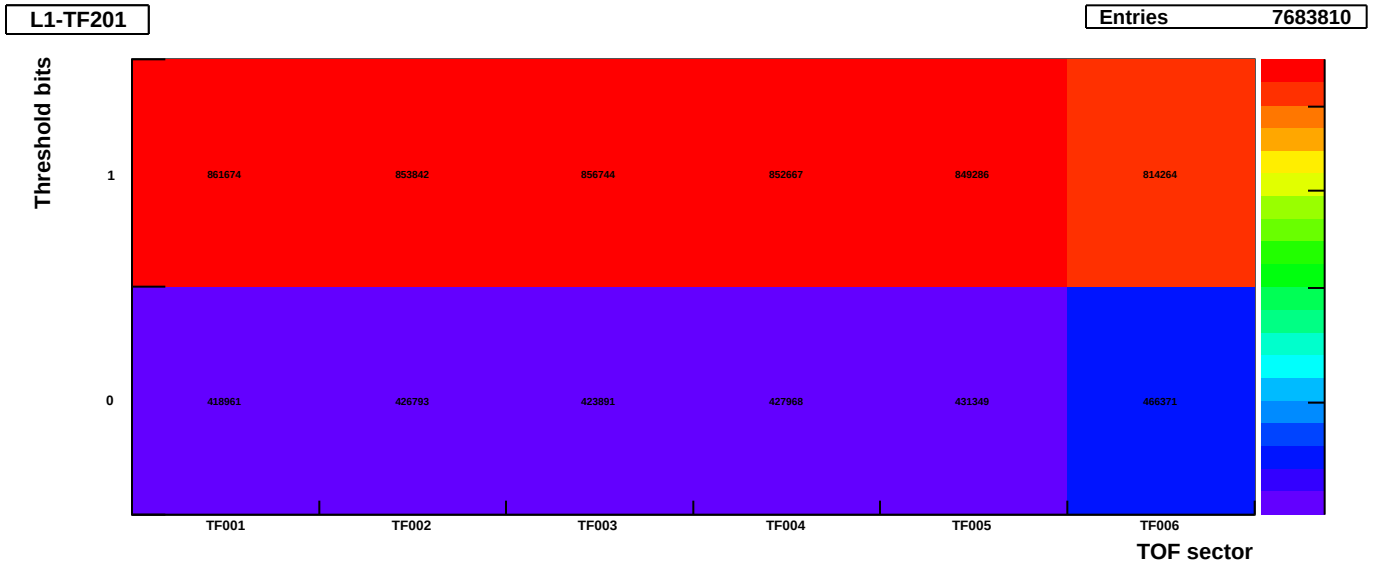

MIX-TF004
Entries 5.378667e+07
TOF MULT
TOF tray

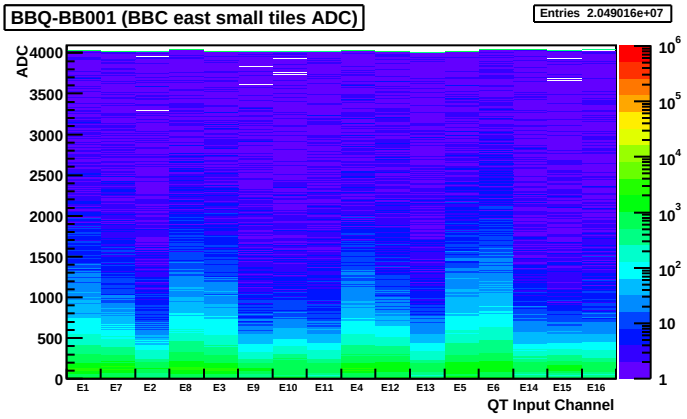

MIX-TF005
Entries 5.378667e+07
TOF MULT
TOF tray

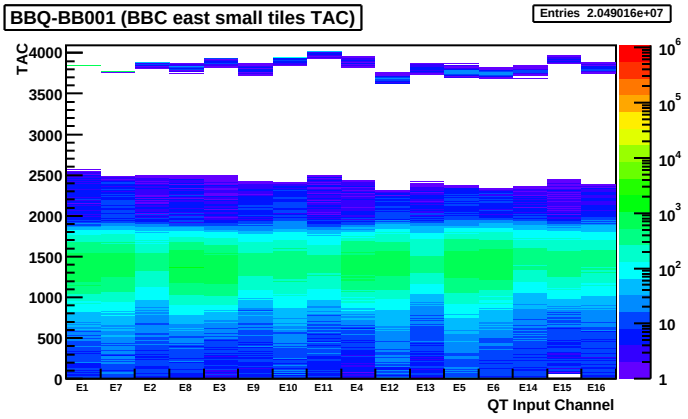

MIX-TF006
Entries 5.378667e+07
TOF MULT
TOF tray

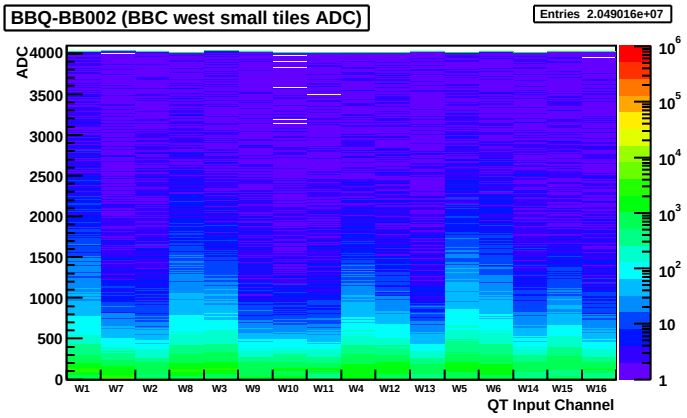

MIX-TF101
Entries 7683810
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

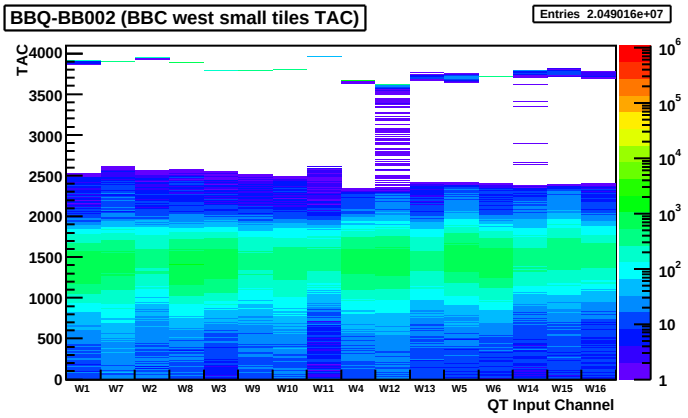

L1-TF201
Entries 1280635
TOF total mult

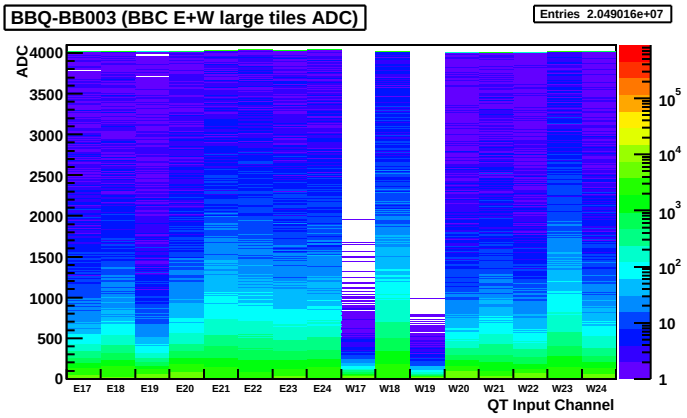

L1-TF201
Entries 7683810
Threshold bits
861674
853842
856744
852667
849286
814264
418961
426793
423891
427968
431349
466371
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

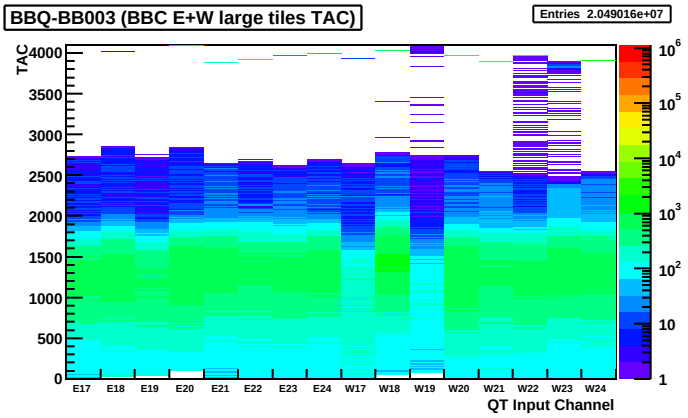

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.049016e+07
ADC
QT Input Channel

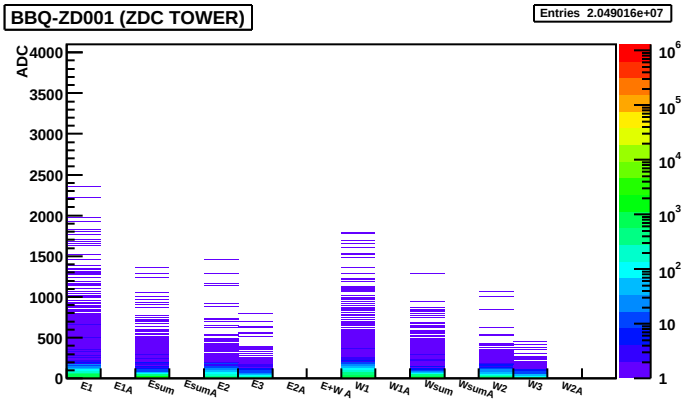

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.049016e+07
TAC
QT Input Channel

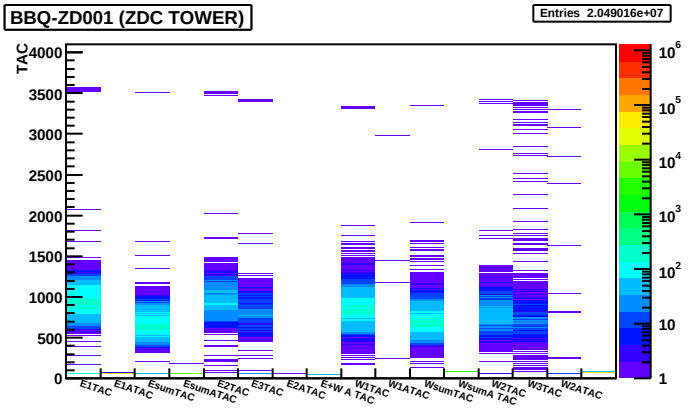

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.049016e+07
ADC
QT Input Channel

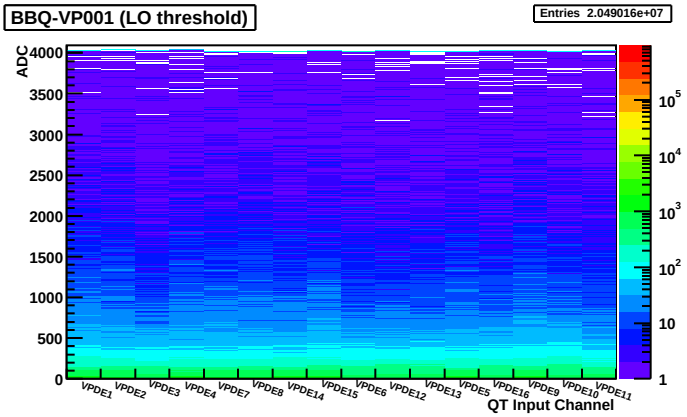

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.049016e+07
TAC
QT Input Channel

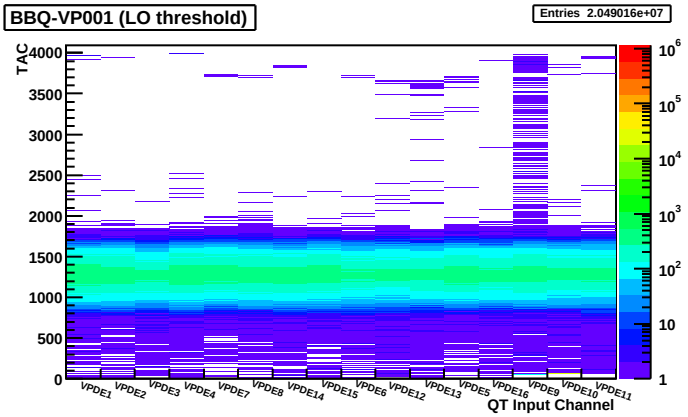

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.049016e+07
ADC
QT Input Channel

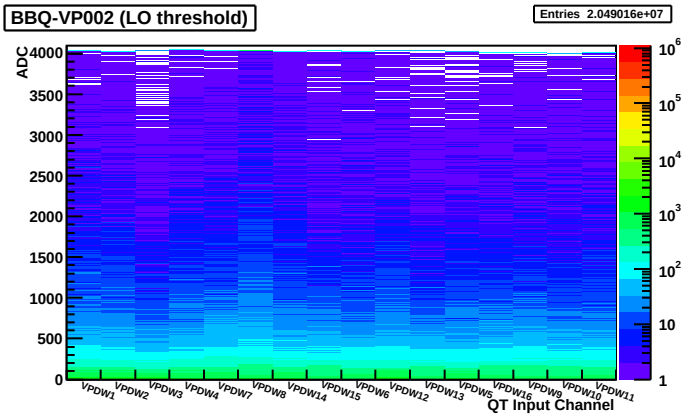

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.049016e+07
TAC
QT Input Channel

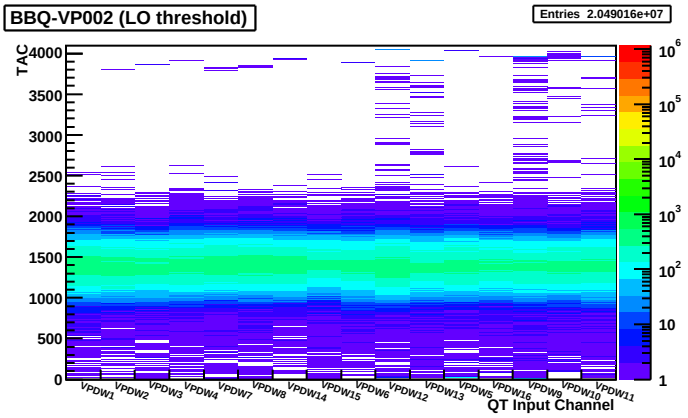

BBQ-ZD001 (ZDC TOWER)
Entries 2.049016e+07
ADC

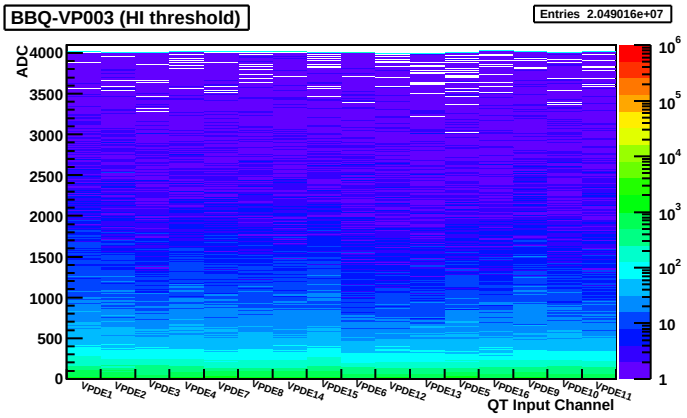

BBQ-ZD001 (ZDC TOWER)
Entries 2.049016e+07
TAC

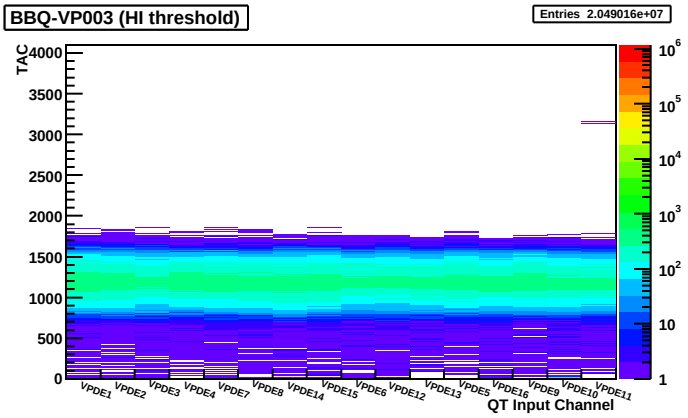

BBQ-VP001 (LO threshold)
Entries 2.049016e+07
ADC
QT Input Channel

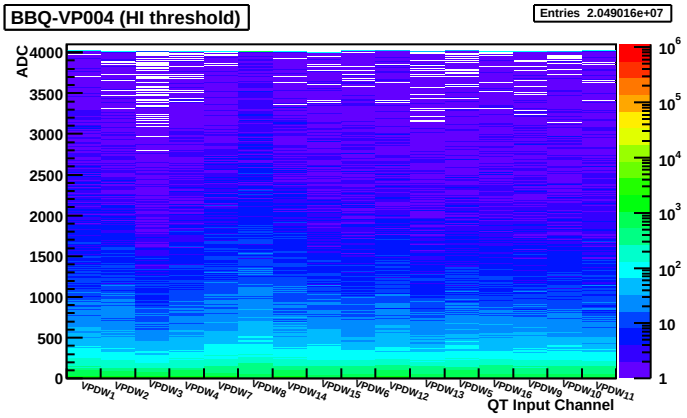

BBQ-VP001 (LO threshold)
Entries 2.049016e+07
TAC
QT Input Channel

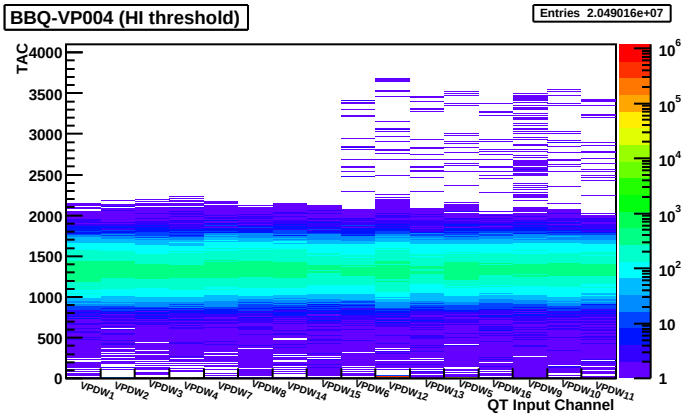

BBQ-VP002 (LO threshold)
Entries 2.049016e+07
ADC
QT Input Channel

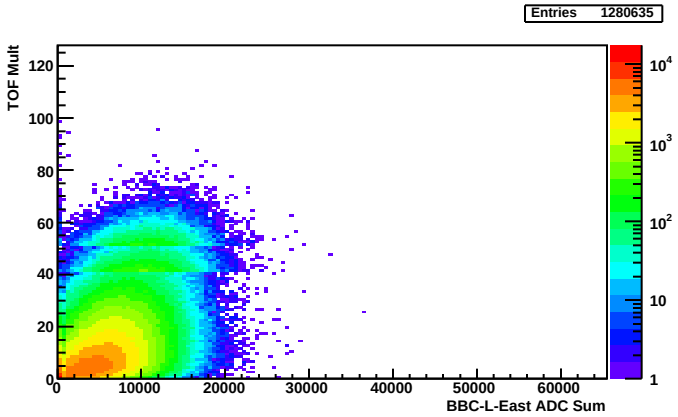

BBQ-VP002 (LO threshold)
Entries 2.049016e+07
TAC
QT Input Channel

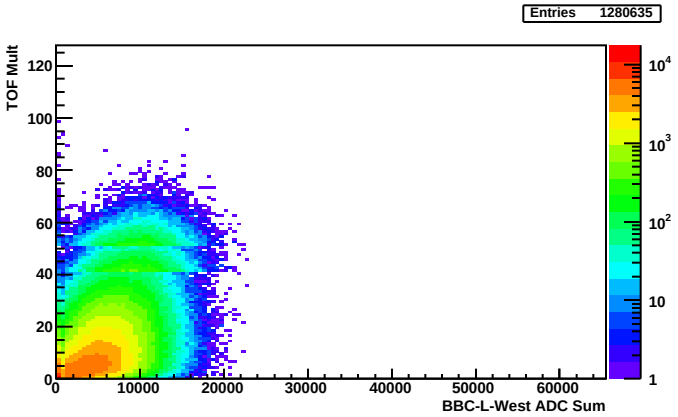

BBQ-VP003 (HI threshold)
Entries 2.049016e+07
ADC
QT Input Channel

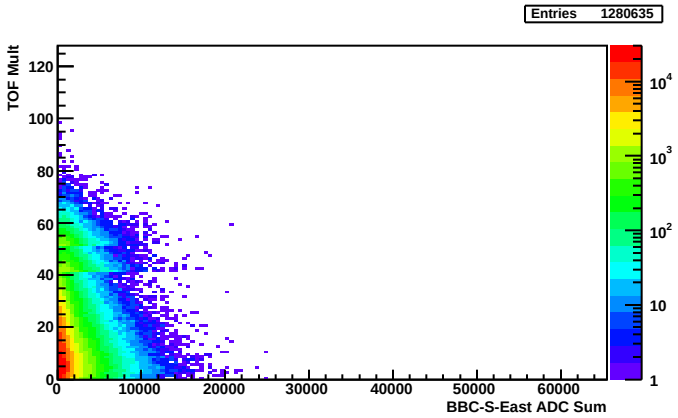

BBQ-VP003 (HI threshold)
Entries 2.049016e+07
TAC
QT Input Channel

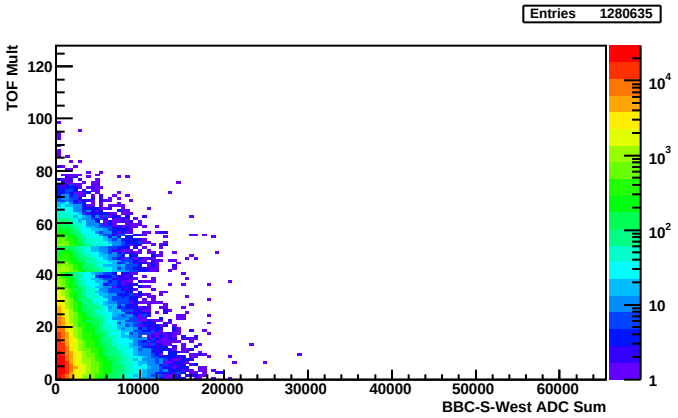

BBQ-VP004 (HI threshold)
Entries 2.049016e+07
ADC
QT Input Channel

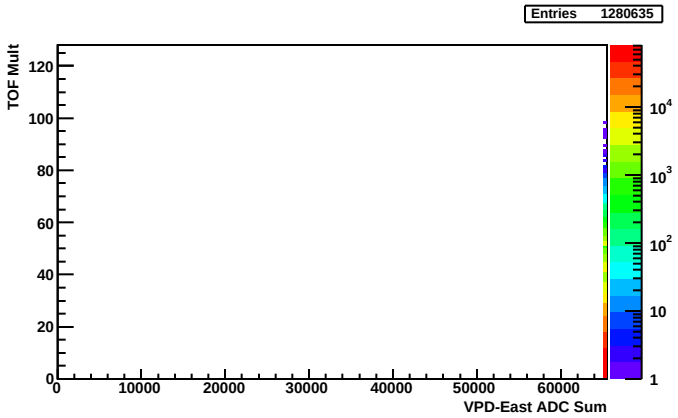

BBQ-VP004 (HI threshold)
Entries 2.049016e+07
TAC
QT Input Channel

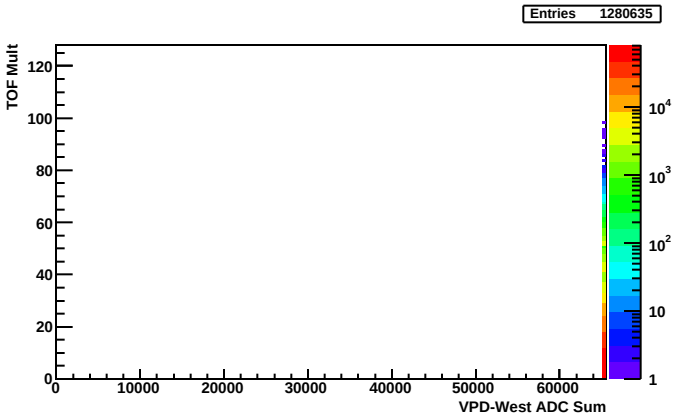

Entries 1280635
TOF Mult
BBC-L-East ADC Sum

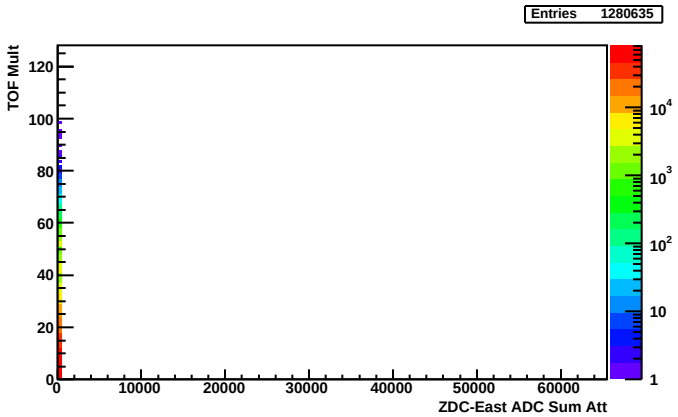

Entries 1280635
TOF Mult
BBC-L-West ADC Sum

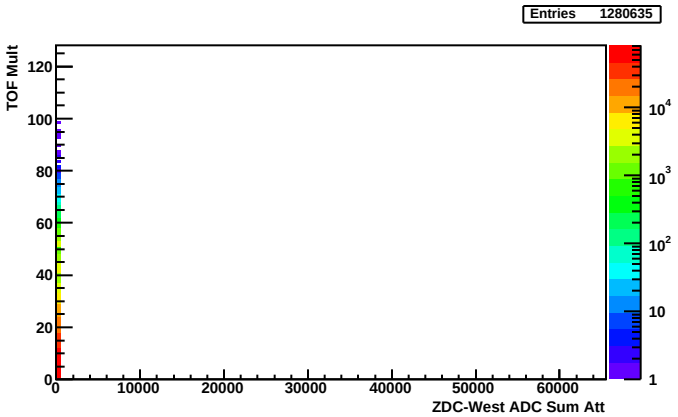

Entries 1280635
TOF Mult
BBC-S-East ADC Sum

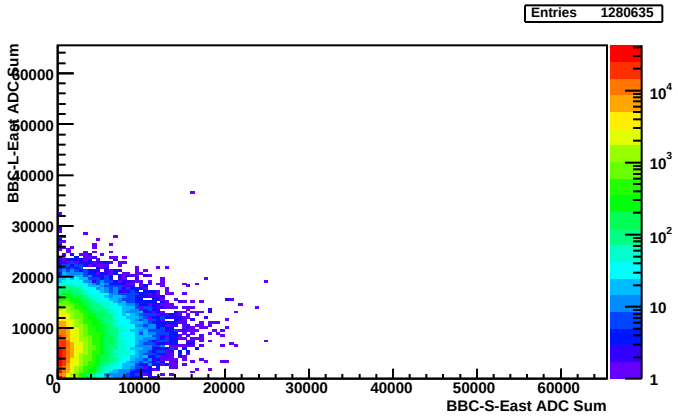

Entries 1280635
TOF Mult
BBC-S-West ADC Sum

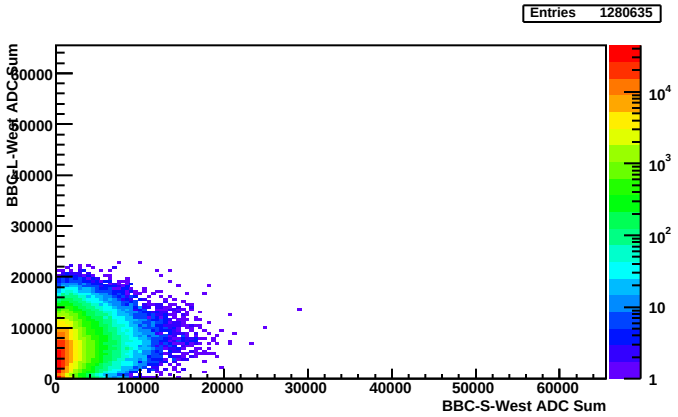

Entries 1280635
TOF Mult
VPD-East ADC Sum

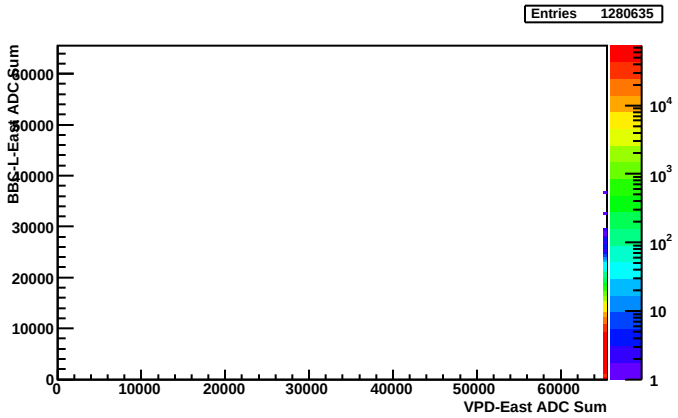

Entries 1280635
TOF Mult
VPD-West ADC Sum

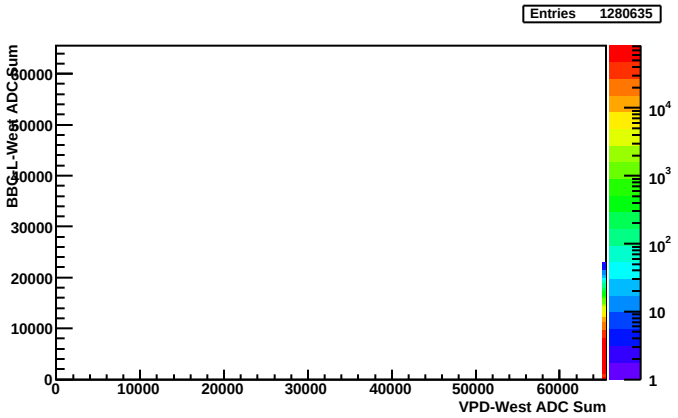

Entries 1280635
TOF Mult
ZDC-East ADC Sum Att

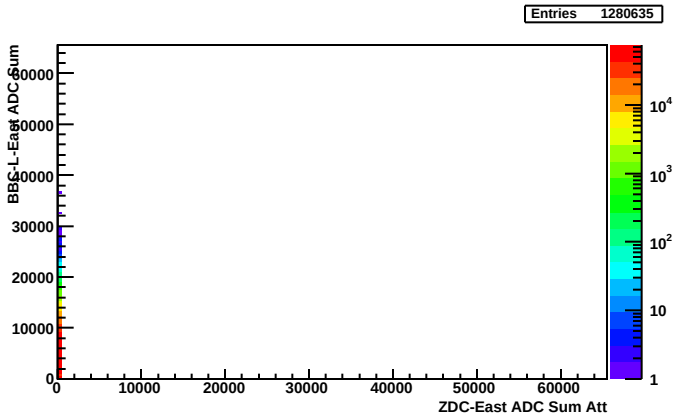

Entries 1280635
TOF Mult
ZDC-West ADC Sum Att

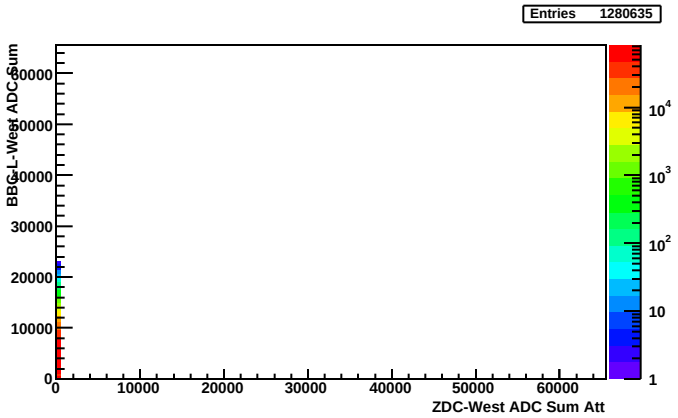

Entries 1280635
BBC-L-East ADC Sum
BBC-S-East ADC Sum

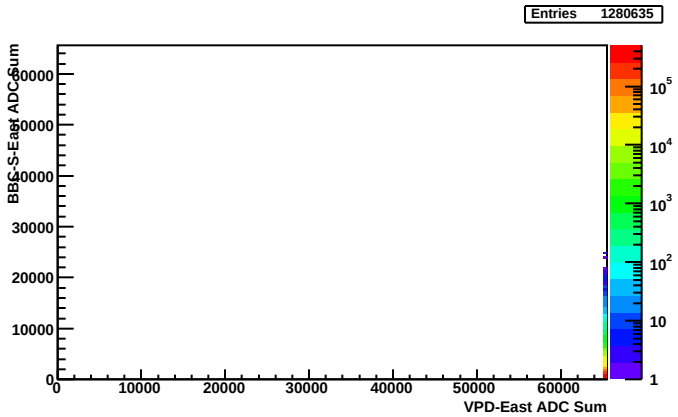

Entries 1280635
BBC-L-West ADC Sum
BBC-S-West ADC Sum

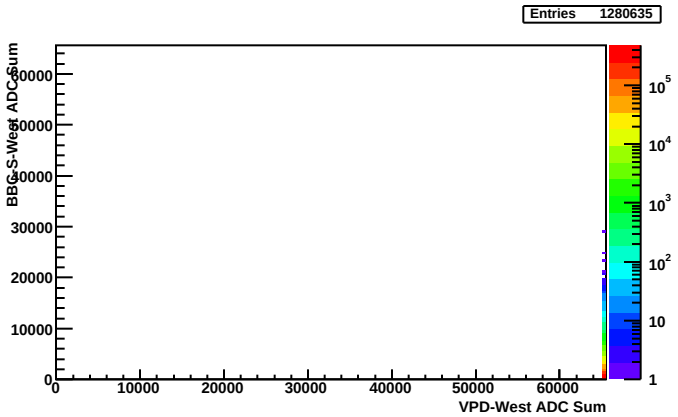

Entries 1280635
BBC-L-East ADC Sum
VPD-East ADC Sum

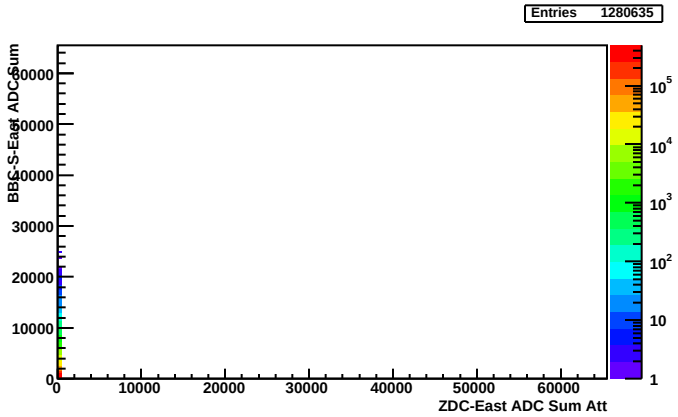

Entries 1280635
BBC-L-West ADC Sum
VPD-West ADC Sum

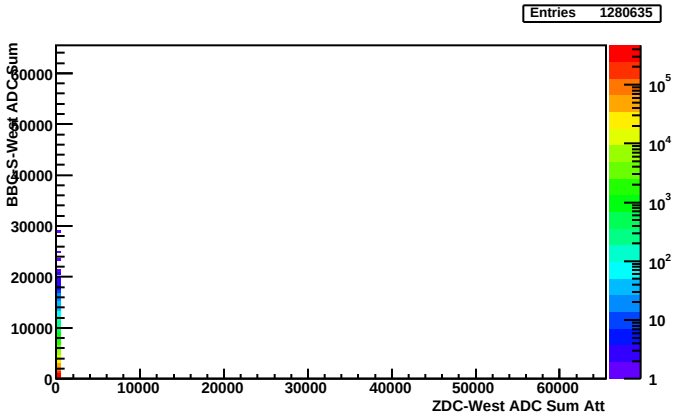

Entries 1280635
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

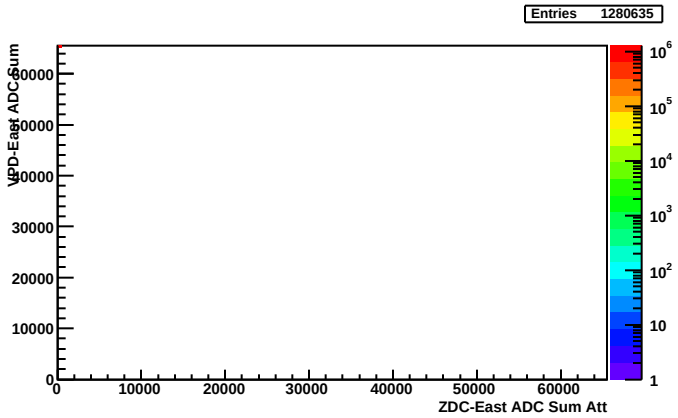

Entries 1280635
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

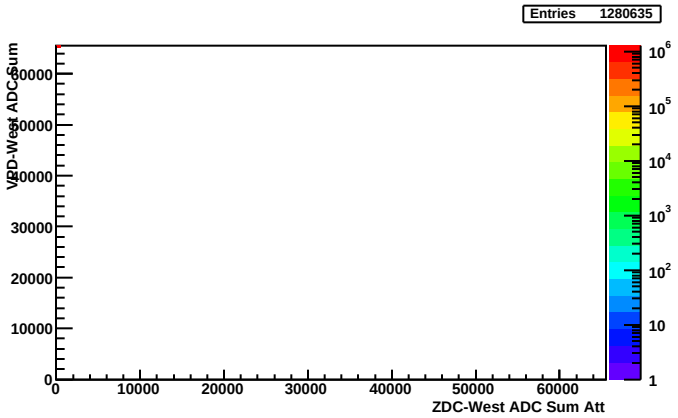

Entries 1280635
BBC-S-East ADC Sum
VPD-East ADC Sum

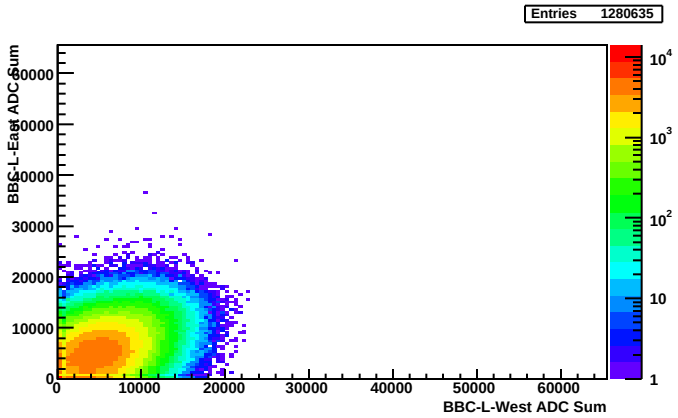

Entries 1280635
BBC-S-West ADC Sum
VPD-West ADC Sum

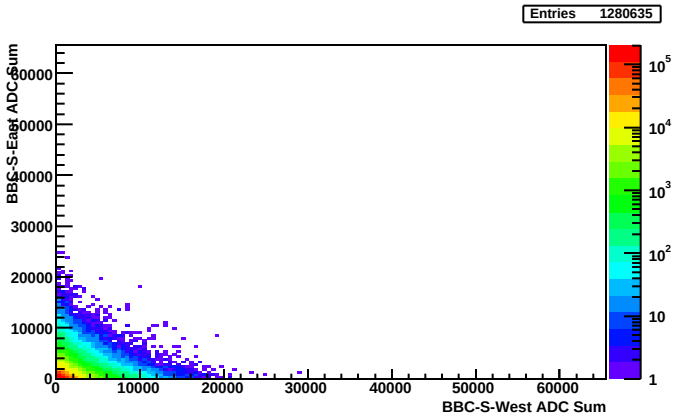

Entries 1280635
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1280635
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1280635
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 1280635
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1280635
BBC-L-East ADC Sum
BBC-L-West ADC Sum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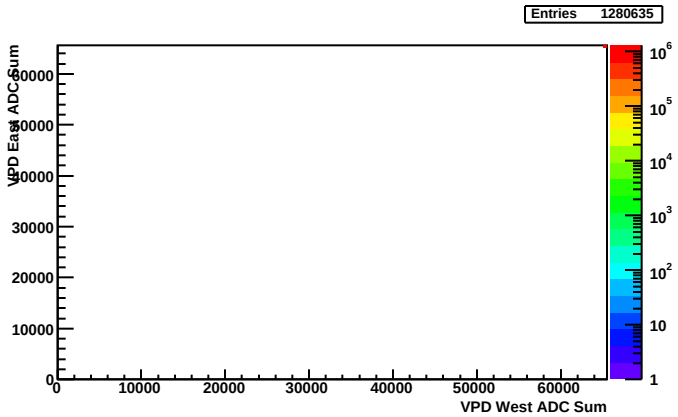
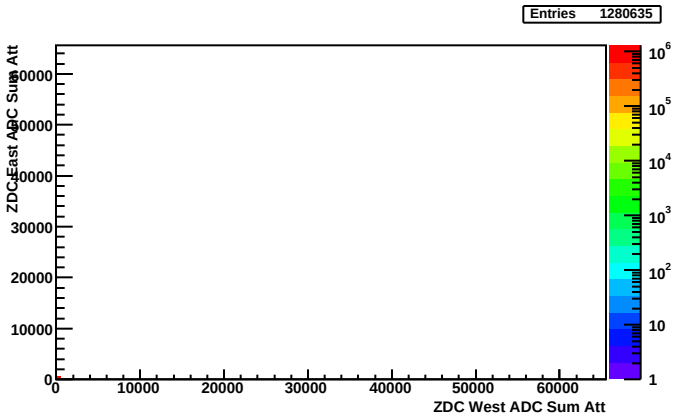
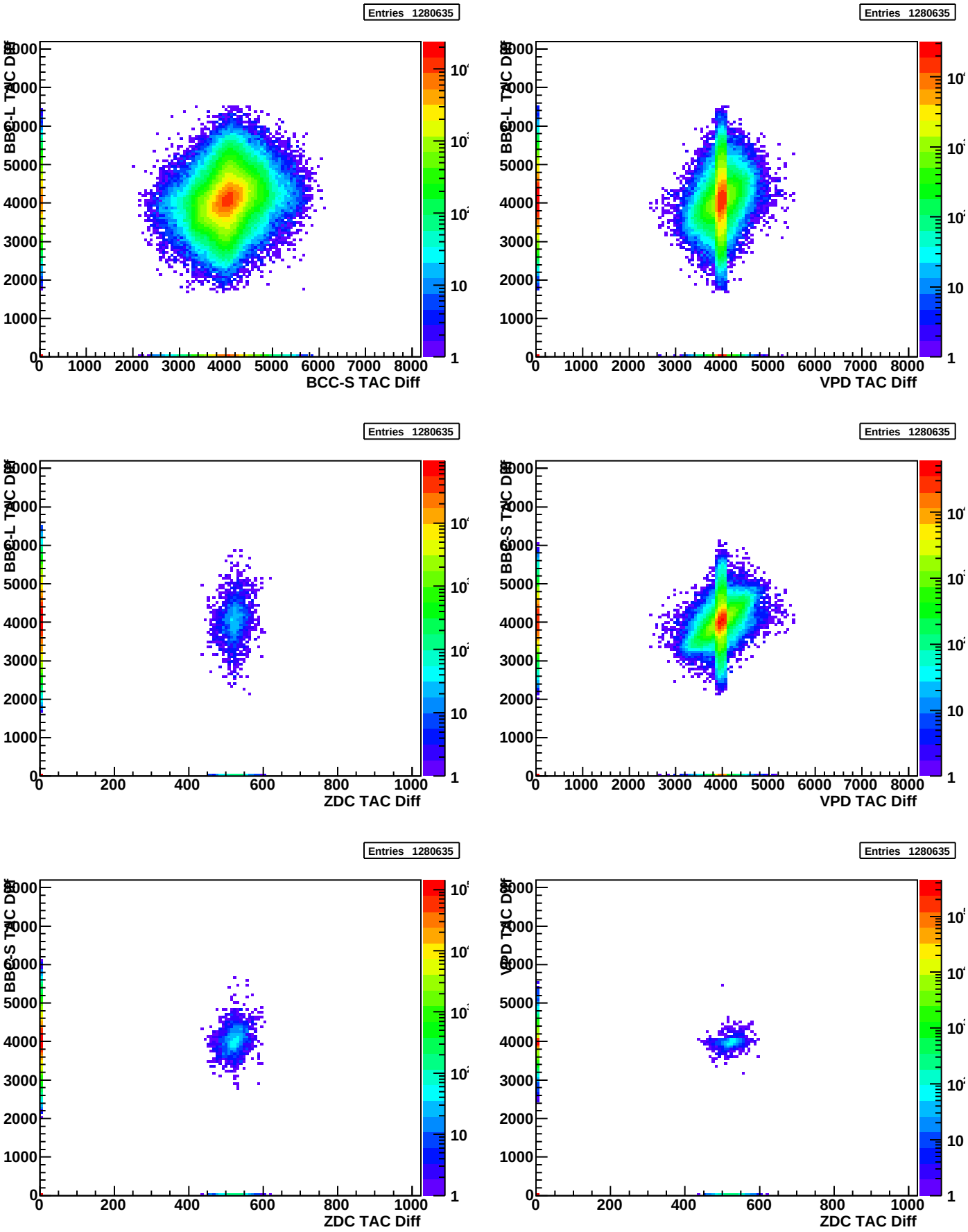
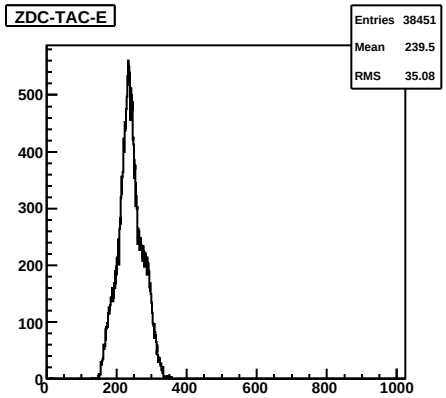
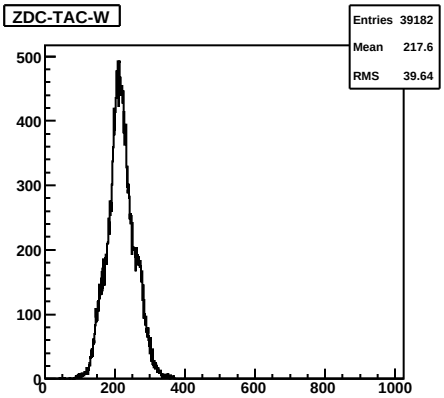
BBC-S-East ADC Sum
BBC-S-West ADC Sum

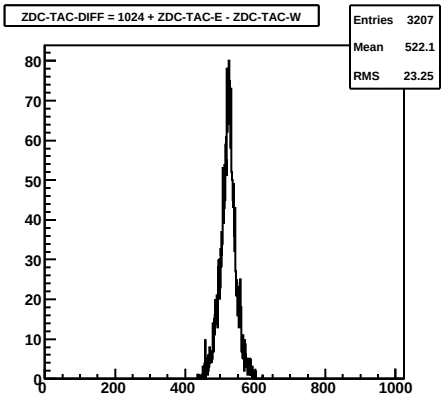

Entries 1280635
VPD East ADC Sum
VPD West ADC Sum

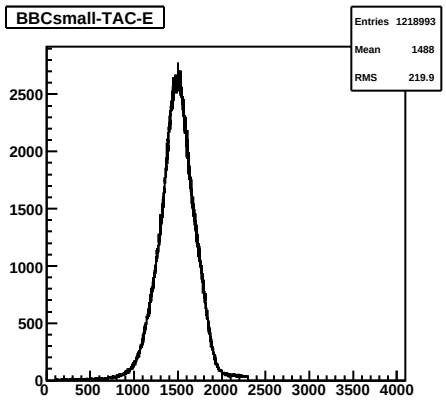

Entries 1280635
ZDC East ADC Sum Att
ZDC West ADC Sum Att

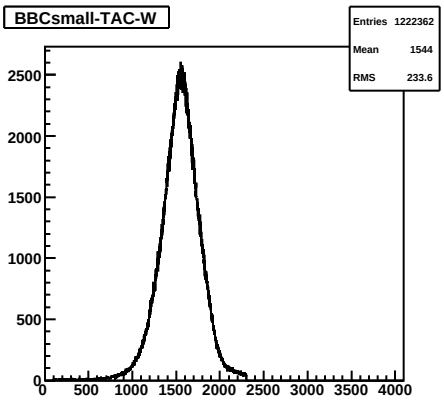

Entries 1280635
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1280635
BBC-L TAC Diff
VPD TAC Diff
Entries 1280635
BBC-L TAC Diff
ZDC TAC Diff
Entries 1280635
BBC-S TAC Diff
VPD TAC Diff
Entries 1280635
BBC-S TAC Diff
ZDC TAC Diff
Entries 1280635
VPD TAC Diff
ZDC TAC Diff

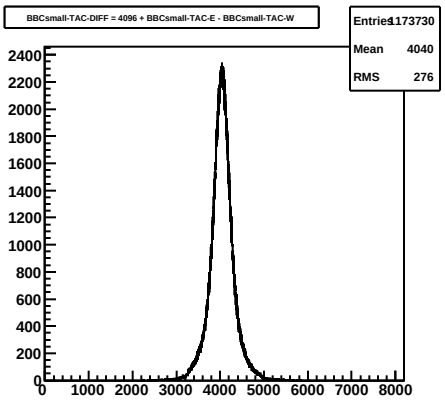

ZDC-TAC-E
Entries 38451
Mean 239.5
RMS 35.08

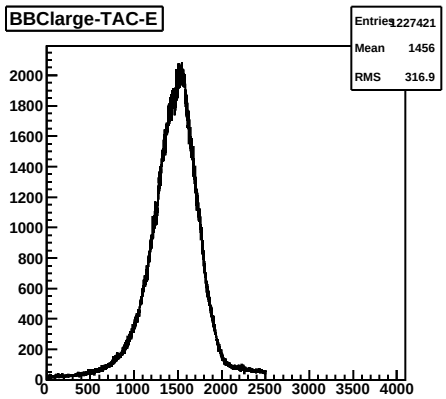

ZDC-TAC-W
Entries 39182
Mean 217.6
RMS 39.64

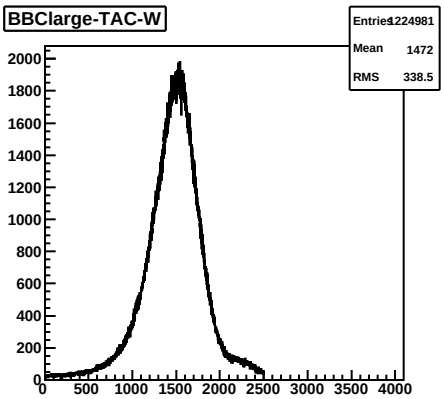

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3207
Mean 522.1
RMS 23.25

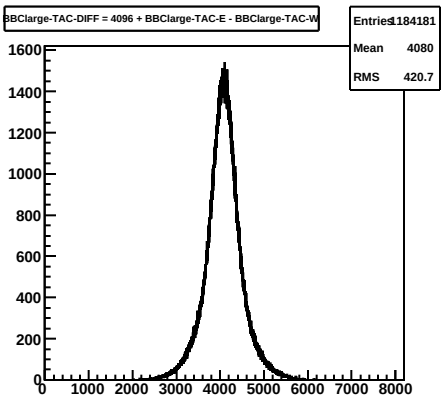

BBCsmall-TAC-E
Entries 1218993
Mean 1488
RMS 219.9

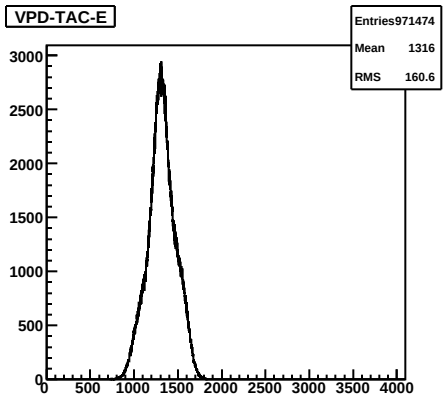

BBCsmall-TAC-W
Entries 1222362
Mean 1544
RMS 233.6

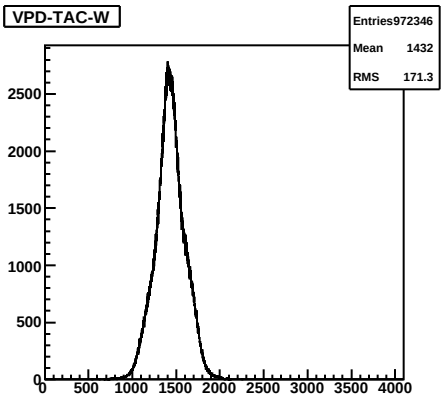

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1173730
Mean 4040
RMS 276

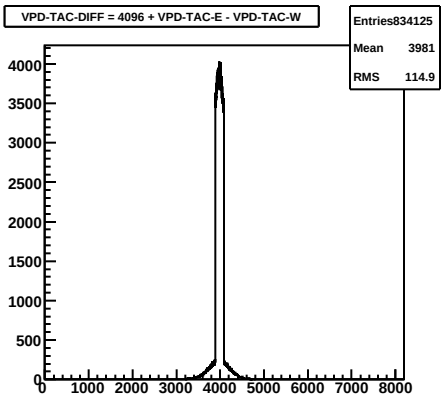

BBClarge-TAC-E
Entries 1227421
Mean 1456
RMS 316.9

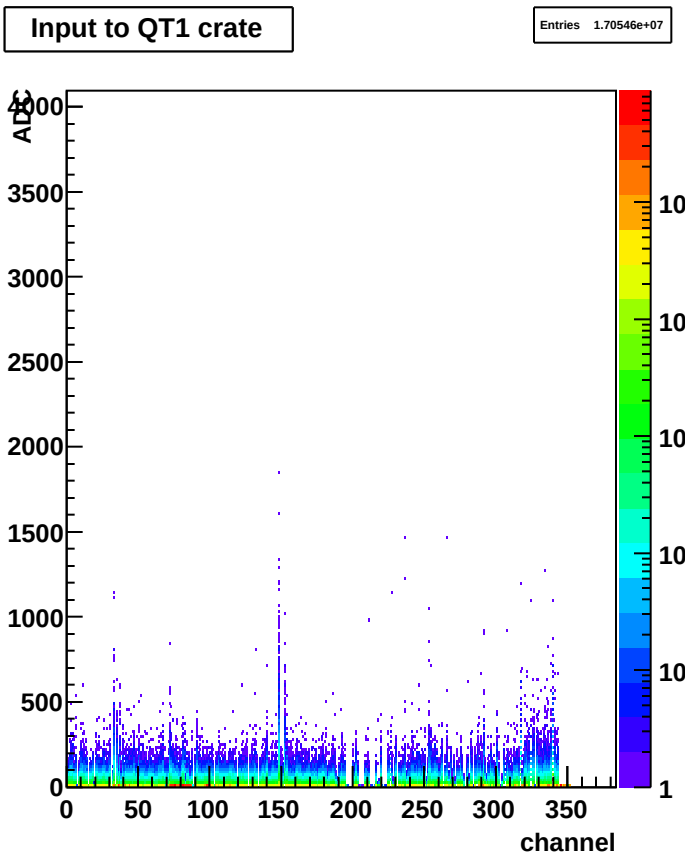

BBClarge-TAC-W
Entries 1224981
Mean 1472
RMS 338.5

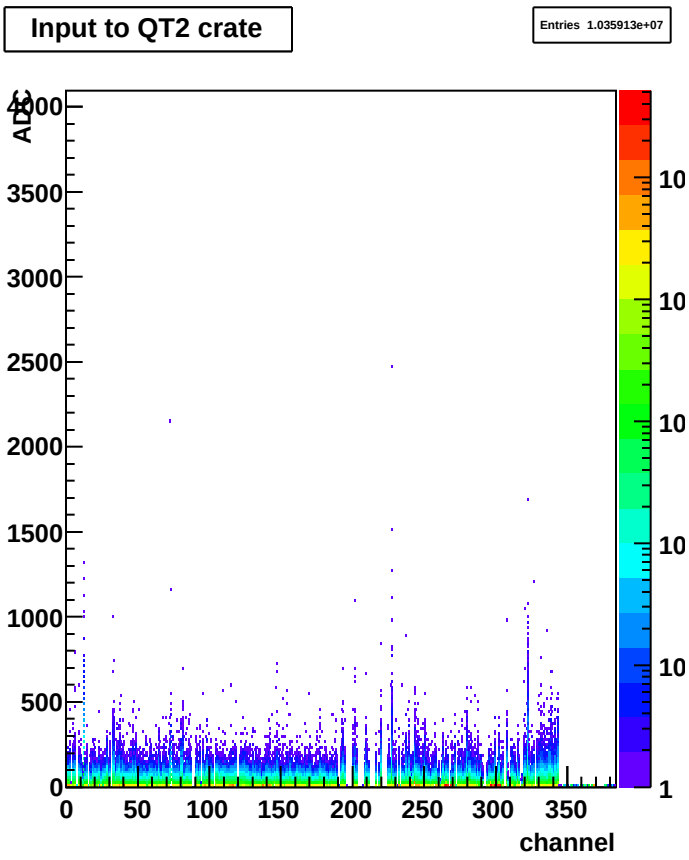

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1184181
Mean 4080
RMS 420.7

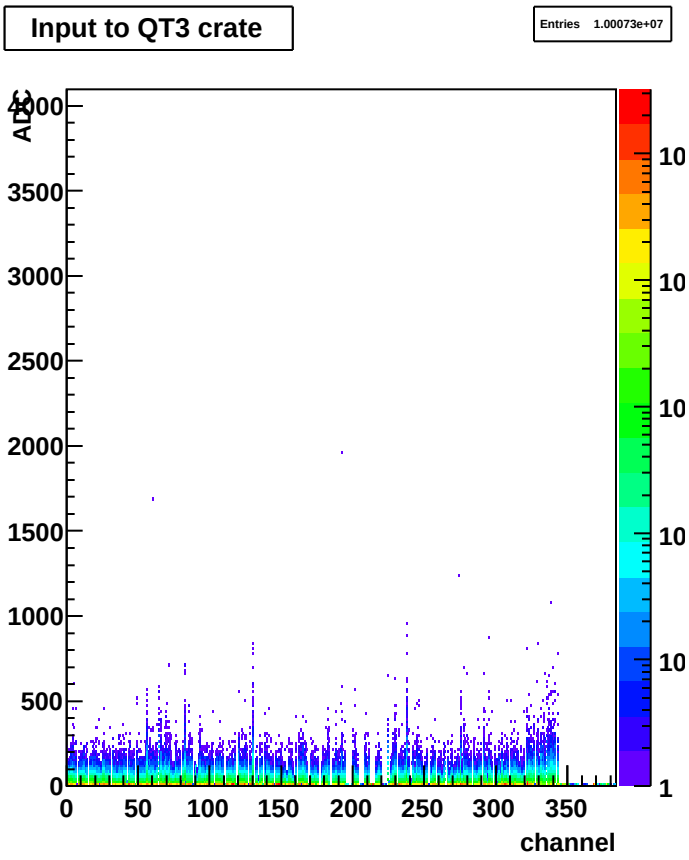

VPD-TAC-E
Entries 971474
Mean 1316
RMS 160.6

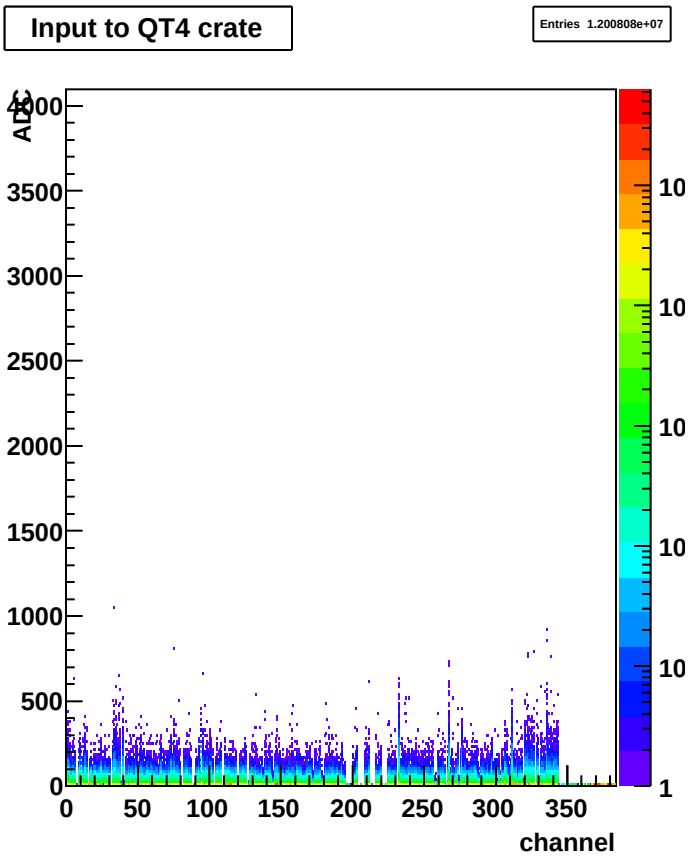

VPD-TAC-W
Entries 972346
Mean 1432
RMS 171.3

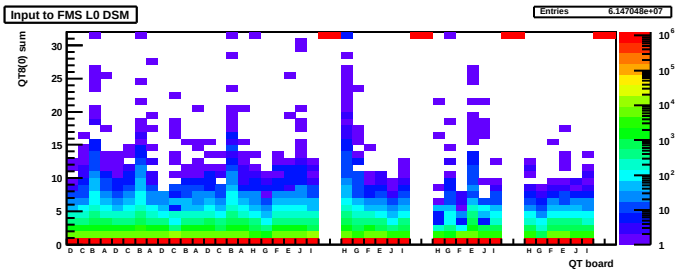

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 834125
Mean 3981
RMS 114.9

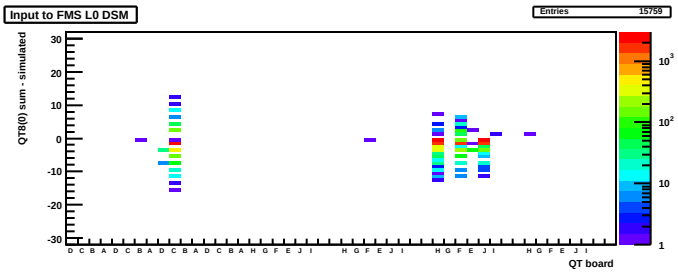

Input to QT1 crate
Entries 1.70546e+07
ADC
channel

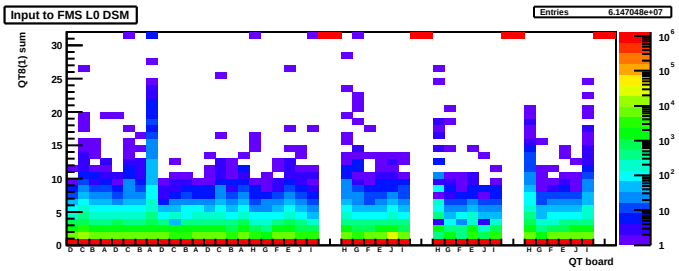

Input to QT2 crate
Entries 1.035913e+07
ADC
channel

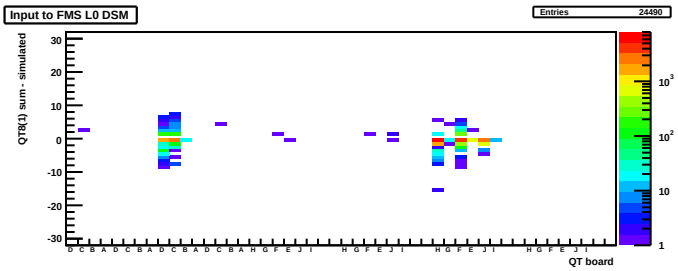

Input to QT3 crate
Entries 1.00073e+07
ADC
channel

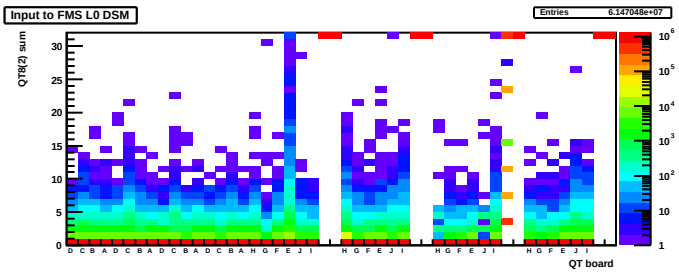

Input to QT4 crate
Entries 1.200808e+07
ADC
channel

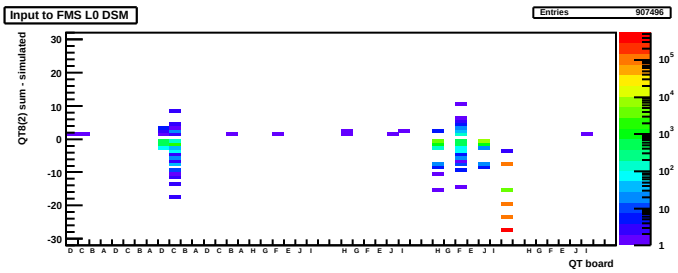

Input to FMS L0 DSM
Entries 6.147048e+07
QT8(0) sum
QT board

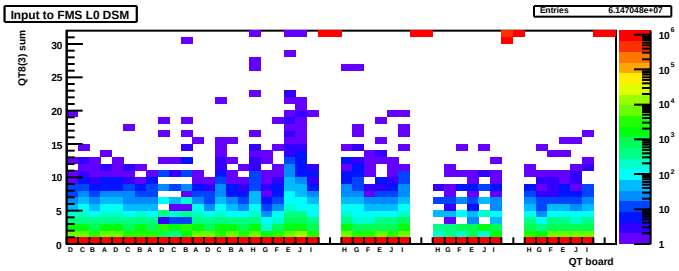

Input to FMS L0 DSM
Entries 15759
QT8(0) sum - simulated
QT board

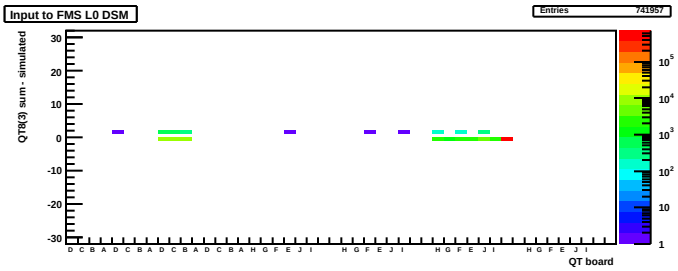

Input to FMS L0 DSM
Entries 6.147048e+07
QT8(1) sum
QT board

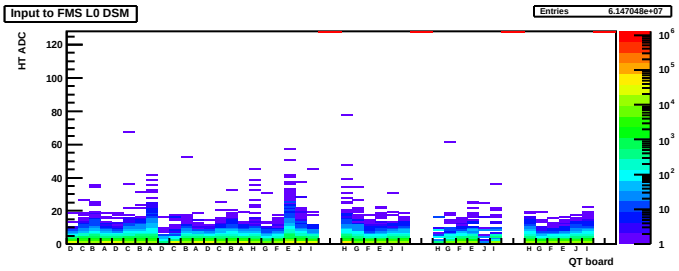

Input to FMS L0 DSM
Entries 24490
QT8(1) sum - simulated
QT board

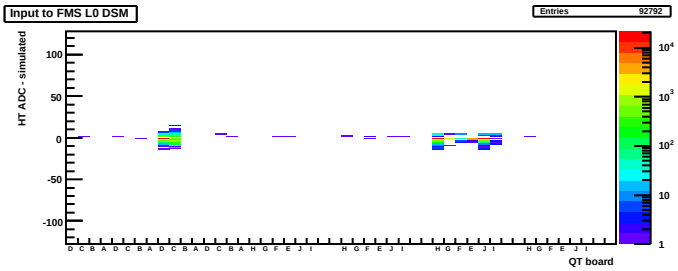

Input to FMS L0 DSM
Entries 6.147048e+07
QT8(2) sum
QT board

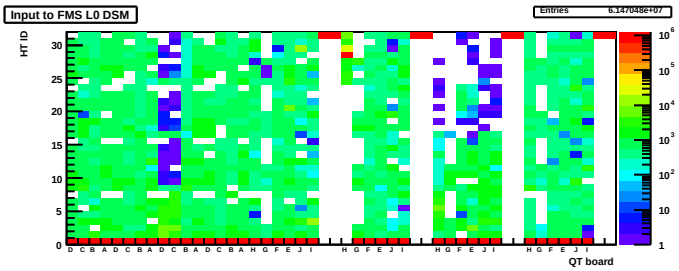

Input to FMS L0 DSM
Entries 907496
QT8(2) sum - simulated
QT board

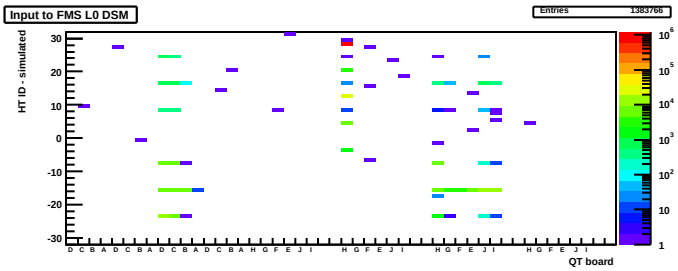

Input to FMS L0 DSM
Entries 6.147048e+07
QT8(3) sum
QT board

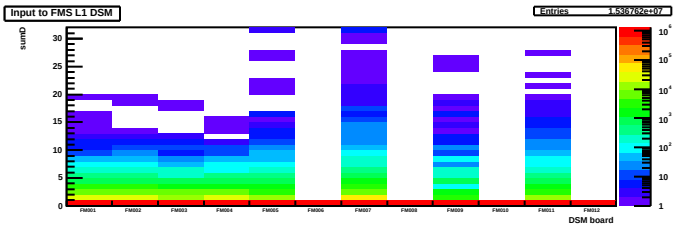

Input to FMS L0 DSM
Entries 741957
QT8(3) sum - simulated
QT board

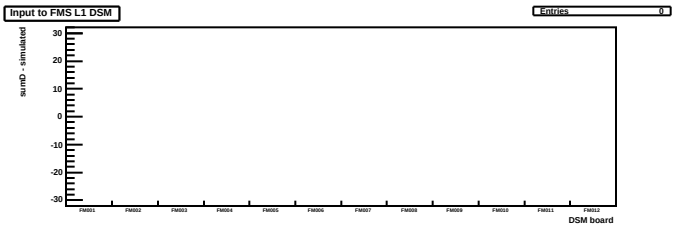

Input to FMS L0 DSM
Entries 6.147048e+07
HT ADC
QT board

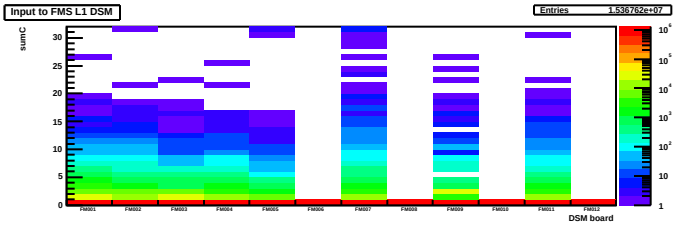

Input to FMS L0 DSM
Entries 92792
HT ADC - simulated
QT board

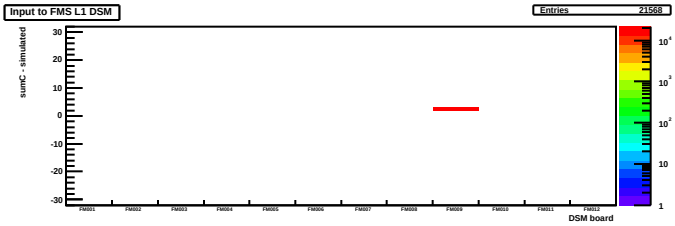

Input to FMS L0 DSM
Entries 6.147048e+07
HT ID
QT board

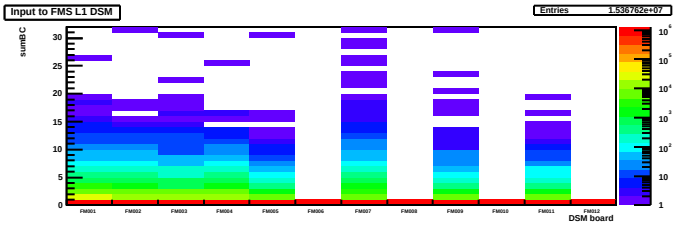

Input to FMS L0 DSM
Entries 1383766
HT ID - simulated
QT board

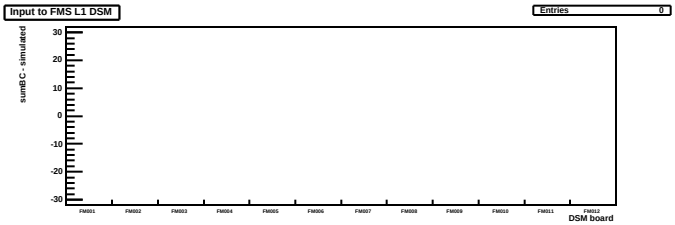

Input to FMS L1 DSM
Entries 1.536762e+07
sumD
DSM board

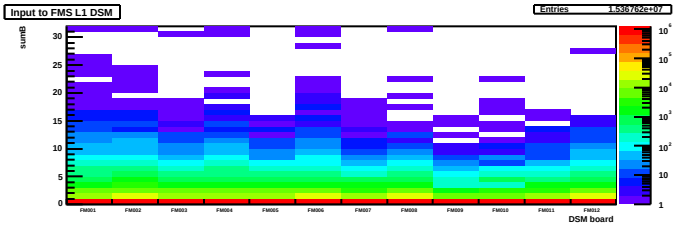

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

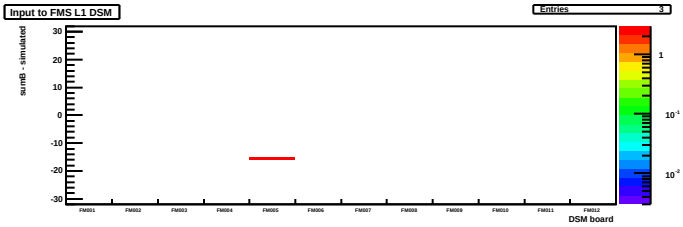

Input to FMS L1 DSM
Entries 1.536762e+07
sumC
DSM board

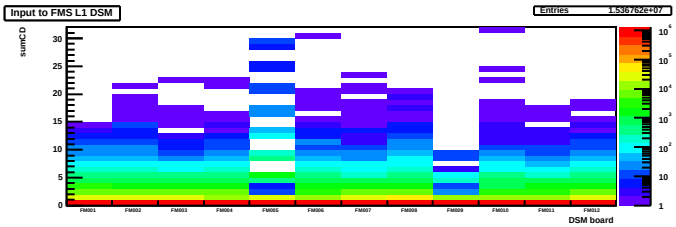

Input to FMS L1 DSM
Entries 21568
sumC - simulated
DSM board

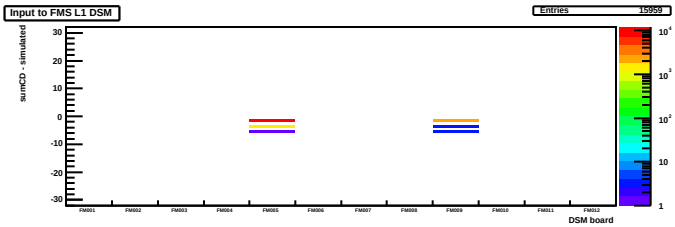

Input to FMS L1 DSM
Entries 1.536762e+07
sumBC
DSM board

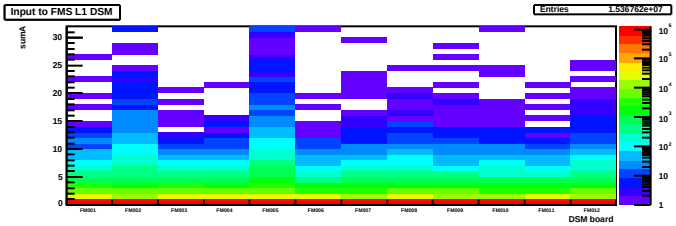

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

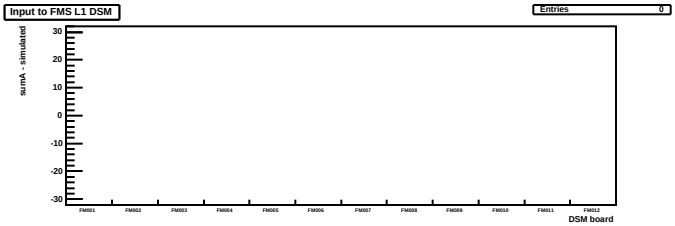

Input to FMS L1 DSM
Entries 1.536762e+07
sumB
DSM board

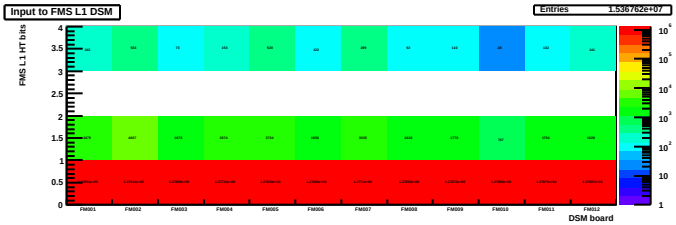

Input to FMS L1 DSM
Entries 3
sumB - simulated
DSM board

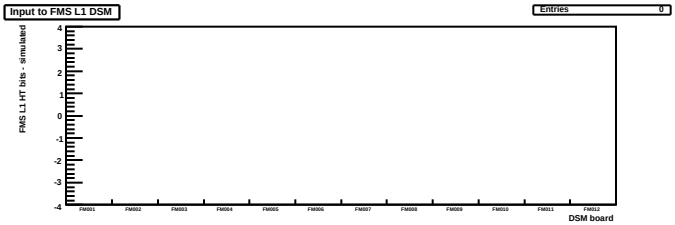

Input to FMS L1 DSM
Entries 1.536762e+07
sumCD
DSM board

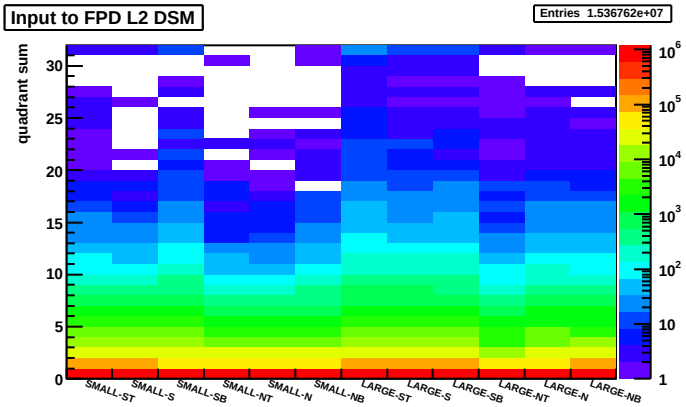

Input to FMS L1 DSM
Entries 15959
sumCD - simulated
DSM board

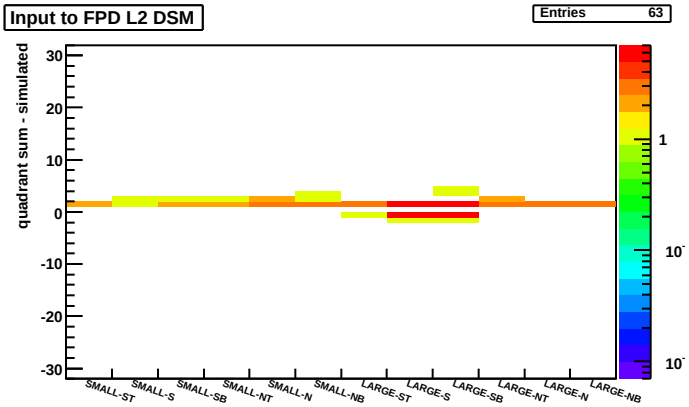

Input to FMS L1 DSM
Entries 1.536762e+07
sumA
DSM board

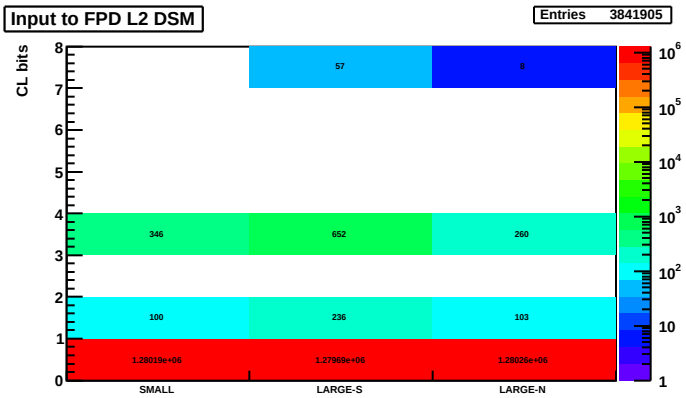

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

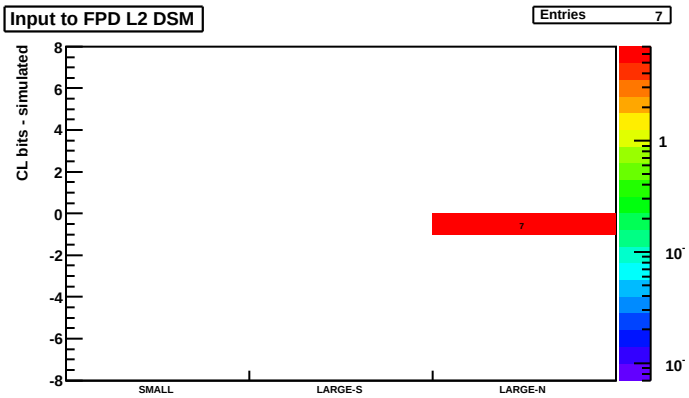

Input to FMS L1 DSM
Entries 1.536762e+07
FMS L1 HT bits
DSM board

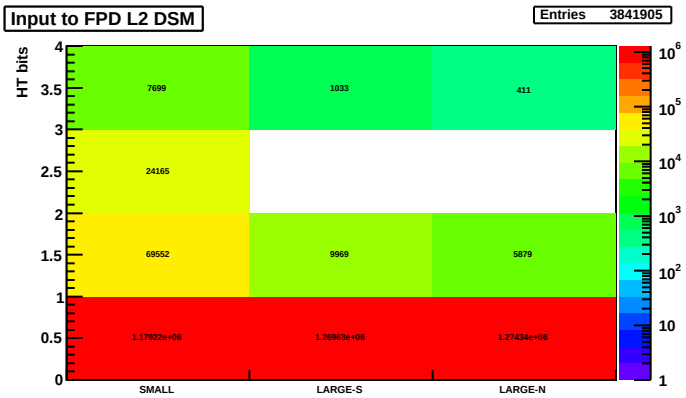

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

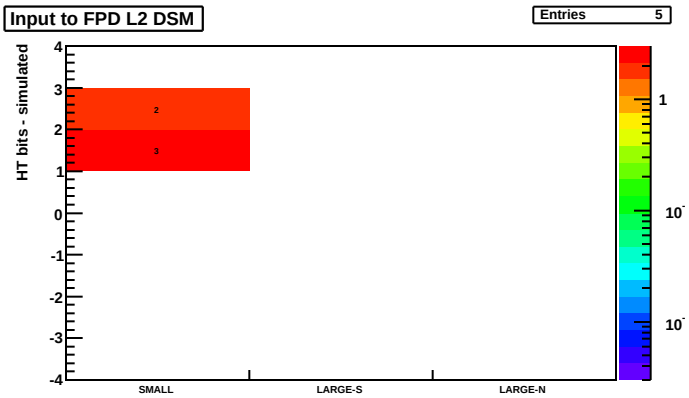

Input to FPD L2 DSM
Entries 1.536762e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

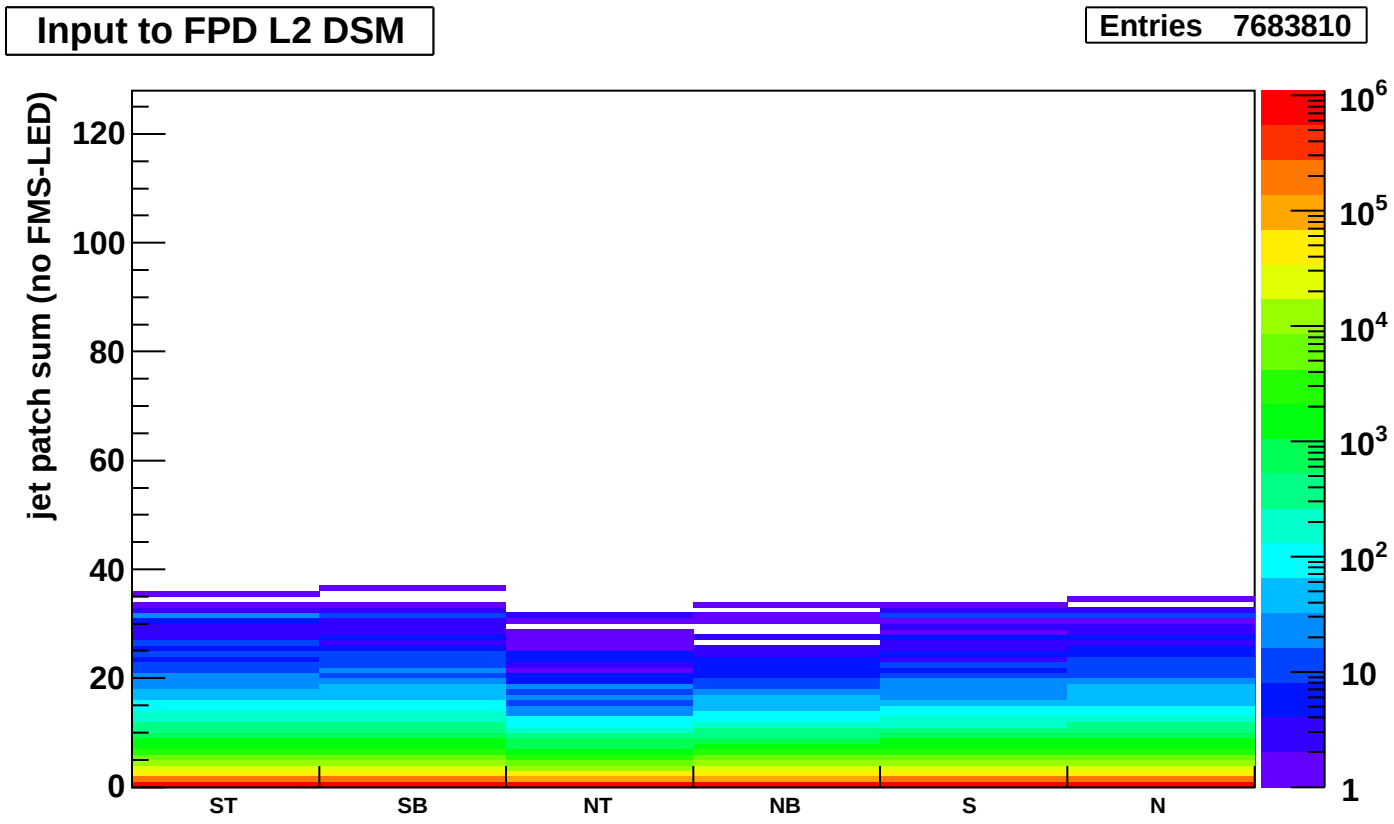

Input to FPD L2 DSM
Entries 63
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

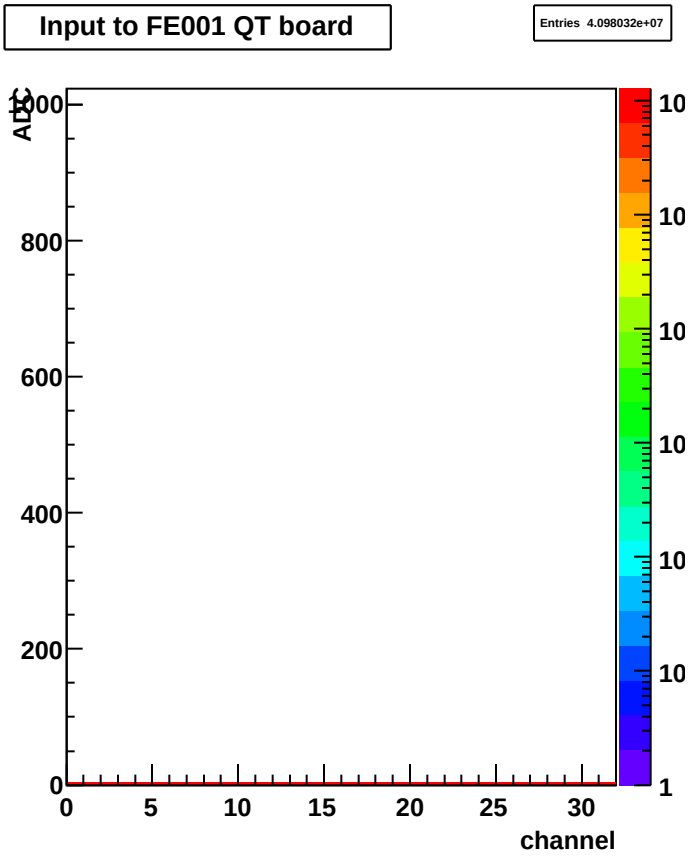

Input to FPD L2 DSM
Entries 3841905
CL bits
57
8
346
652
260
100
236
103
1.28019e+06
1.27969e+06
1.28026e+06
SMALL
LARGE-S
LARGE-N

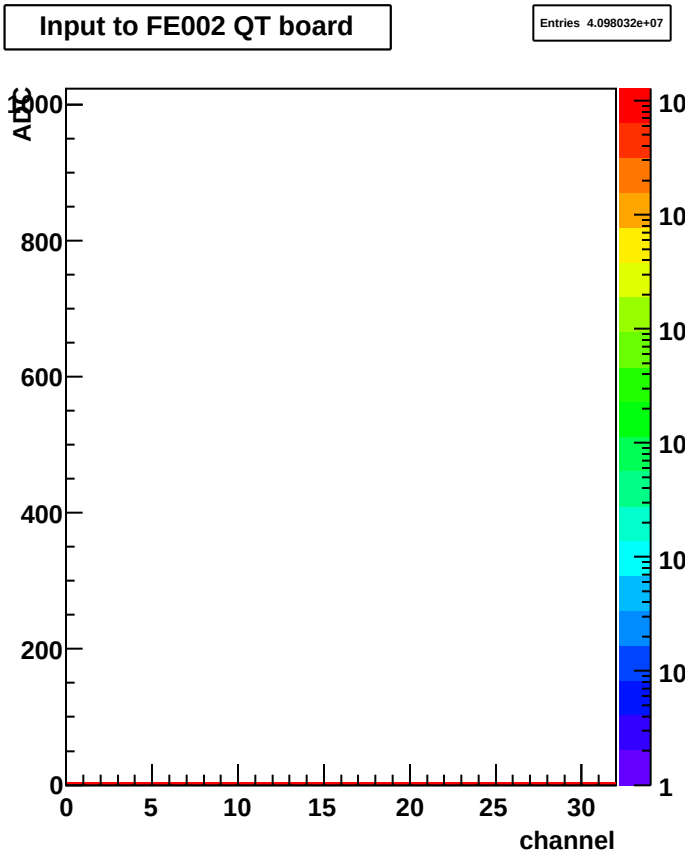

Input to FPD L2 DSM
Entries 7
CL bits - simulated
7
SMALL
LARGE-S
LARGE-N

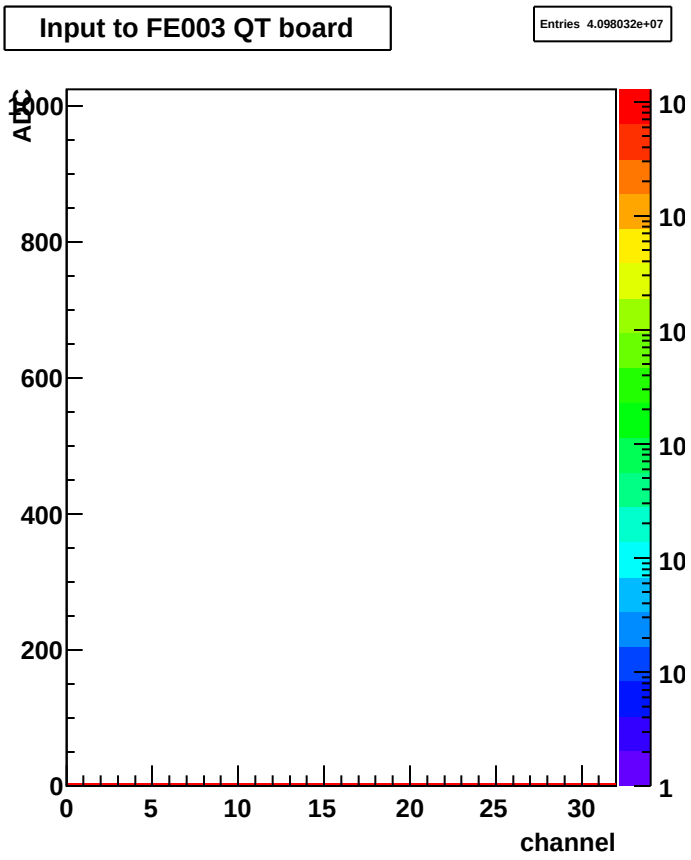

Input to FPD L2 DSM
Entries 3841905
HT bits
7699
1033
411
24165
69552
9969
5879
1.17922e+06
1.26963e+06
1.27434e+06
SMALL
LARGE-S
LARGE-N

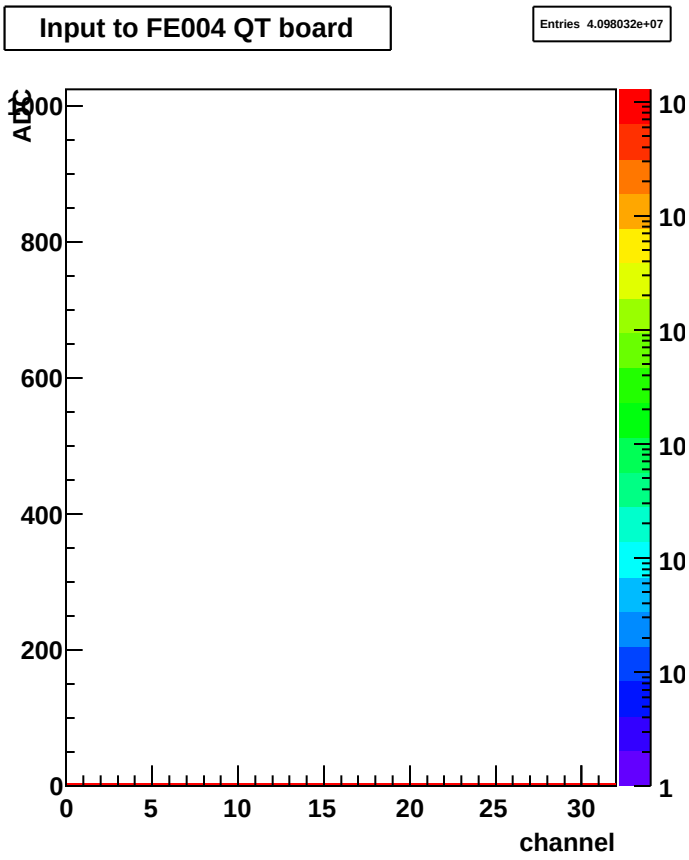

Input to FPD L2 DSM
Entries 5
HT bits - simulated
2
3
SMALL
LARGE-S
LARGE-N

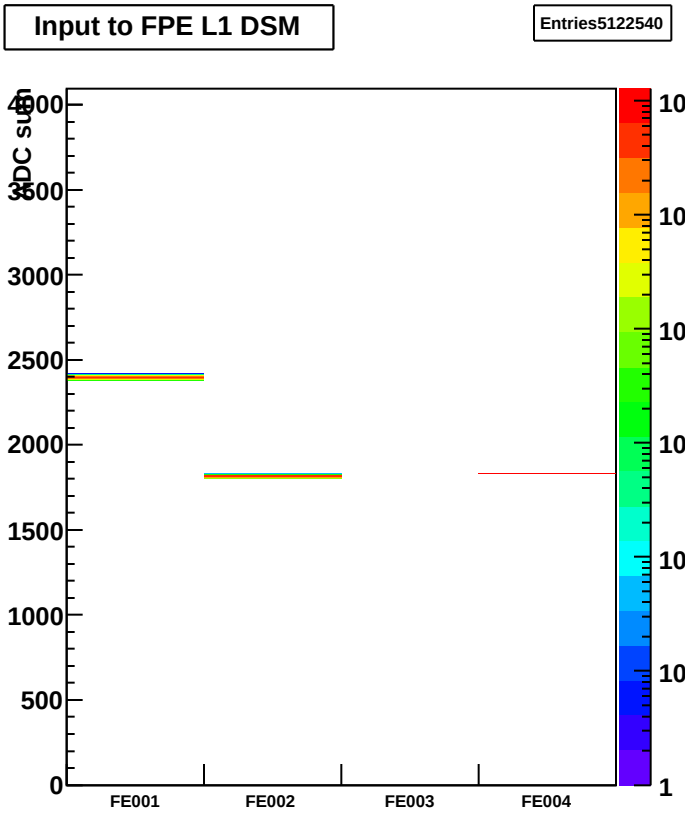

Input to FPD L2 DSM
Entries 7683810
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 4.098032e+07
ADC
channel

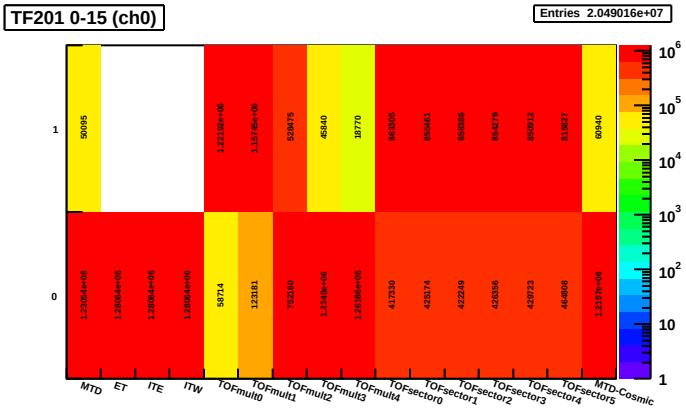

Input to FE002 QT board
Entries 4.098032e+07
ADC
channel


Input to FE003 QT board
Entries 4.098032e+07
ADC
channel

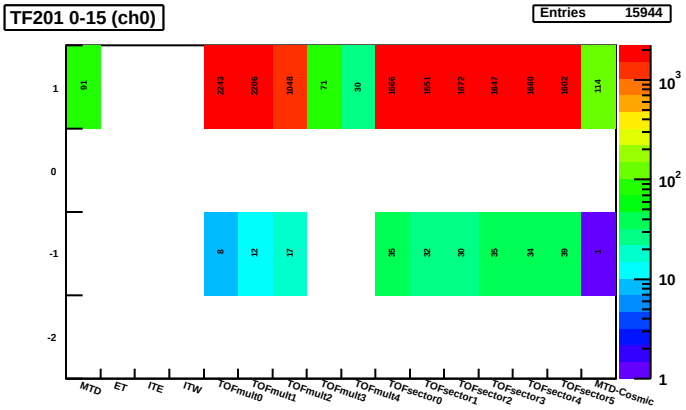

Input to FE004 QT board
Entries 4.098032e+07
ADC
channel


Input to FPE L1 DSM
Entries5122540
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

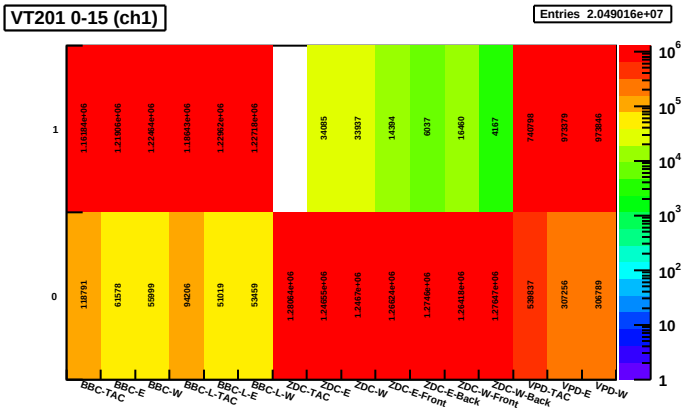
TF201 0-15 (ch0)



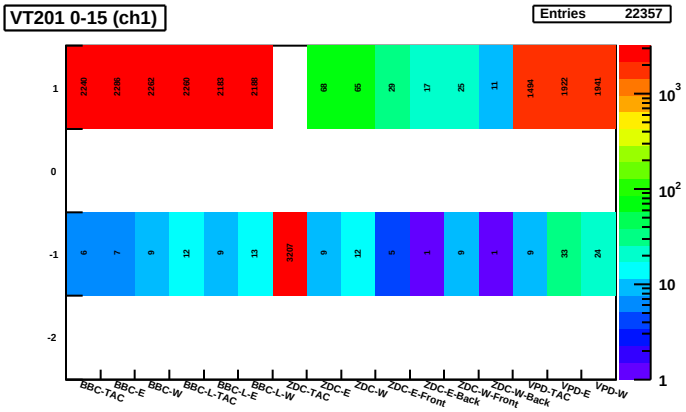
TF201 0-15 (ch0)



VT201 0-15 (ch1)



VT201 0-15 (ch1)



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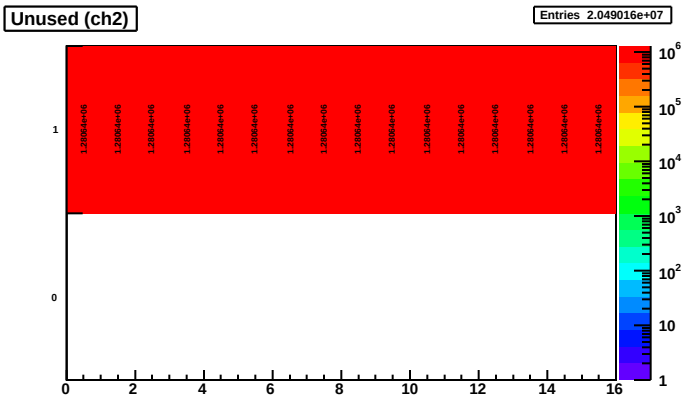
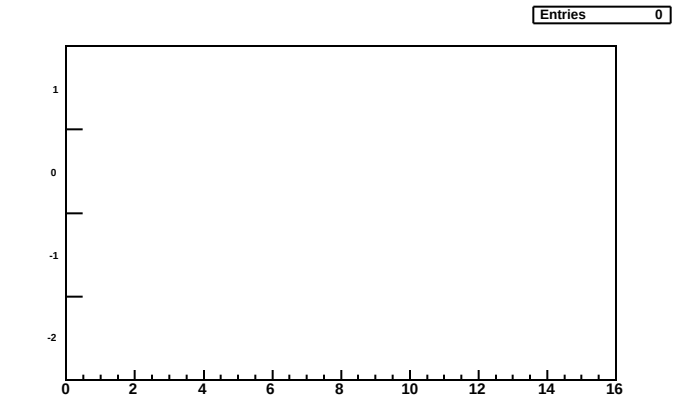
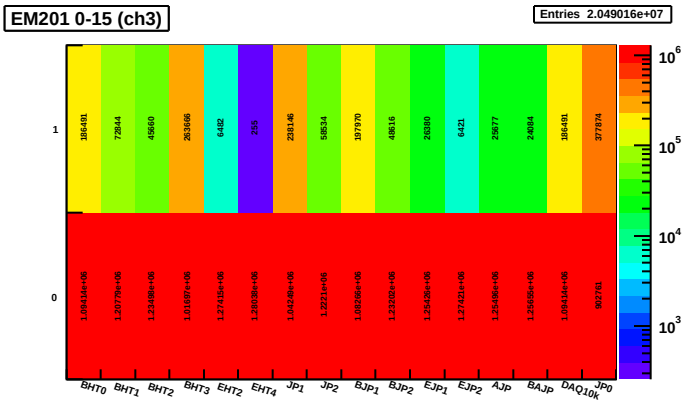




EM201 0-15 (ch3)



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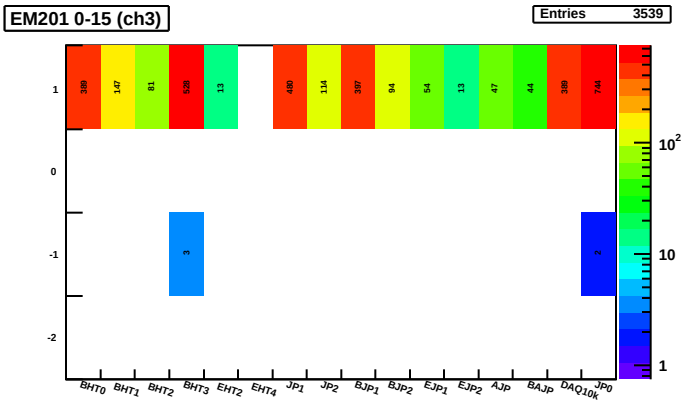
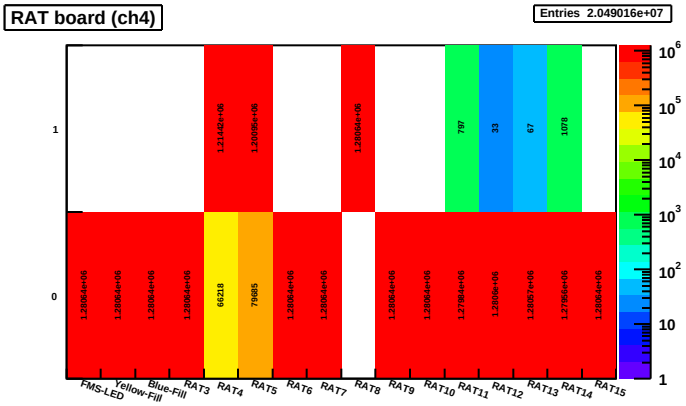
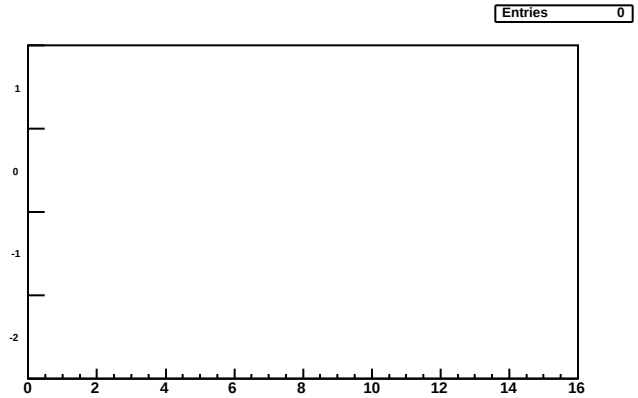
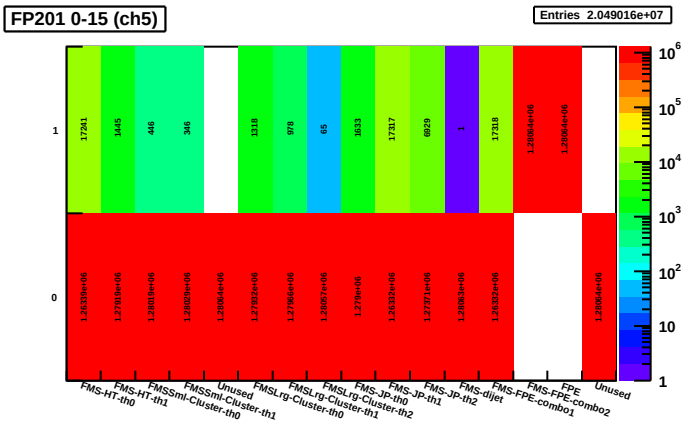


RAT board (ch4)

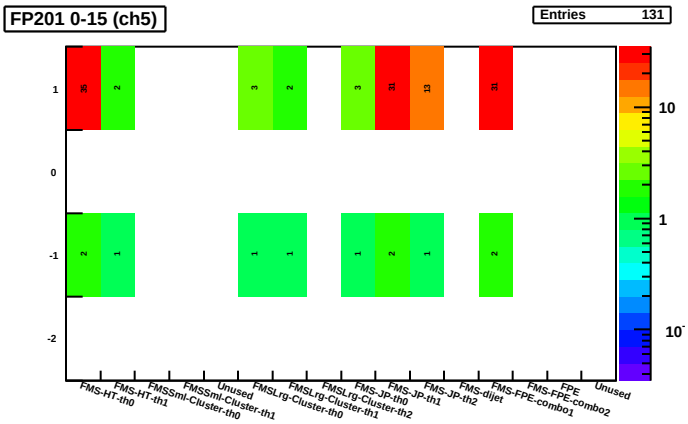




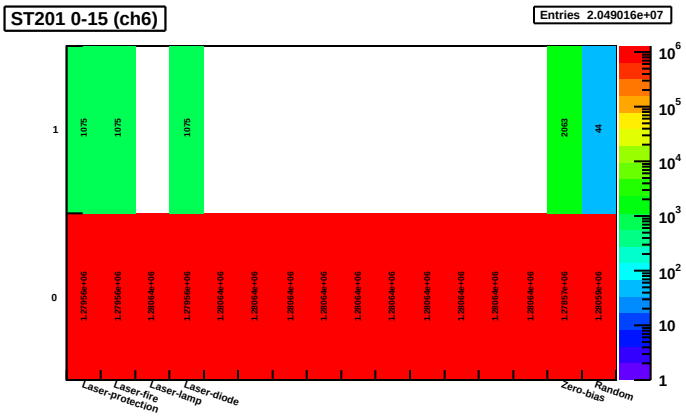
FP201 0-15 (ch5)

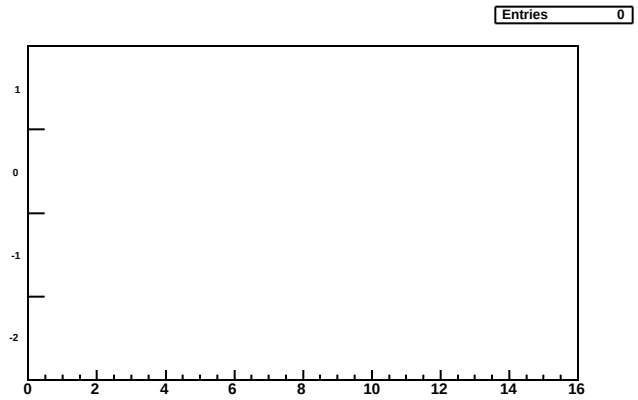
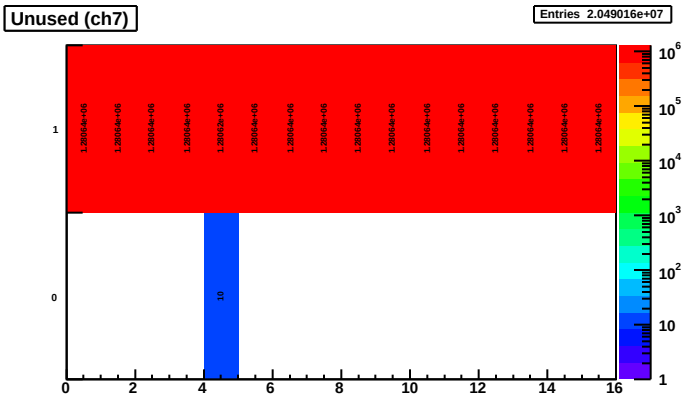
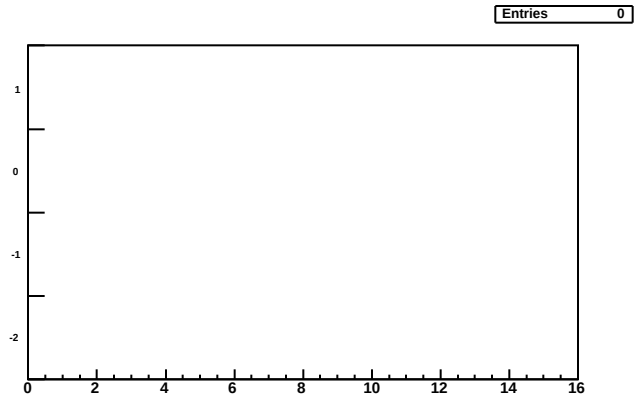


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)