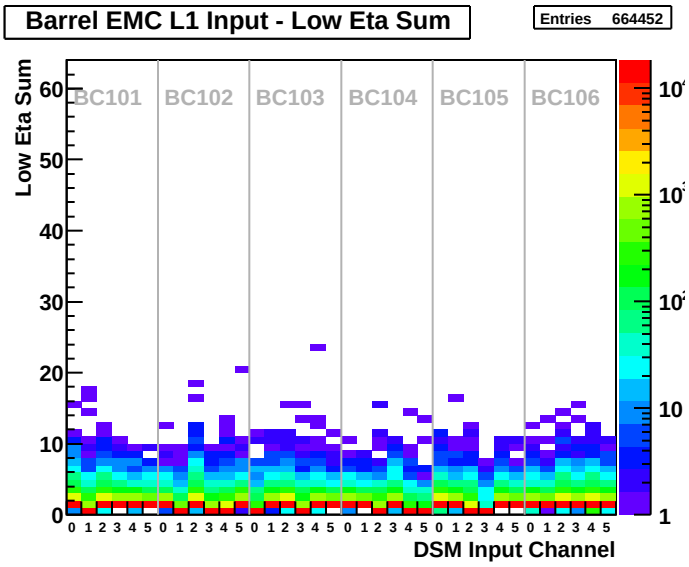
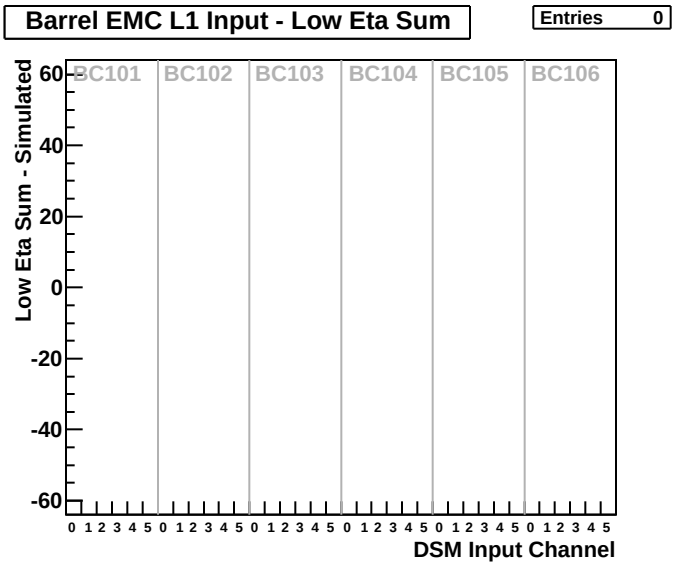
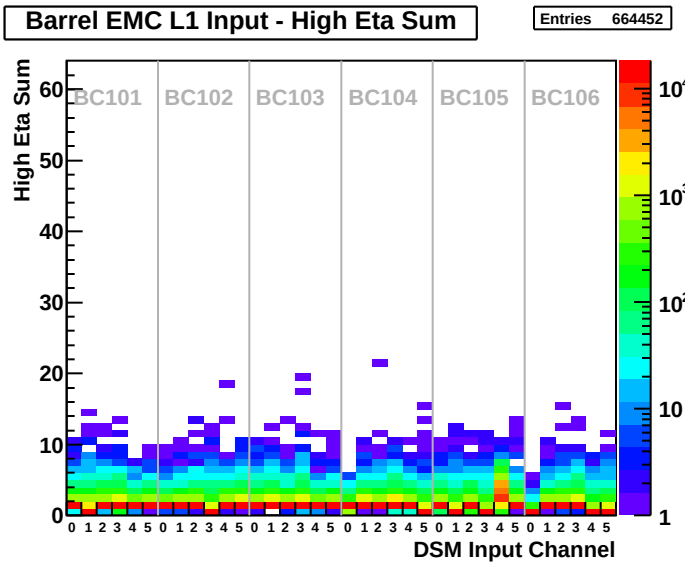
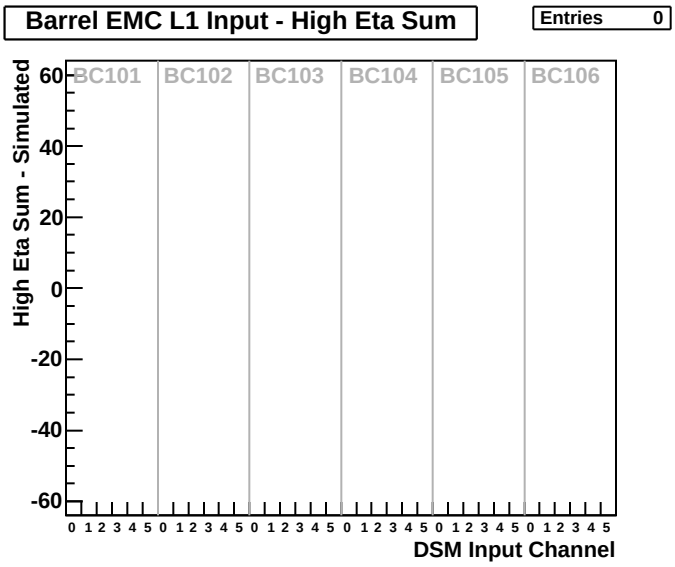
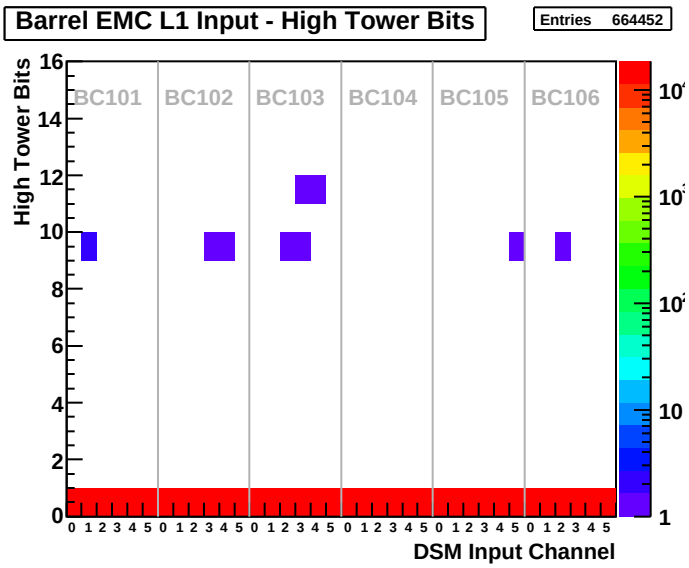
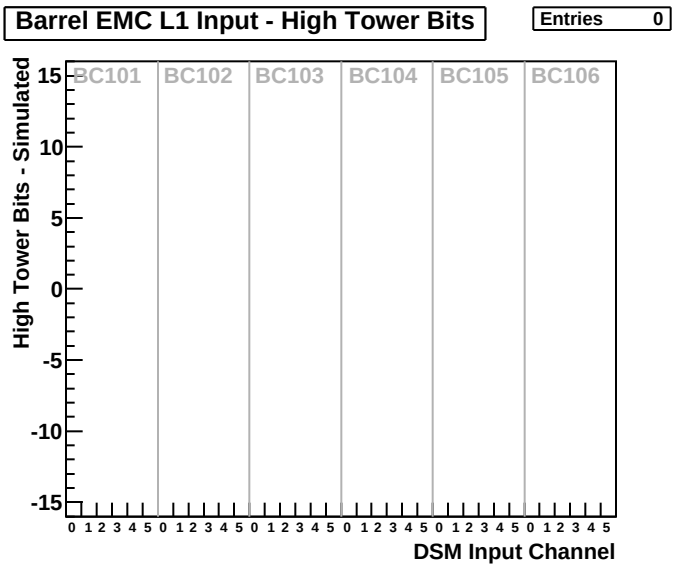

Barrel EMC L1 Input - Low Eta Sum
Entries 664452
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

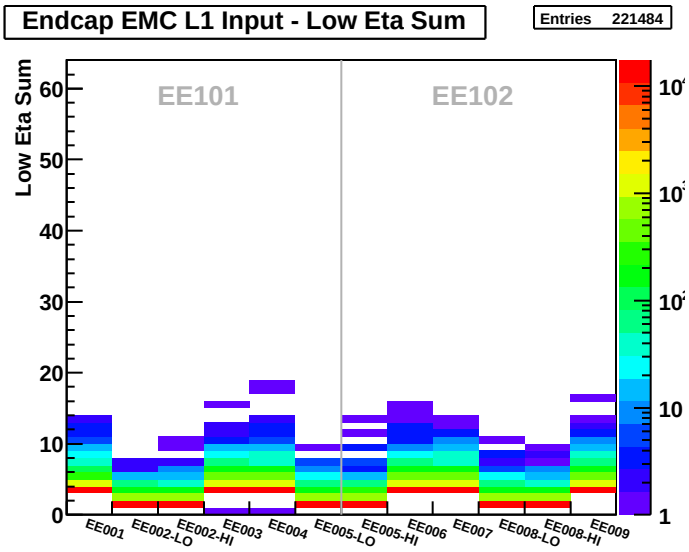

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

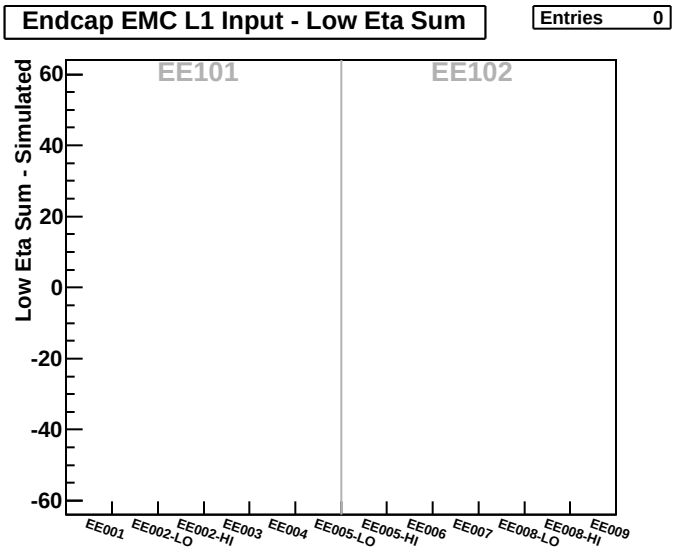

Barrel EMC L1 Input - High Eta Sum
Entries 664452
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

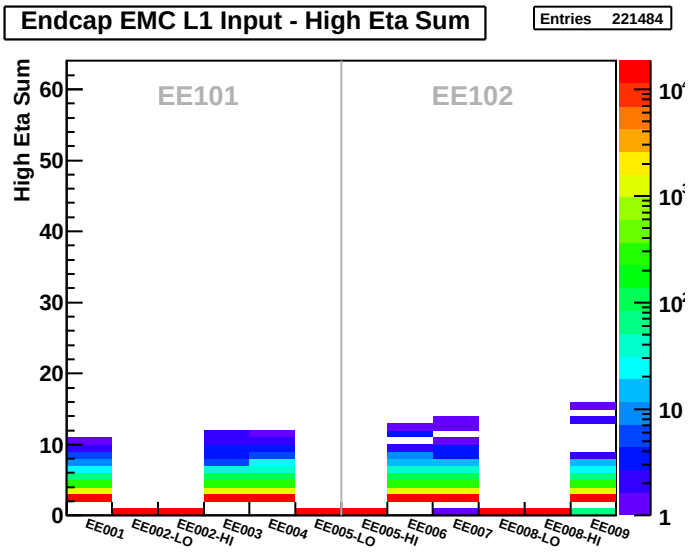

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

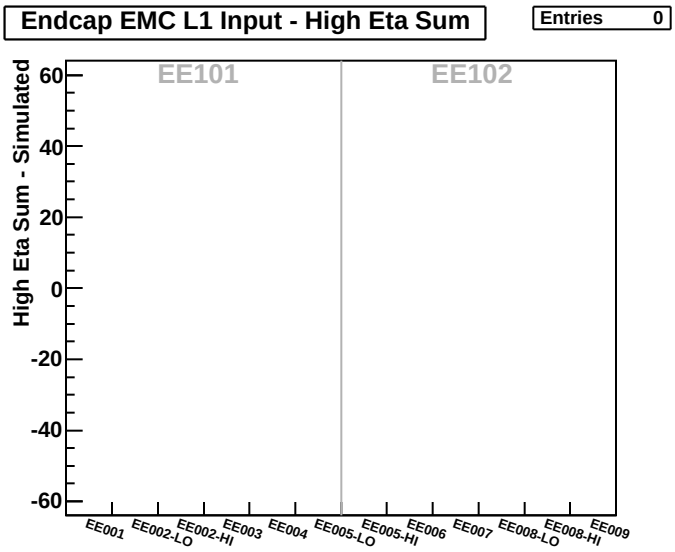

Barrel EMC L1 Input - High Tower Bits
Entries 664452
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

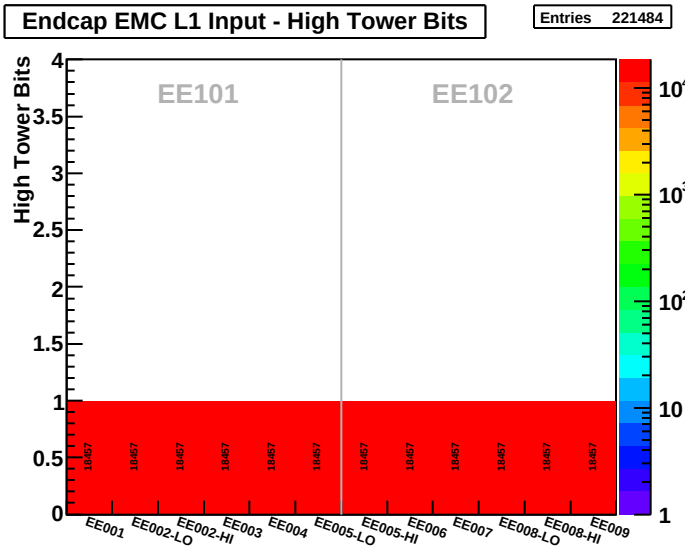

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

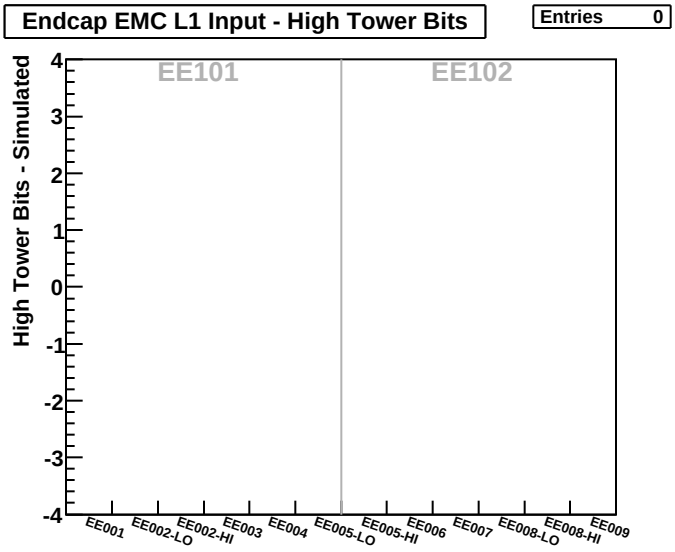

Endcap EMC L1 Input - Low Eta Sum
Entries 221484
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

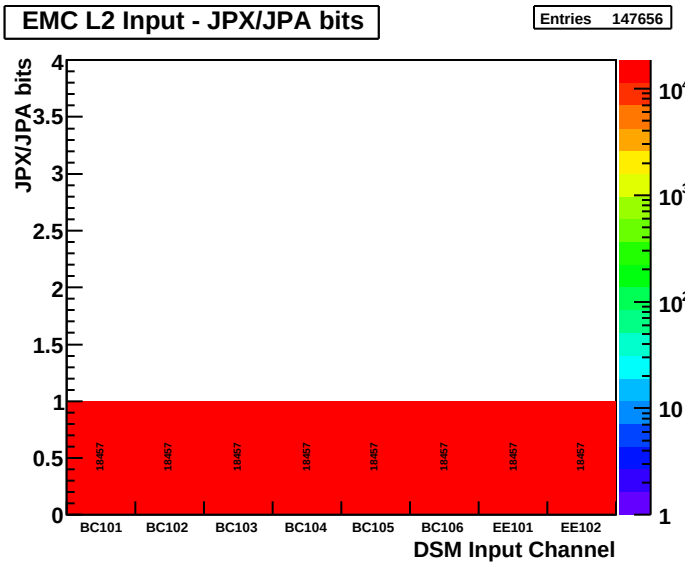

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

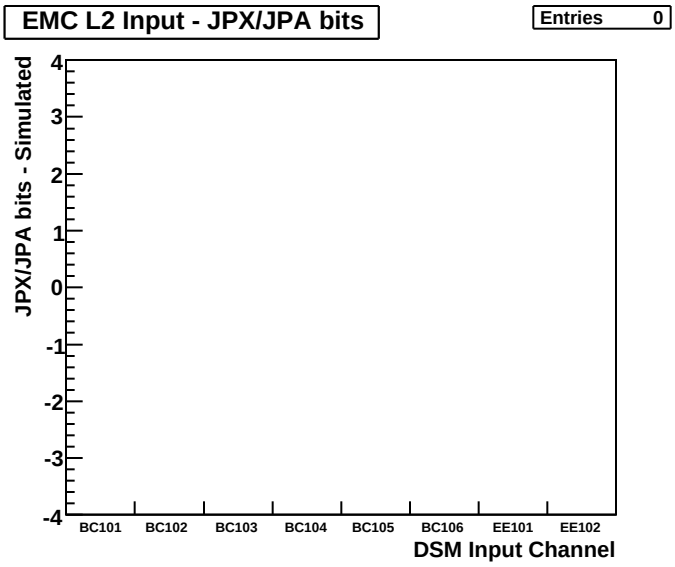

Endcap EMC L1 Input - High Eta Sum
Entries 221484
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

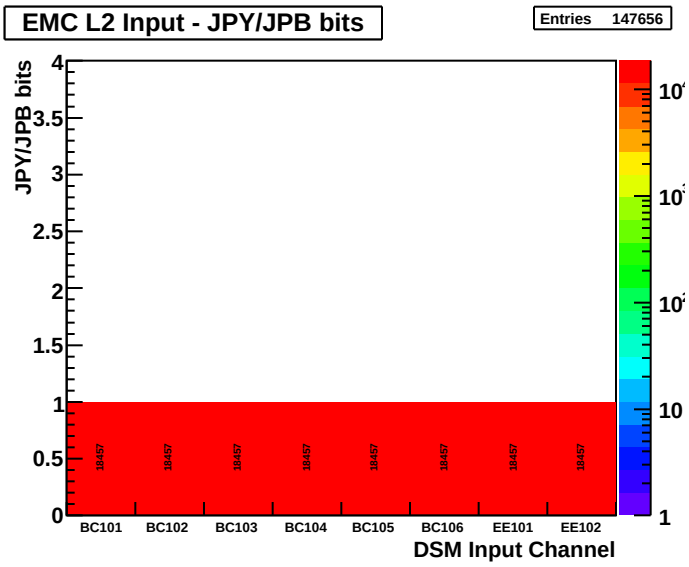

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

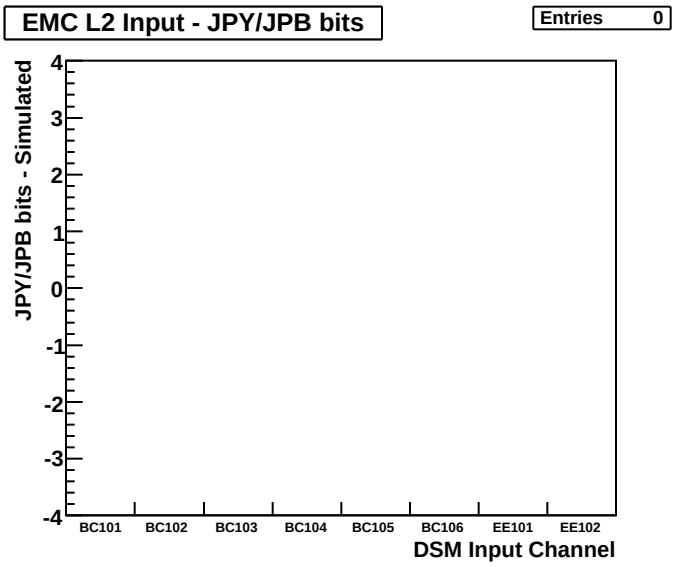

Endcap EMC L1 Input - High Tower Bits
Entries 221484
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

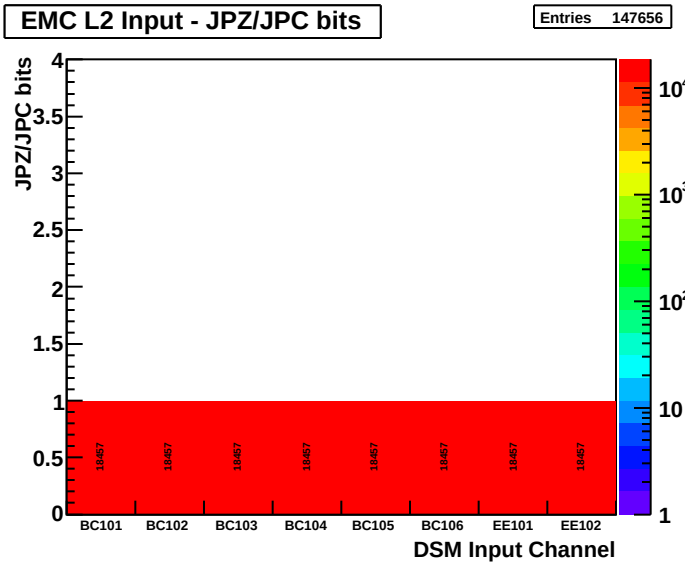

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

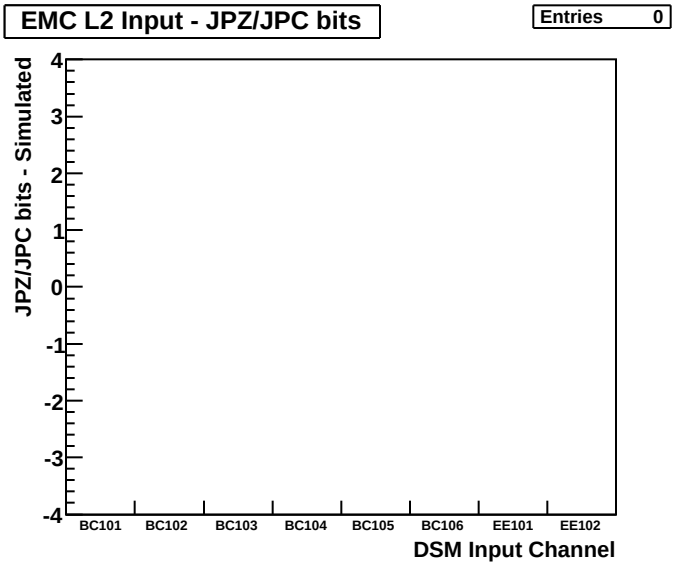

EMC L2 Input - JPX/JPA bits
Entries 147656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
18457

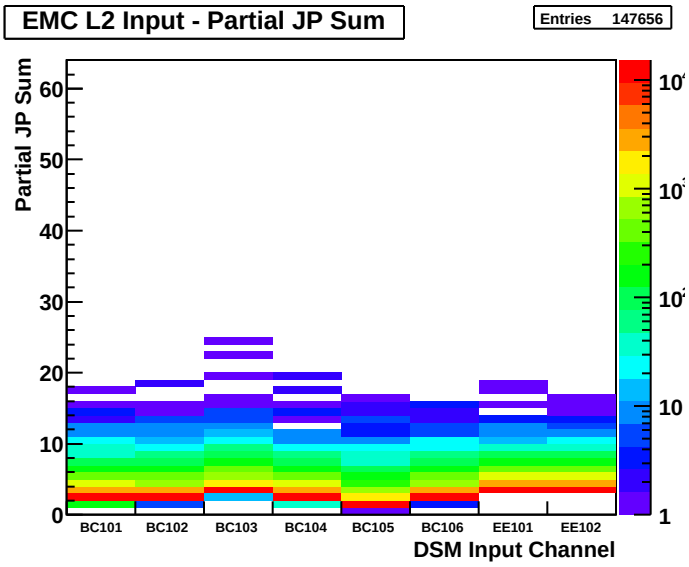

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

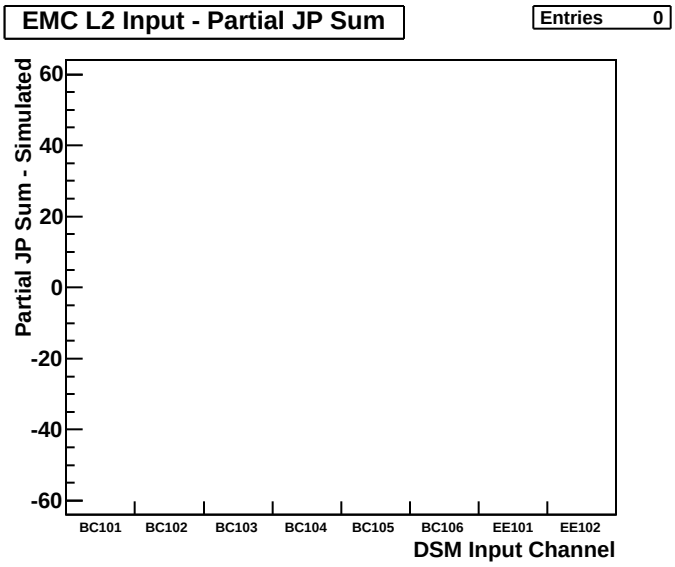

EMC L2 Input - JPY/JPB bits
Entries 147656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
18457

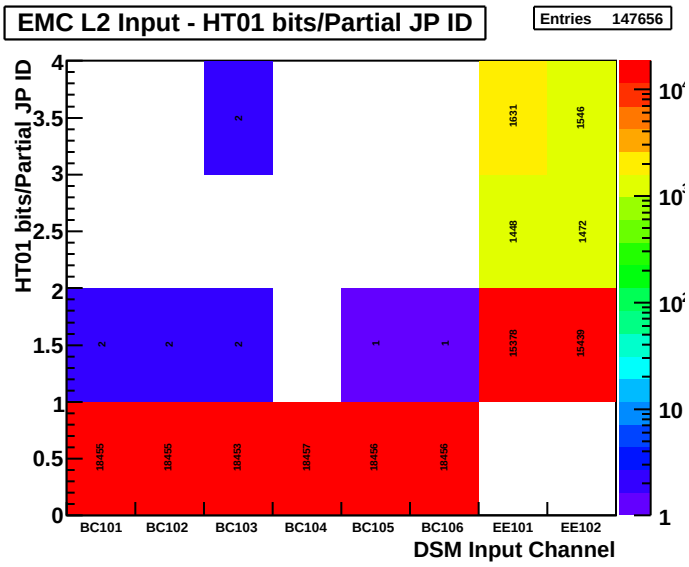

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

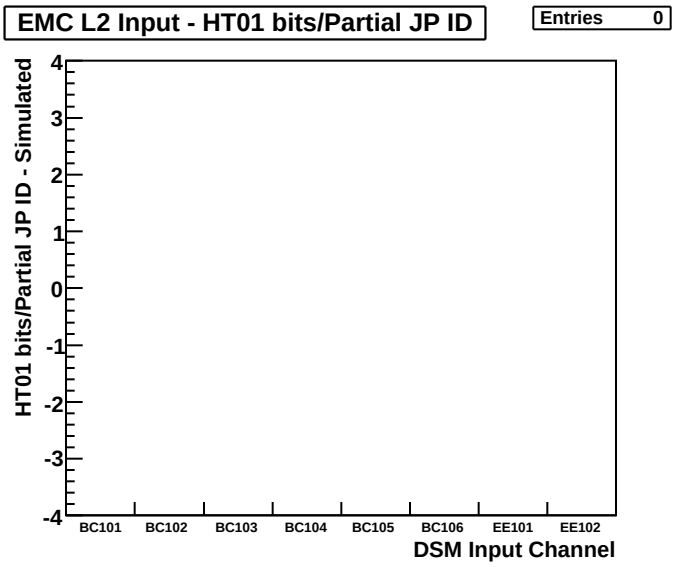

EMC L2 Input - JPZ/JPC bits
Entries 147656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
18457

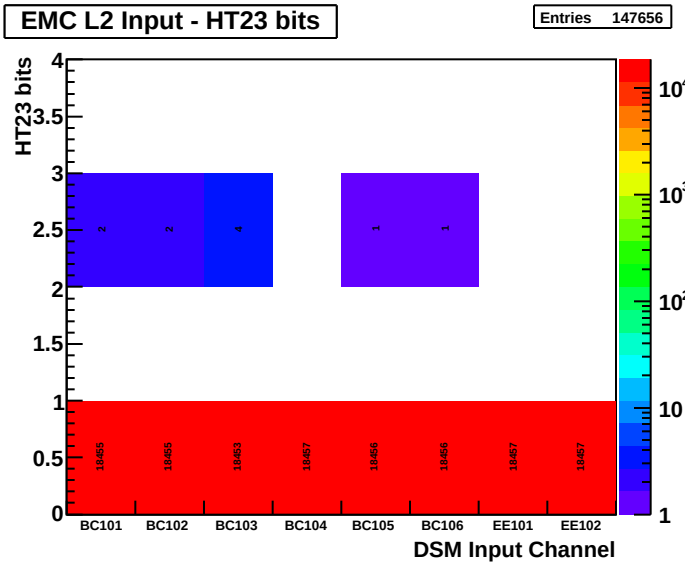

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

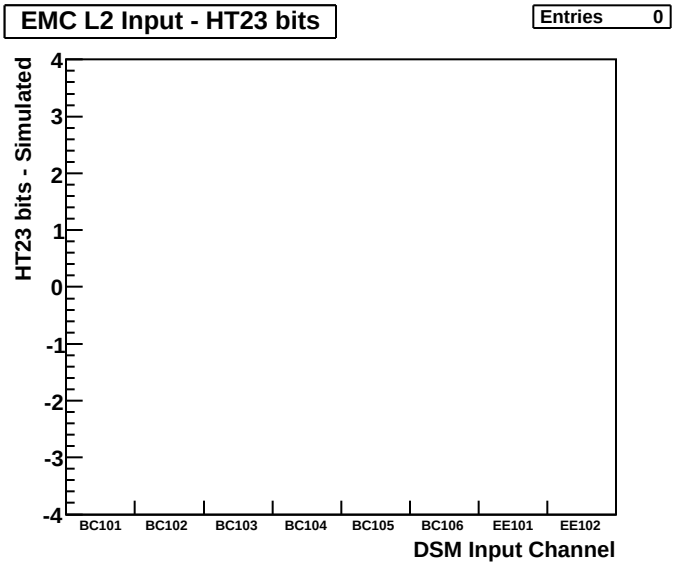

EMC L2 Input - Partial JP Sum
Entries 147656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

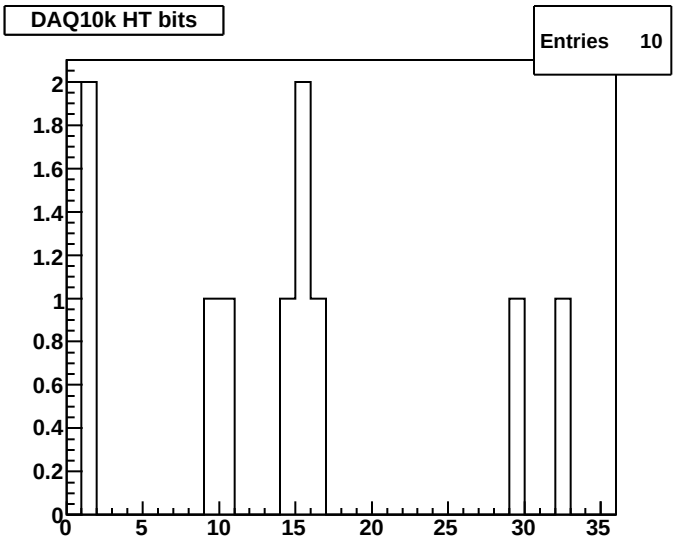

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

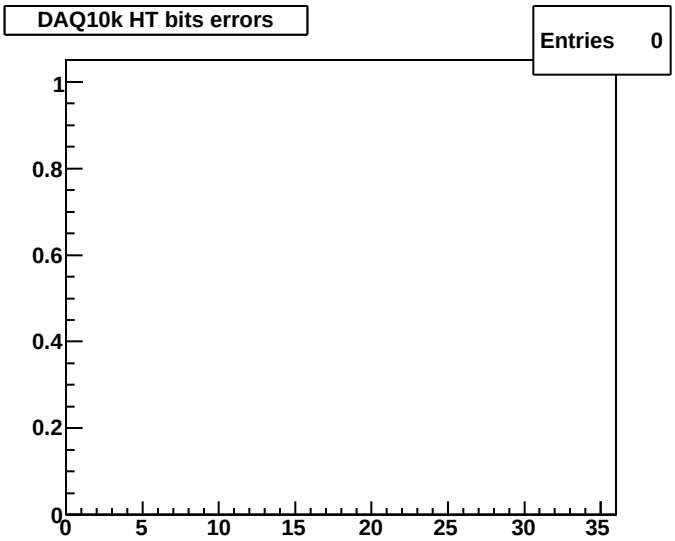

EMC L2 Input - HT01 bits/Partial JP ID
Entries 147656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

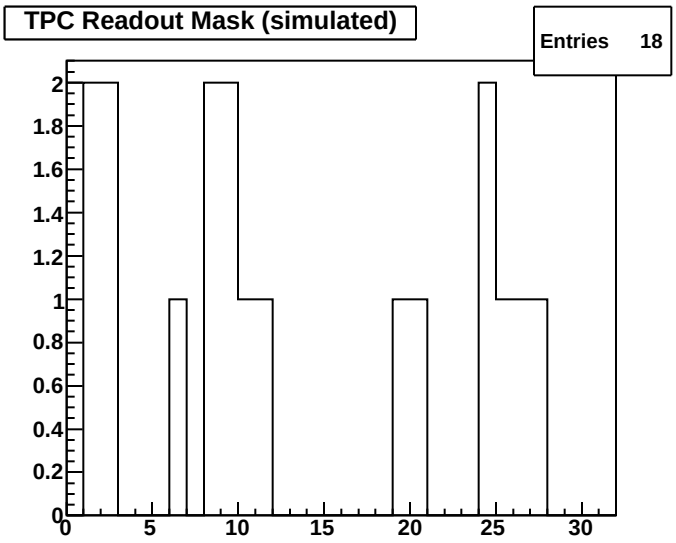

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

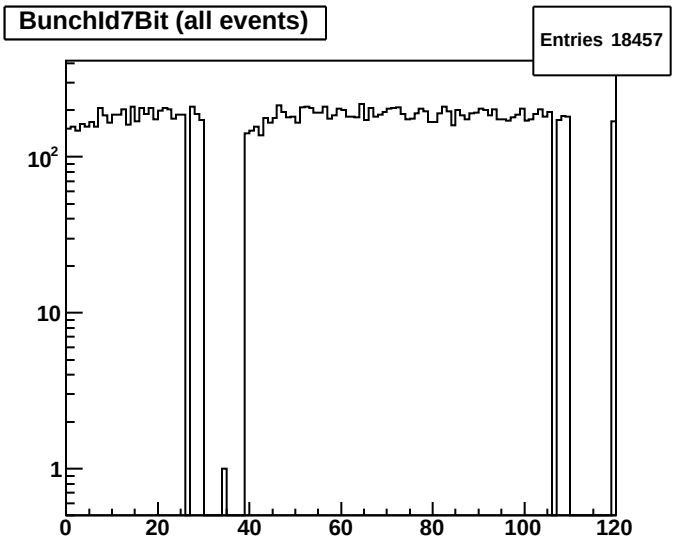

EMC L2 Input - HT23 bits
Entries 147656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

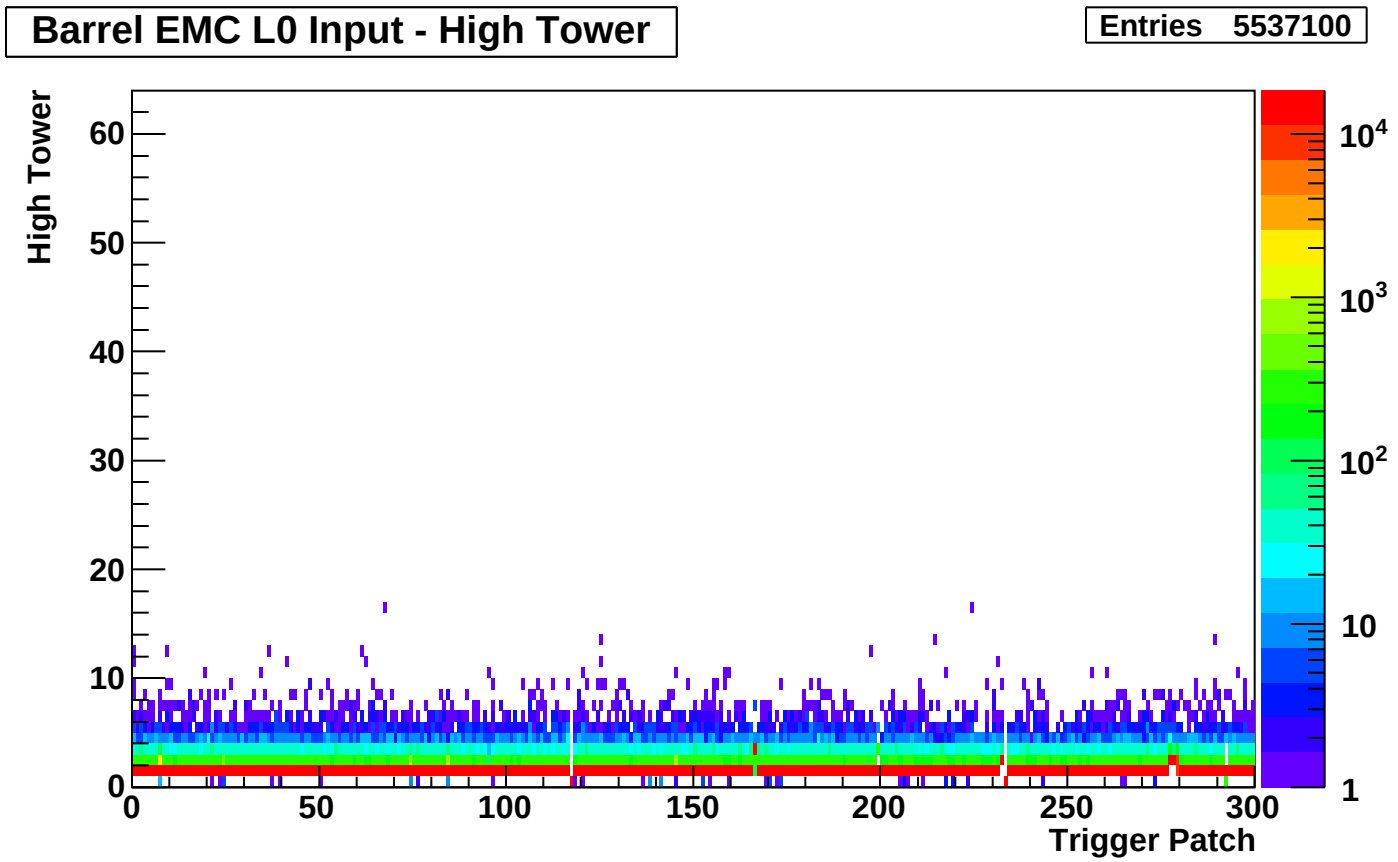

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

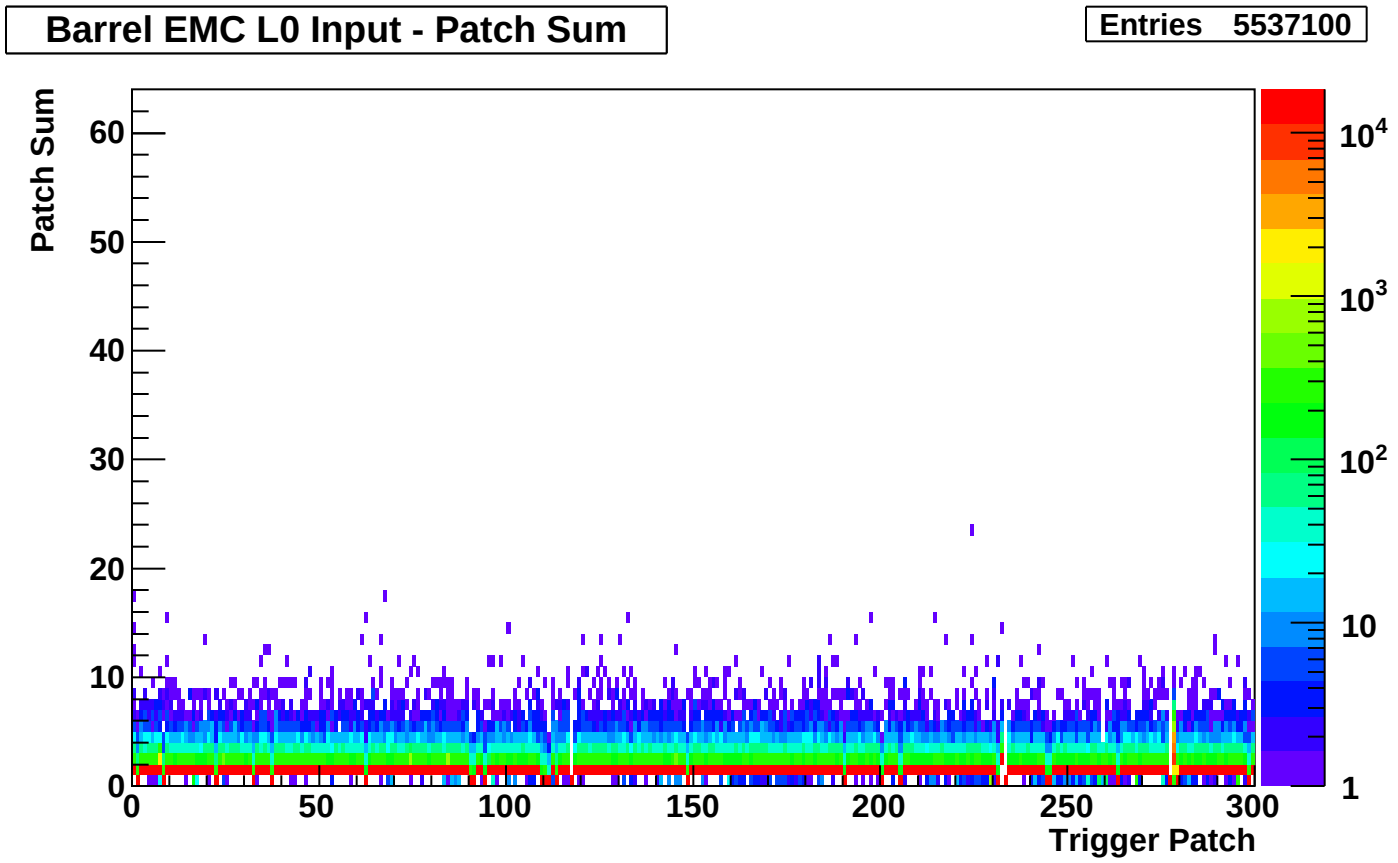

DAQ10k HT bits
Entries 10

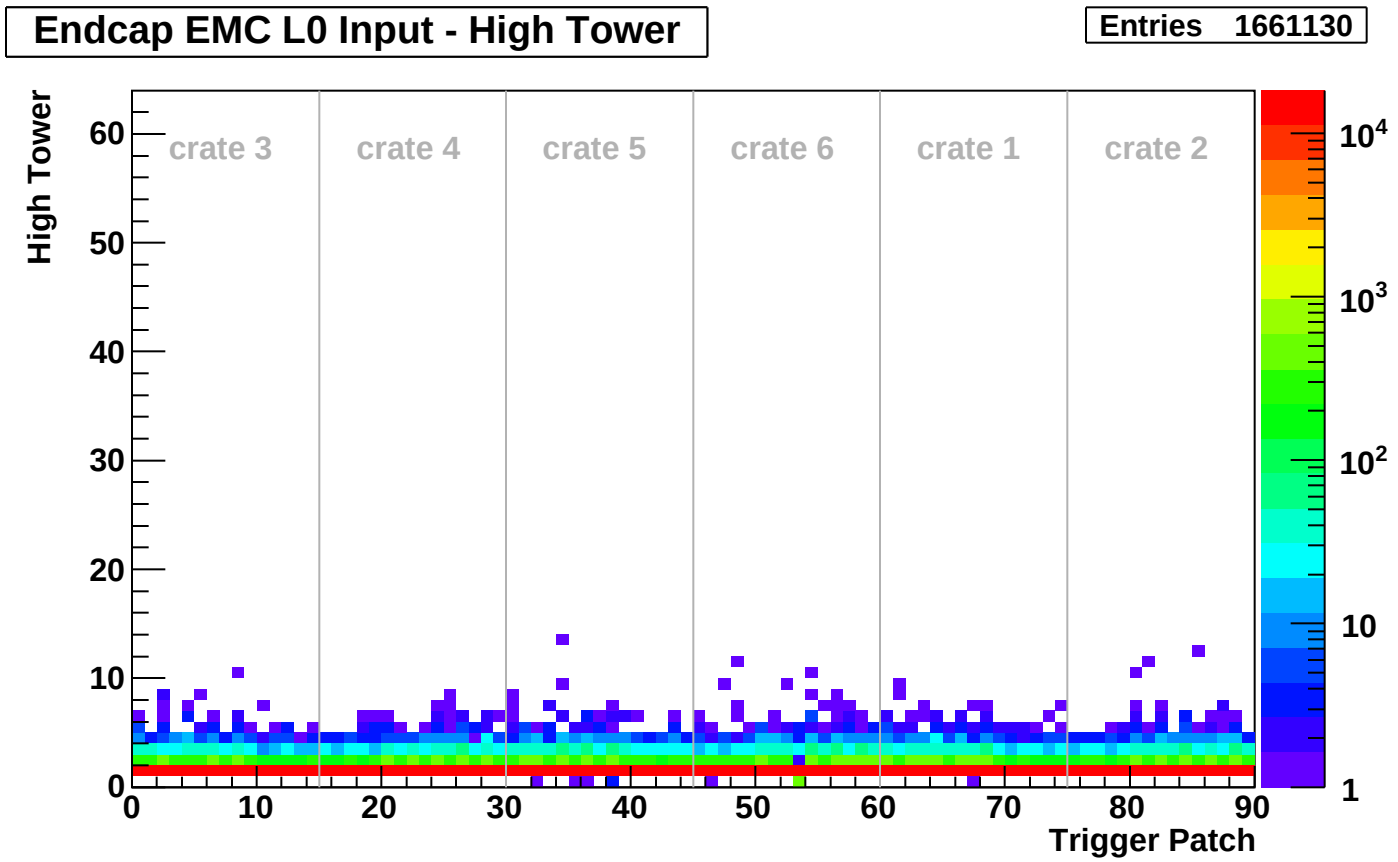

DAQ10k HT bits errors
Entries 0

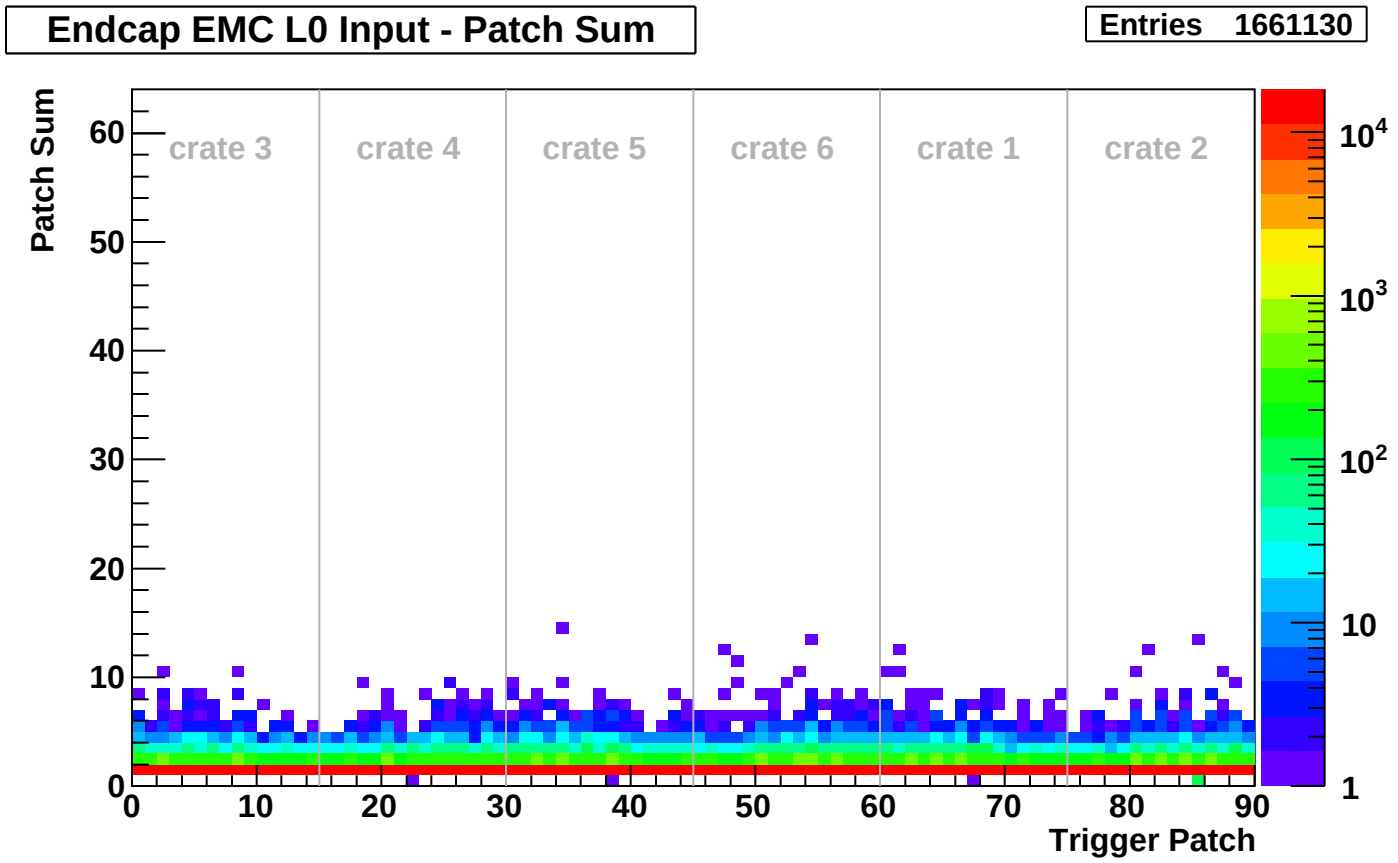

TPC Readout Mask (simulated)
Entries 18

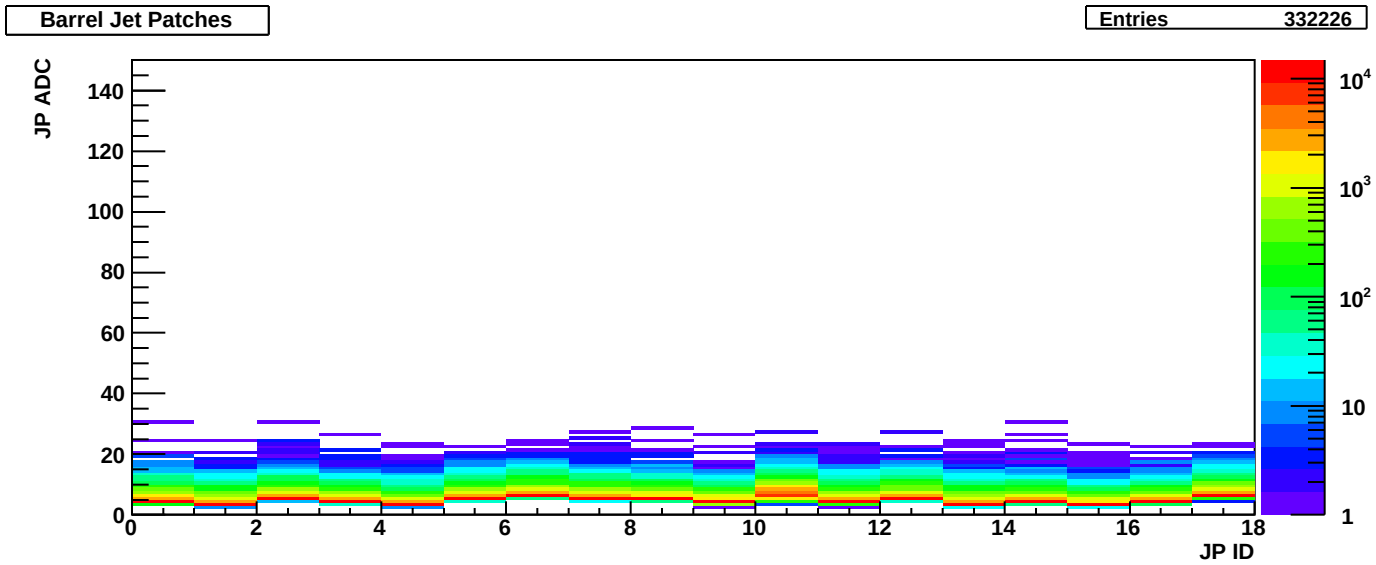

BunchId7Bit (all events)
Entries 18457

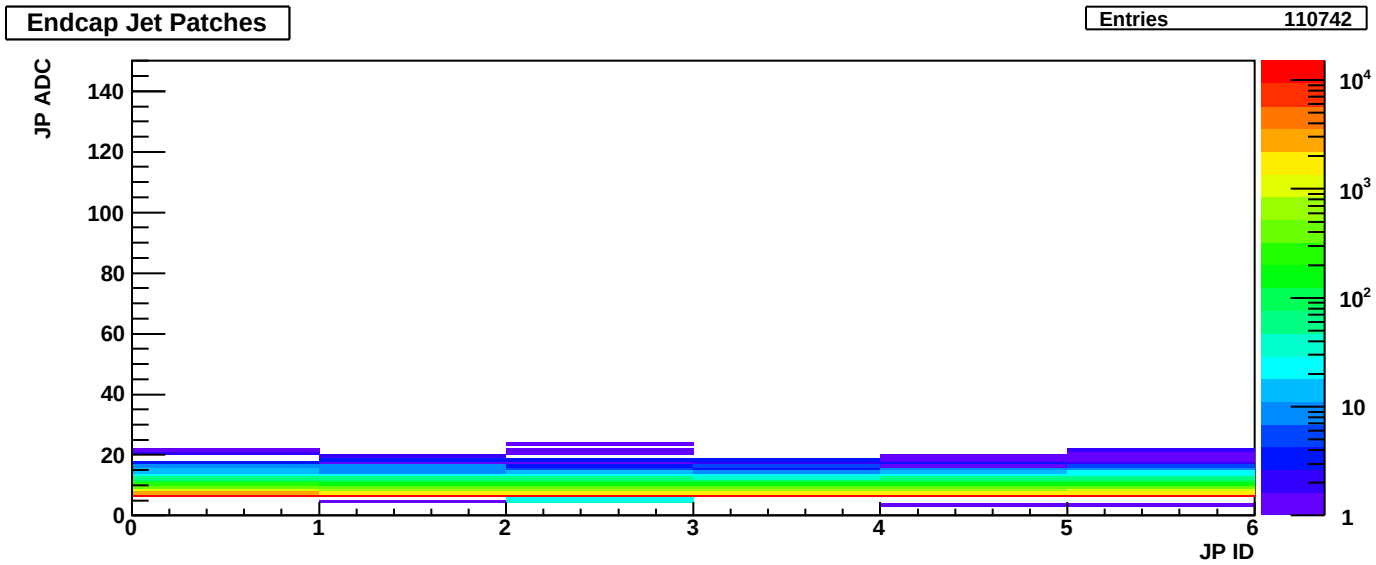

Barrel EMC L0 Input - High Tower
Entries 5537100
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

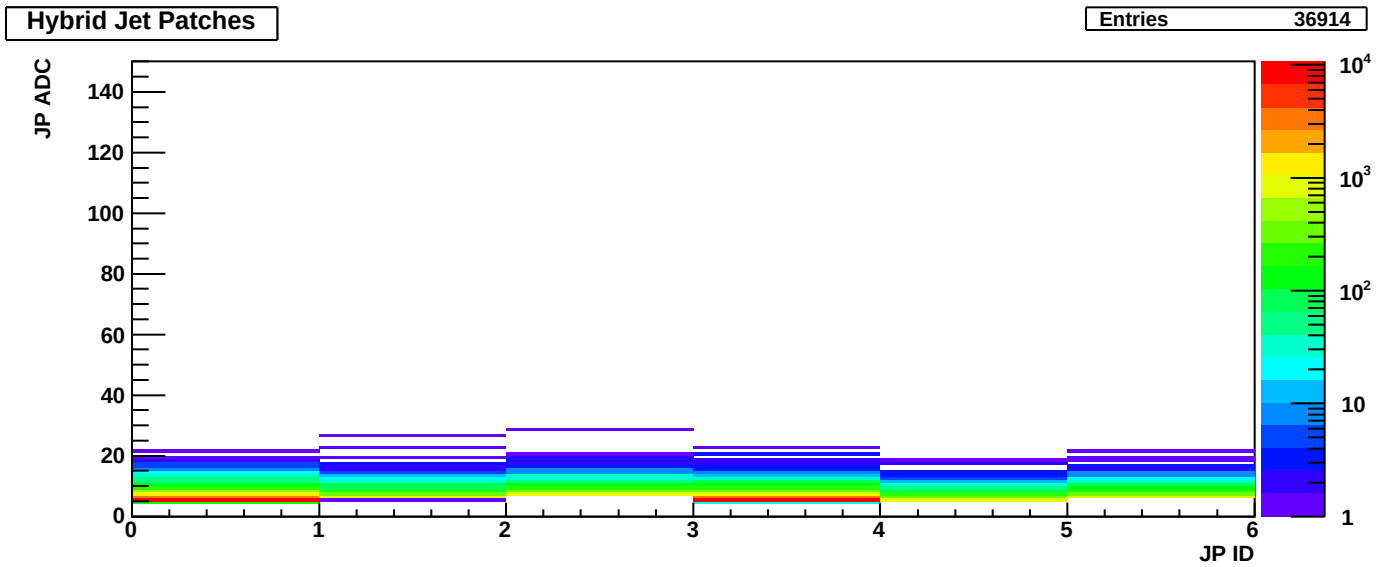

Barrel EMC L0 Input - Patch Sum
Entries 5537100
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

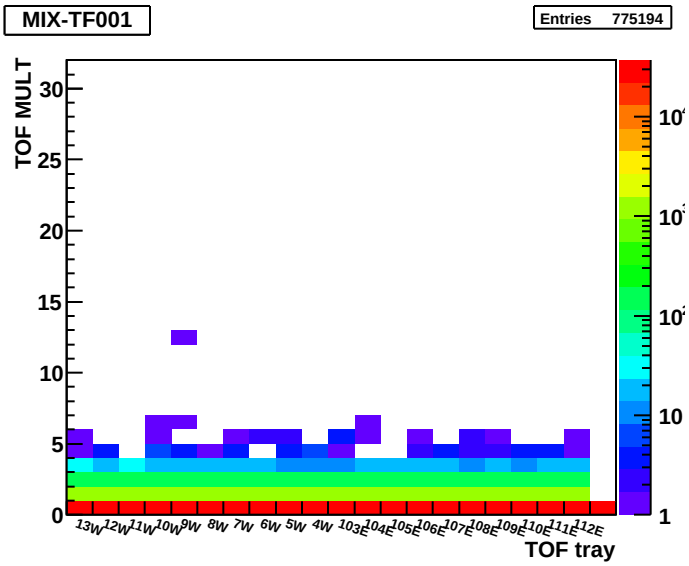

Endcap EMC L0 Input - High Tower
Entries 1661130
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

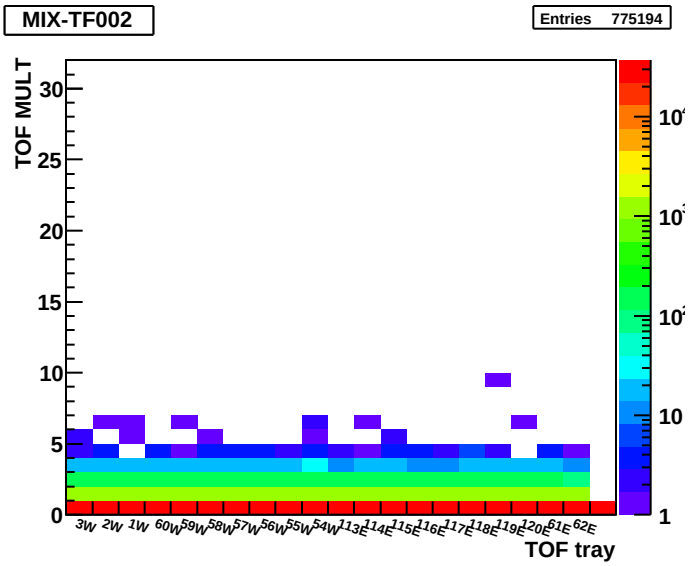

Endcap EMC L0 Input - Patch Sum
Entries 1661130
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

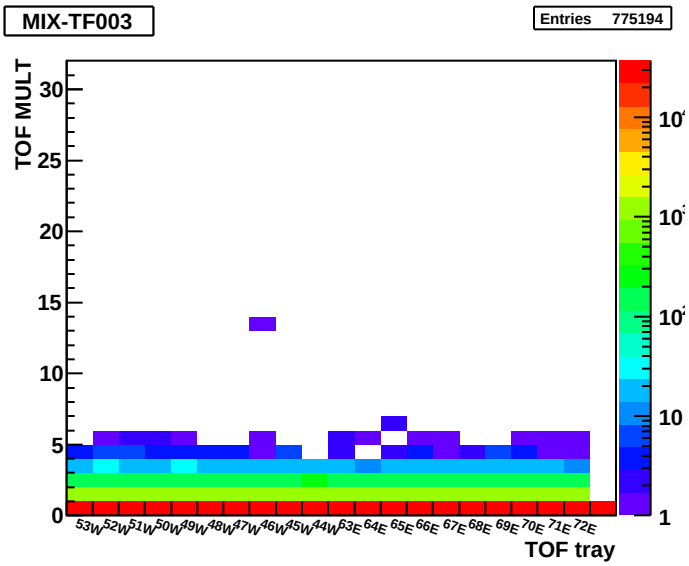

Barrel Jet Patches
Entries 332226
JP ADC
JP ID

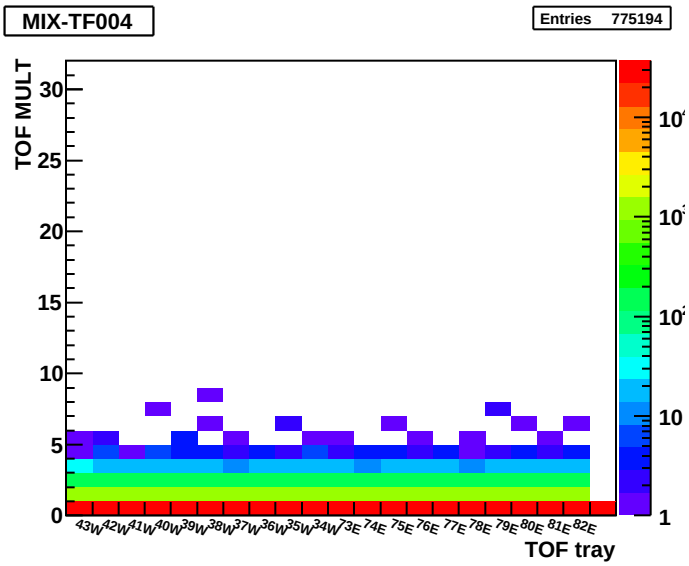

Endcap Jet Patches
Entries 110742
JP ADC
JP ID

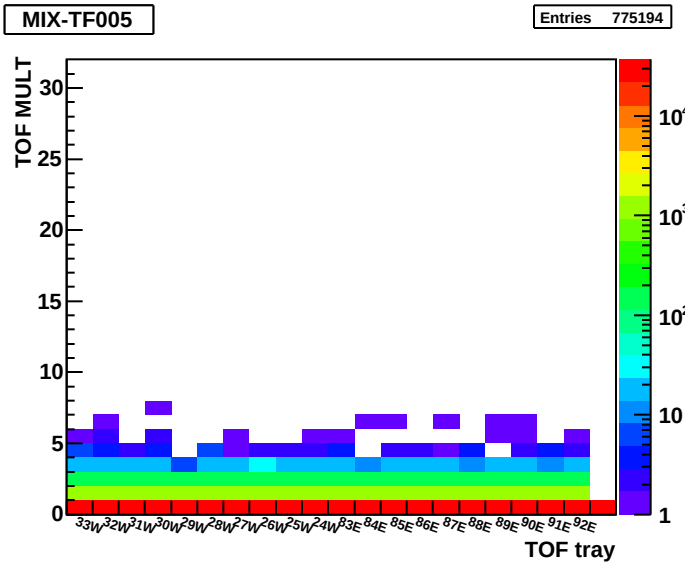

Hybrid Jet Patches
Entries 36914
JP ADC
JP ID

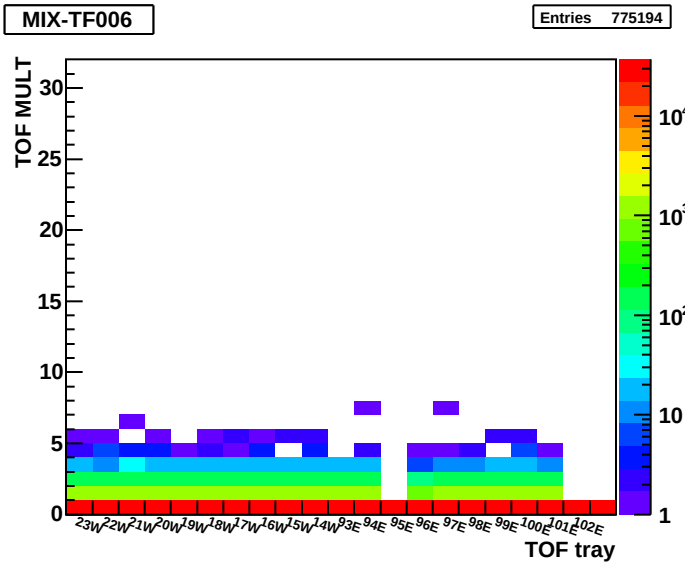

MIX-TF001
Entries 775194
TOF MULT
TOF tray

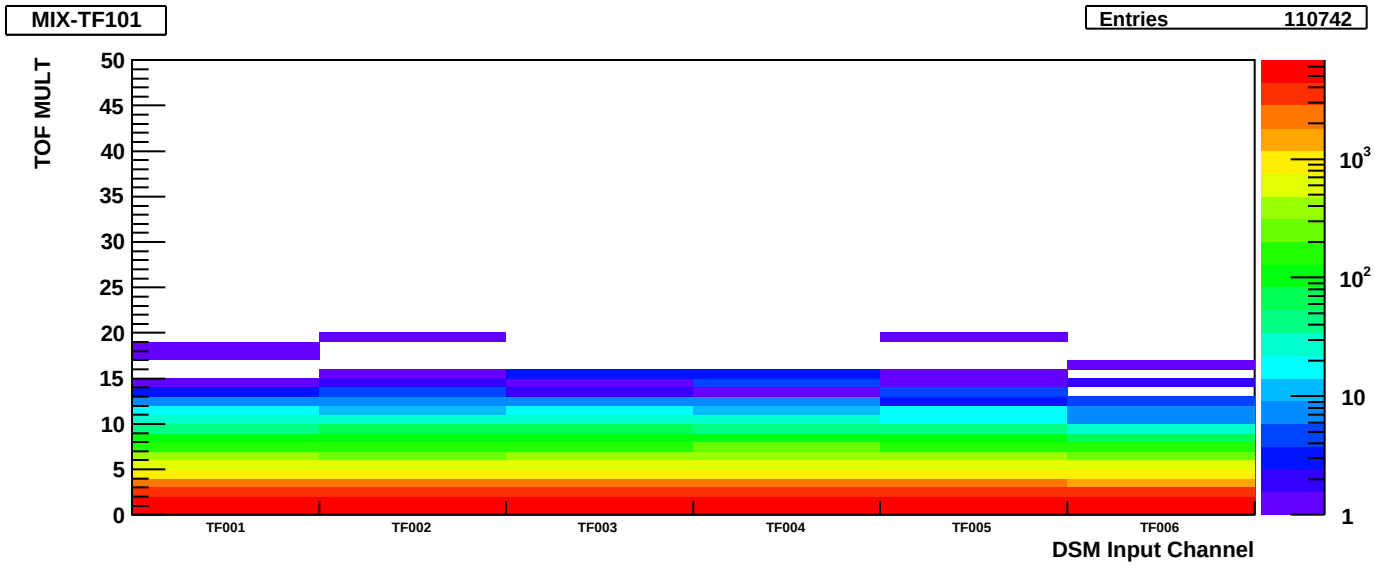

MIX-TF002
Entries 775194
TOF MULT
TOF tray

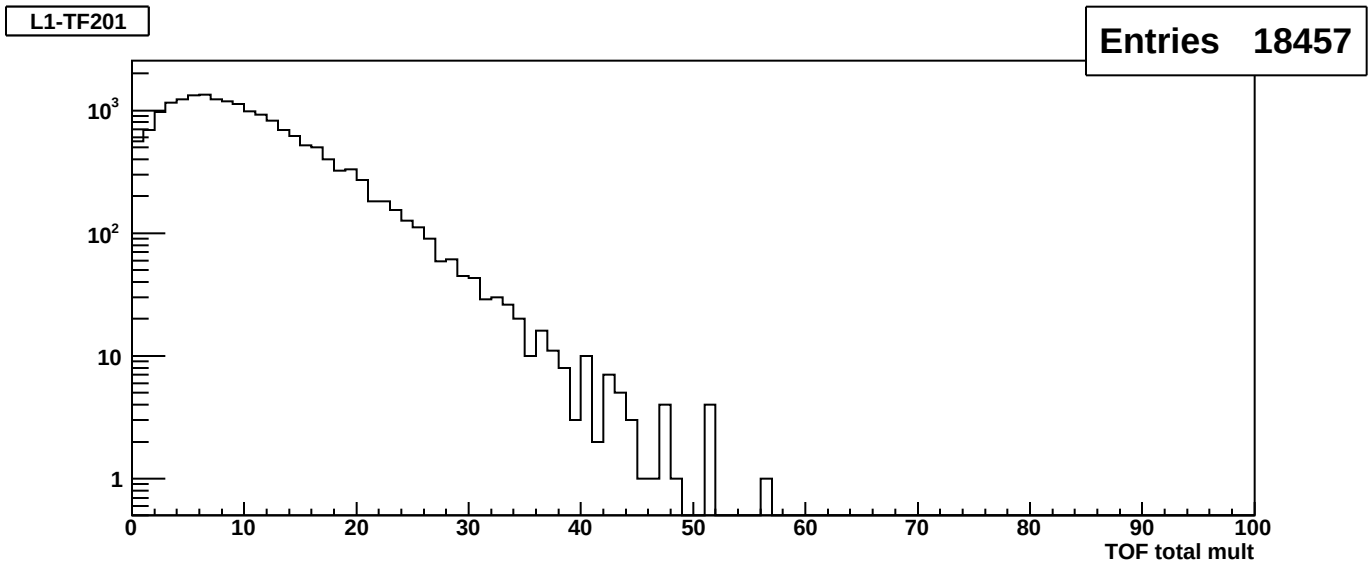

MIX-TF003
Entries 775194
TOF MULT
TOF tray

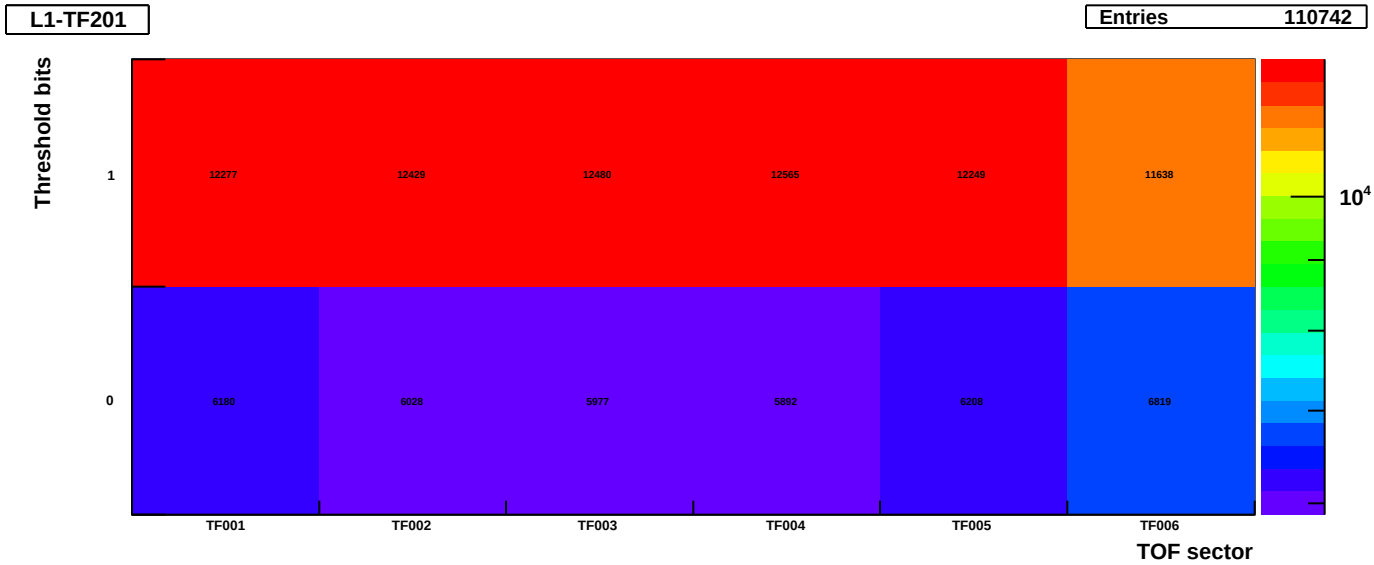

MIX-TF004
Entries 775194
TOF MULT
TOF tray

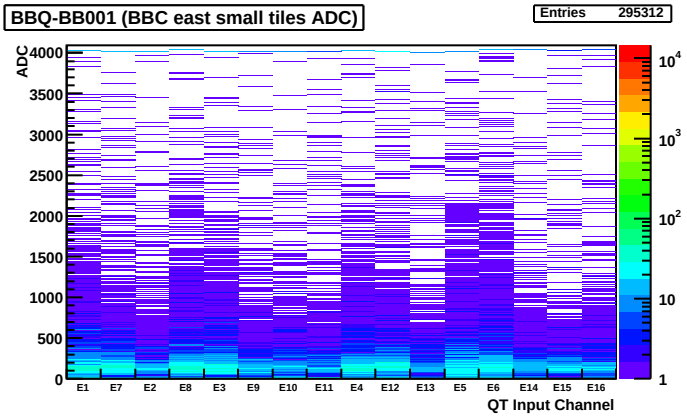

MIX-TF005
Entries 775194
TOF MULT
TOF tray

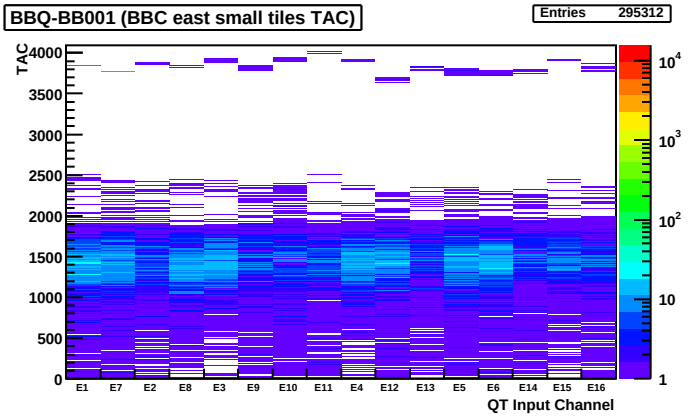

MIX-TF006
Entries 775194
TOF MULT
TOF tray

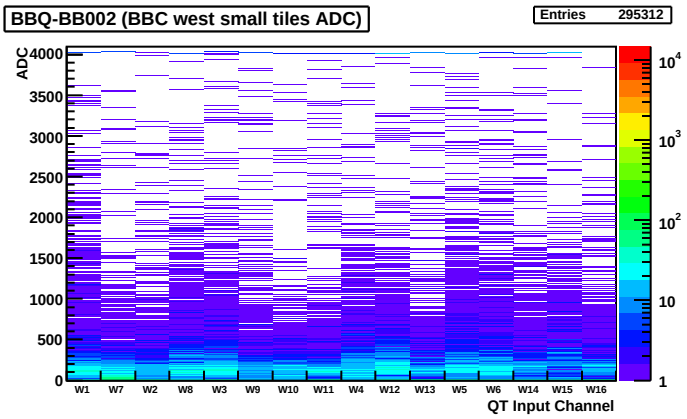

MIX-TF101
Entries 110742
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

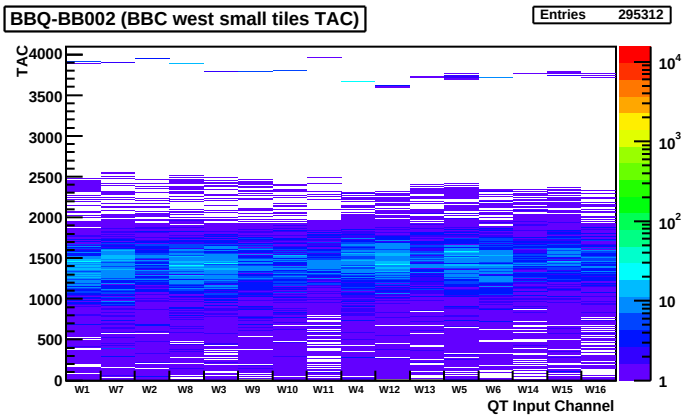

L1-TF201
Entries 18457
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

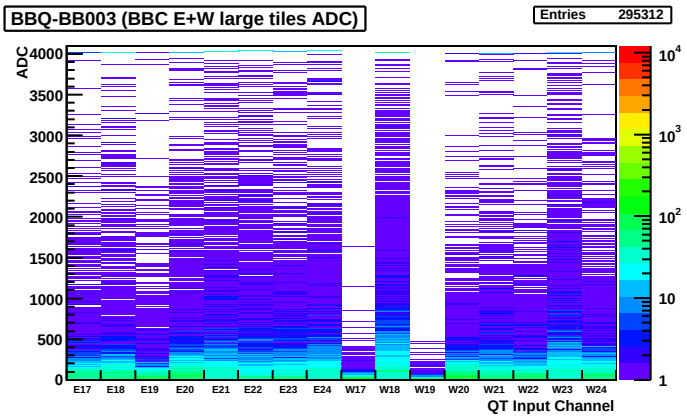

L1-TF201
Entries 110742
Threshold bits
1
0
12277
12429
12480
12565
12249
11638
6180
6028
5977
5892
6208
6819
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

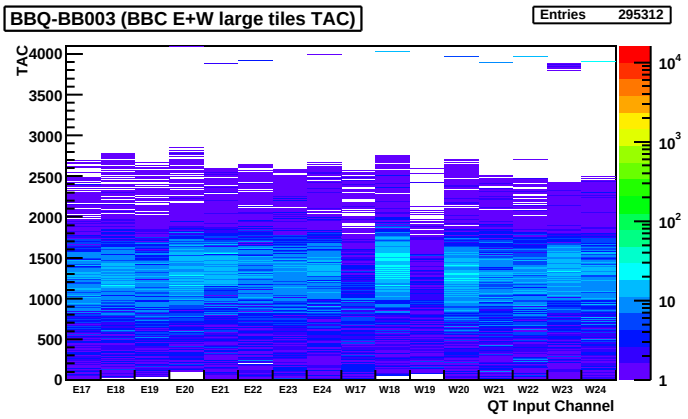

BBQ-BB001 (BBC east small tiles ADC)
Entries 295312
ADC
QT Input Channel

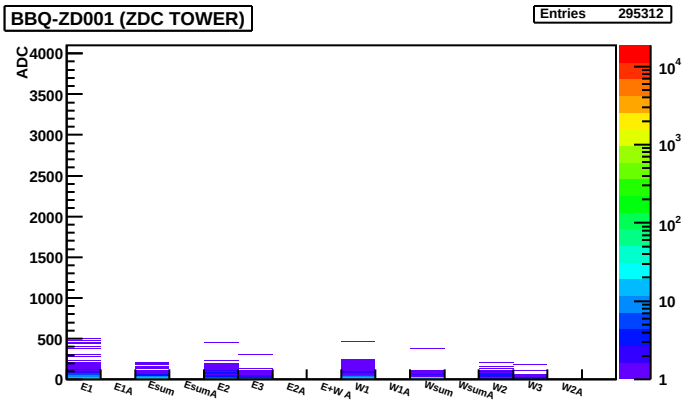

BBQ-BB001 (BBC east small tiles TAC)
Entries 295312
TAC
QT Input Channel

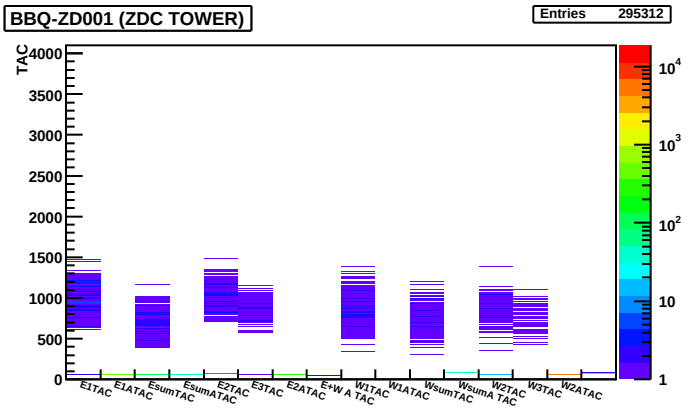

BBQ-BB002 (BBC west small tiles ADC)
Entries 295312
ADC
QT Input Channel

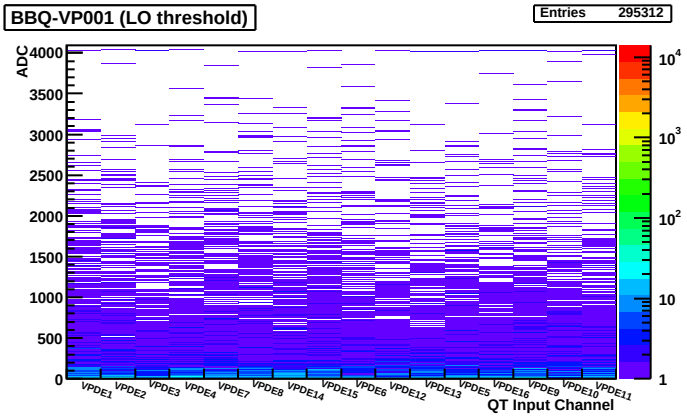

BBQ-BB002 (BBC west small tiles TAC)
Entries 295312
TAC
QT Input Channel

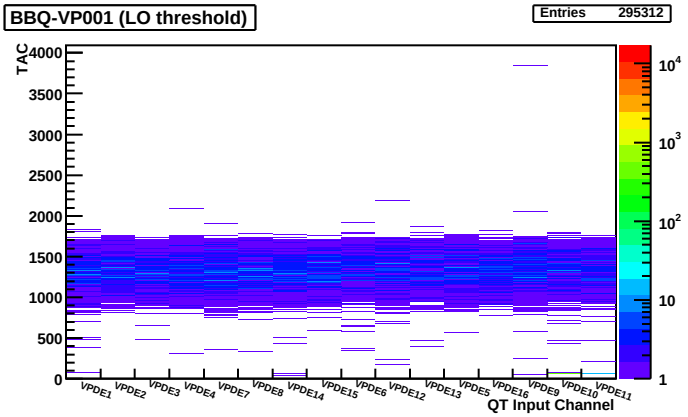

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 295312
ADC
QT Input Channel

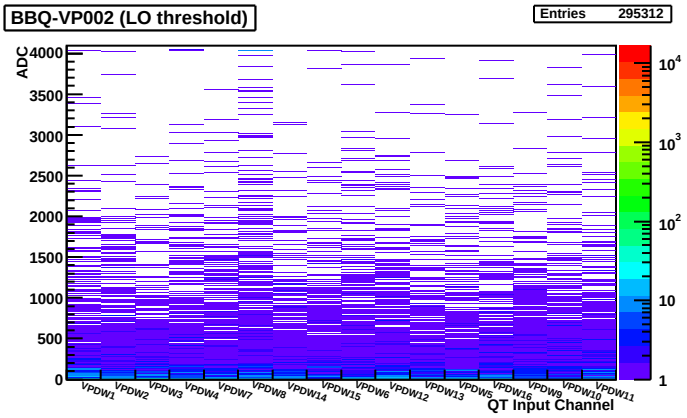

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 295312
TAC
QT Input Channel

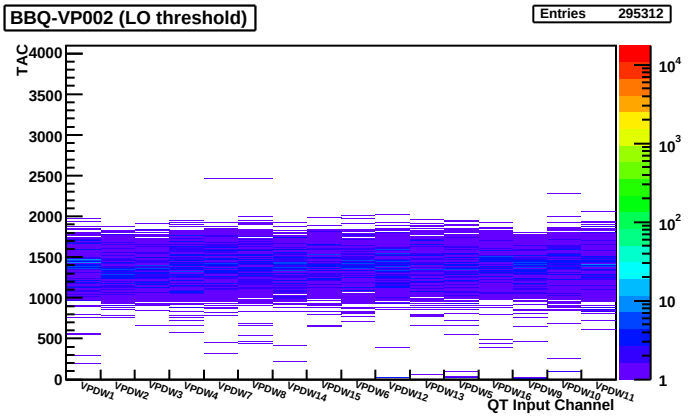

BBQ-ZD001 (ZDC TOWER)
Entries 295312
ADC

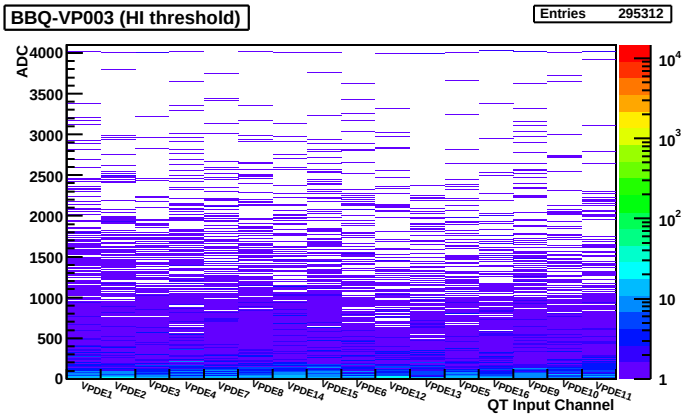

BBQ-ZD001 (ZDC TOWER)
Entries 295312
TAC

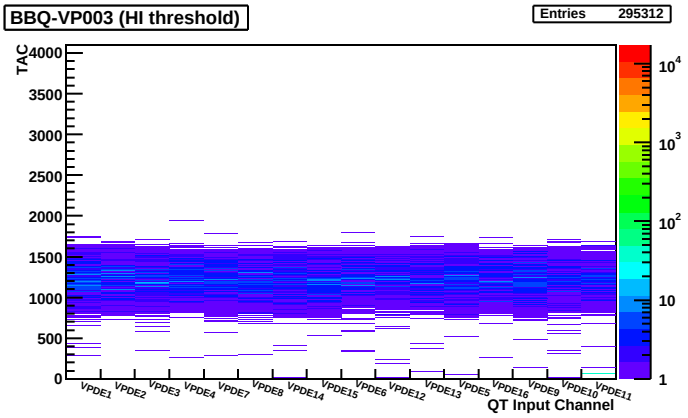

BBQ-VP001 (LO threshold)
Entries 295312
ADC
QT Input Channel

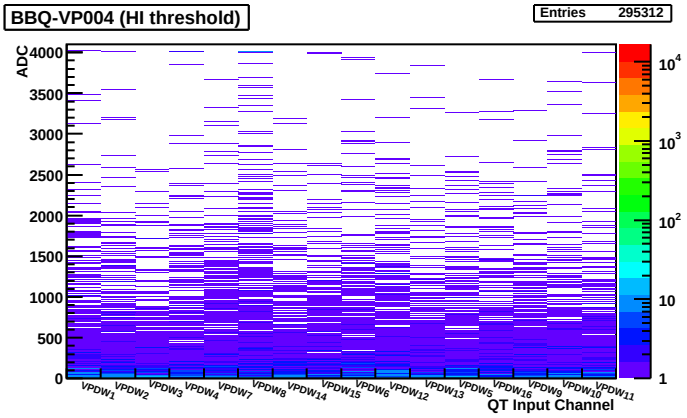

BBQ-VP001 (LO threshold)
Entries 295312
TAC
QT Input Channel

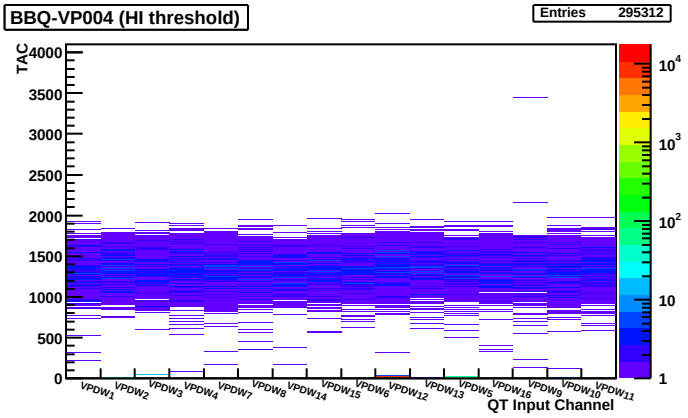

BBQ-VP002 (LO threshold)
Entries 295312
ADC
QT Input Channel

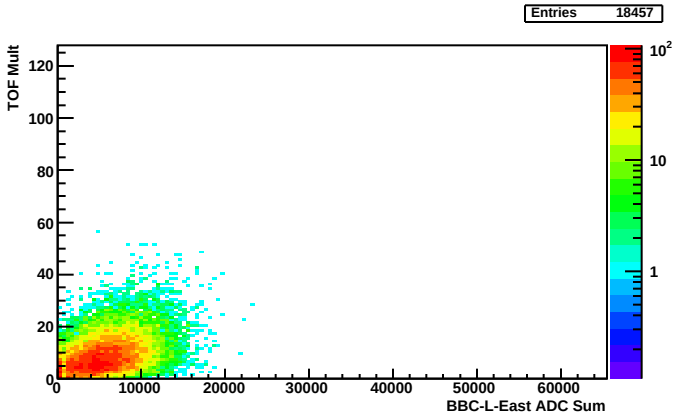

BBQ-VP002 (LO threshold)
Entries 295312
TAC
QT Input Channel

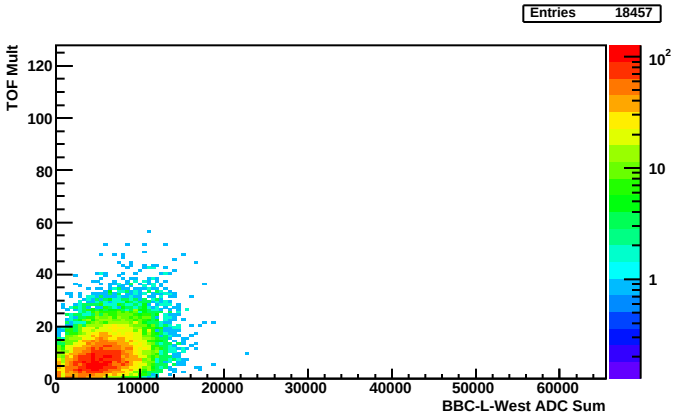

BBQ-VP003 (HI threshold)
Entries 295312
ADC
QT Input Channel

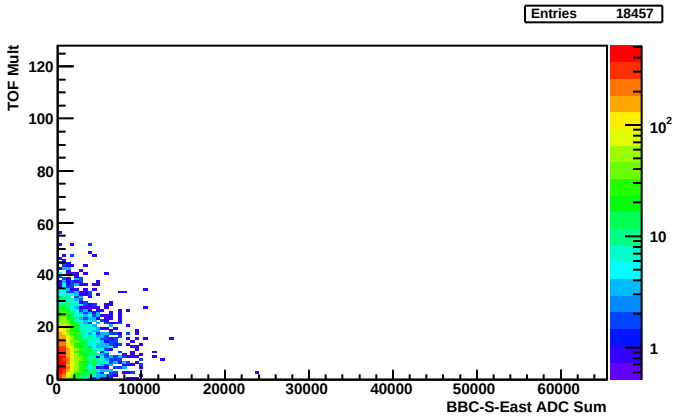

BBQ-VP003 (HI threshold)
Entries 295312
TAC
QT Input Channel

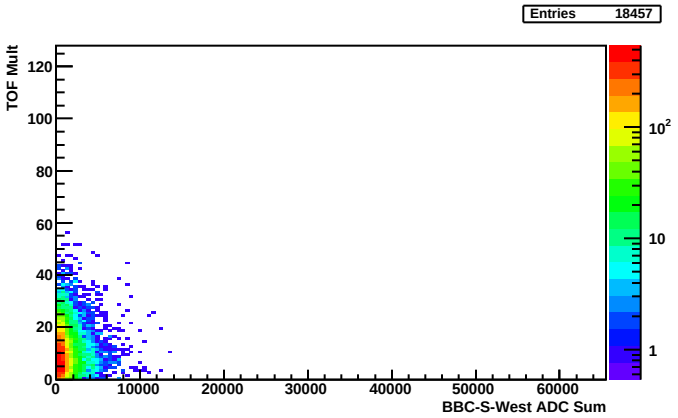

BBQ-VP004 (HI threshold)
Entries 295312
ADC
QT Input Channel

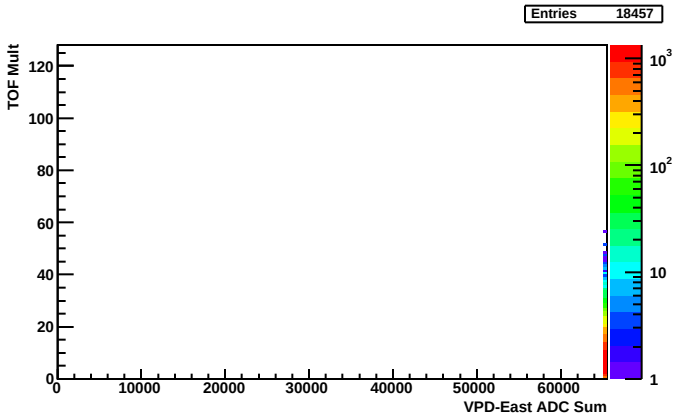

BBQ-VP004 (HI threshold)
Entries 295312
TAC
QT Input Channel

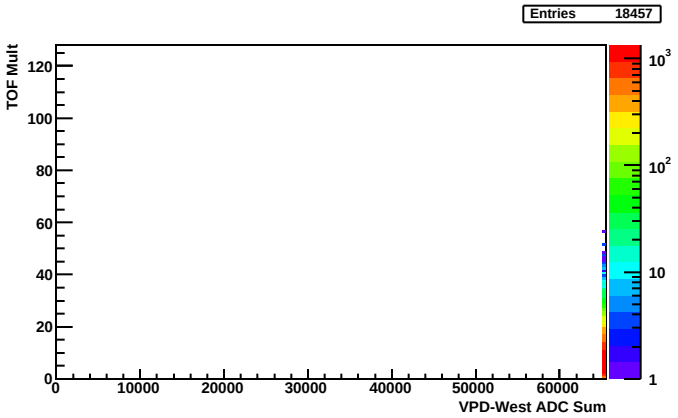

Entries 18457
TOF Mult
BBC-L-East ADC Sum

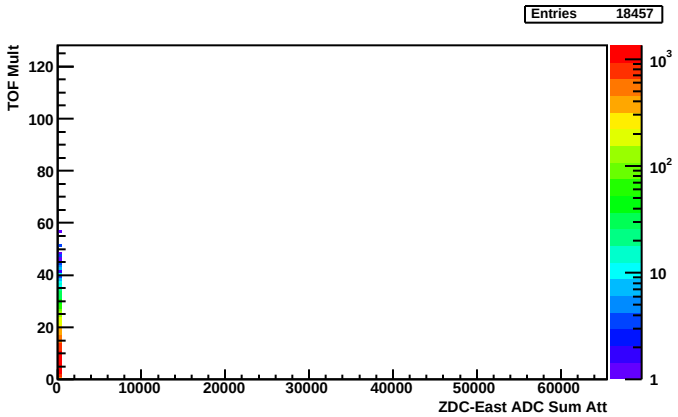

Entries 18457
TOF Mult
BBC-L-West ADC Sum

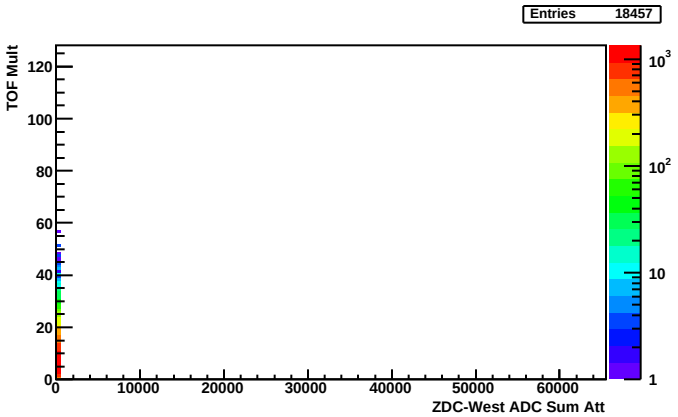

Entries 18457
TOF Mult
BBC-S-East ADC Sum

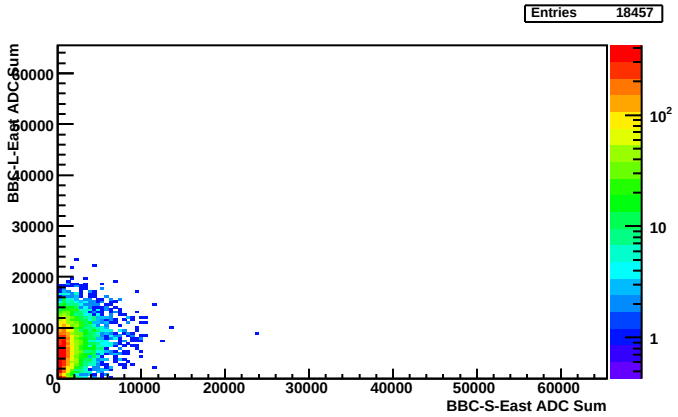

Entries 18457
TOF Mult
BBC-S-West ADC Sum

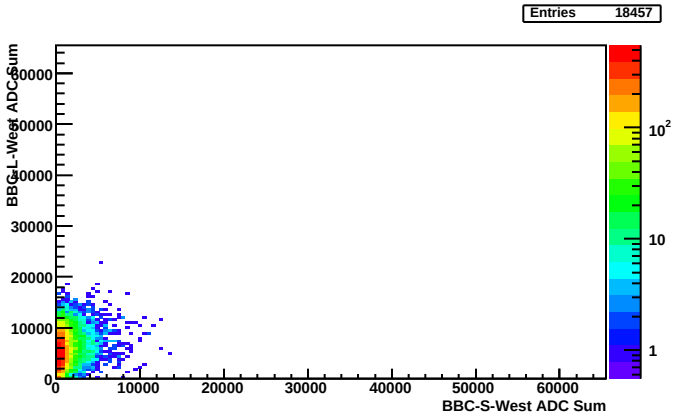

Entries 18457
TOF Mult
VPD-East ADC Sum

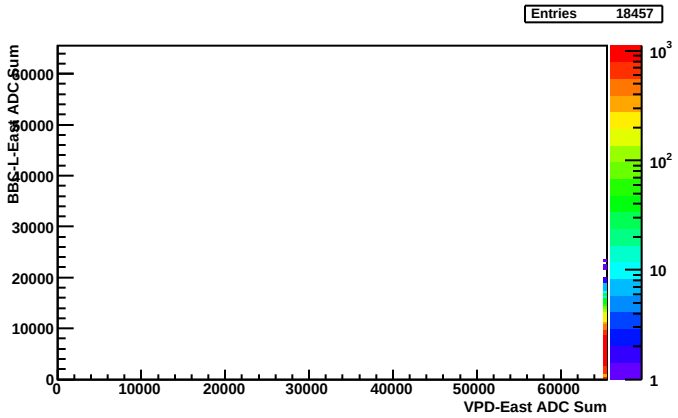

Entries 18457
TOF Mult
VPD-West ADC Sum

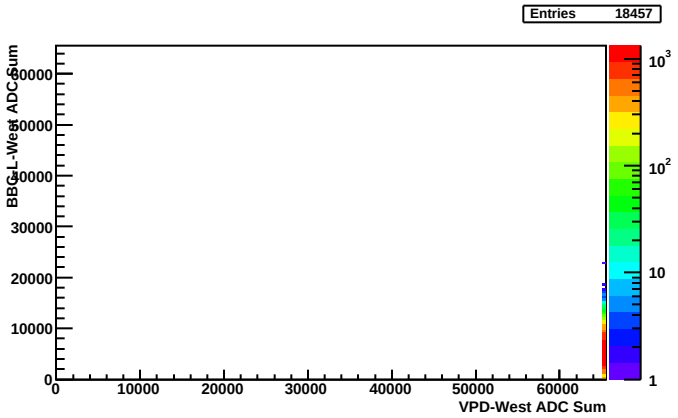

Entries 18457
TOF Mult
ZDC-East ADC Sum Att

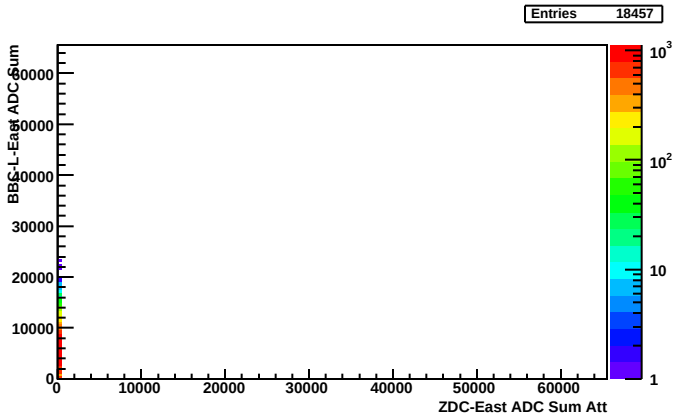

Entries 18457
TOF Mult
ZDC-West ADC Sum Att

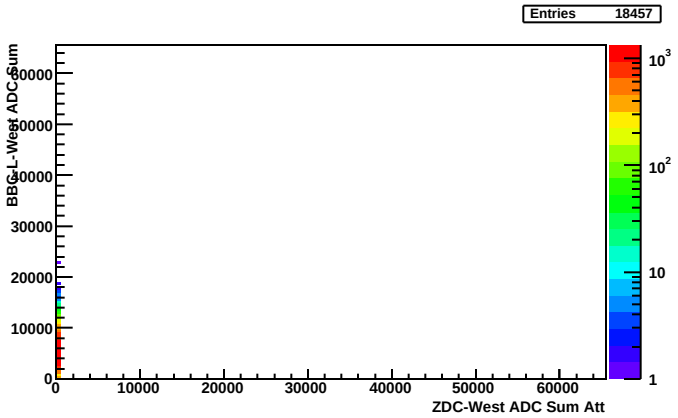

Entries 18457
BBC-L-East ADC Sum
BBC-S-East ADC Sum

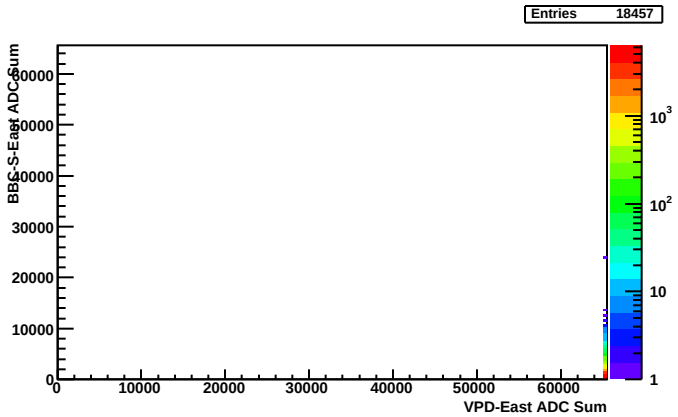

Entries 18457
BBC-L-West ADC Sum
BBC-S-West ADC Sum

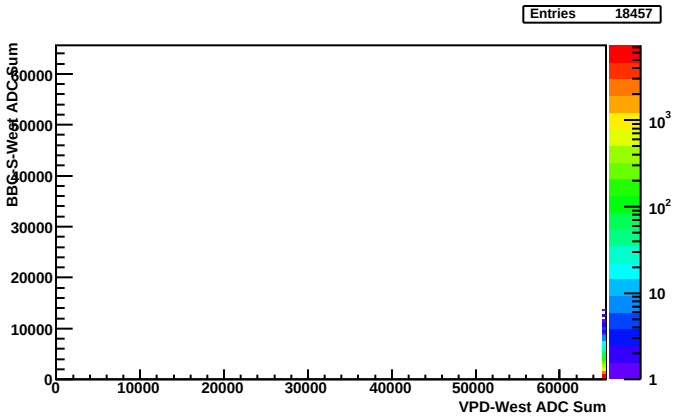

Entries 18457
BBC-L-East ADC Sum
VPD-East ADC Sum

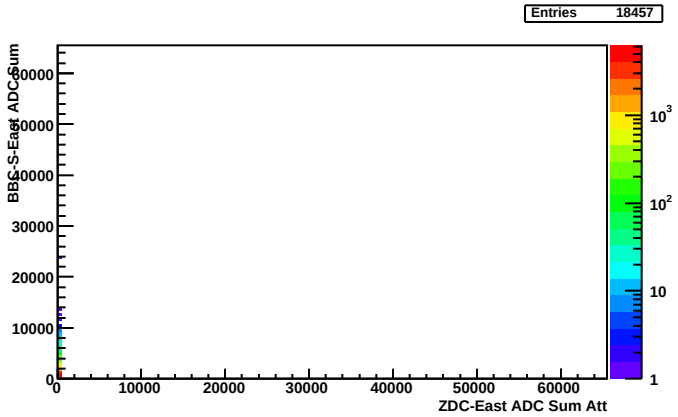

Entries 18457
BBC-L-West ADC Sum
VPD-West ADC Sum

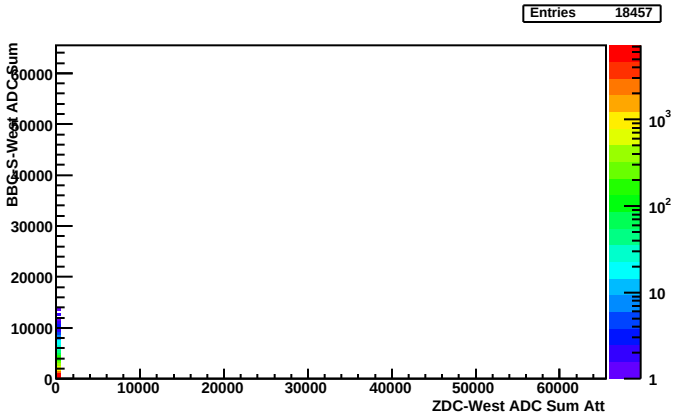

Entries 18457
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

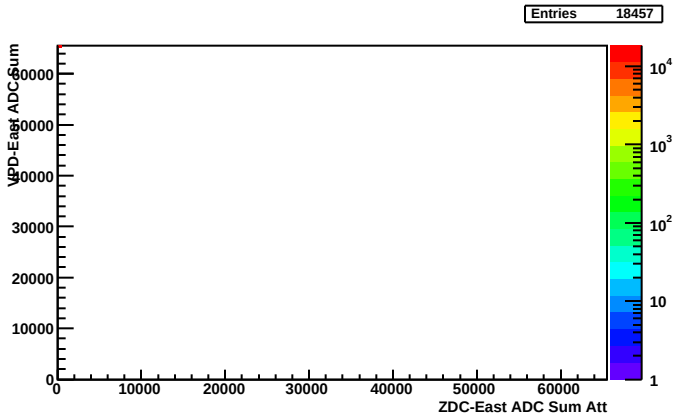

Entries 18457
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

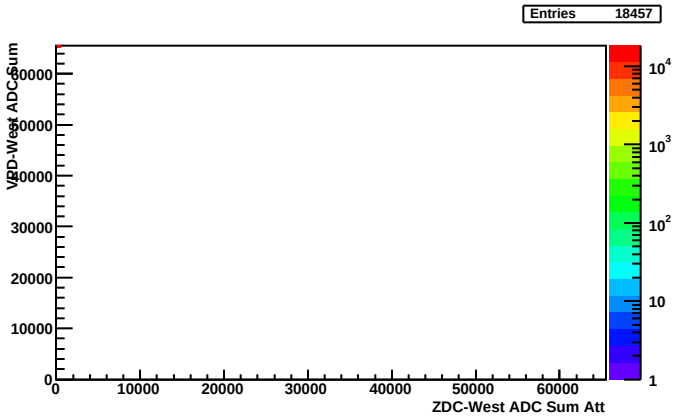

Entries 18457
BBC-S-East ADC Sum
VPD-East ADC Sum

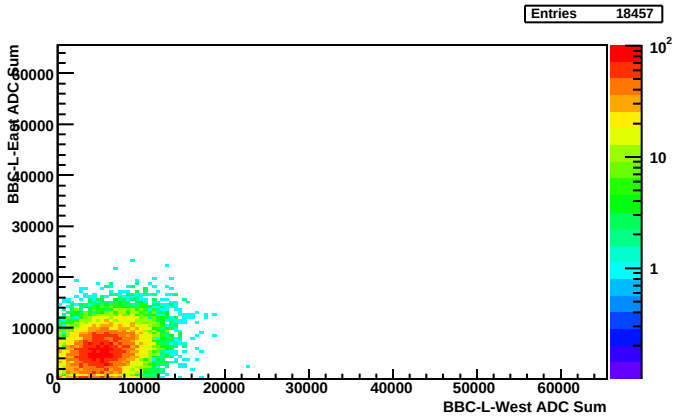

Entries 18457
BBC-S-West ADC Sum
VPD-West ADC Sum

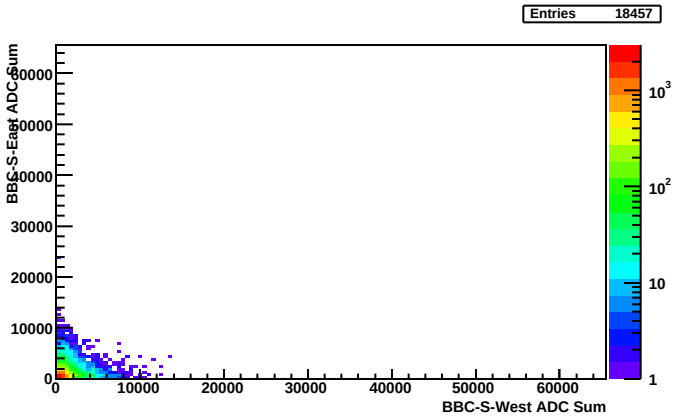

Entries 18457
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

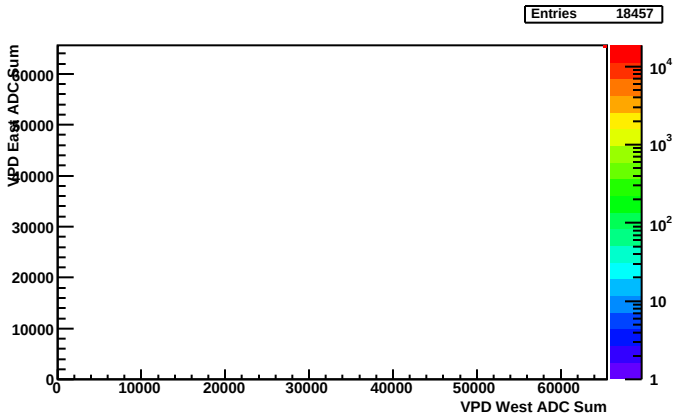

Entries 18457
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

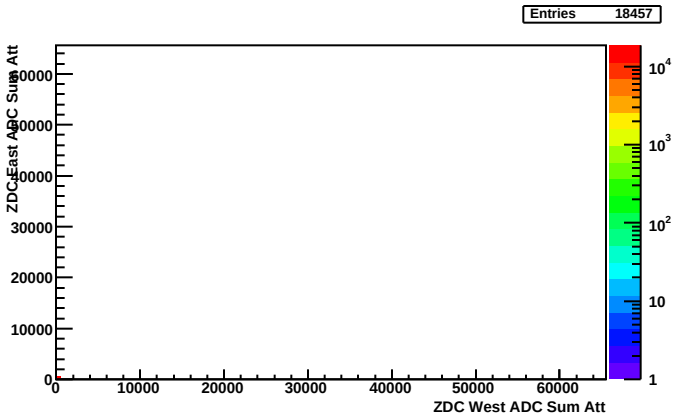

Entries 18457
VPD-East ADC Sum
ZDC-East ADC Sum Att

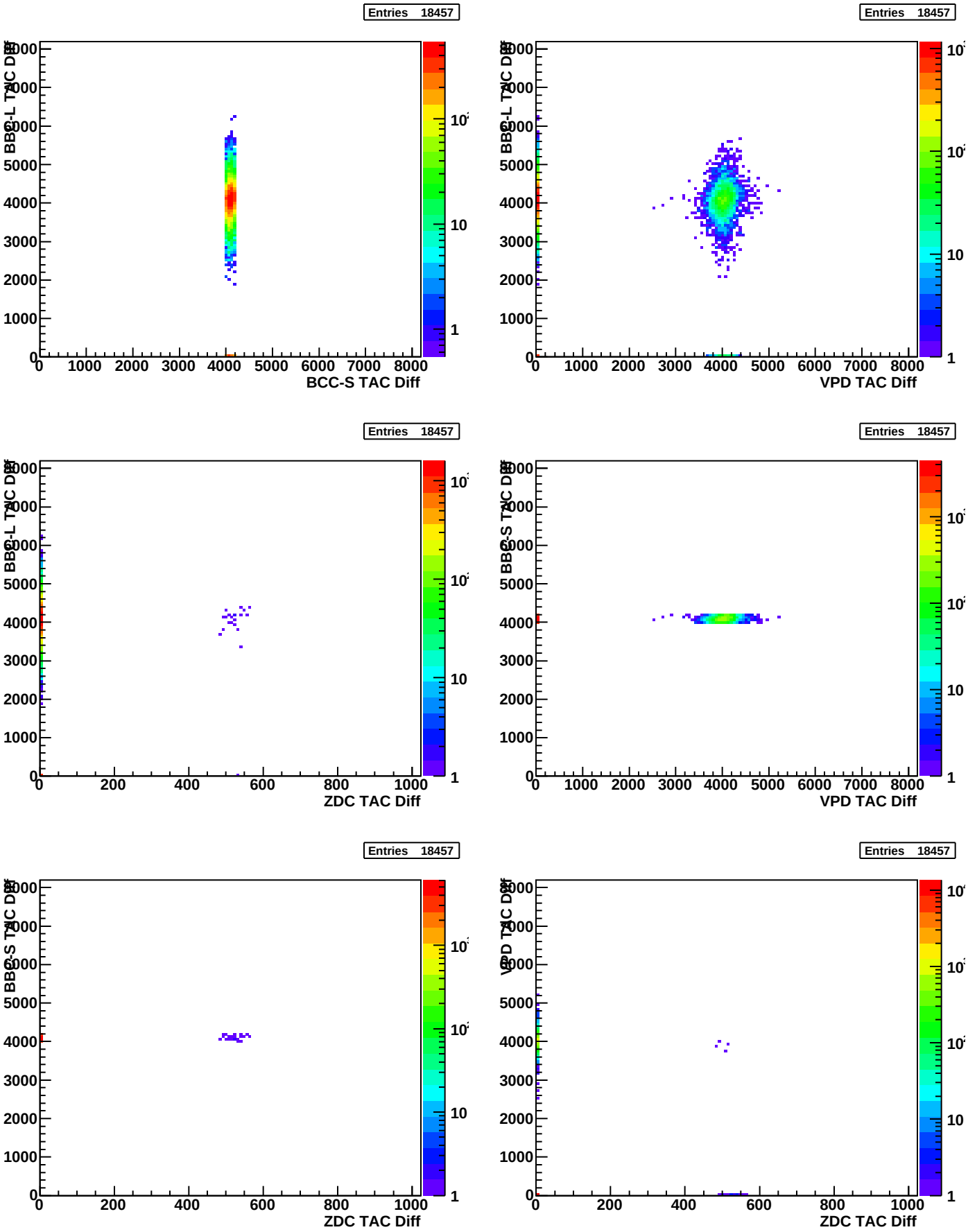

Entries 18457
VPD-West ADC Sum
ZDC-West ADC Sum Att

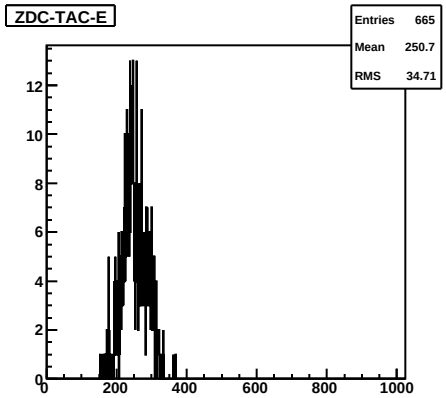

Entries 18457
BBC-L-East ADC Sum
BBC-L-West ADC Sum

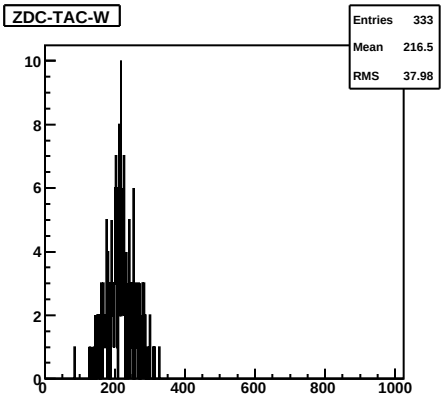

Entries 18457
BBC-S-East ADC Sum
BBC-S-West ADC Sum

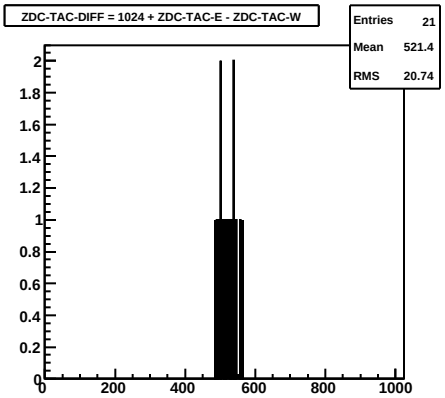

Entries 18457
VPD East ADC Sum
VPD West ADC Sum

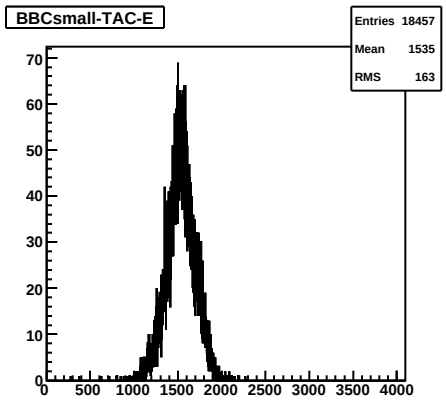

Entries 18457
ZDC East ADC Sum Att
ZDC West ADC Sum Att

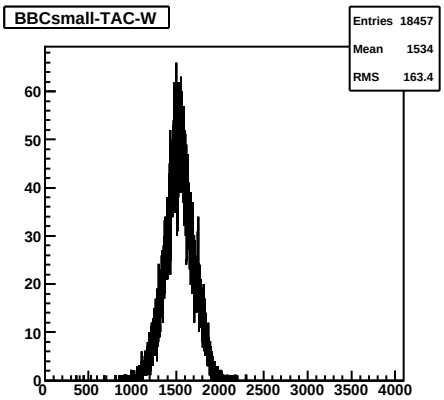

Entries 18457
BBC-L TAC Diff
BCC-S TAC Diff
Entries 18457
BBC-L TAC Diff
VPD TAC Diff
Entries 18457
BBC-L TAC Diff
ZDC TAC Diff
Entries 18457
BBC-S TAC Diff
VPD TAC Diff
Entries 18457
BBC-S TAC Diff
ZDC TAC Diff
Entries 18457
VPD TAC Diff
ZDC TAC Diff

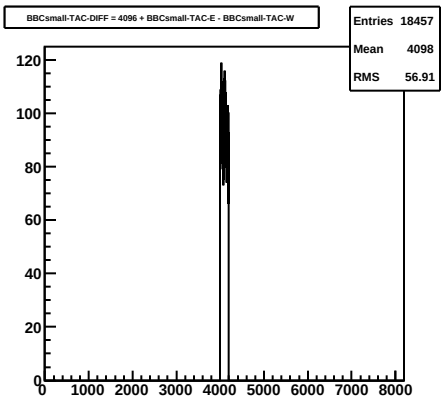

ZDC-TAC-E
Entries 665
Mean 250.7
RMS 34.71

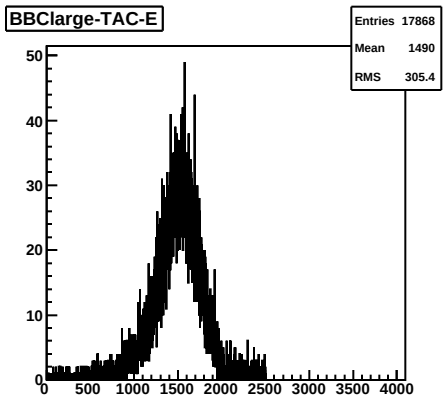

ZDC-TAC-W
Entries 333
Mean 216.5
RMS 37.98

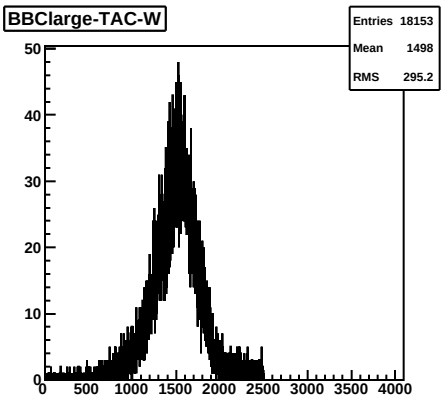

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 21
Mean 521.4
RMS 20.74

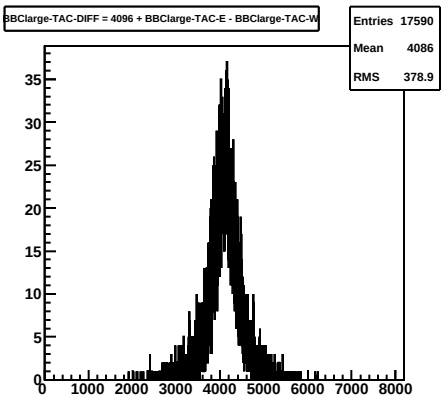

BBCsmall-TAC-E
Entries 18457
Mean 1535
RMS 163

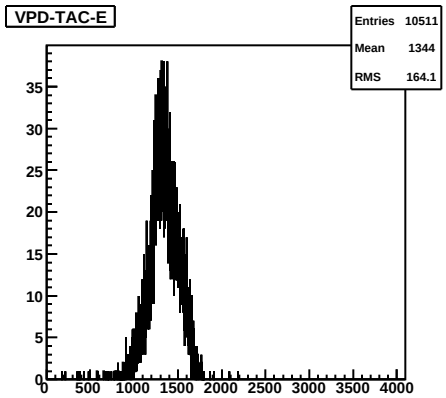

BBCsmall-TAC-W
Entries 18457
Mean 1534
RMS 163.4

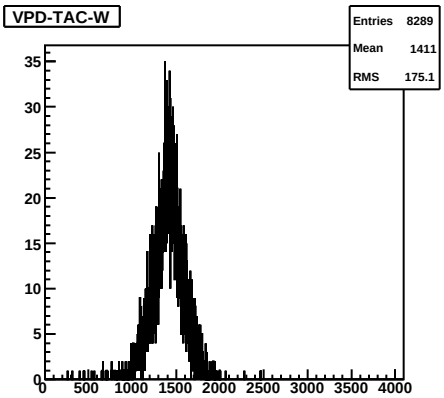

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 18457
Mean 4098
RMS 56.91

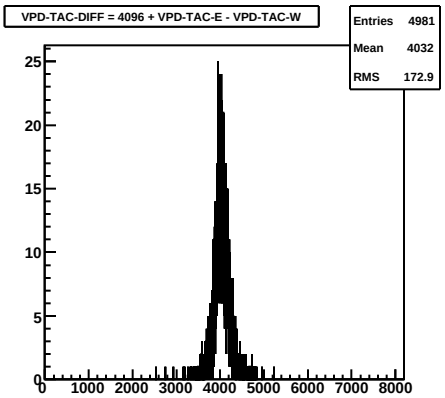

BBClarge-TAC-E
Entries 17868
Mean 1490
RMS 305.4

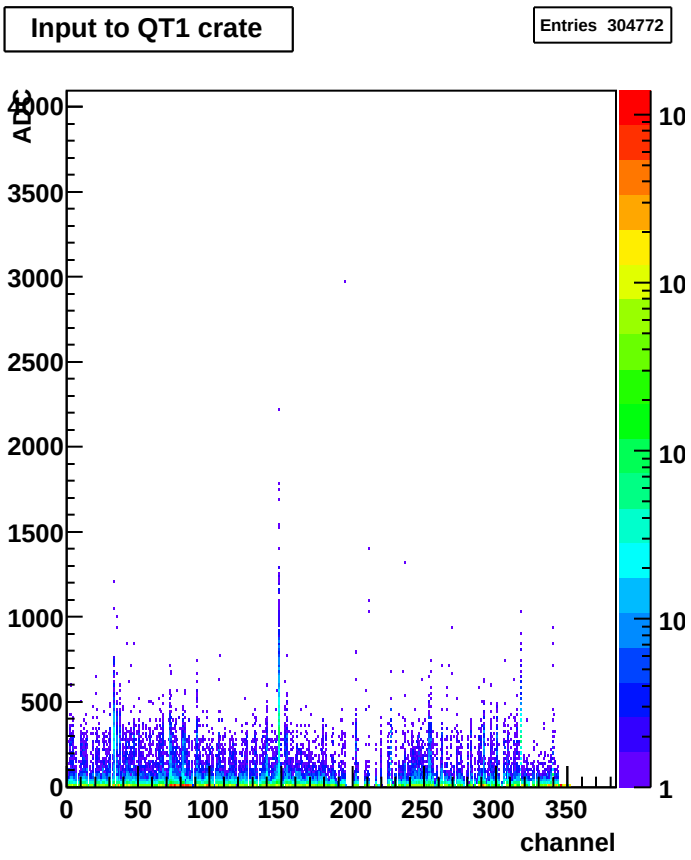

BBClarge-TAC-W
Entries 18153
Mean 1498
RMS 295.2

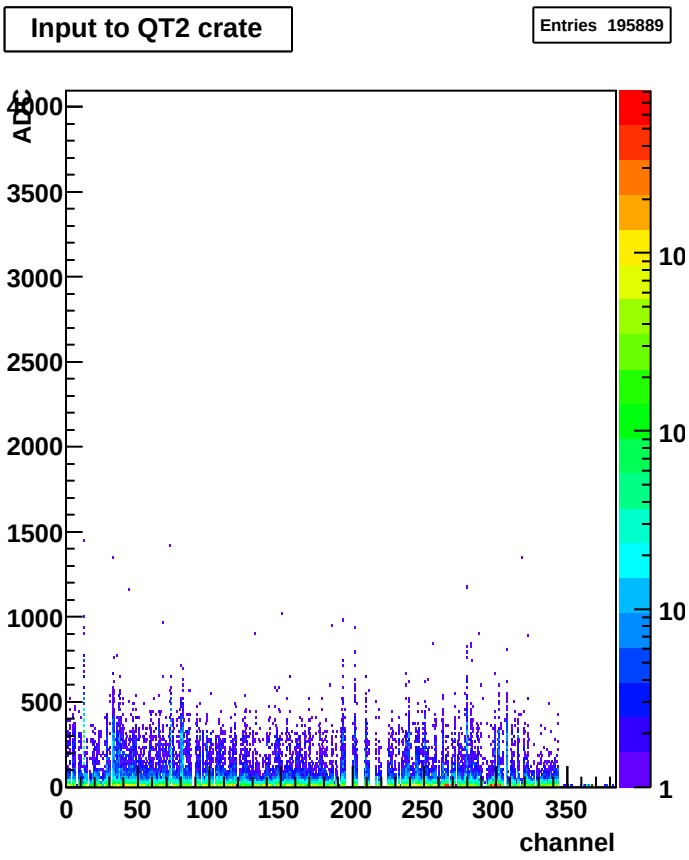

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 17590
Mean 4086
RMS 378.9

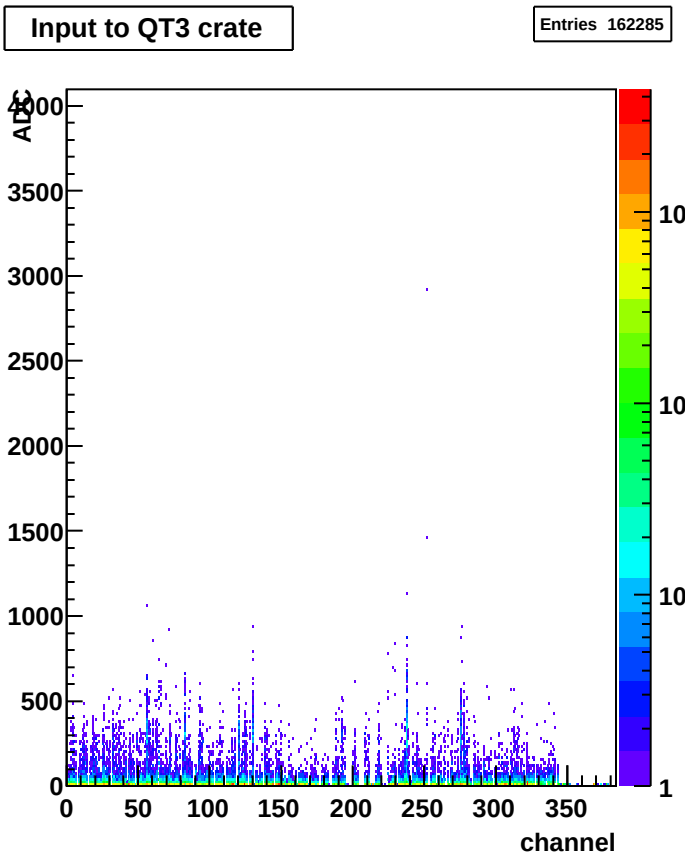

VPD-TAC-E
Entries 10511
Mean 1344
RMS 164.1

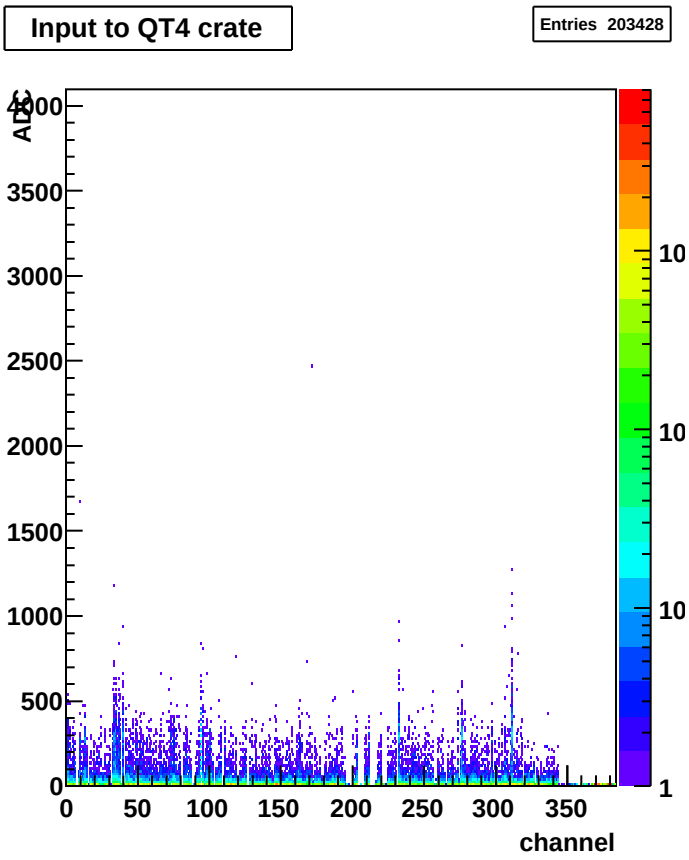

VPD-TAC-W
Entries 8289
Mean 1411
RMS 175.1

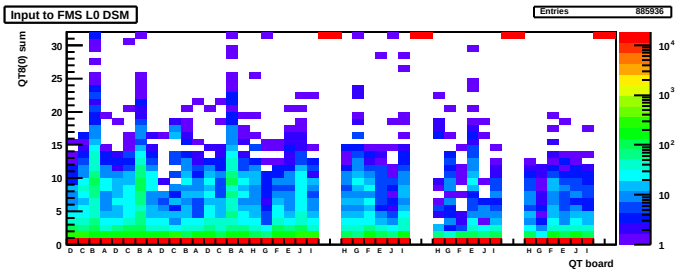

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 4981
Mean 4032
RMS 172.9

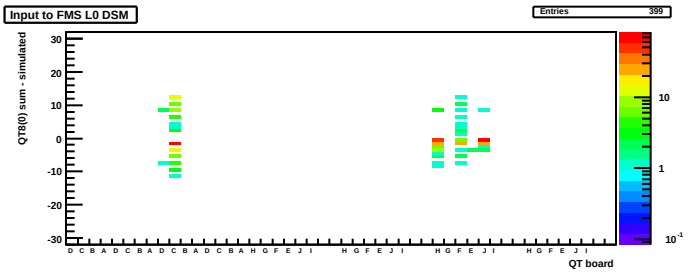

Input to QT1 crate
Entries 304772
ADC
channel

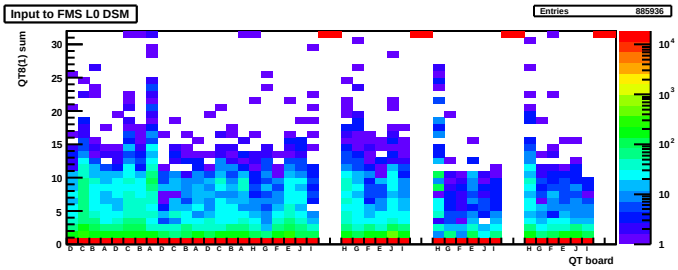

Input to QT2 crate
Entries 195889
ADC
channel

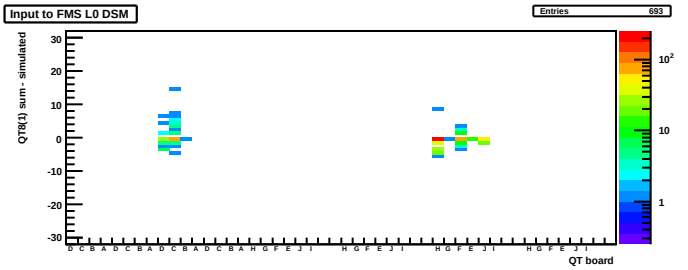

Input to QT3 crate
Entries 162285
ADC
channel

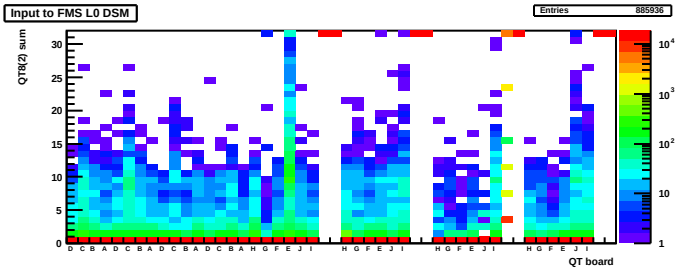

Input to QT4 crate
Entries 203428
ADC
channel

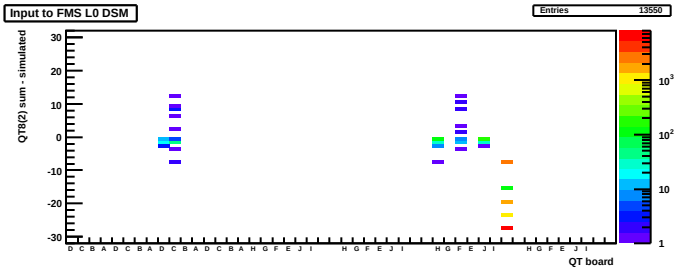

Input to FMS L0 DSM
Entries 885936
QT8(0) sum
QT board

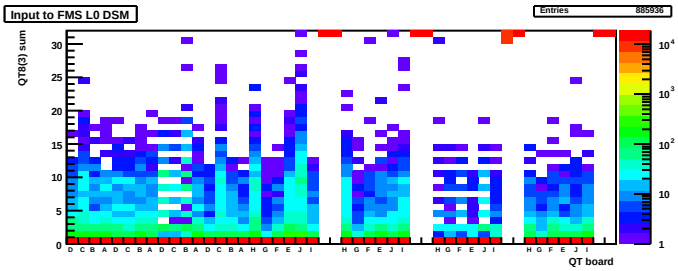

Input to FMS L0 DSM
Entries 399
QT8(0) sum - simulated
QT board

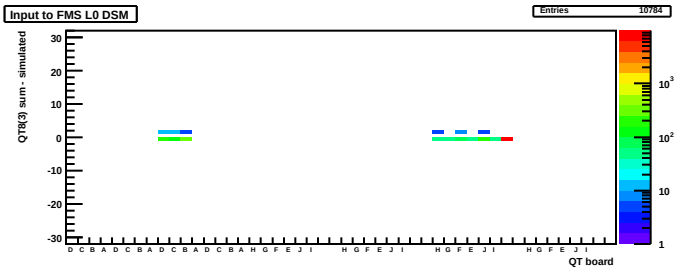

Input to FMS L0 DSM
Entries 885936
QT8(1) sum
QT board

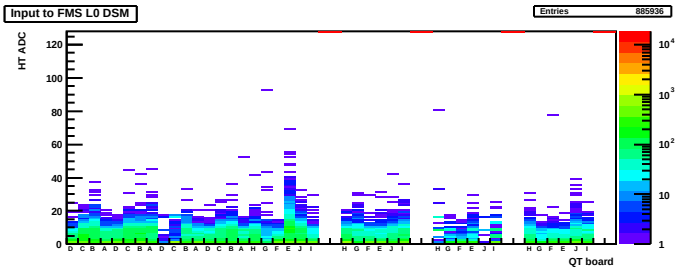

Input to FMS L0 DSM
Entries 693
QT8(1) sum - simulated
QT board

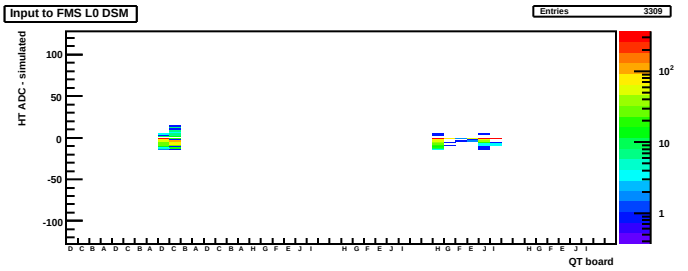

Input to FMS L0 DSM
Entries 885936
QT8(2) sum
QT board

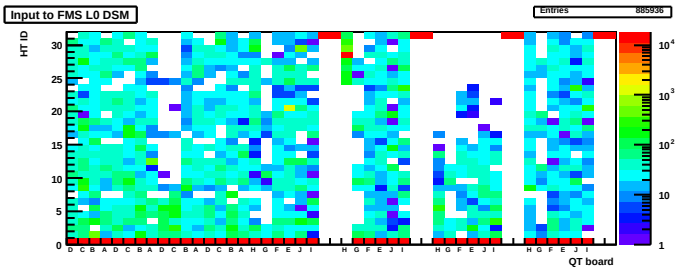

Input to FMS L0 DSM
Entries 13550
QT8(2) sum - simulated
QT board

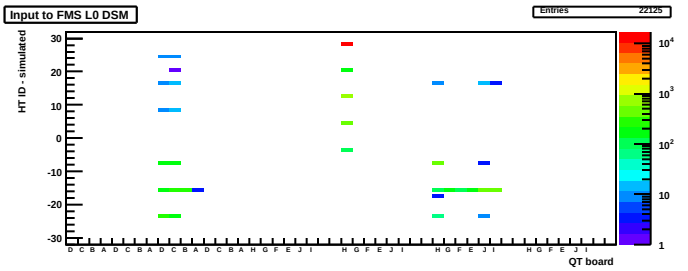

Input to FMS L0 DSM
Entries 885936
QT8(3) sum
QT board

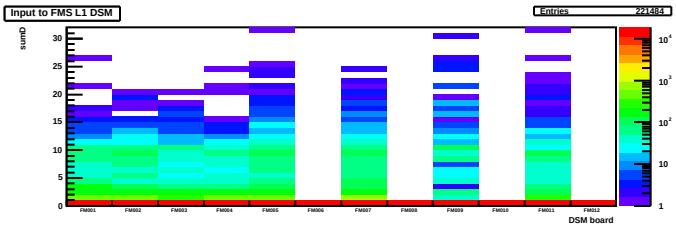

Input to FMS L0 DSM
Entries 10784
QT8(3) sum - simulated
QT board

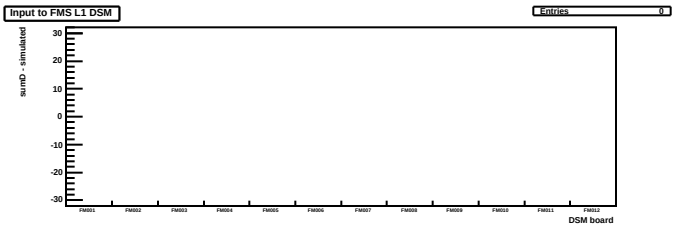

Input to FMS L0 DSM
Entries 885936
HT ADC
QT board

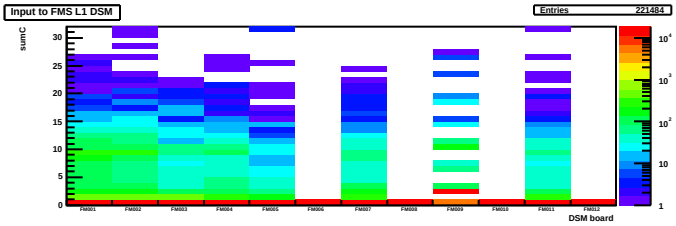

Input to FMS L0 DSM
Entries 3309
HT ADC - simulated
QT board

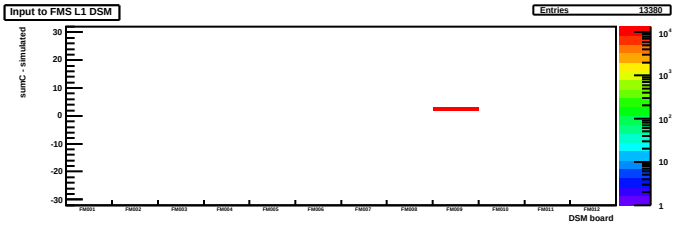

Input to FMS L0 DSM
Entries 885936
HT ID
QT board

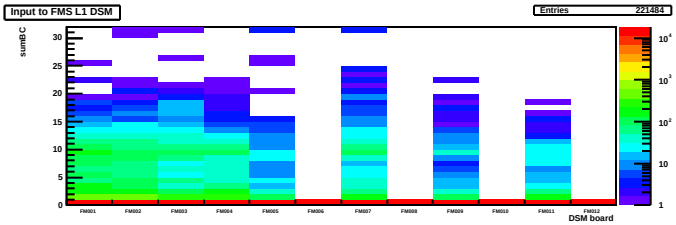

Input to FMS L0 DSM
Entries 22125
HT ID - simulated
QT board

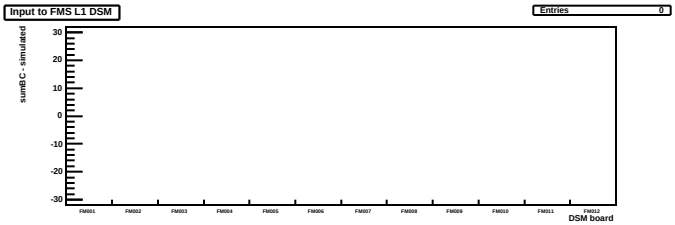

Input to FMS L1 DSM
Entries 221484
sumD
DSM board

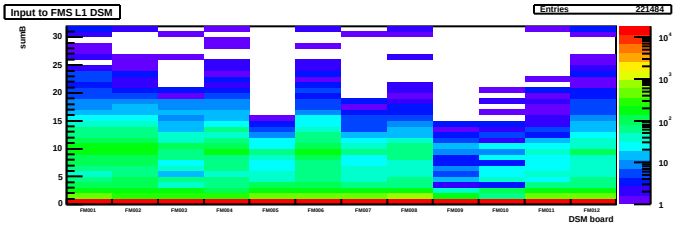

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

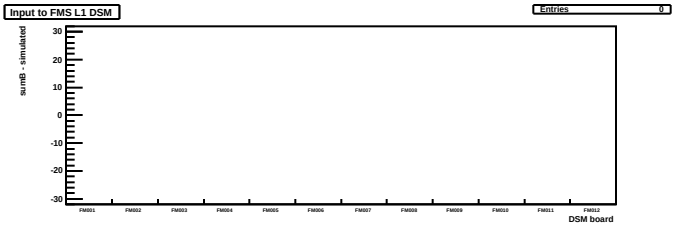

Input to FMS L1 DSM
Entries 221484
sumC
DSM board

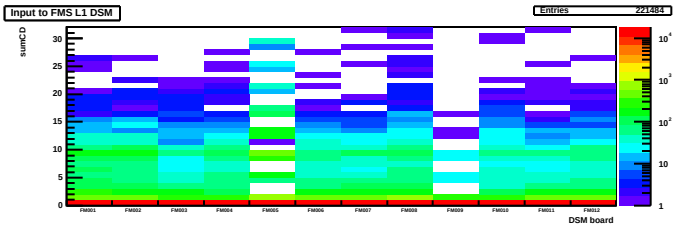

Input to FMS L1 DSM
Entries 13380
sumC - simulated
DSM board

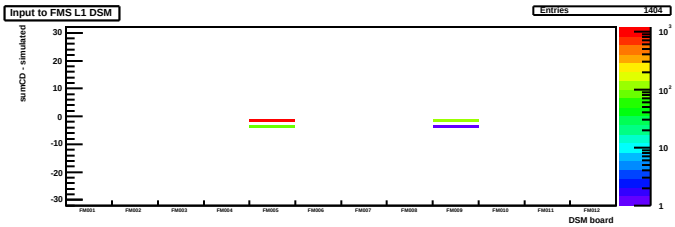

Input to FMS L1 DSM
Entries 221484
sumBC
DSM board

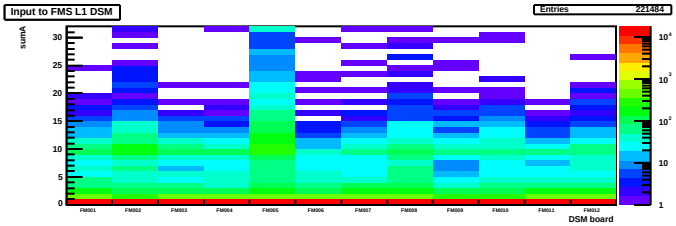

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

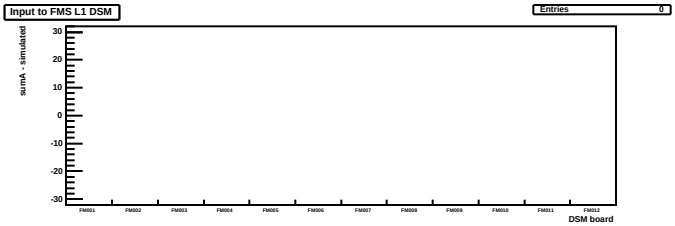

Input to FMS L1 DSM
Entries 221484
sumB
DSM board

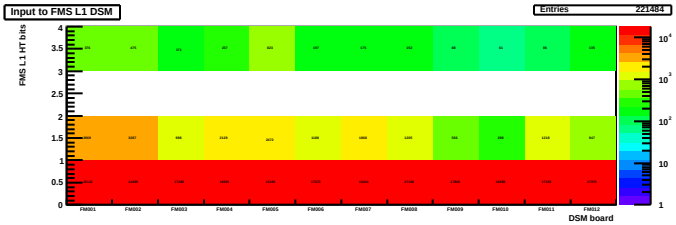

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

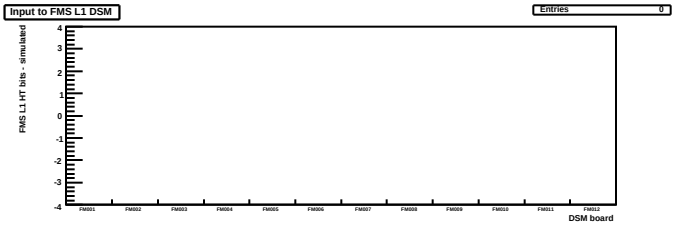

Input to FMS L1 DSM
Entries 221484
sumCD
DSM board

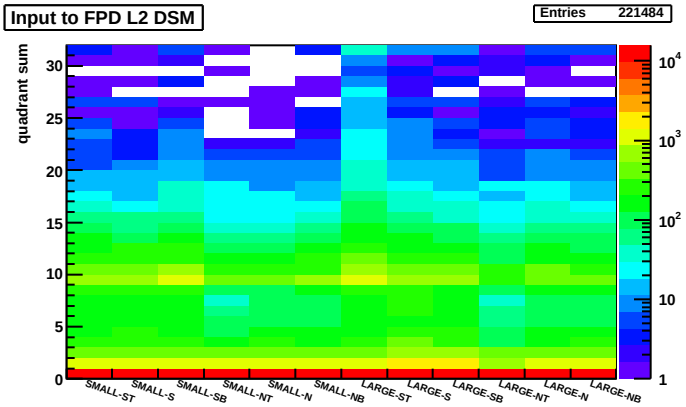

Input to FMS L1 DSM
Entries 1404
sumCD - simulated
DSM board

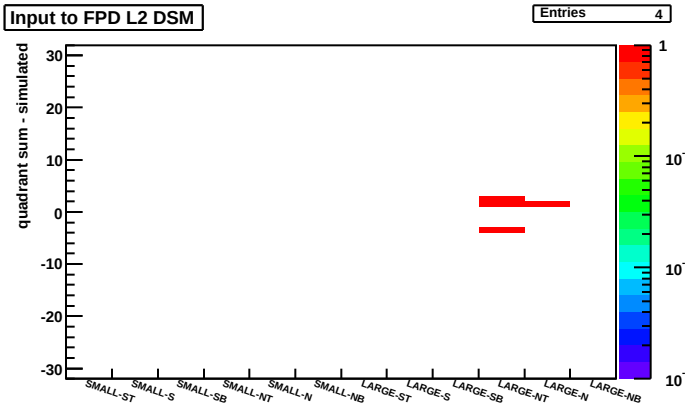

Input to FMS L1 DSM
Entries 221484
sumA
DSM board

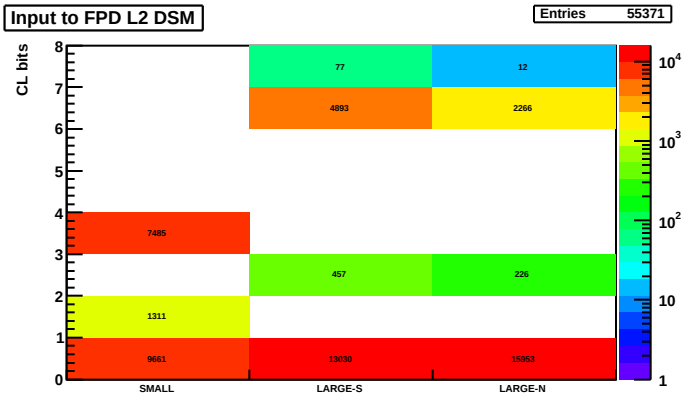

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

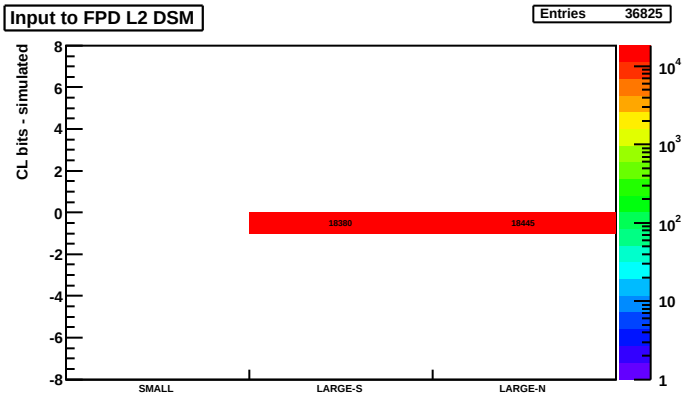

Input to FMS L1 DSM
Entries 221484
FMS L1 HT bits
DSM board

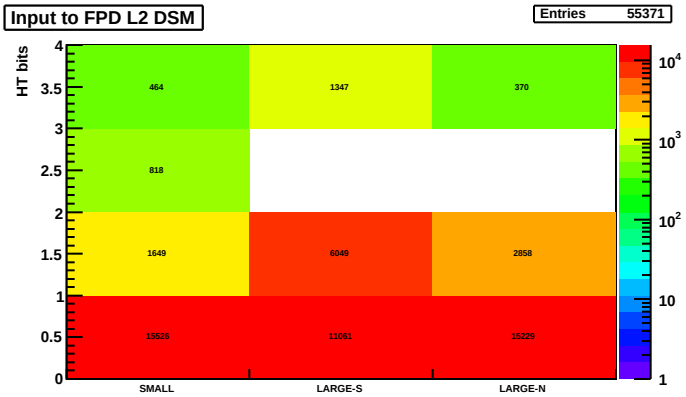

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

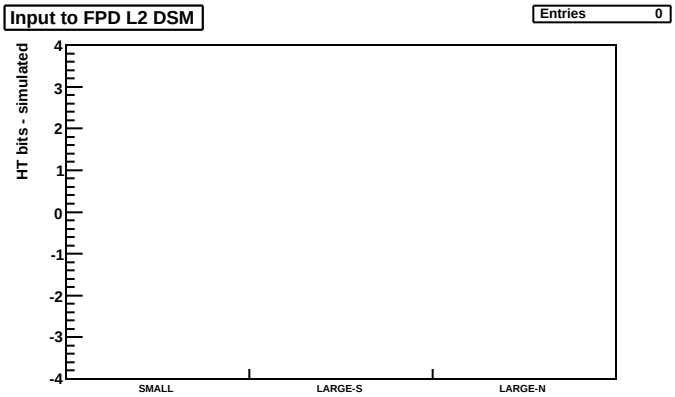

Input to FPD L2 DSM
Entries 221484
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

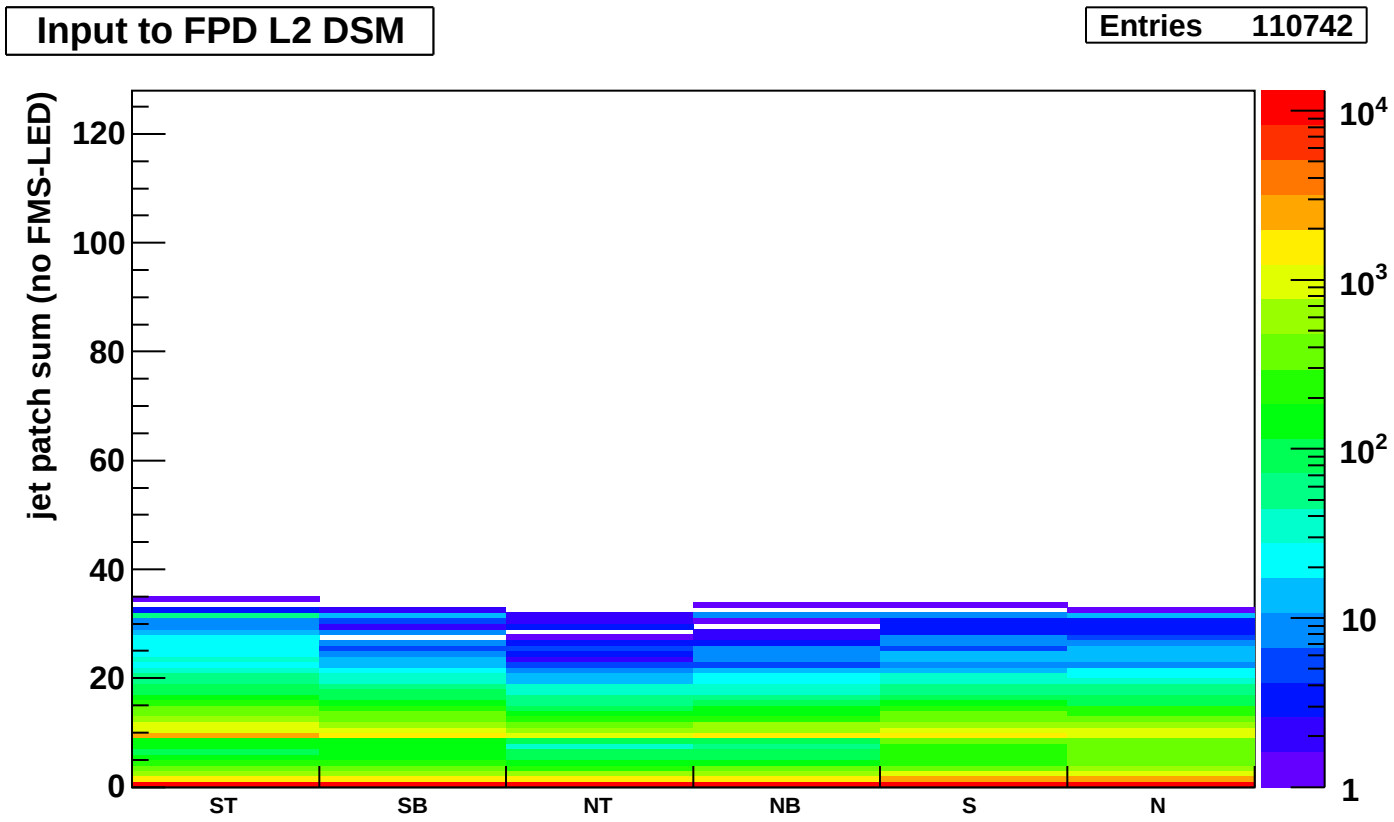

Input to FPD L2 DSM
Entries 4
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

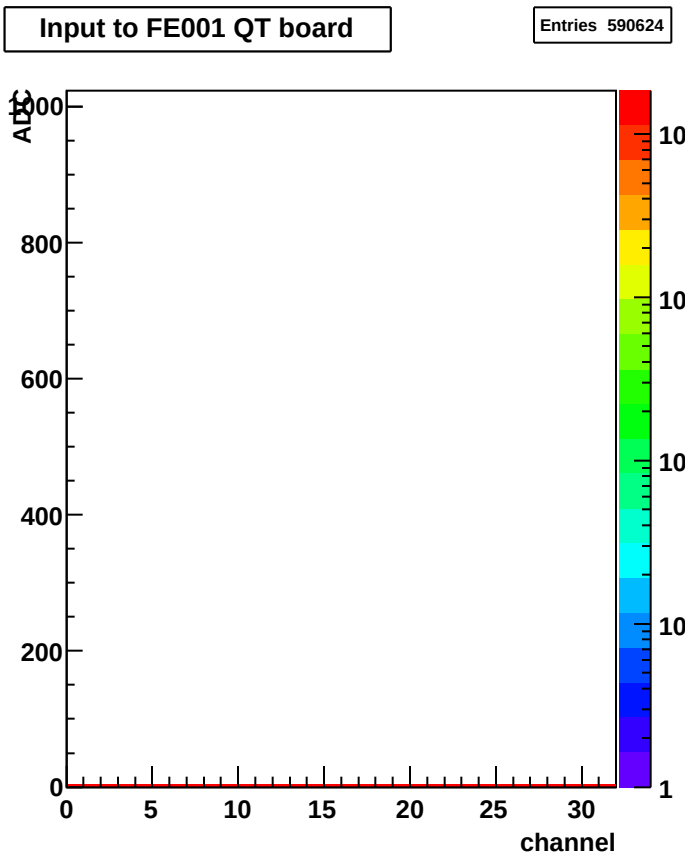

Input to FPD L2 DSM
Entries 55371
CL bits
77
12
4893
2266
7485
457
226
1311
9661
13030
15953
SMALL
LARGE-S
LARGE-N

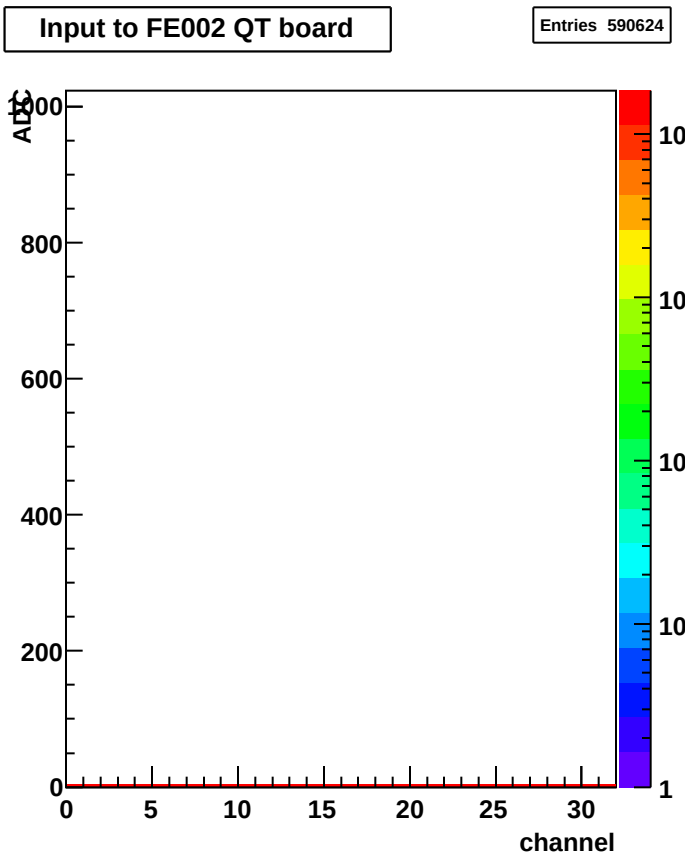

Input to FPD L2 DSM
Entries 36825
CL bits - simulated
18380
18445
SMALL
LARGE-S
LARGE-N

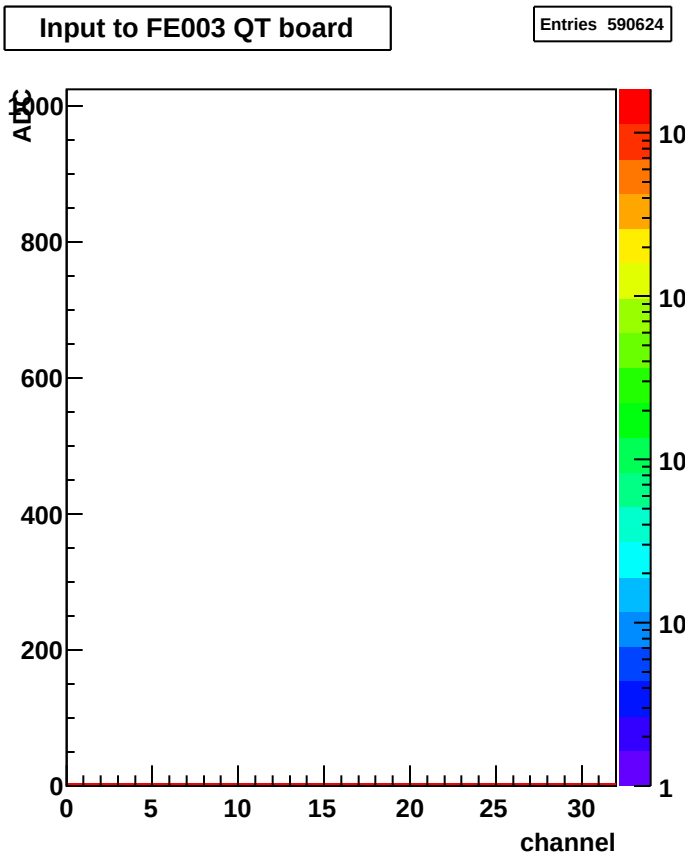

Input to FPD L2 DSM
Entries 55371
HT bits
464
1347
370
818
1649
6049
2858
15526
11061
15229
SMALL
LARGE-S
LARGE-N

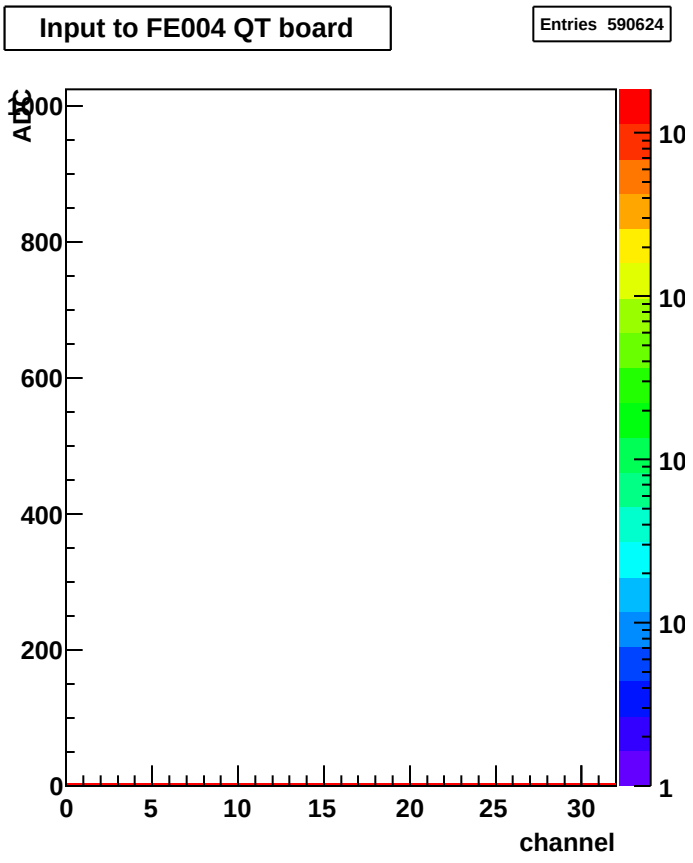

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

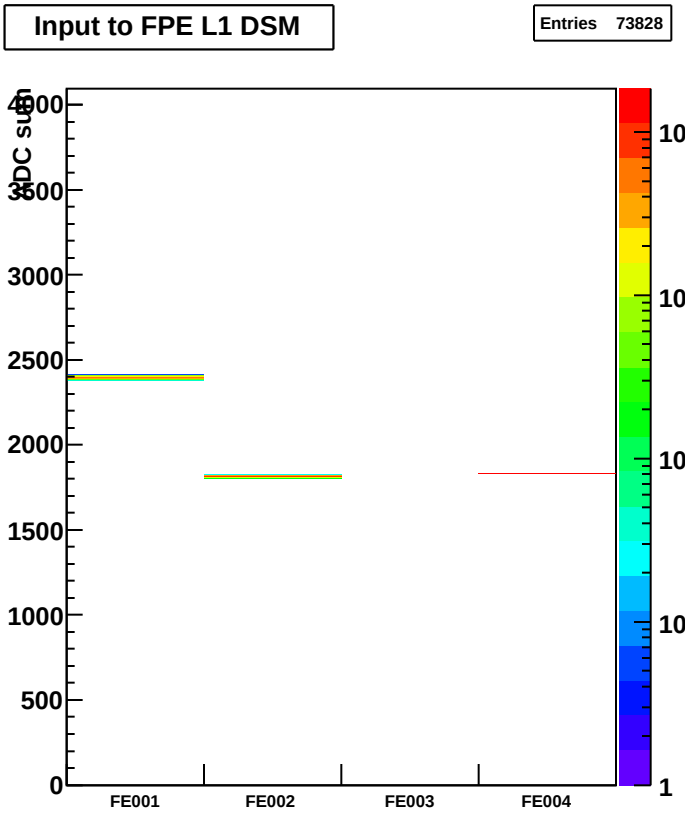

Input to FPD L2 DSM
Entries 110742
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

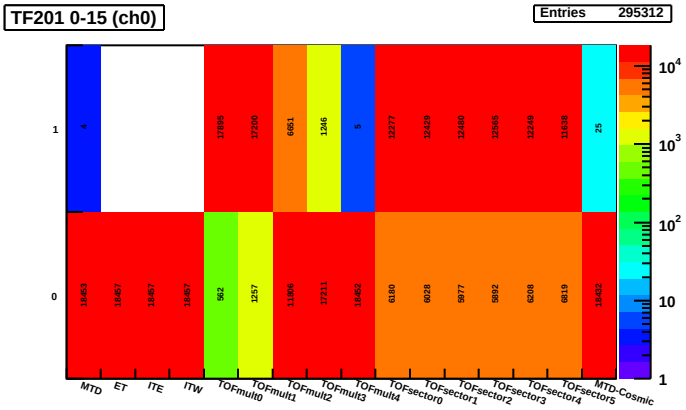

Input to FE001 QT board
Entries 590624
ADC
channel

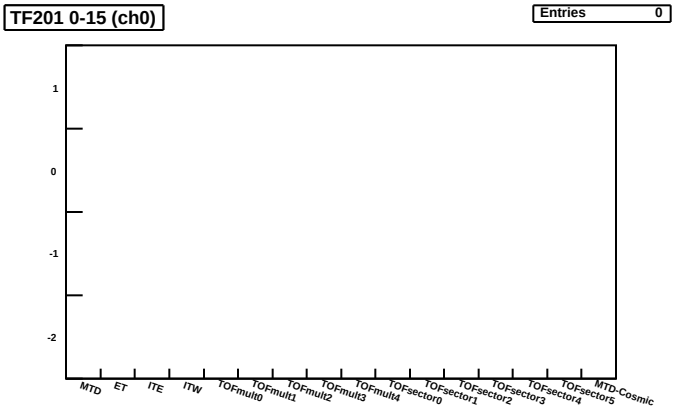

Input to FE002 QT board
Entries 590624
ADC
channel

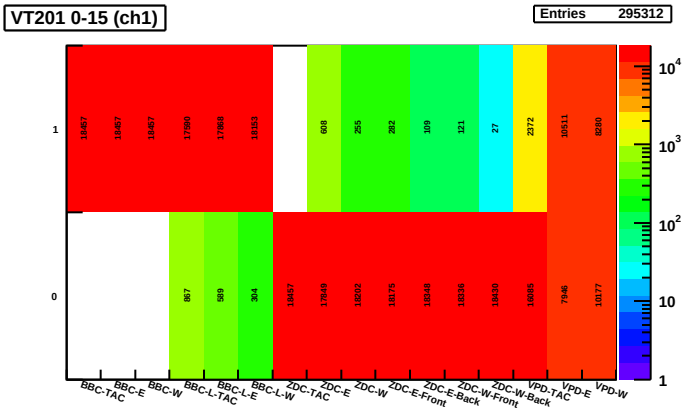

Input to FE003 QT board
Entries 590624
ADC
channel

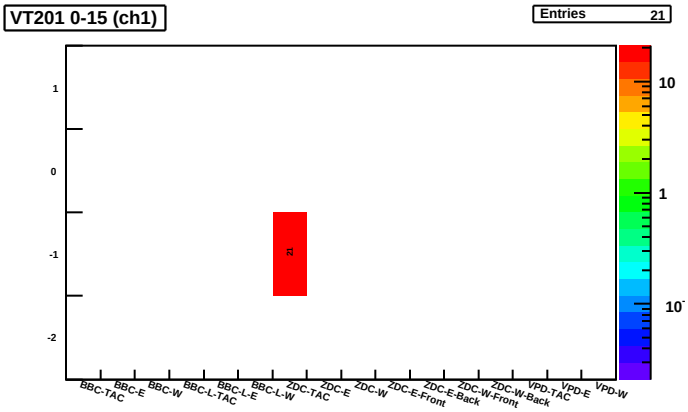

Input to FE004 QT board
Entries 590624
ADC
channel

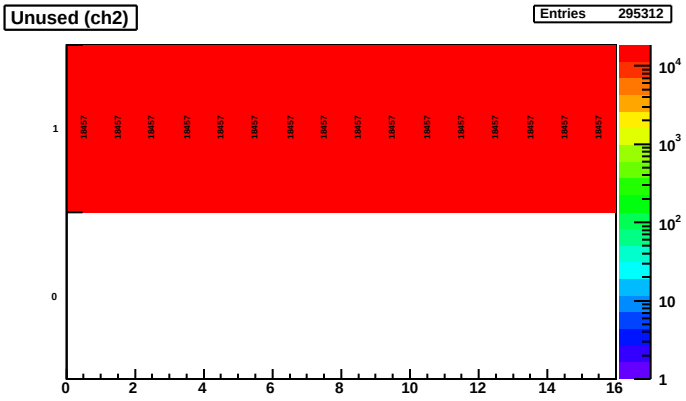

Input to FPE L1 DSM
Entries 73828
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

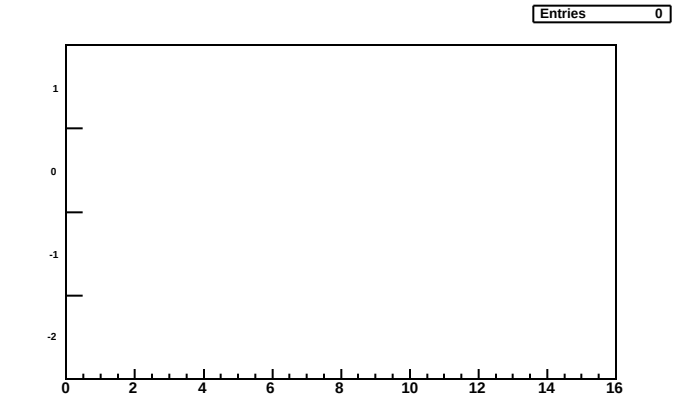

TF201 0-15 (ch0)
Entries 295312
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

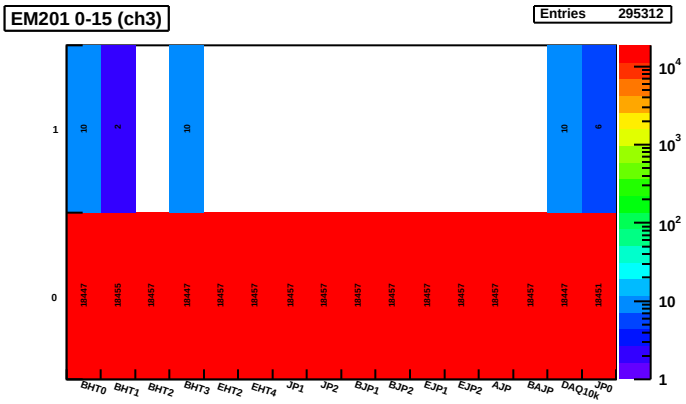

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

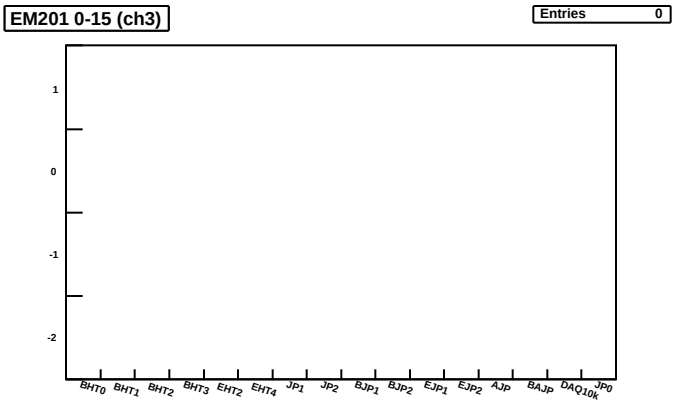

VT201 0-15 (ch1)
Entries 295312
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 21
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 295312


Entries 0


EM201 0-15 (ch3)
Entries 295312
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 0
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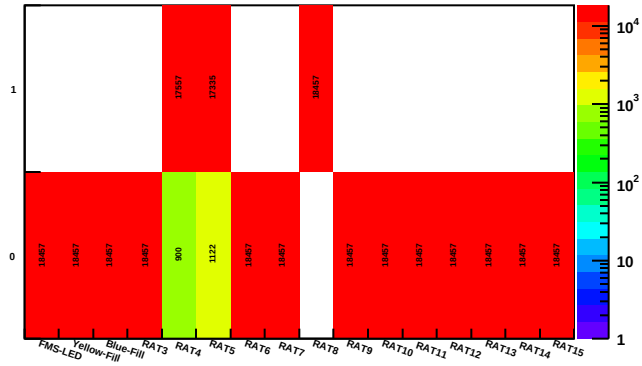
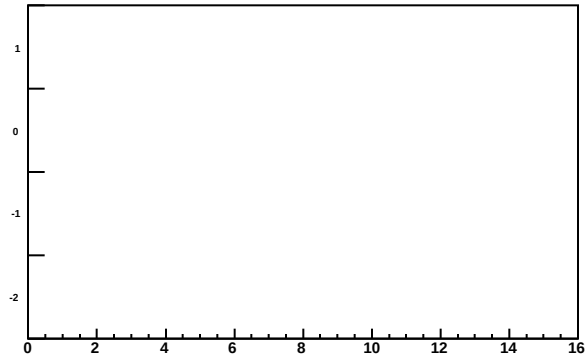
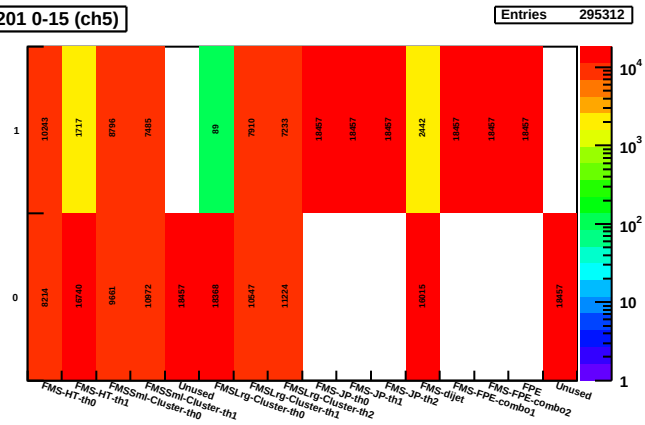
RAT board (ch4)





FP201 0-15 (ch5)



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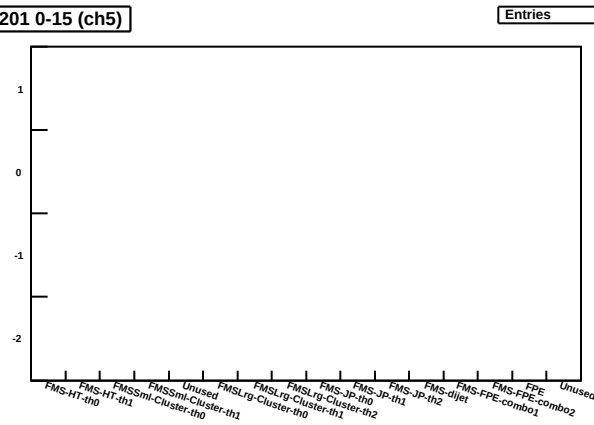
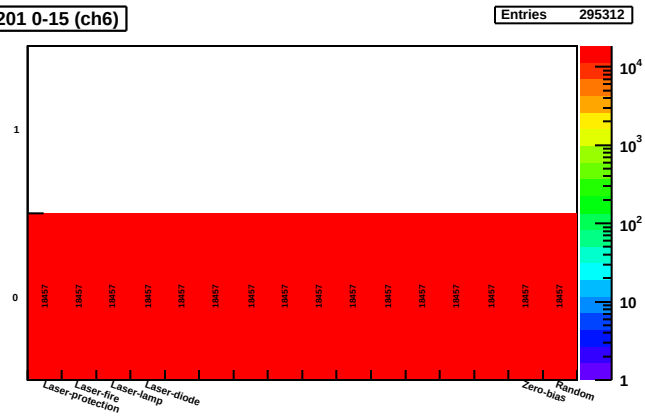
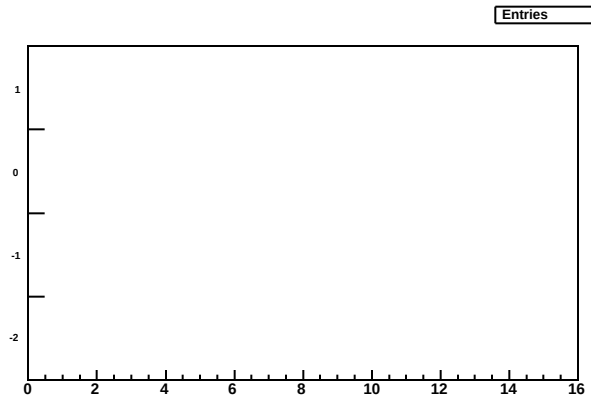
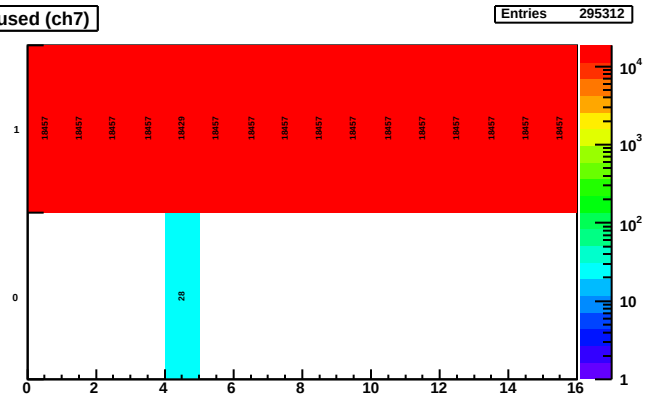
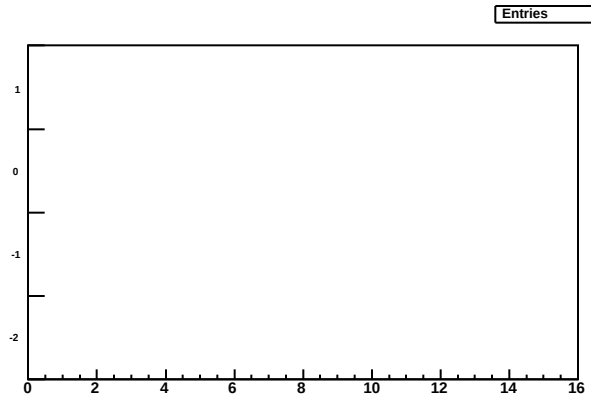


ST201 0-15 (ch6)





Unused (ch7)