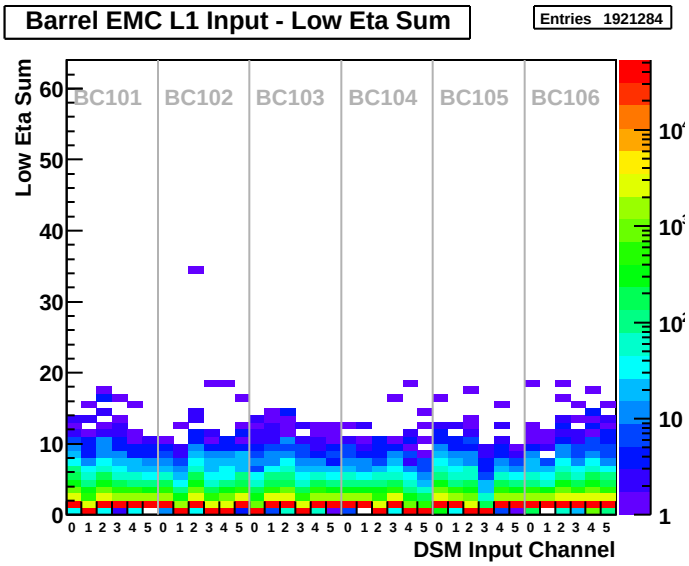
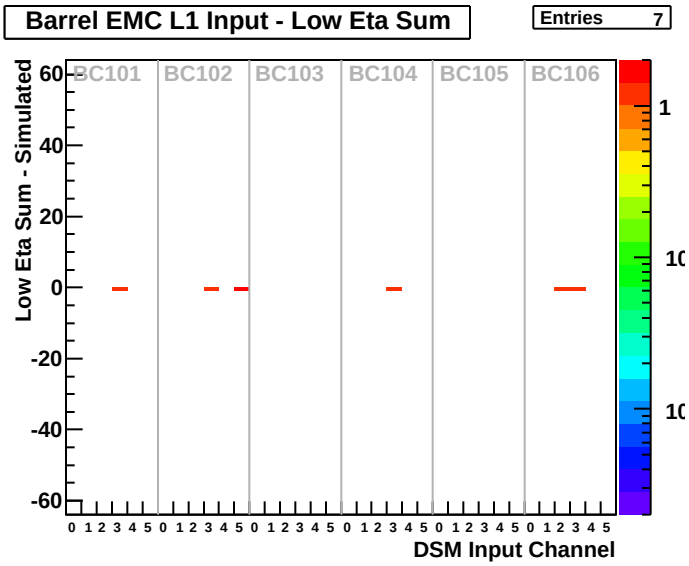
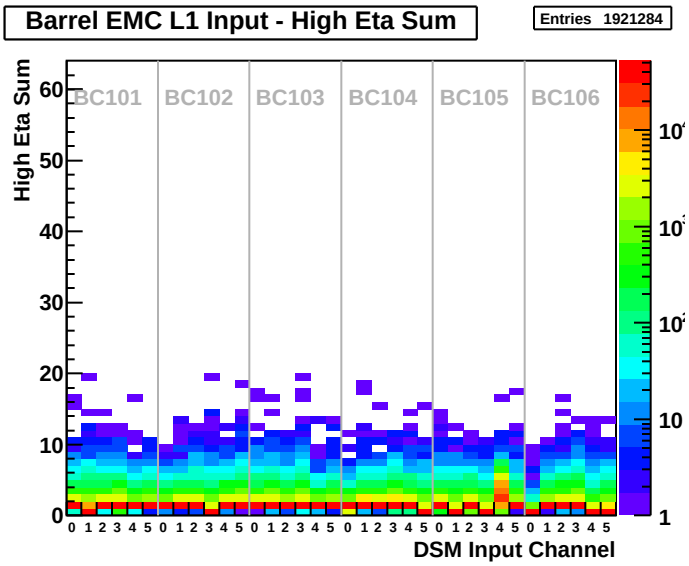
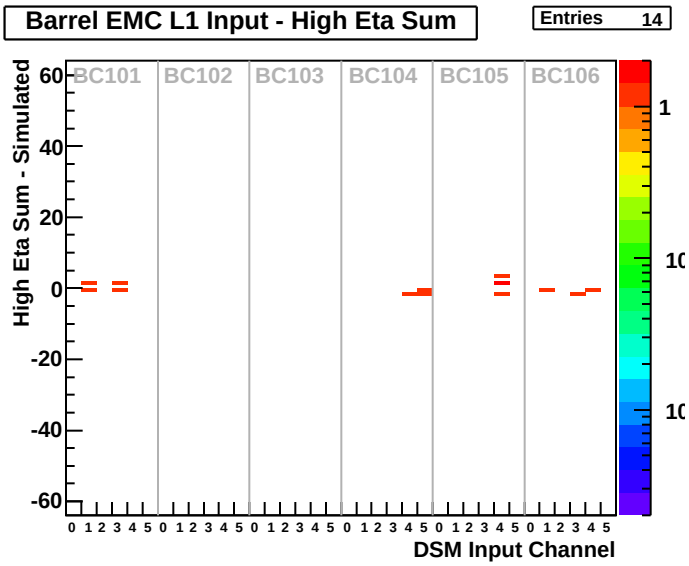
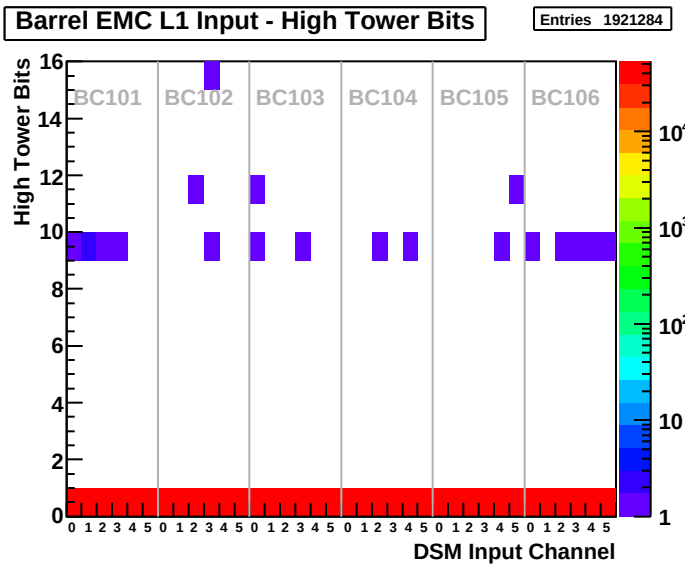
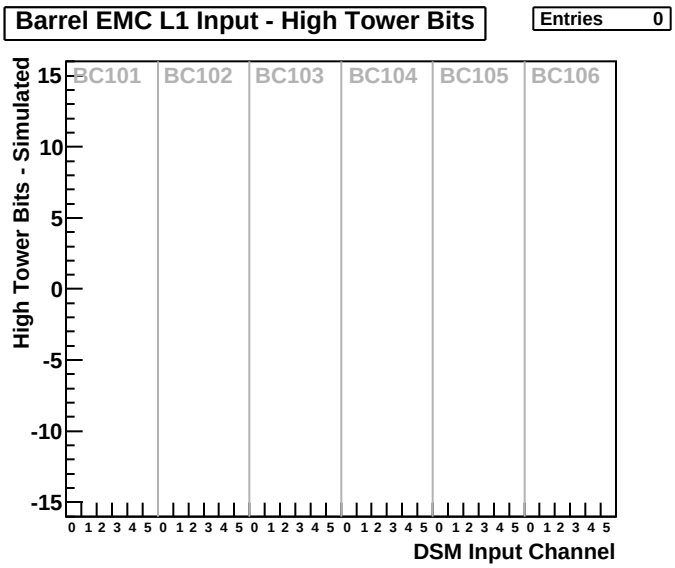

Barrel EMC L1 Input - Low Eta Sum
Entries 1921284
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

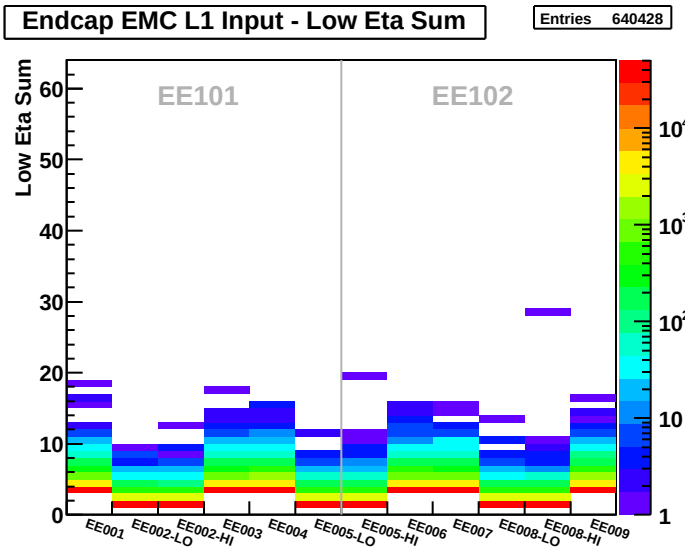

Barrel EMC L1 Input - Low Eta Sum
Entries 7
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

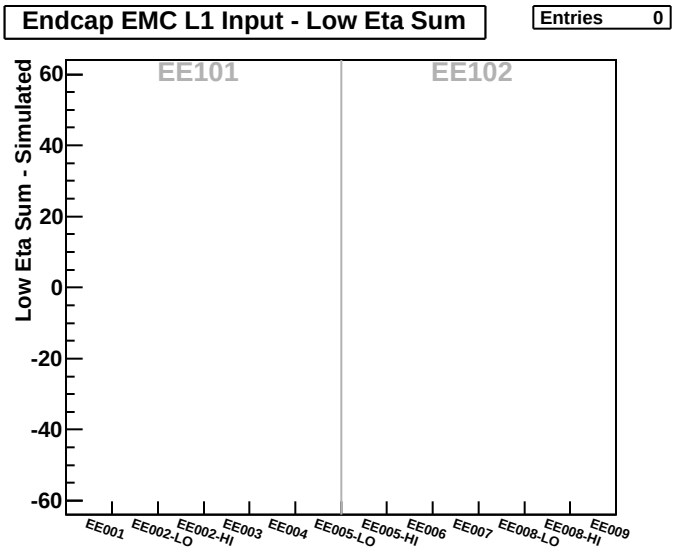

Barrel EMC L1 Input - High Eta Sum
Entries 1921284
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

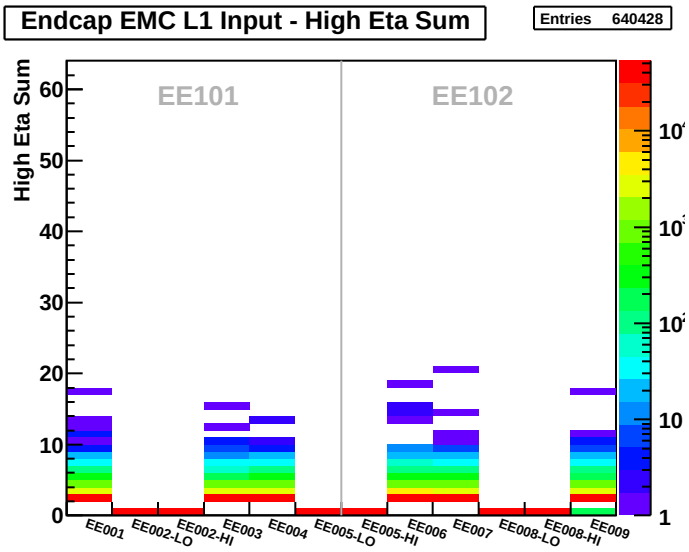

Barrel EMC L1 Input - High Eta Sum
Entries 14
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

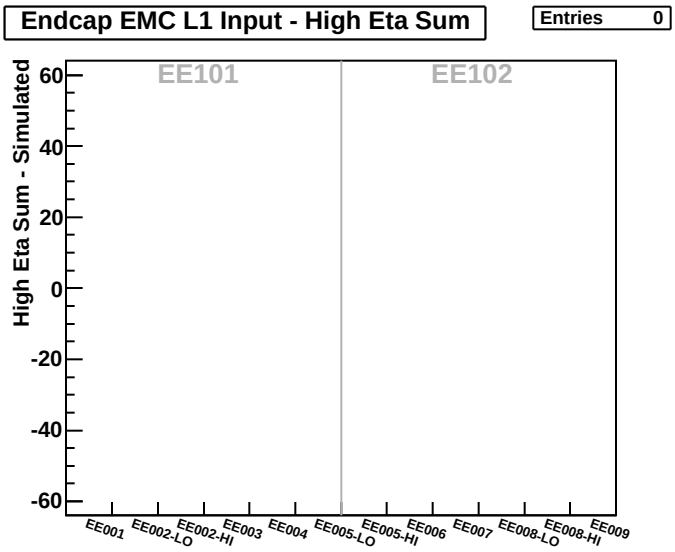

Barrel EMC L1 Input - High Tower Bits
Entries 1921284
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

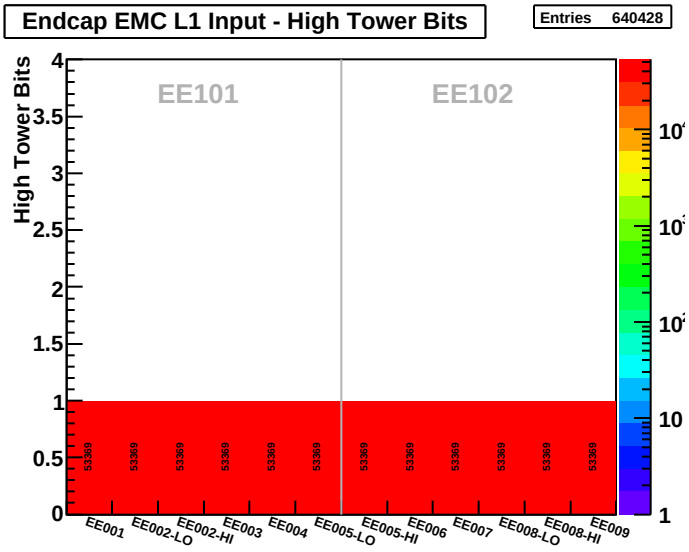

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

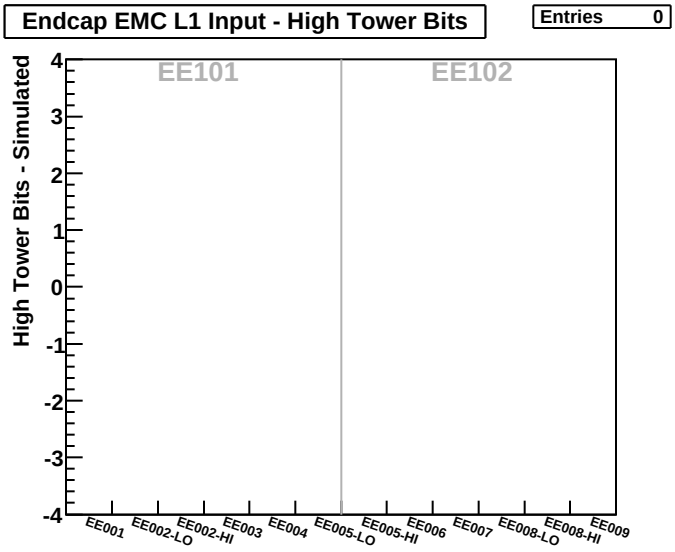

Endcap EMC L1 Input - Low Eta Sum
Entries 640428
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

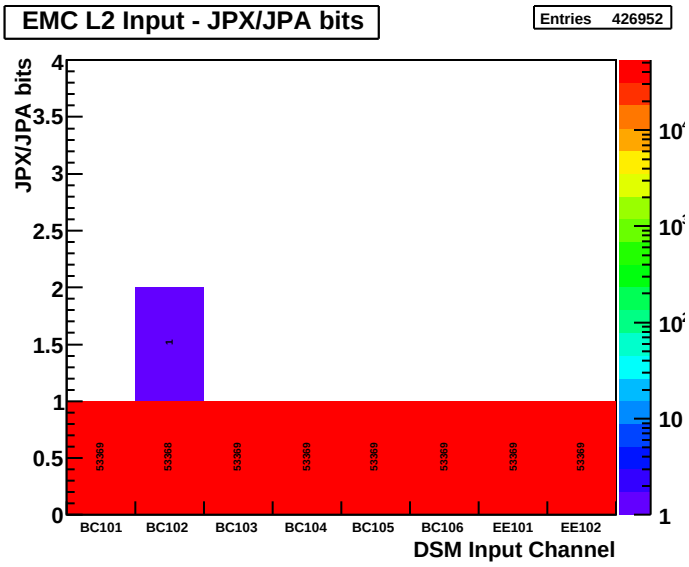

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

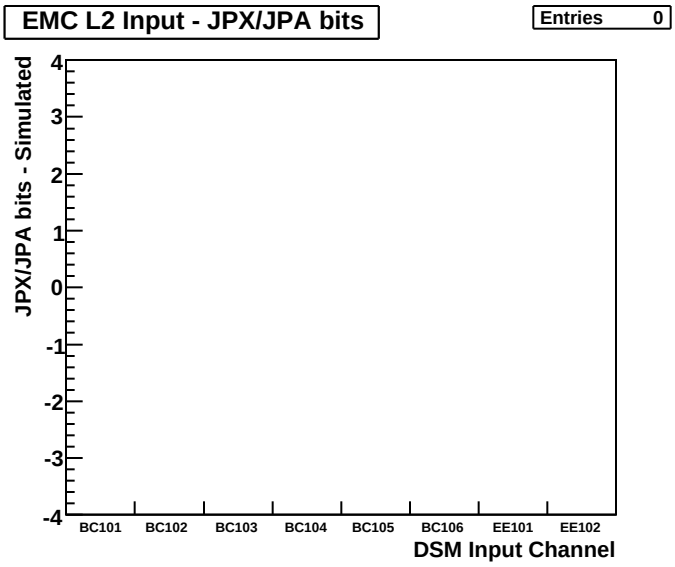

Endcap EMC L1 Input - High Eta Sum
Entries 640428
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

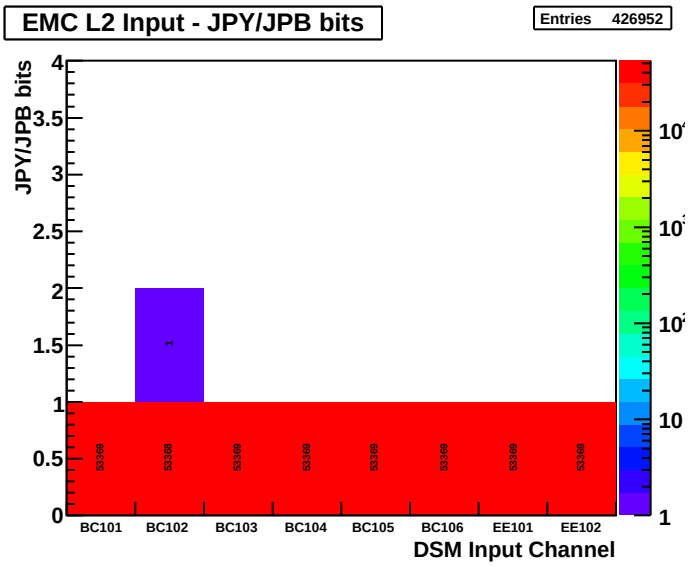

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

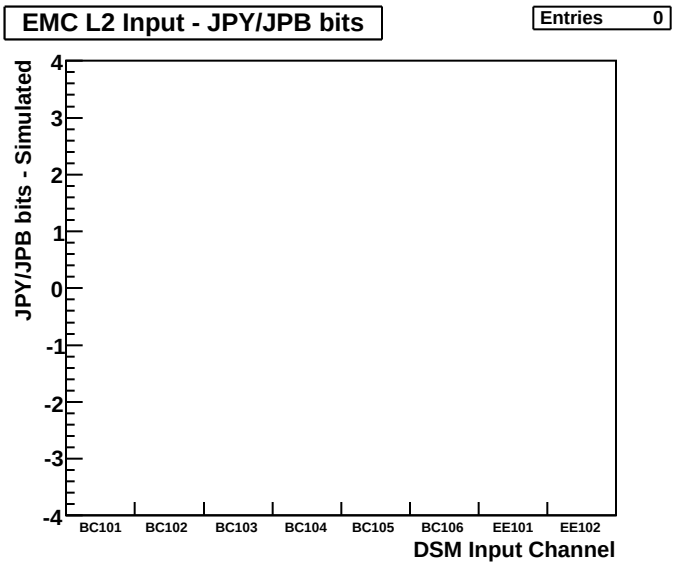

Endcap EMC L1 Input - High Tower Bits
Entries 640428
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

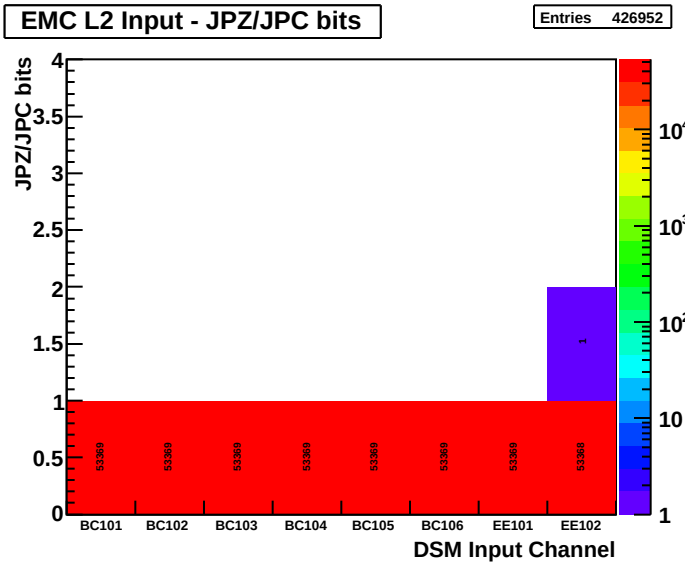

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

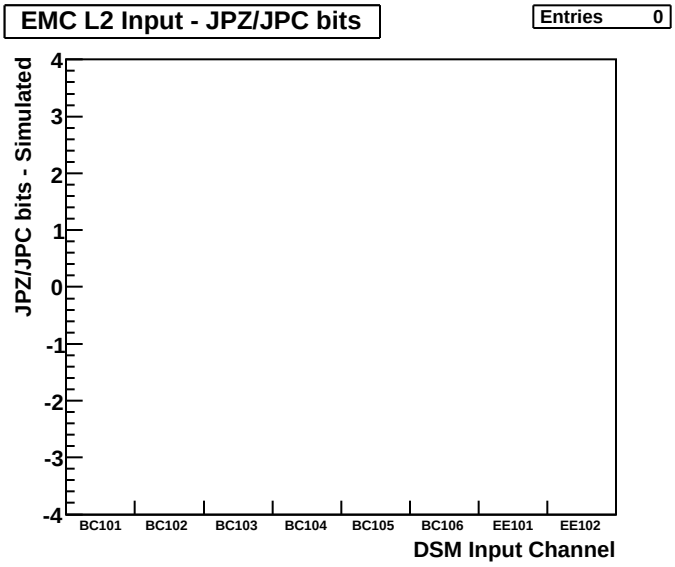

EMC L2 Input - JPX/JPA bits
Entries 426952
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

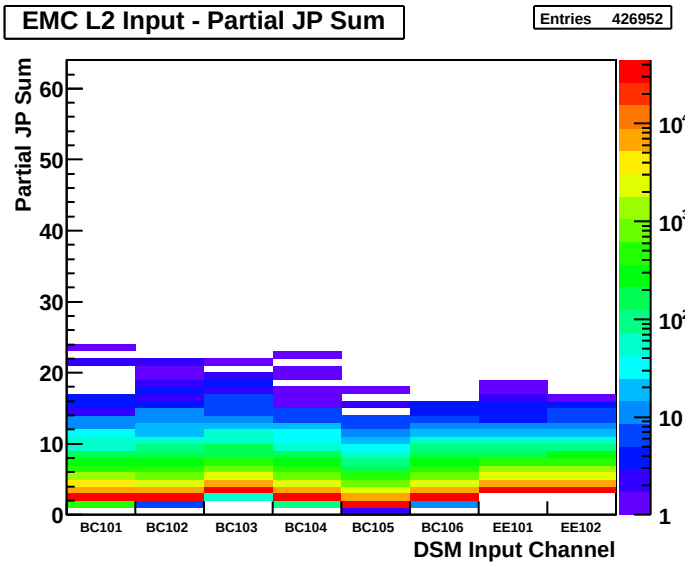

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

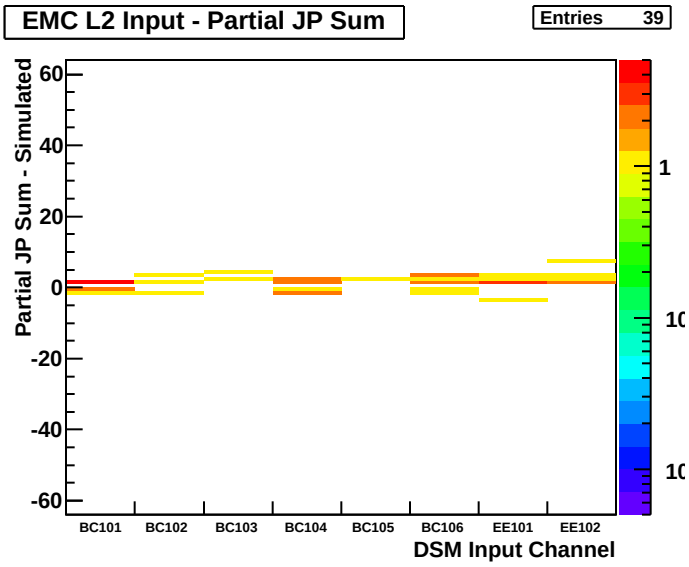

EMC L2 Input - JPY/JPB bits
Entries 426952
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

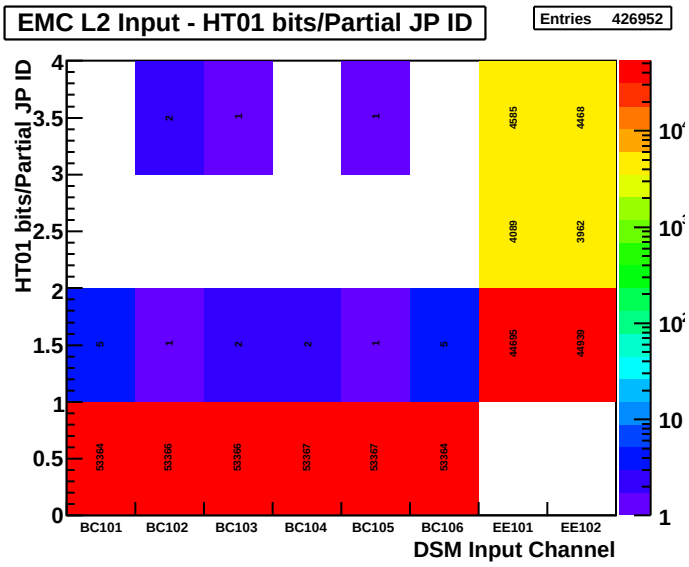

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

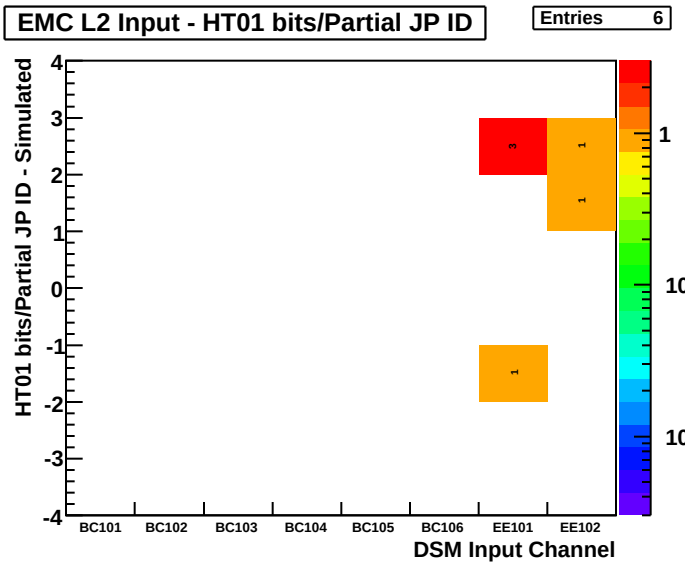

EMC L2 Input - JPZ/JPC bits
Entries 426952
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

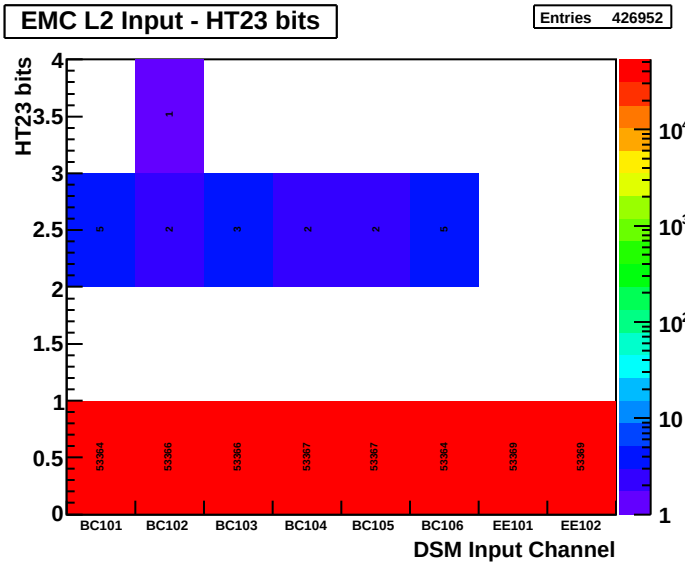

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

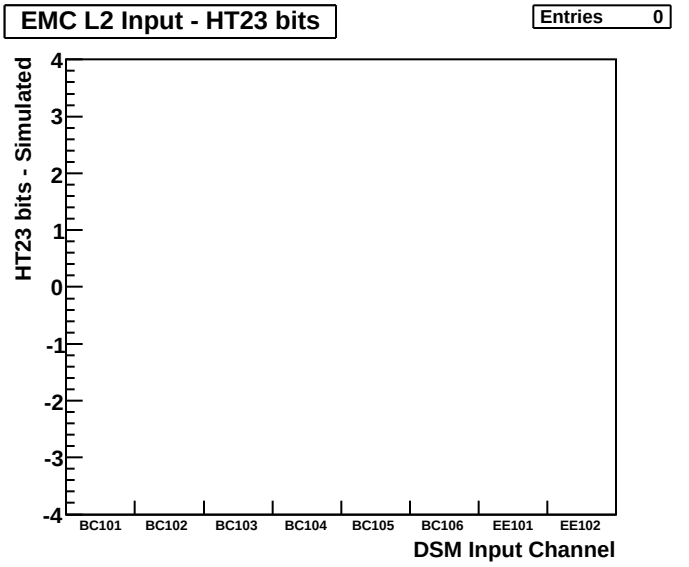

EMC L2 Input - Partial JP Sum
Entries 426952
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

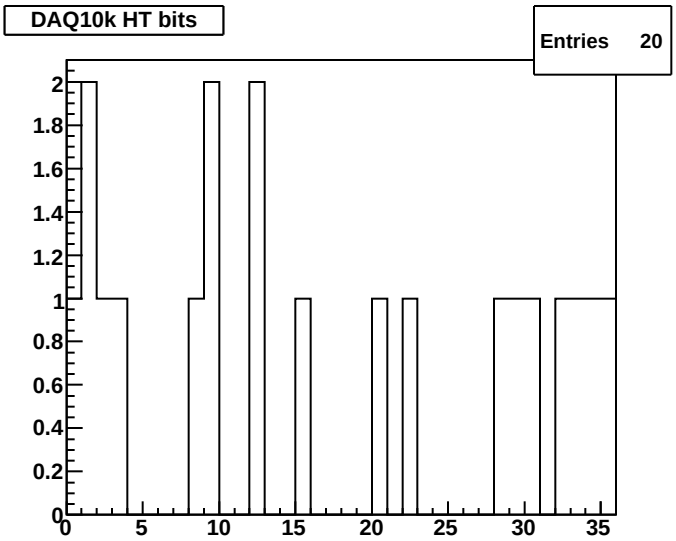

EMC L2 Input - Partial JP Sum
Entries 39
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

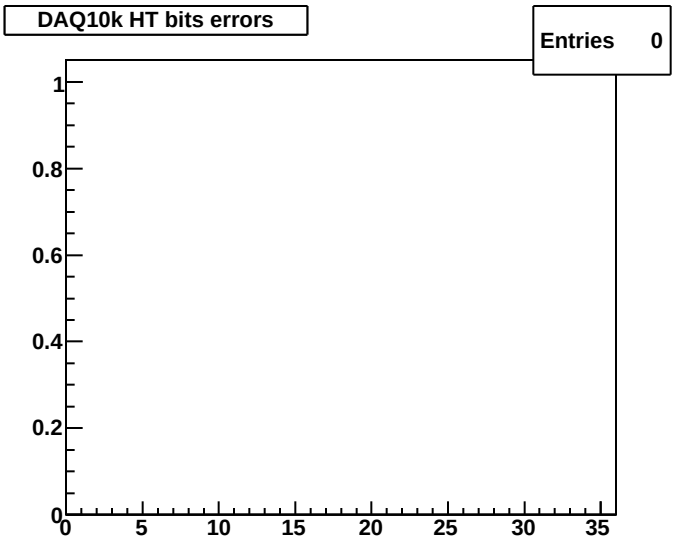

EMC L2 Input - HT01 bits/Partial JP ID
Entries 426952
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

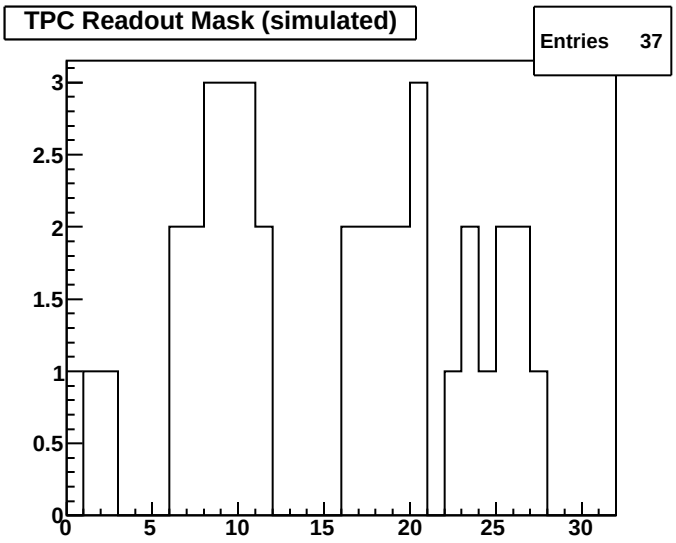

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

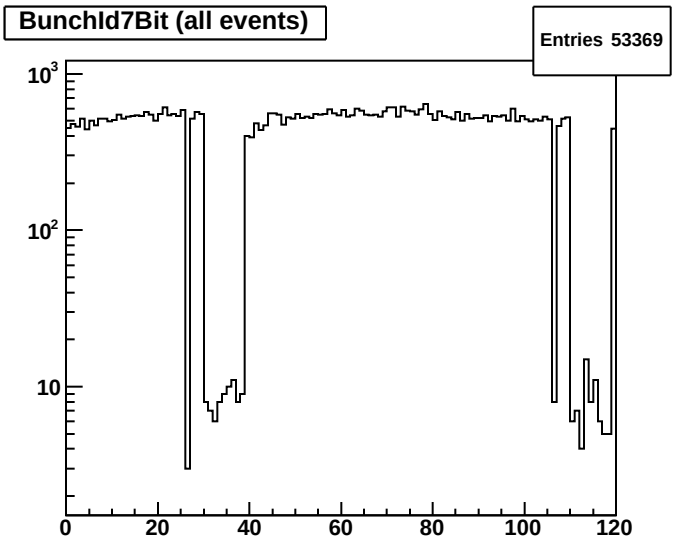

EMC L2 Input - HT23 bits
Entries 426952
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

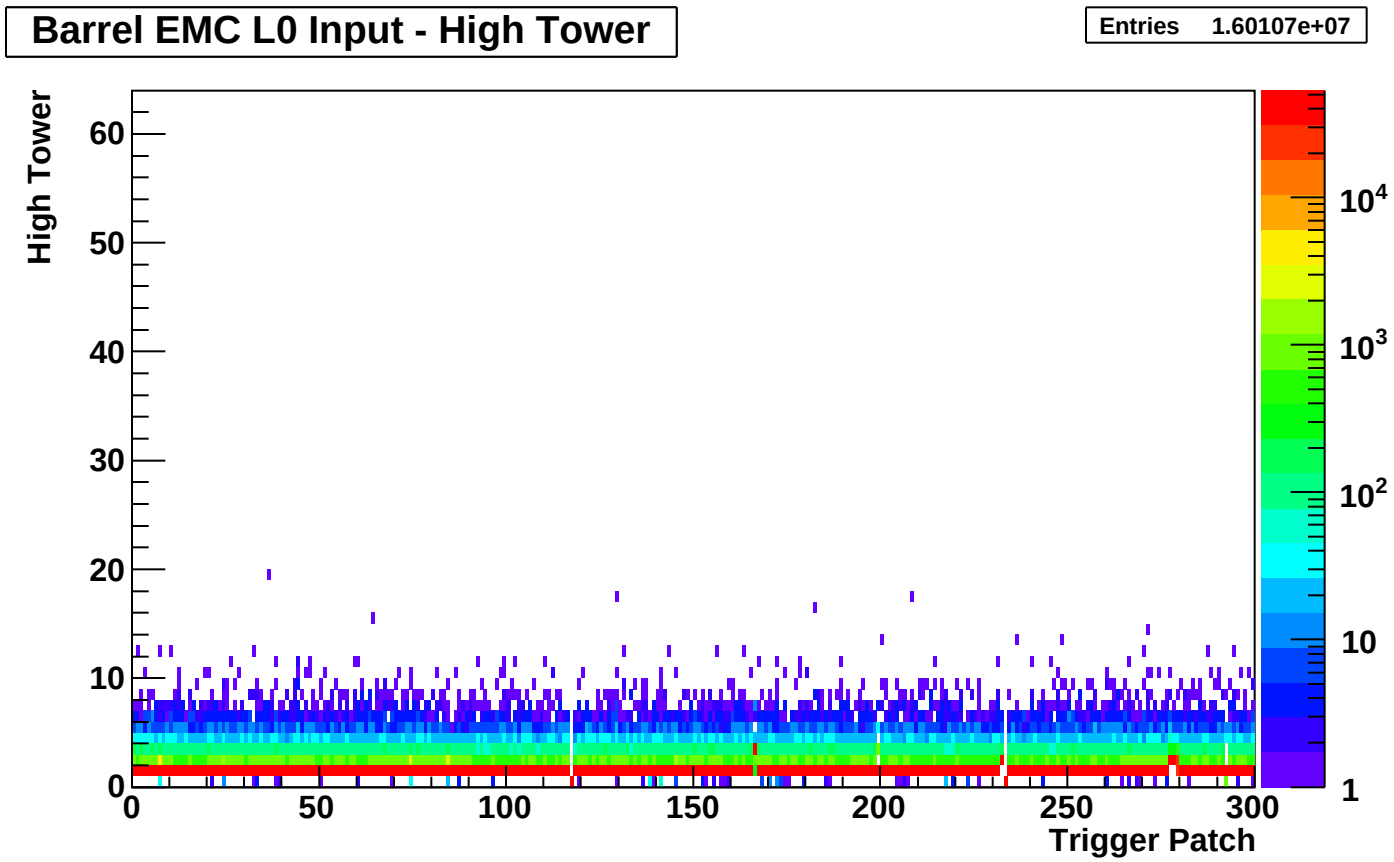

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

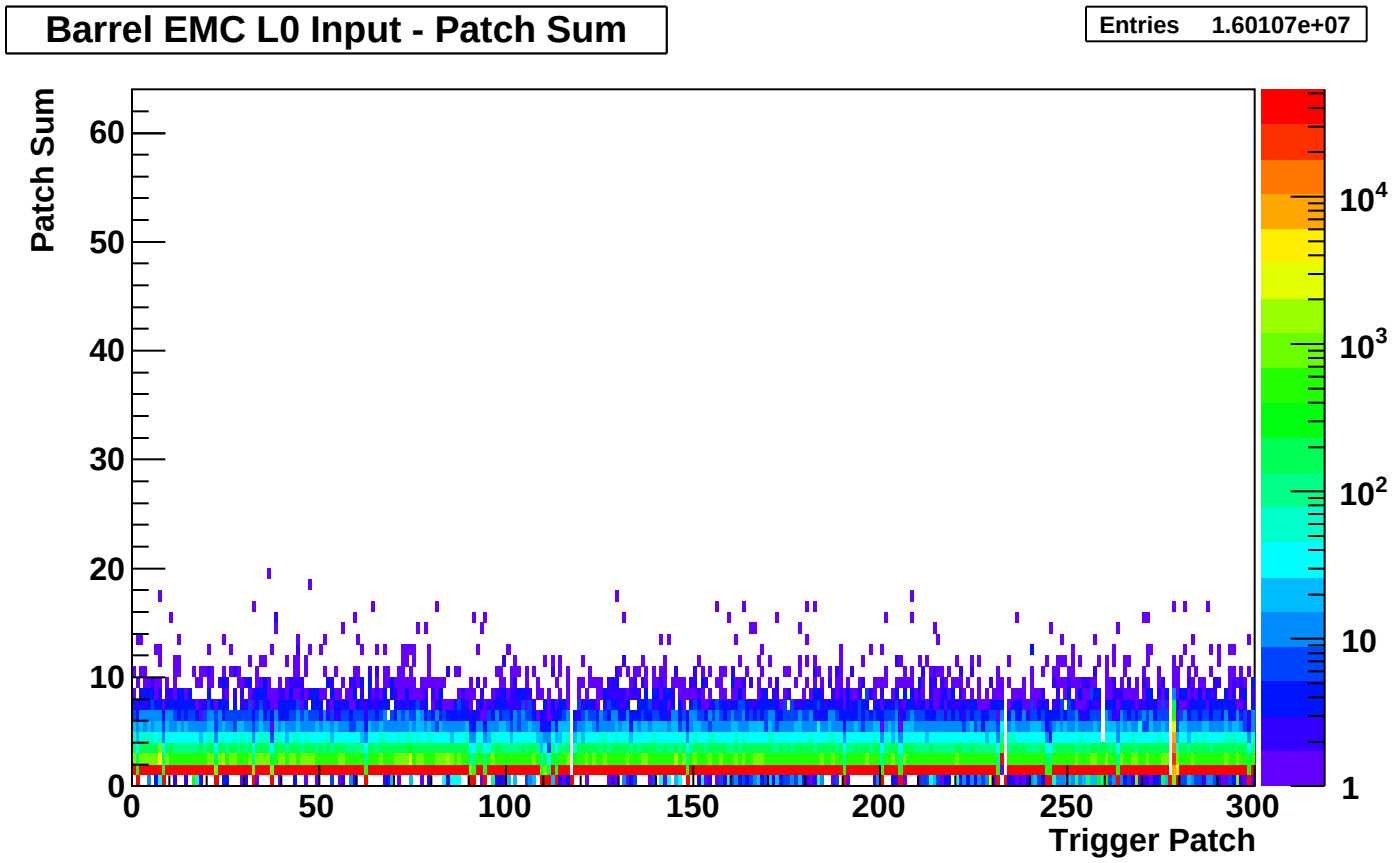

DAQ10k HT bits
Entries 20

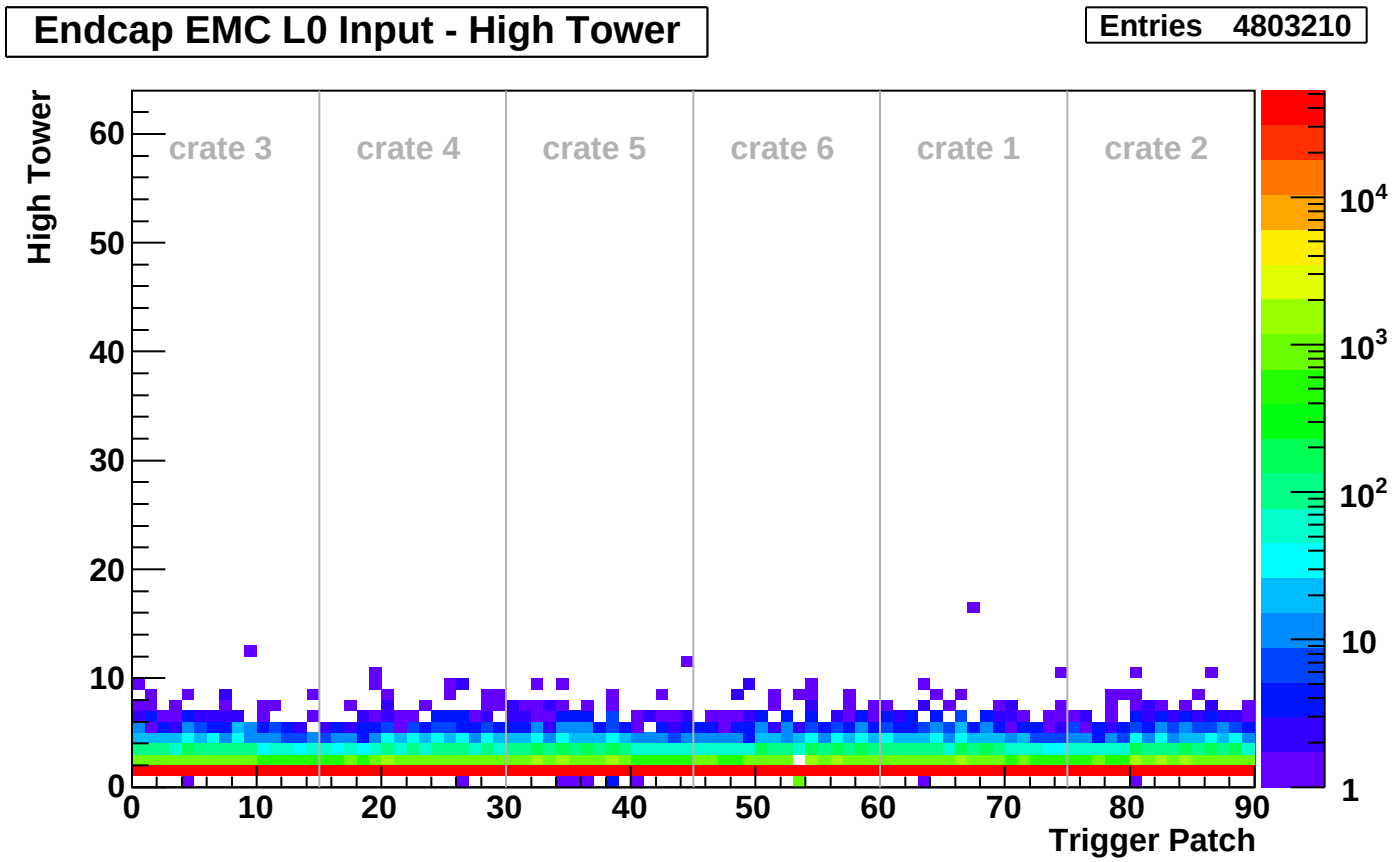

DAQ10k HT bits errors
Entries 0

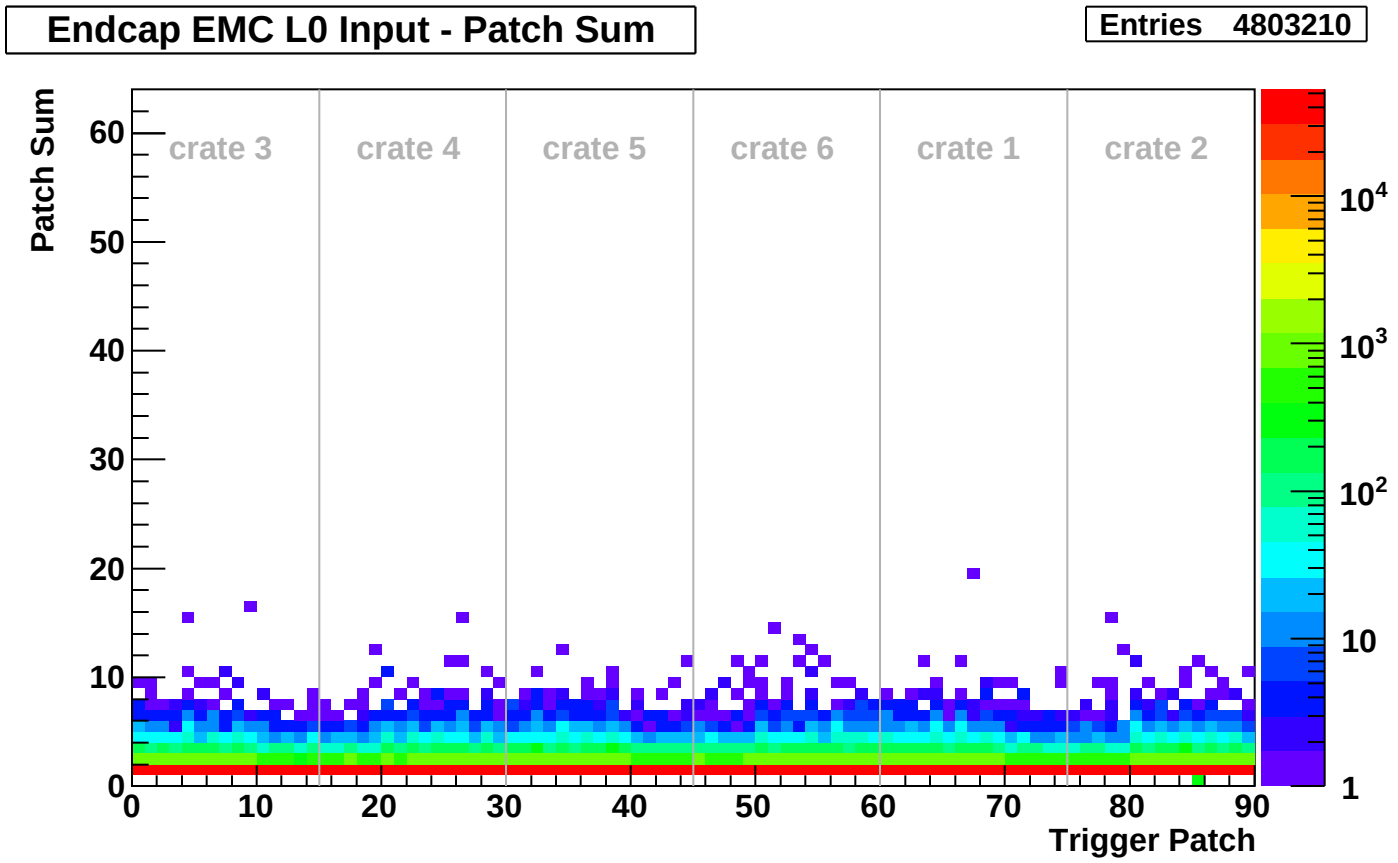

TPC Readout Mask (simulated)
Entries 37

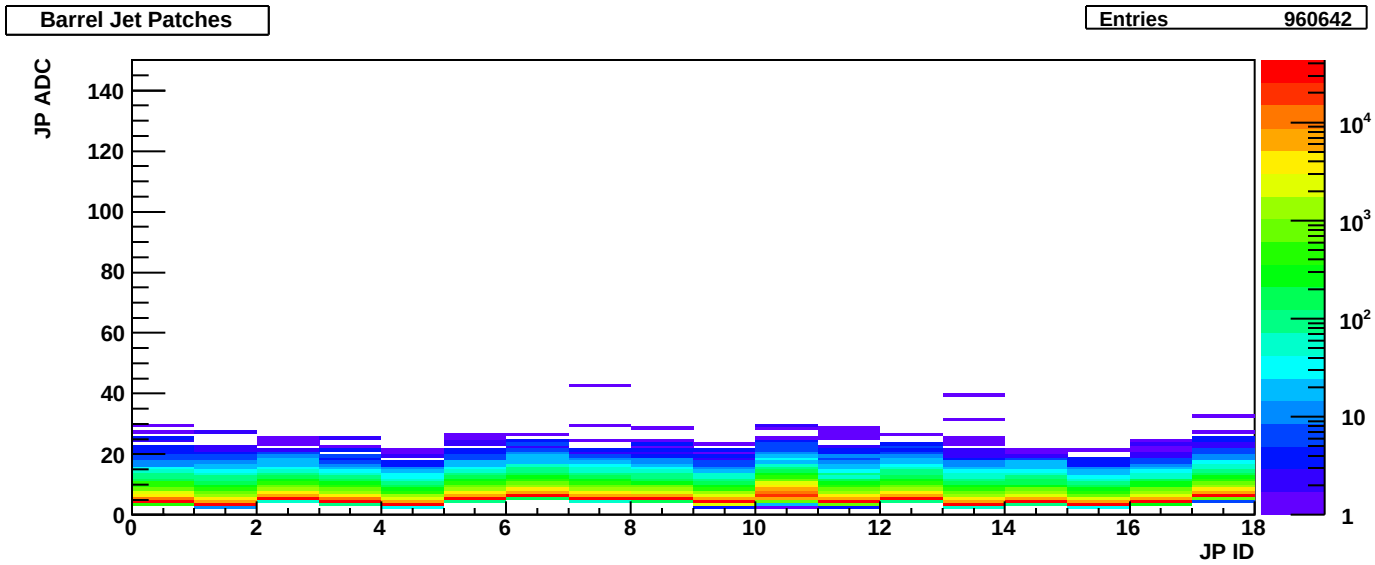

BunchId7Bit (all events)
Entries 53369

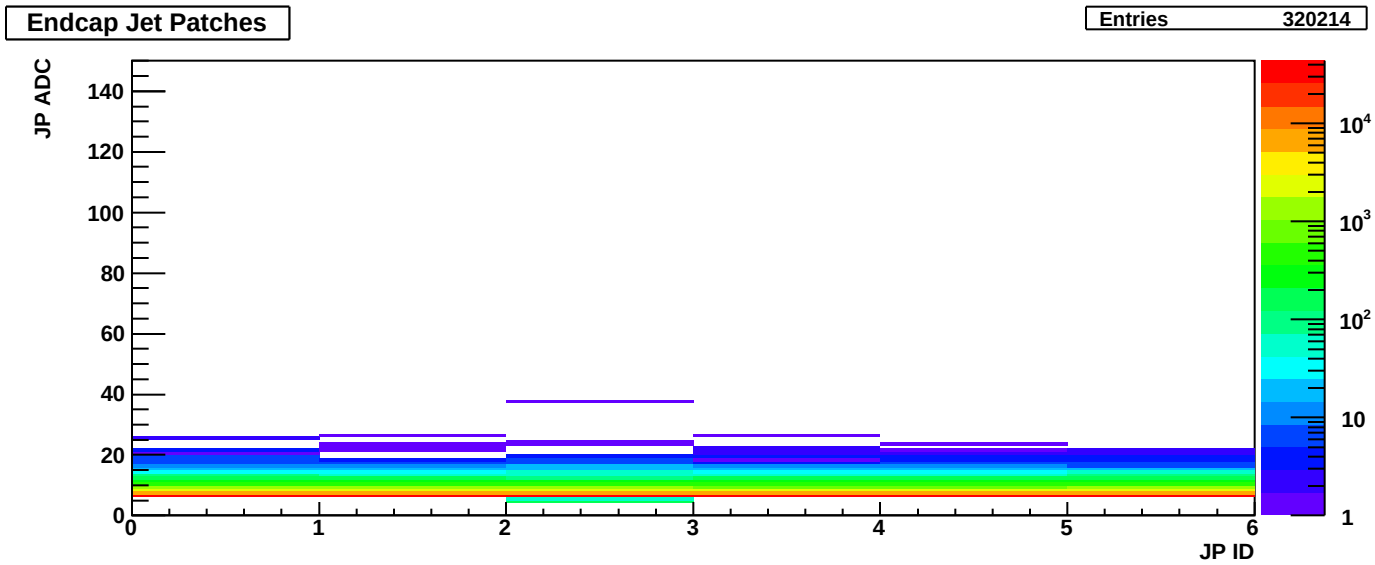

Barrel EMC L0 Input - High Tower
Entries 1.60107e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

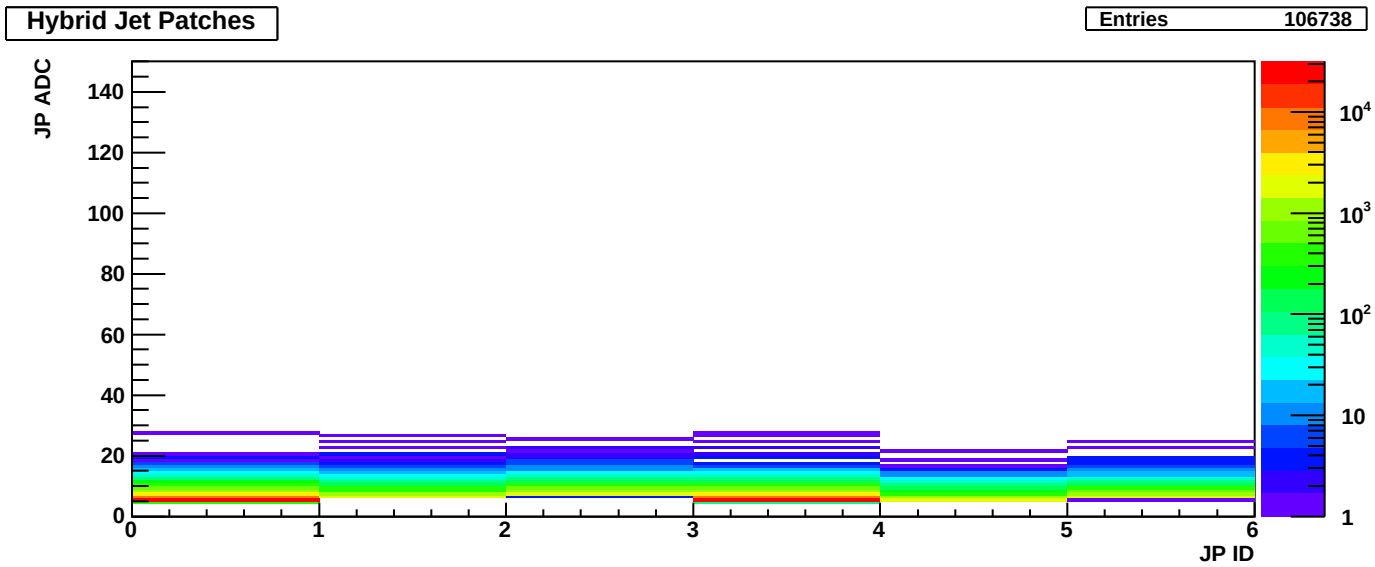

Barrel EMC L0 Input - Patch Sum
Entries 1.60107e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

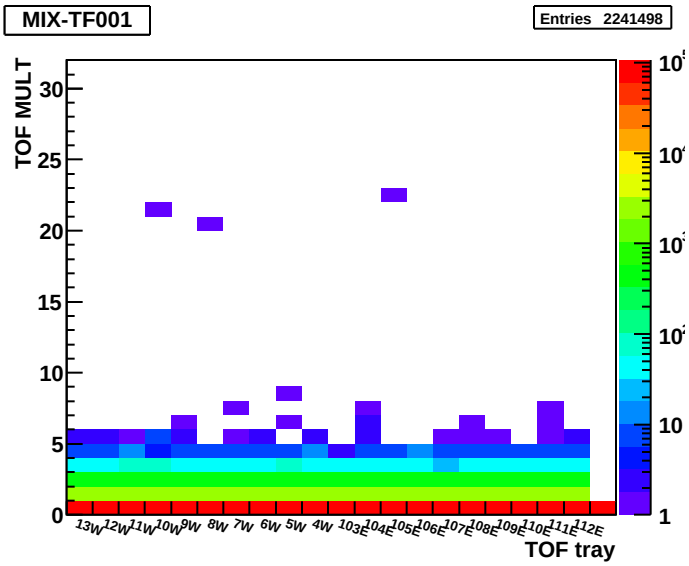

Endcap EMC L0 Input - High Tower
Entries 4803210
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

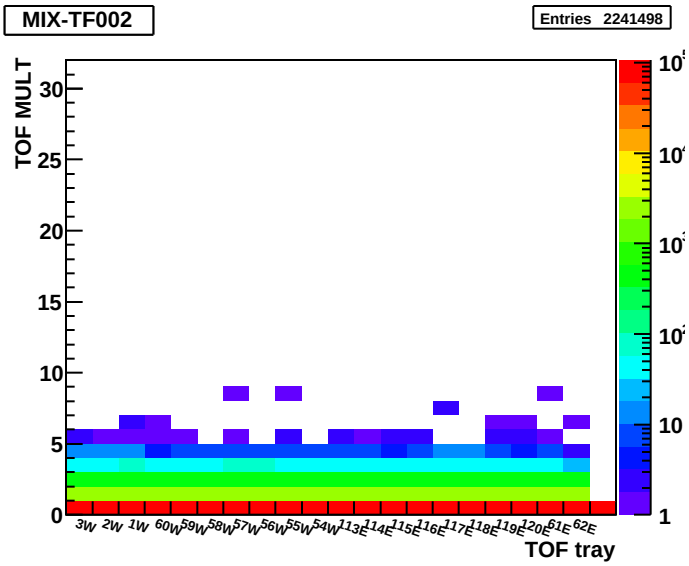

Endcap EMC L0 Input - Patch Sum
Entries 4803210
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

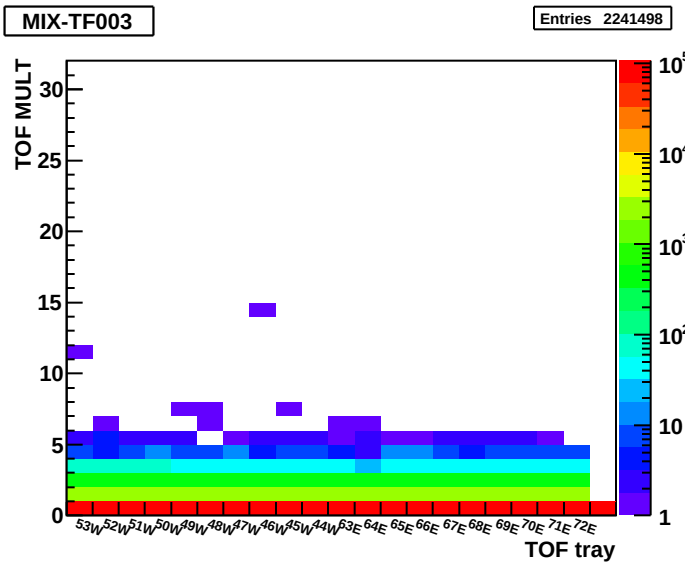

Barrel Jet Patches
Entries 960642
JP ADC
JP ID

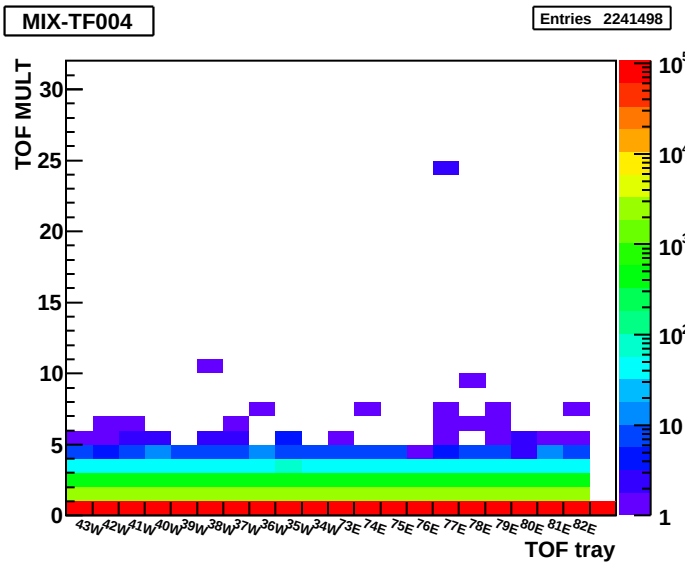

Endcap Jet Patches
Entries 320214
JP ADC
JP ID

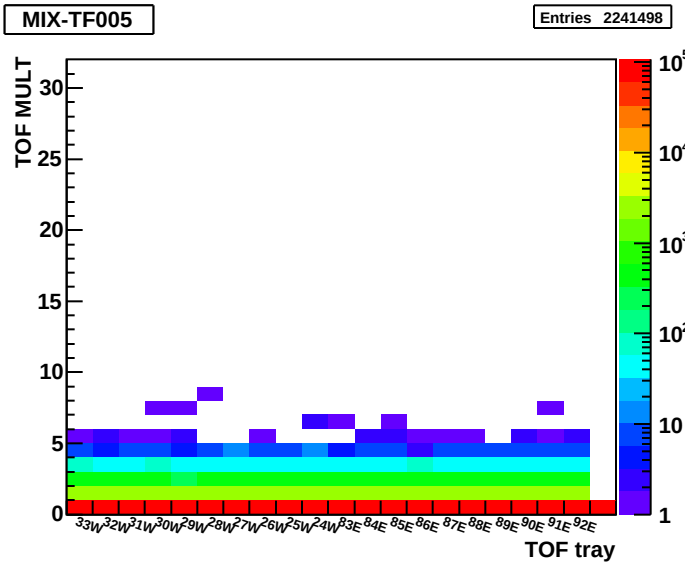

Hybrid Jet Patches
Entries 106738
JP ADC
JP ID

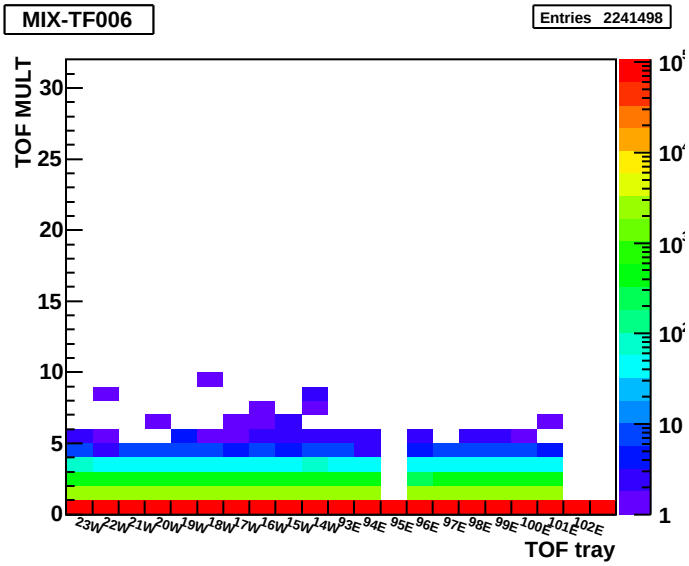

MIX-TF001
Entries 2241498
TOF MULT
TOF tray

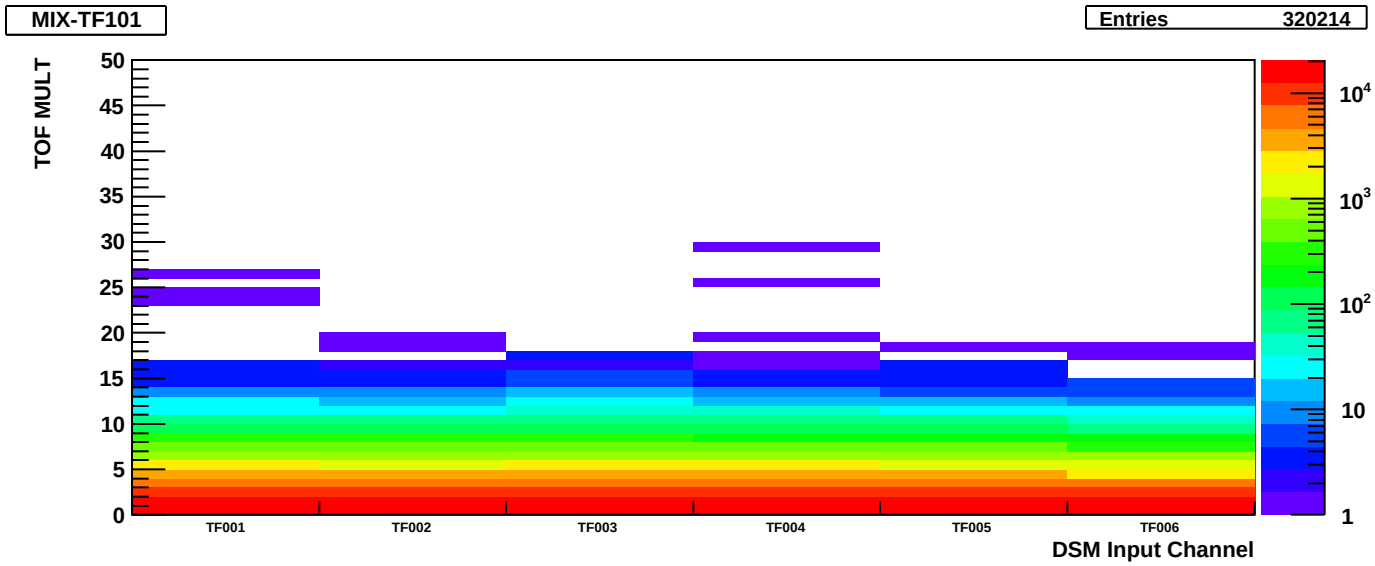

MIX-TF002
Entries 2241498
TOF MULT
TOF tray

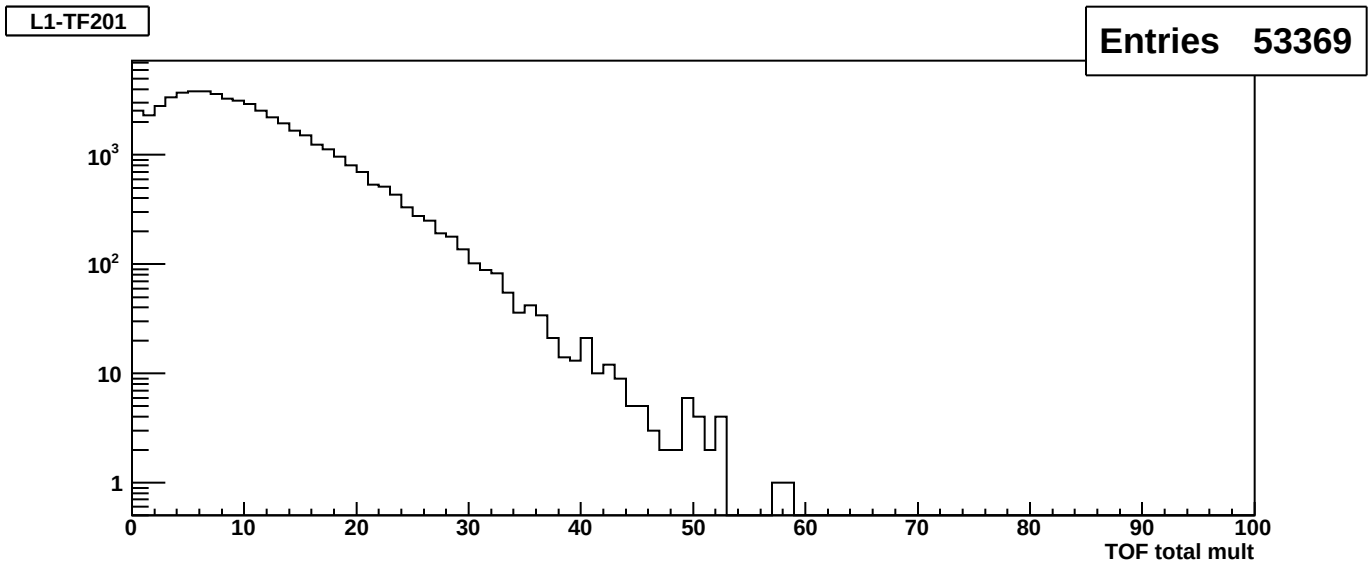

MIX-TF003
Entries 2241498
TOF MULT
TOF tray

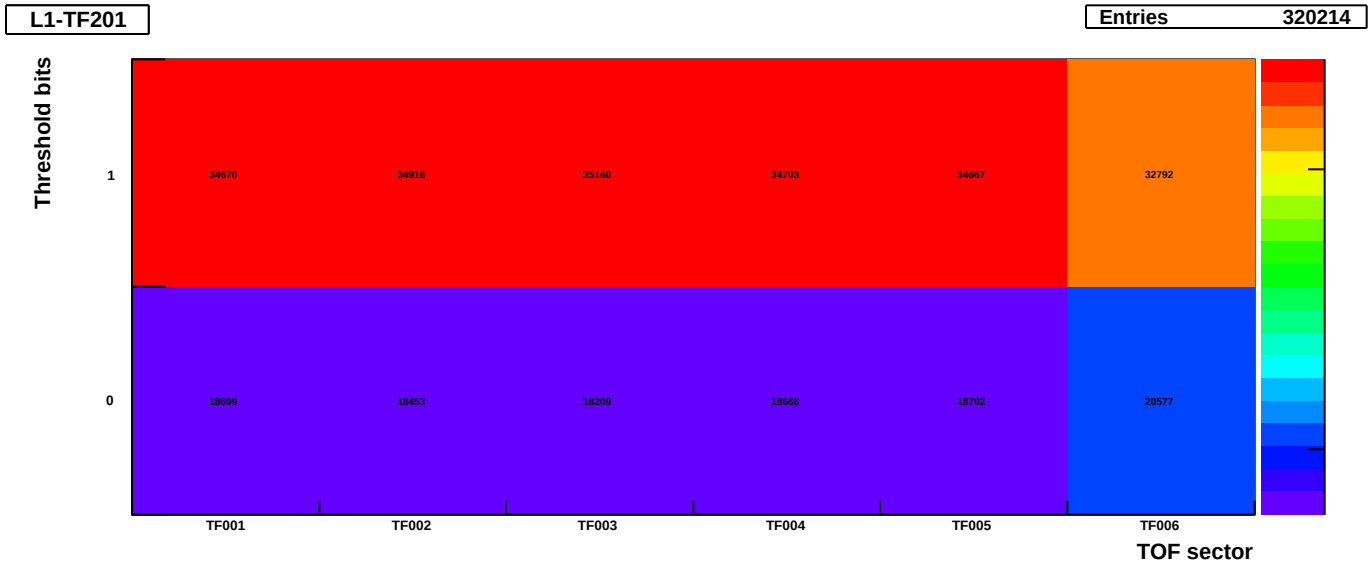

MIX-TF004
Entries 2241498
TOF MULT
TOF tray

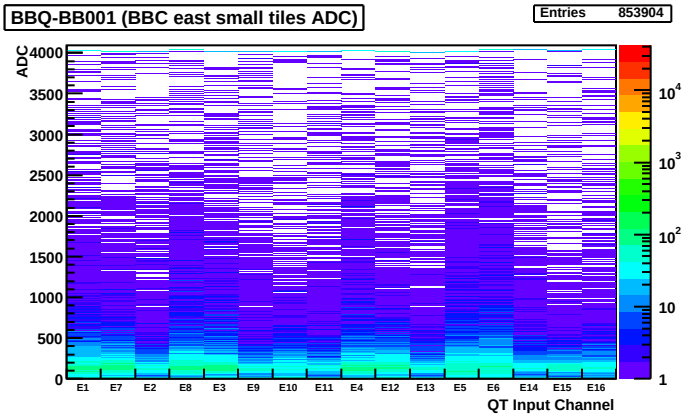

MIX-TF005
Entries 2241498
TOF MULT
TOF tray

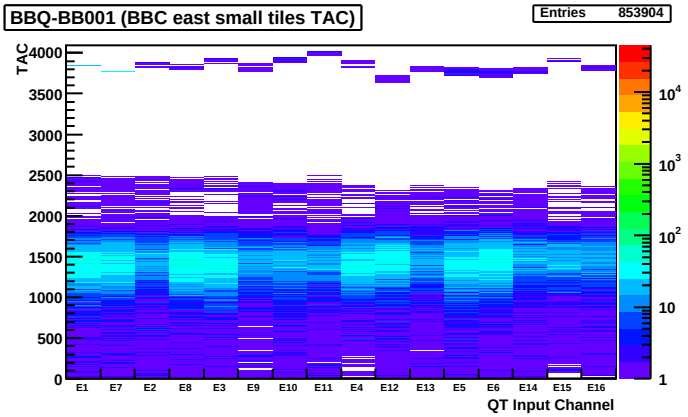

MIX-TF006
Entries 2241498
TOF MULT
TOF tray

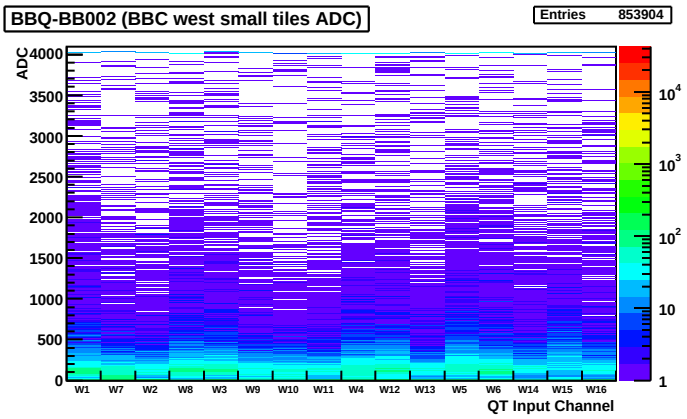

MIX-TF101
Entries 320214
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

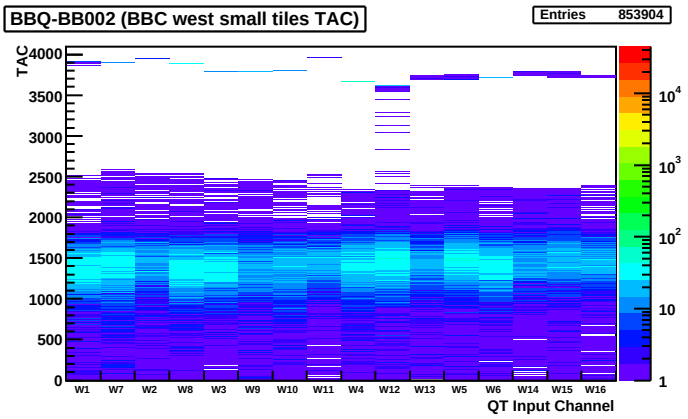

L1-TF201
Entries 53369
TOF total mult

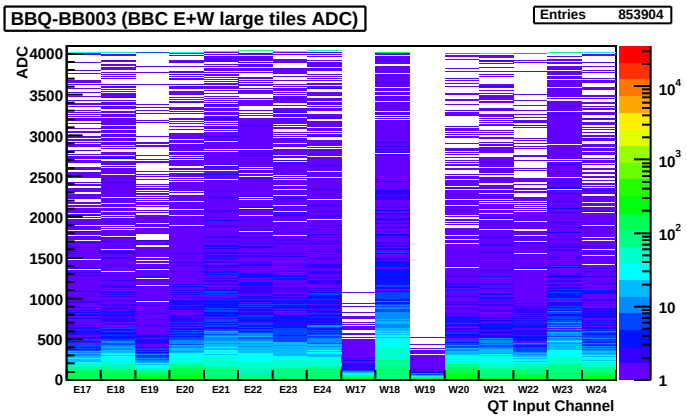

L1-TF201
Entries 320214
Threshold bits
34670
34916
35160
34703
34667
32792
18699
18453
18209
18666
18702
20577
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

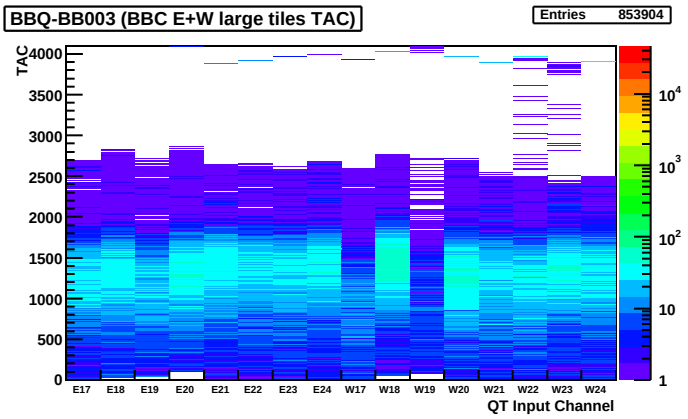

BBQ-BB001 (BBC east small tiles ADC)
Entries 853904
ADC
QT Input Channel

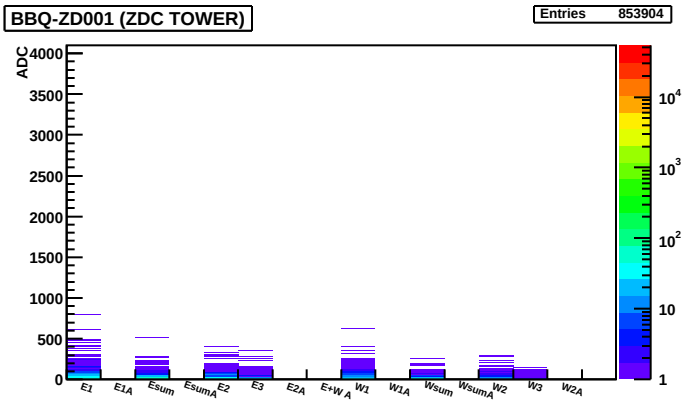

BBQ-BB001 (BBC east small tiles TAC)
Entries 853904
TAC
QT Input Channel

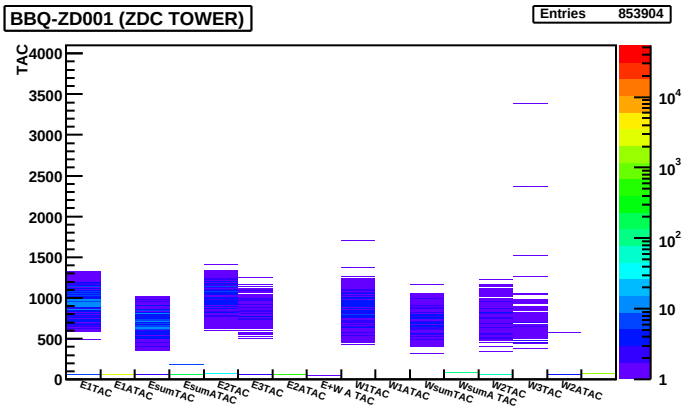

BBQ-BB002 (BBC west small tiles ADC)
Entries 853904
ADC
QT Input Channel

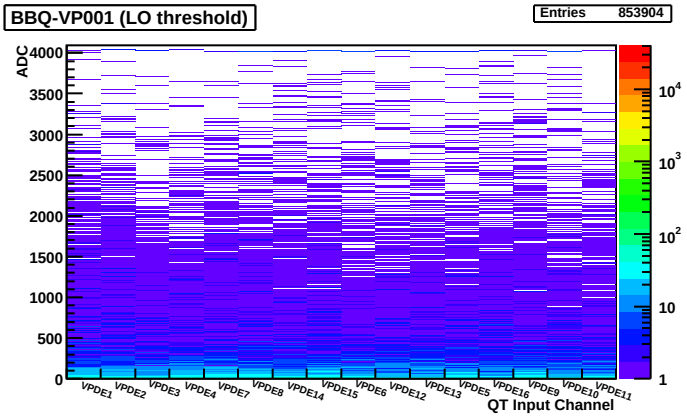

BBQ-BB002 (BBC west small tiles TAC)
Entries 853904
TAC
QT Input Channel

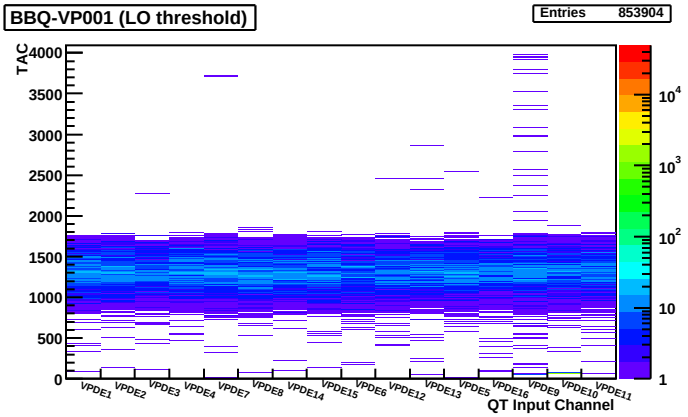

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 853904
ADC
QT Input Channel

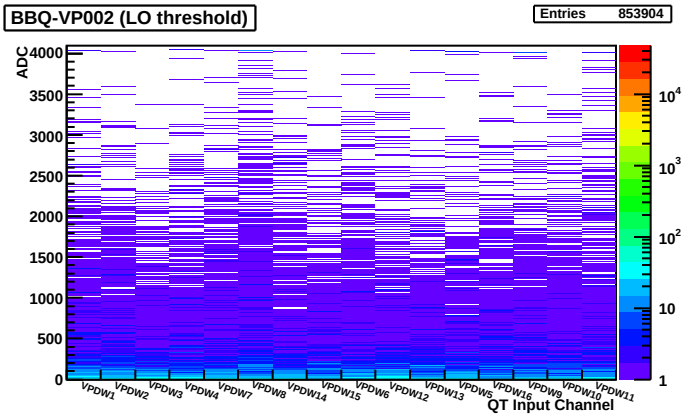

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 853904
TAC
QT Input Channel

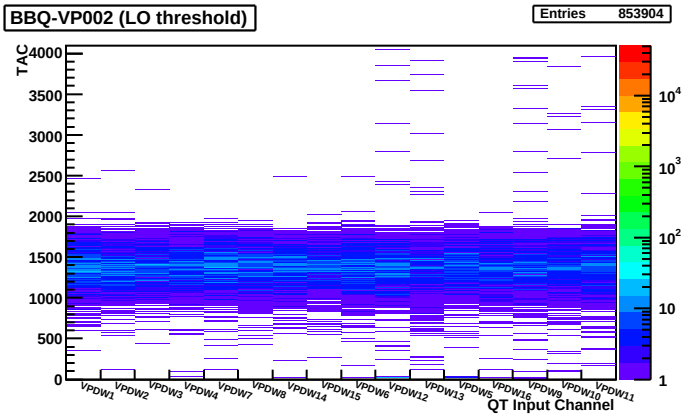

BBQ-ZD001 (ZDC TOWER)
Entries 853904
ADC

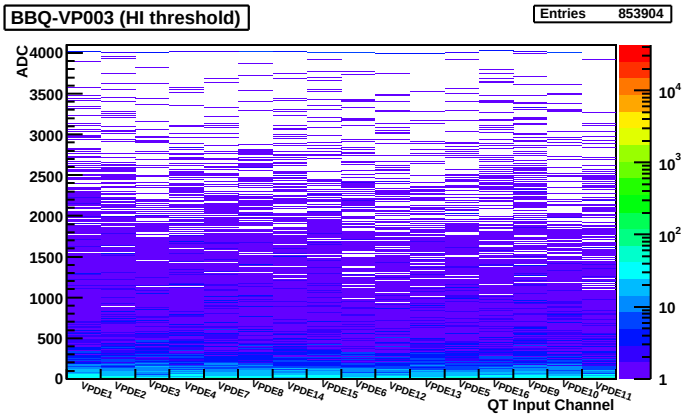

BBQ-ZD001 (ZDC TOWER)
Entries 853904
TAC

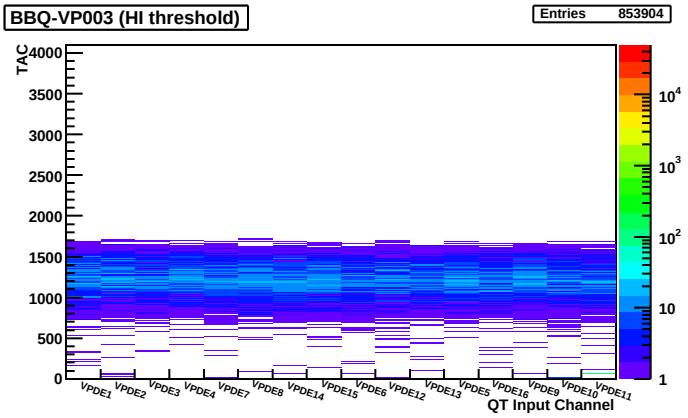

BBQ-VP001 (LO threshold)
Entries 853904
ADC
QT Input Channel

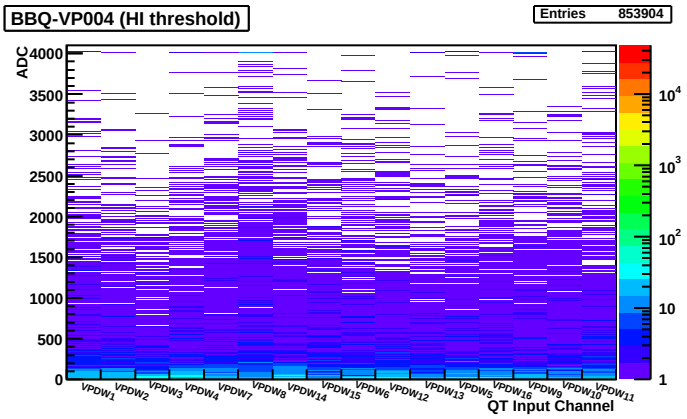

BBQ-VP001 (LO threshold)
Entries 853904
TAC
QT Input Channel

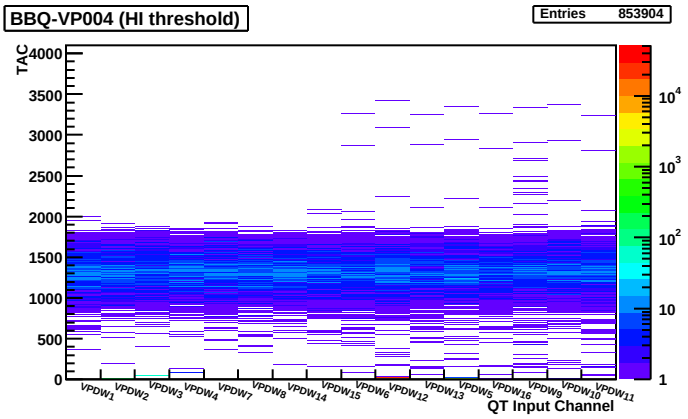

BBQ-VP002 (LO threshold)
Entries 853904
ADC
QT Input Channel

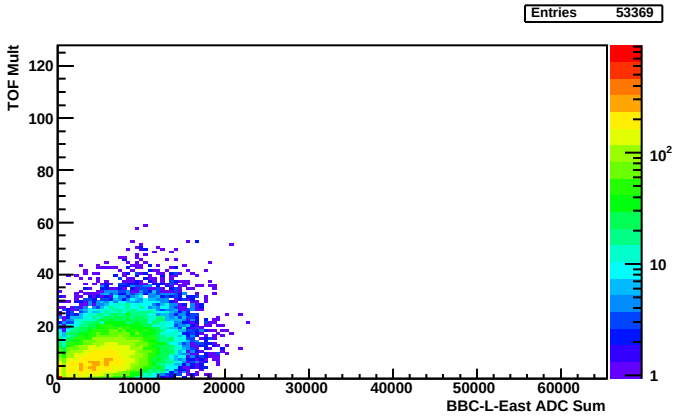

BBQ-VP002 (LO threshold)
Entries 853904
TAC
QT Input Channel

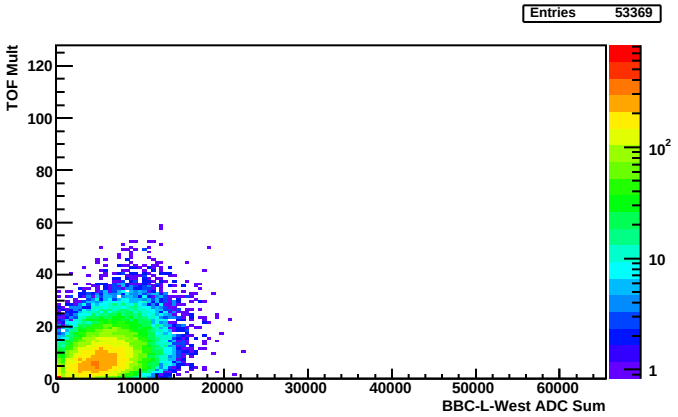

BBQ-VP003 (HI threshold)
Entries 853904
ADC
QT Input Channel

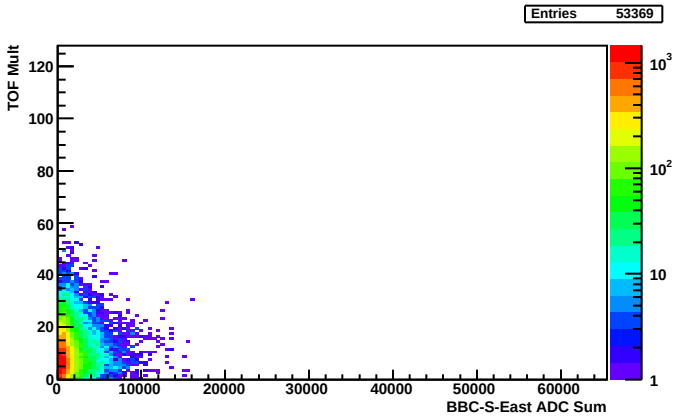

BBQ-VP003 (HI threshold)
Entries 853904
TAC
QT Input Channel

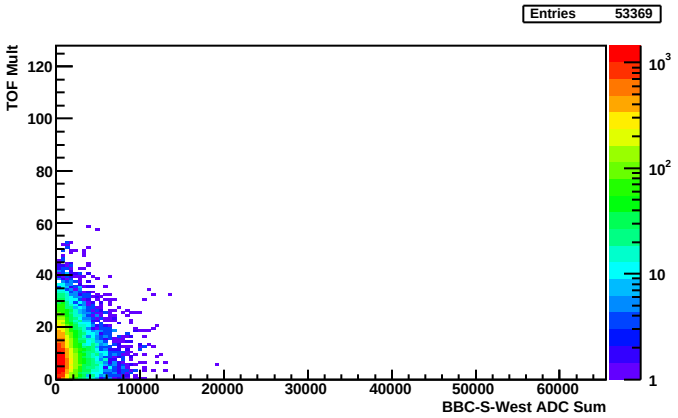

BBQ-VP004 (HI threshold)
Entries 853904
ADC
QT Input Channel

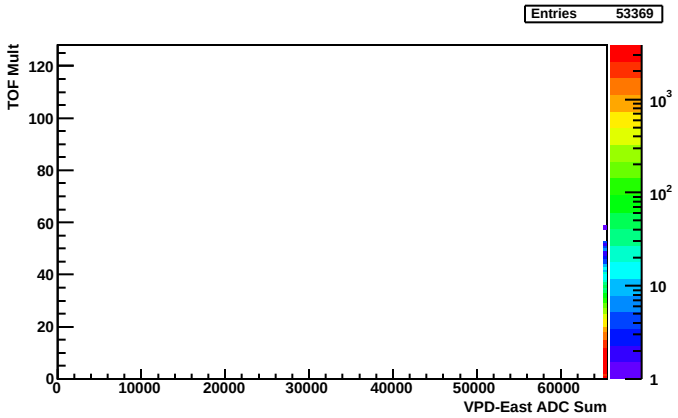

BBQ-VP004 (HI threshold)
Entries 853904
TAC
QT Input Channel

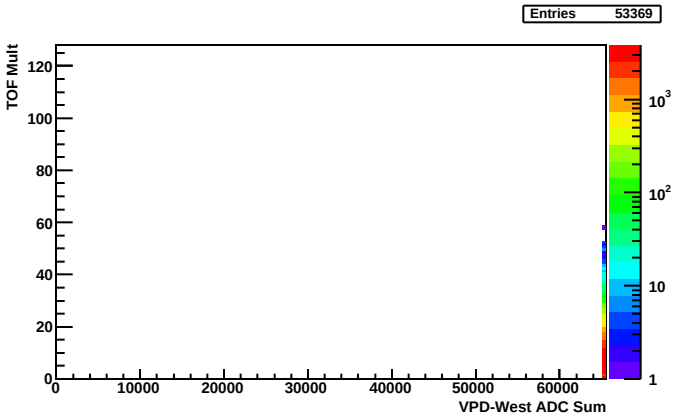

Entries 53369
TOF Mult
BBC-L-East ADC Sum

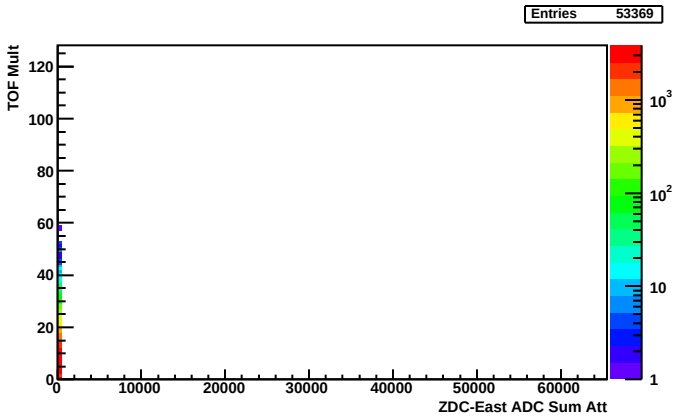

Entries 53369
TOF Mult
BBC-L-West ADC Sum

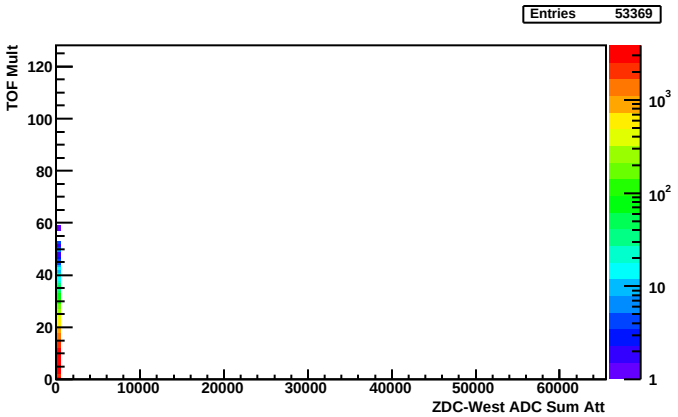

Entries 53369
TOF Mult
BBC-S-East ADC Sum

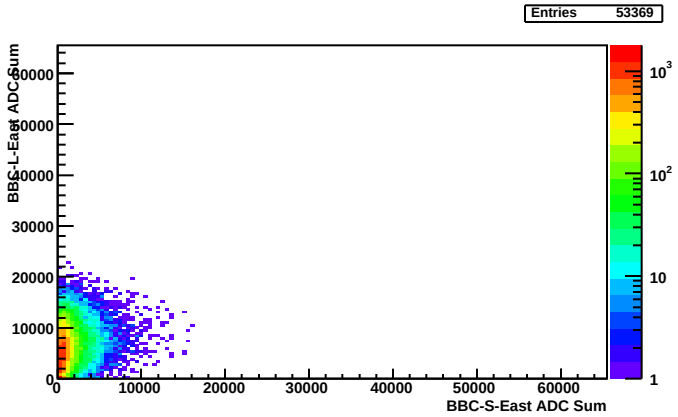

Entries 53369
TOF Mult
BBC-S-West ADC Sum

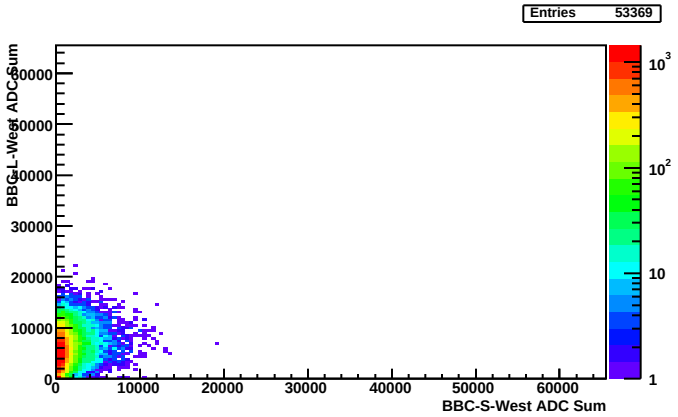

Entries 53369
TOF Mult
VPD-East ADC Sum

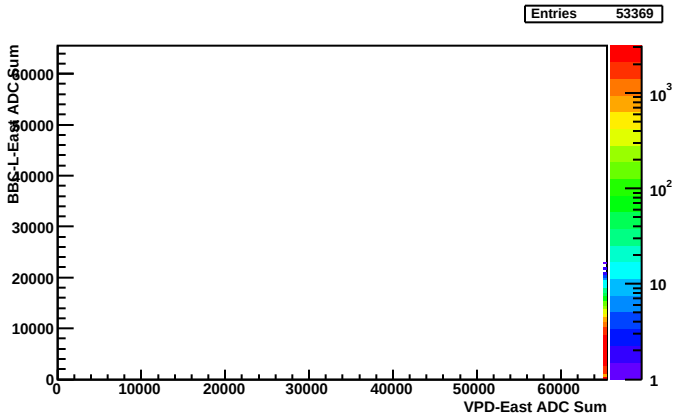

Entries 53369
TOF Mult
VPD-West ADC Sum

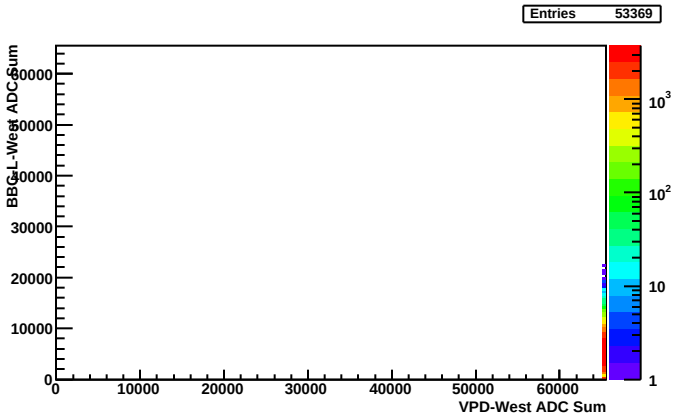

Entries 53369
TOF Mult
ZDC-East ADC Sum Att

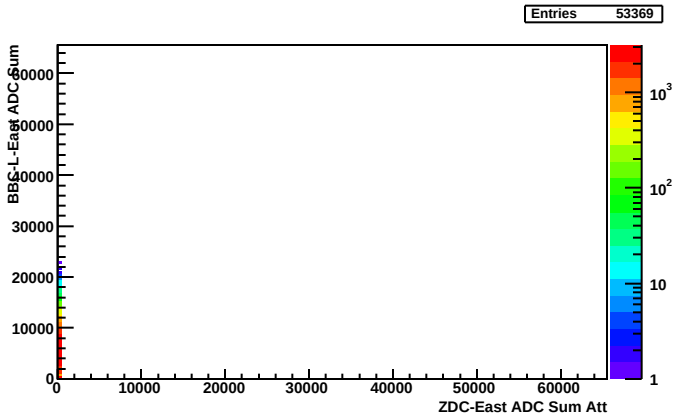

Entries 53369
TOF Mult
ZDC-West ADC Sum Att

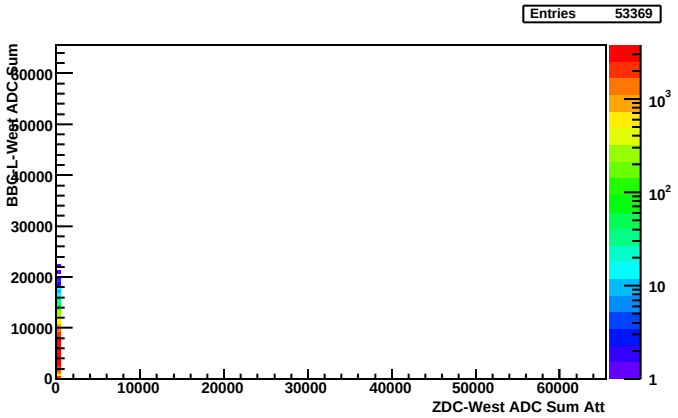

Entries 53369
BBC-L-East ADC Sum
BBC-S-East ADC Sum

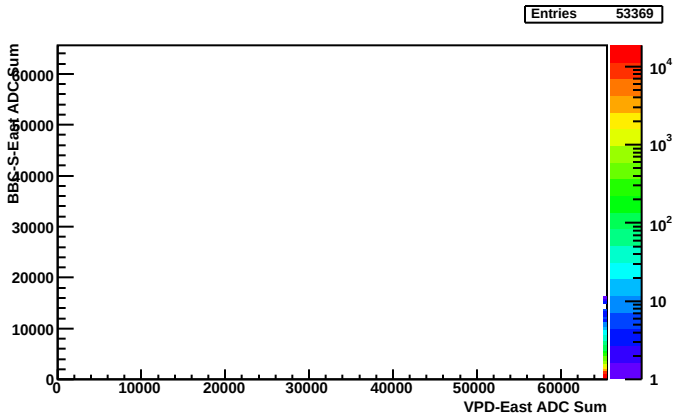

Entries 53369
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 53369
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 53369
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 53369
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 53369
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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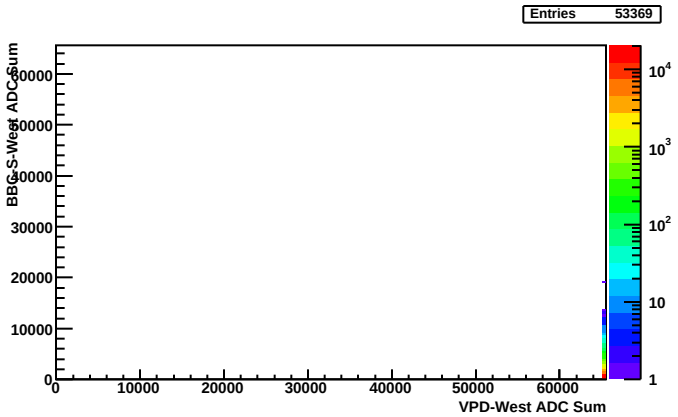
BBC-S-East ADC Sum
VPD-East ADC Sum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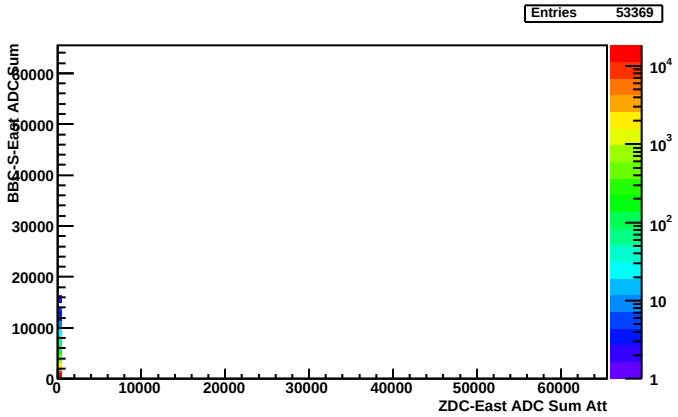
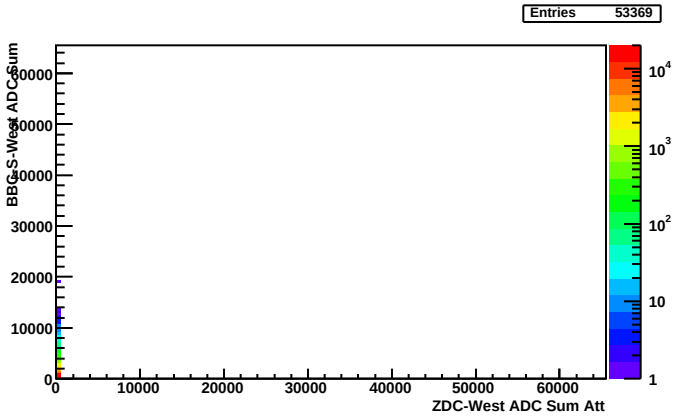
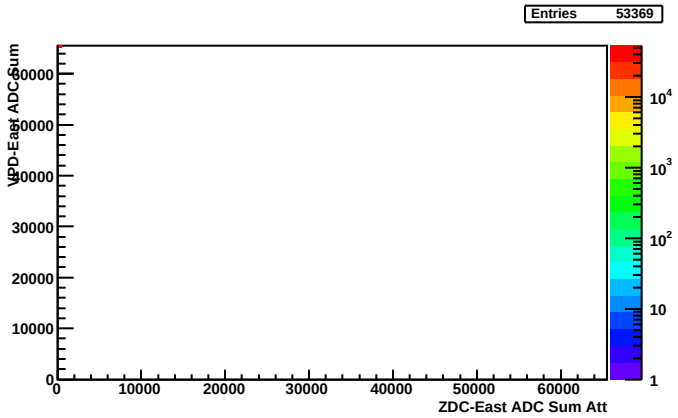
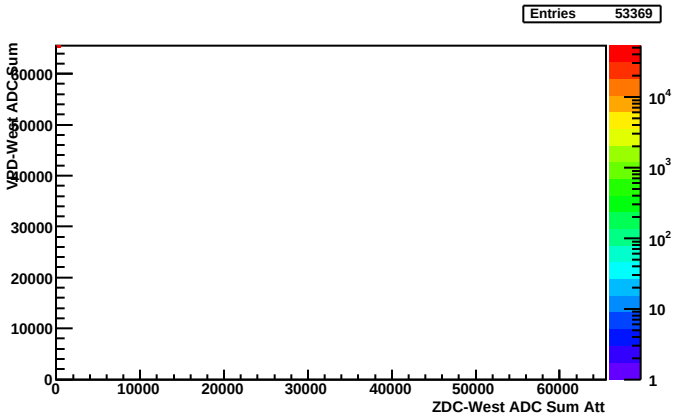
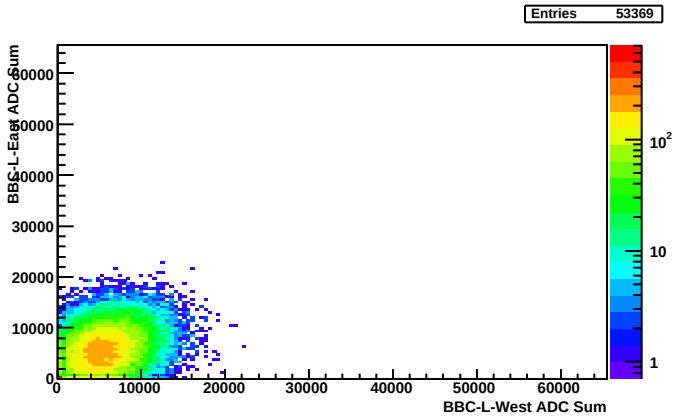
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 53369
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 53369
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 53369
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 53369
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 53369
BBC-L-East ADC Sum
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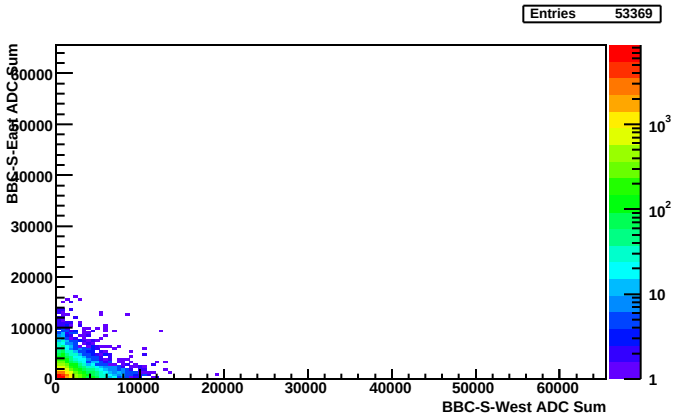
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BBC-S-East ADC Sum
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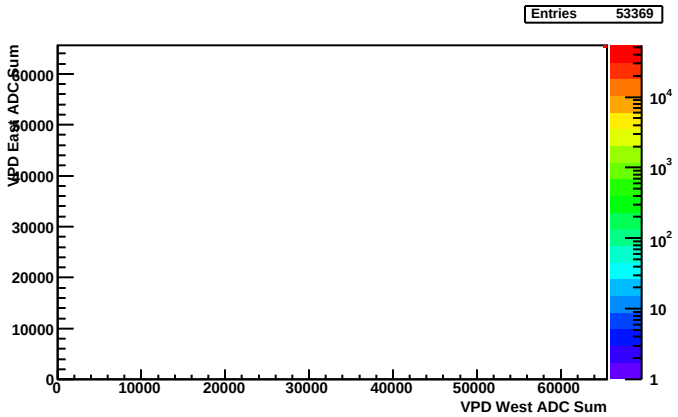
Entries 53369
VPD East ADC Sum
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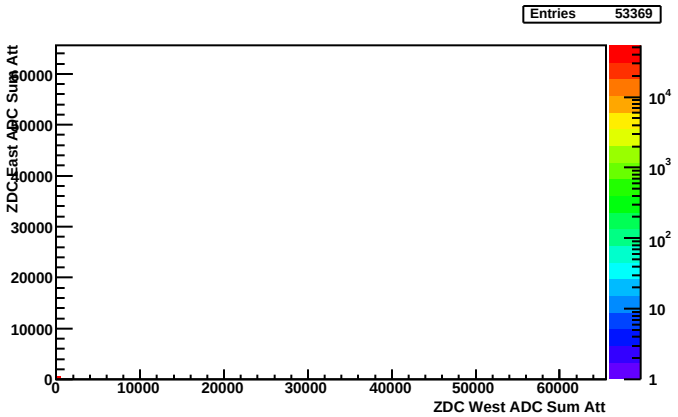
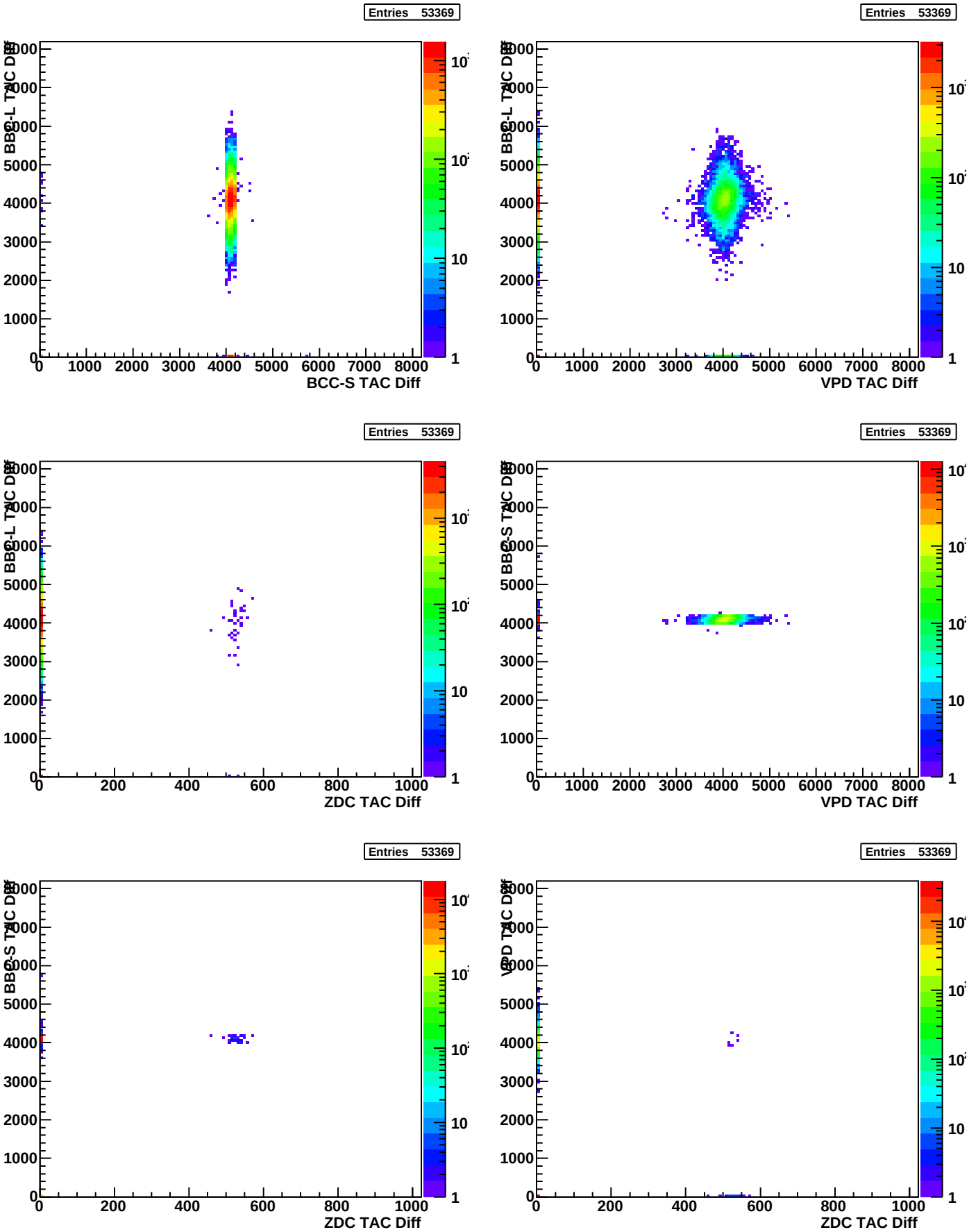
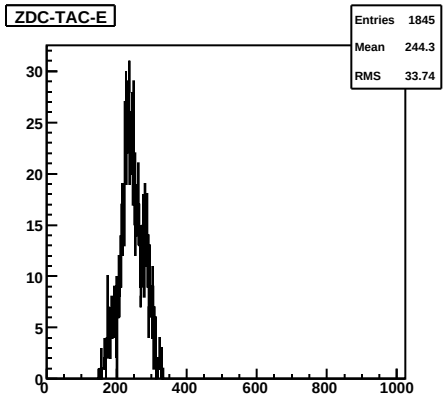
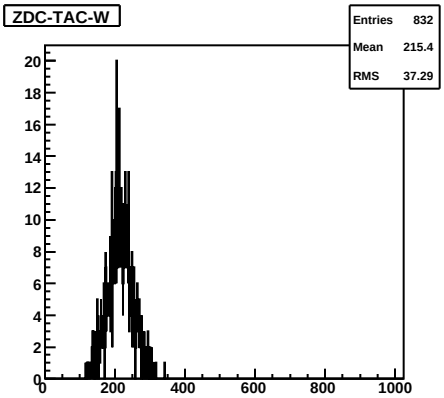
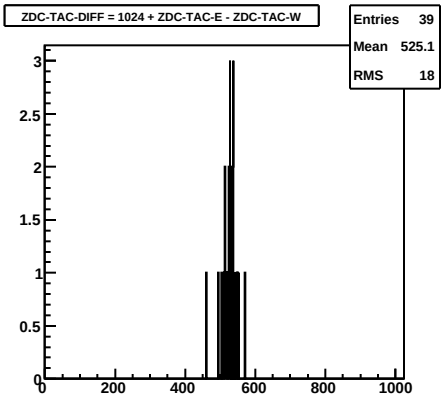
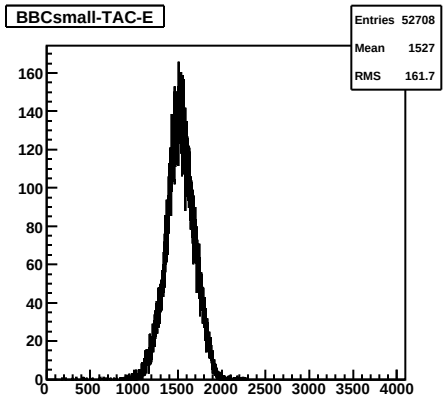

Entries 53369
ZDC East ADC Sum Att
ZDC West ADC Sum Att

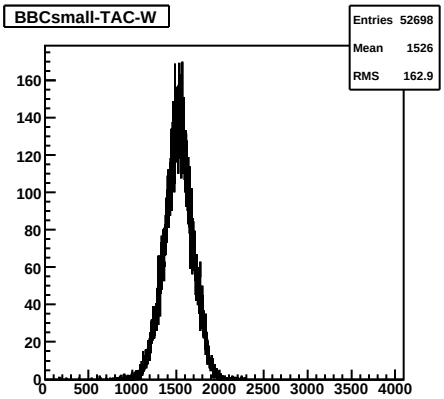

Entries 53369
BBC-L TAC Diff
BCC-S TAC Diff
Entries 53369
BBC-L TAC Diff
VPD TAC Diff
Entries 53369
BBC-L TAC Diff
ZDC TAC Diff
Entries 53369
BBC-S TAC Diff
VPD TAC Diff
Entries 53369
BBC-S TAC Diff
ZDC TAC Diff
Entries 53369
VPD TAC Diff
ZDC TAC Diff

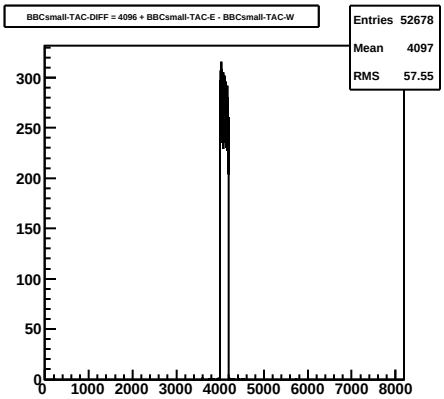

ZDC-TAC-E
Entries 1845
Mean 244.3
RMS 33.74

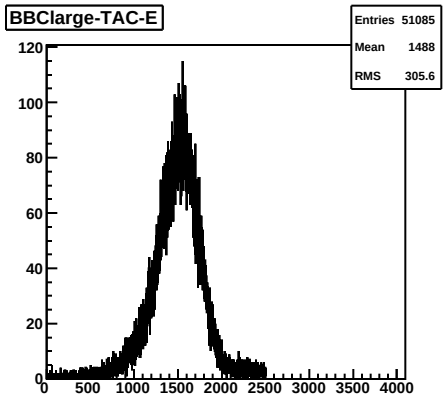

ZDC-TAC-W
Entries 832
Mean 215.4
RMS 37.29

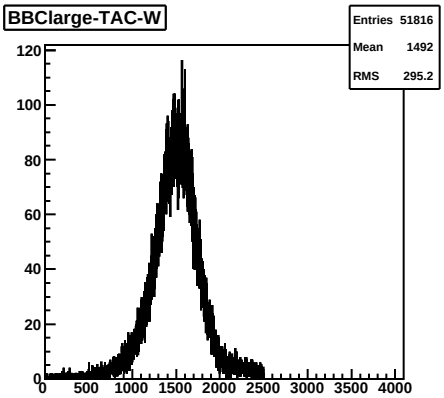

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 39
Mean 525.1
RMS 18

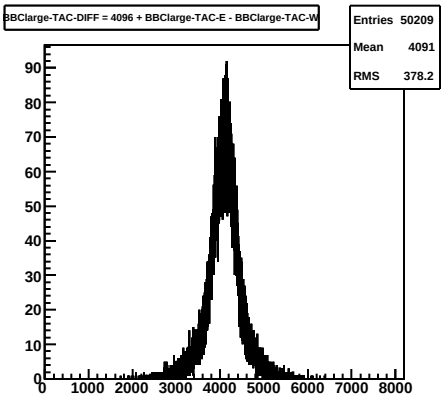

BBCsmall-TAC-E
Entries 52708
Mean 1527
RMS 161.7

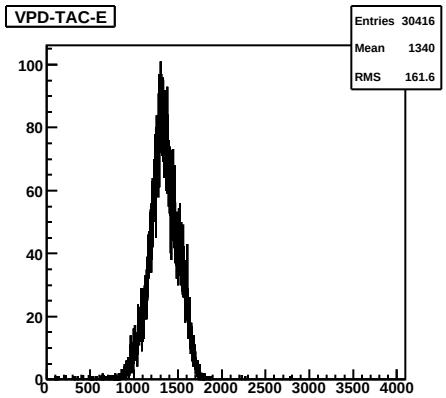

BBCsmall-TAC-W
Entries 52698
Mean 1526
RMS 162.9

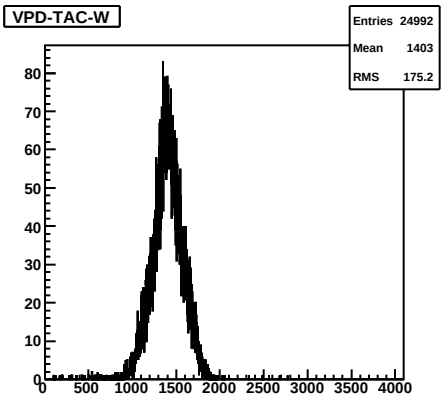

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 52678
Mean 4097
RMS 57.55

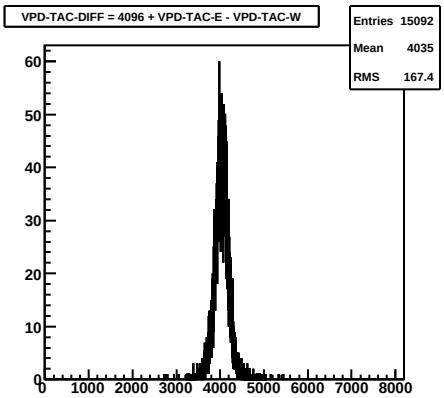

BBClarge-TAC-E
Entries 51085
Mean 1488
RMS 305.6

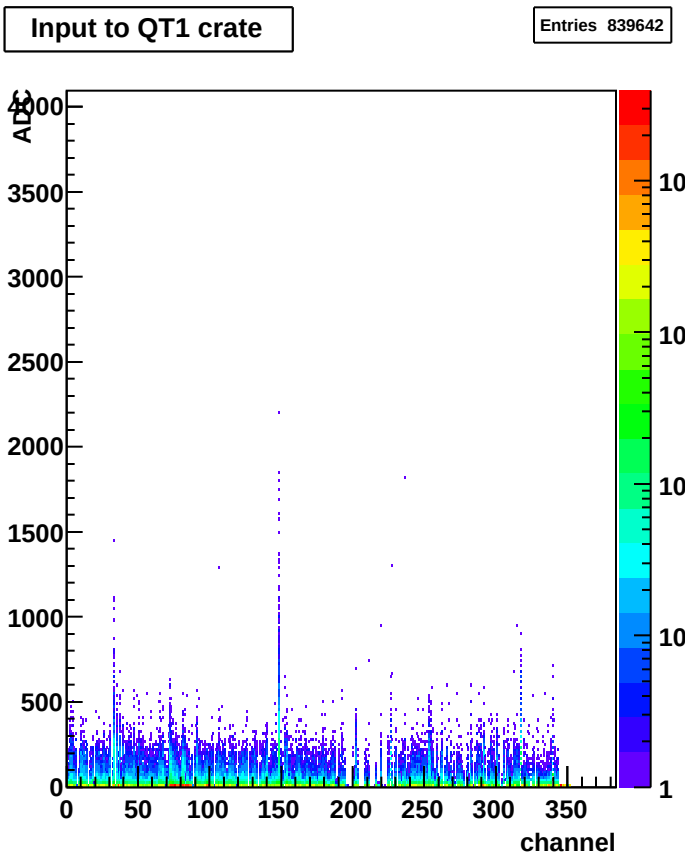

BBClarge-TAC-W
Entries 51816
Mean 1492
RMS 295.2

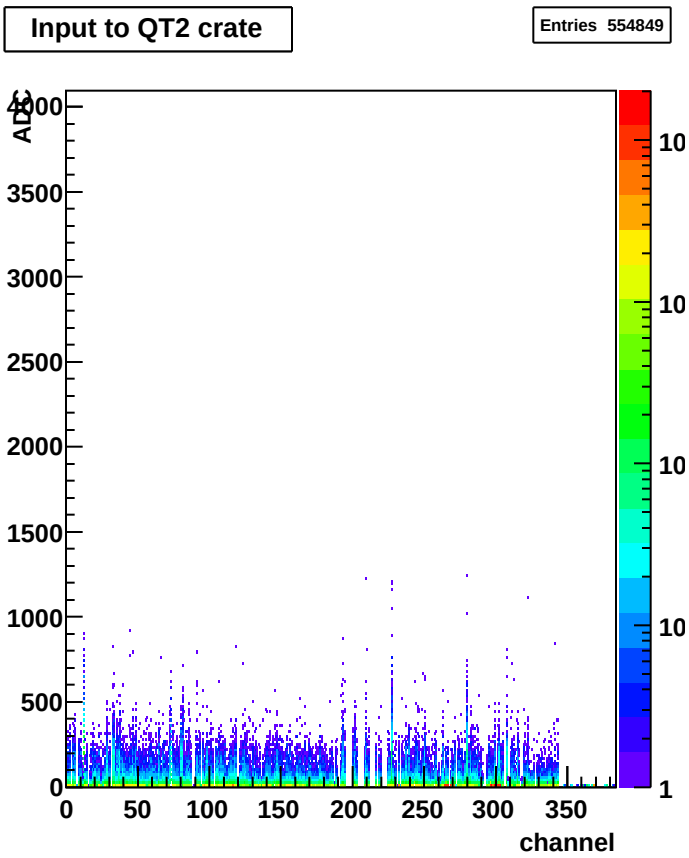

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 50209
Mean 4091
RMS 378.2

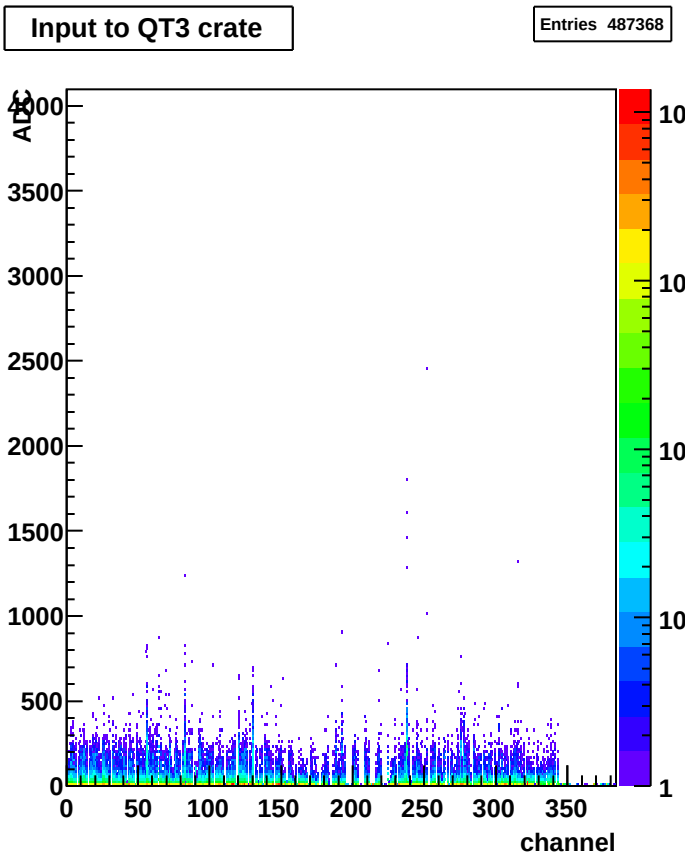

VPD-TAC-E
Entries 30416
Mean 1340
RMS 161.6

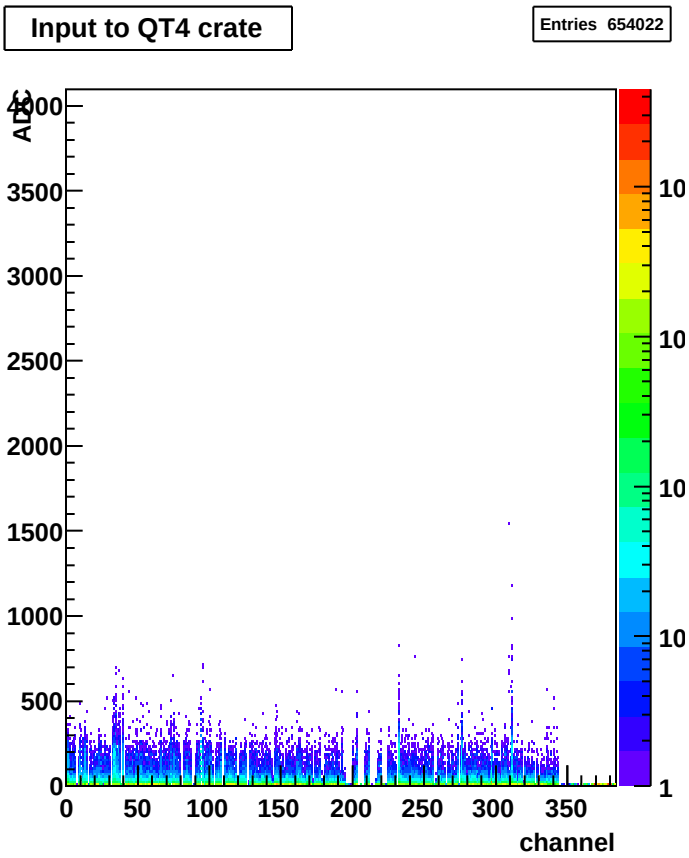

VPD-TAC-W
Entries 24992
Mean 1403
RMS 175.2

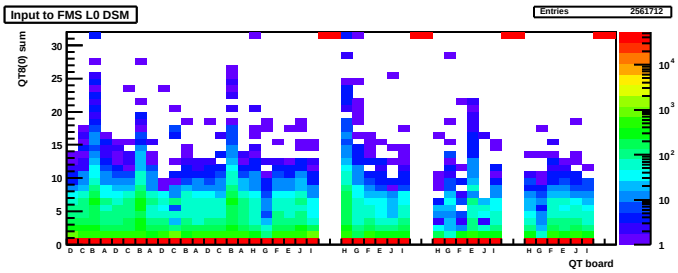

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 15092
Mean 4035
RMS 167.4

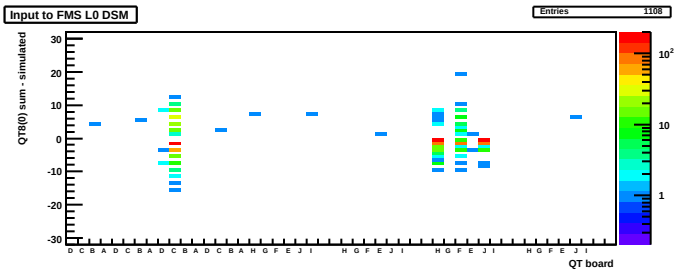

Input to QT1 crate
Entries 839642
ADC
channel

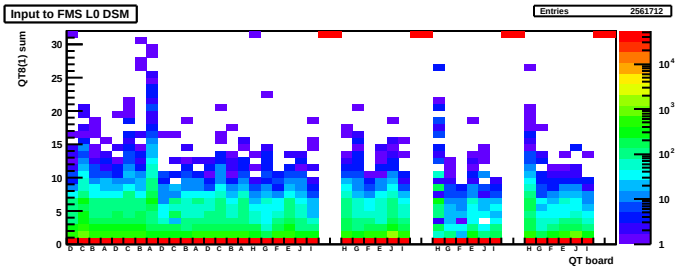

Input to QT2 crate
Entries 554849
ADC
channel

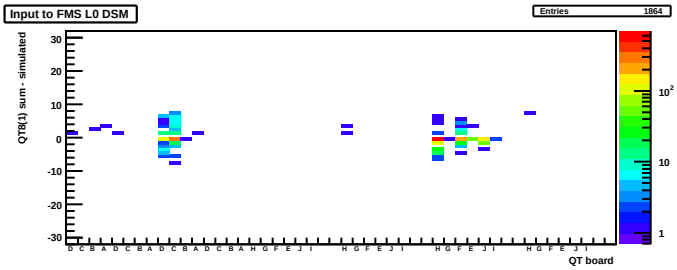

Input to QT3 crate
Entries 487368
ADC
channel

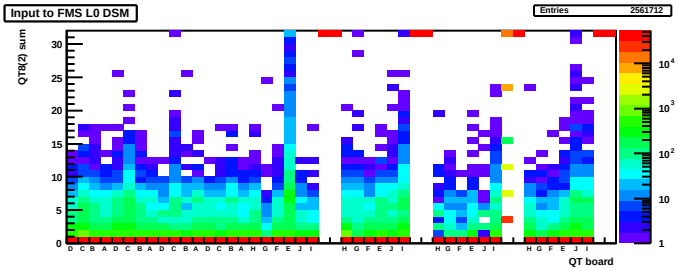

Input to QT4 crate
Entries 654022
ADC
channel

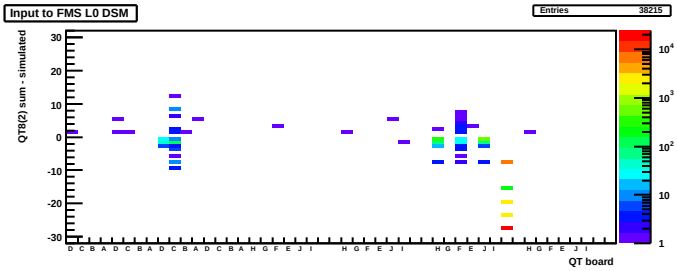

Input to FMS L0 DSM
Entries 2561712
QT8(0) sum
QT board

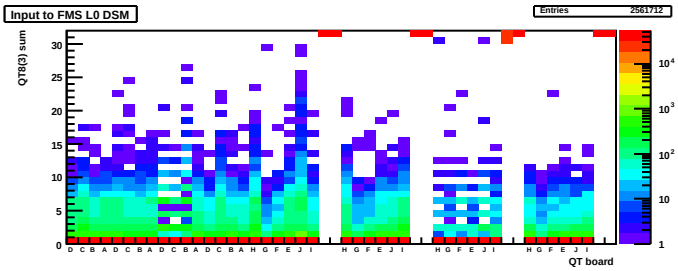

Input to FMS L0 DSM
Entries 1108
QT8(0) sum - simulated
QT board

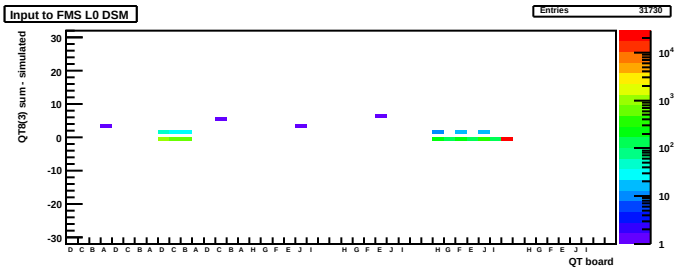

Input to FMS L0 DSM
Entries 2561712
QT8(1) sum
QT board

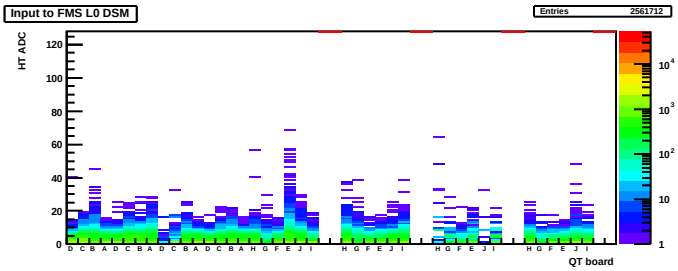

Input to FMS L0 DSM
Entries 1864
QT8(1) sum - simulated
QT board

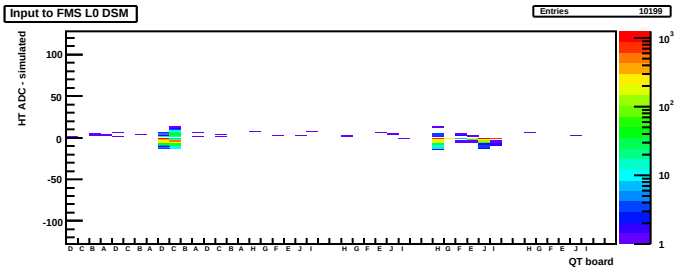

Input to FMS L0 DSM
Entries 2561712
QT8(2) sum
QT board

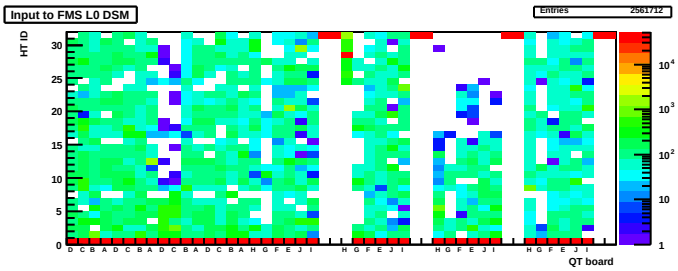

Input to FMS L0 DSM
Entries 38215
QT8(2) sum - simulated
QT board

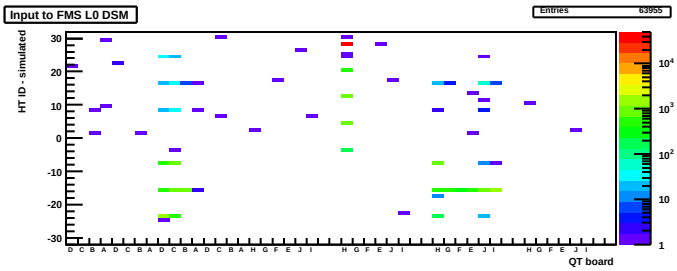

Input to FMS L0 DSM
Entries 2561712
QT8(3) sum
QT board

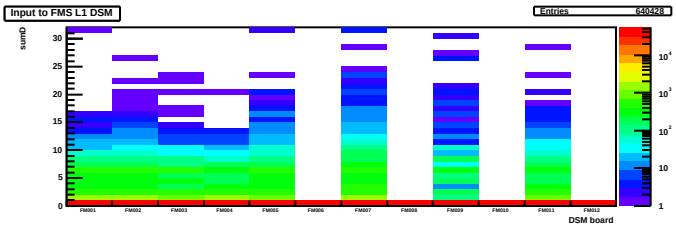

Input to FMS L0 DSM
Entries 31730
QT8(3) sum - simulated
QT board

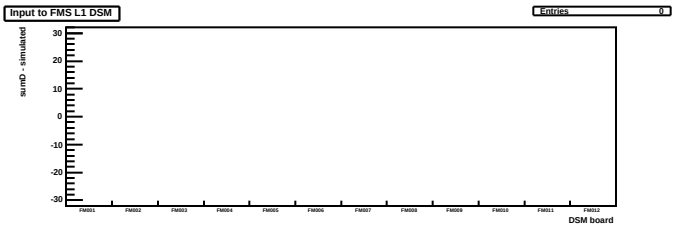

Input to FMS L0 DSM
Entries 2561712
HT ADC
QT board

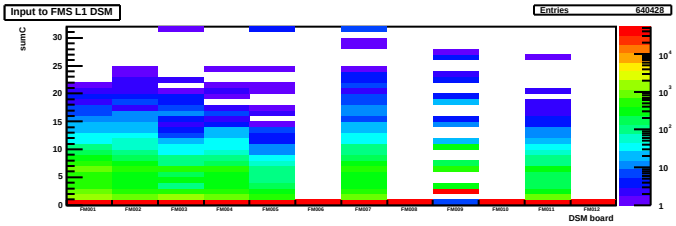

Input to FMS L0 DSM
Entries 10199
HT ADC - simulated
QT board

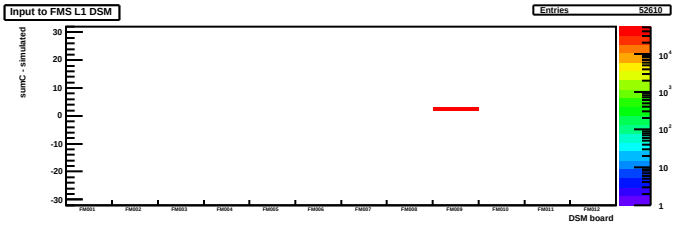

Input to FMS L0 DSM
Entries 2561712
HT ID
QT board

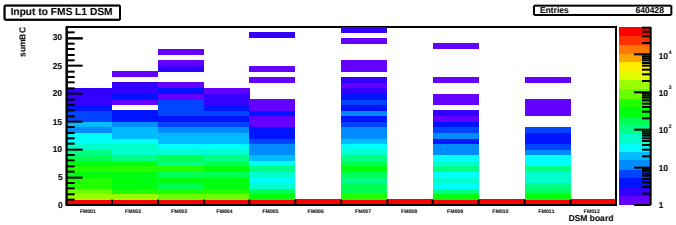

Input to FMS L0 DSM
Entries 63955
HT ID - simulated
QT board

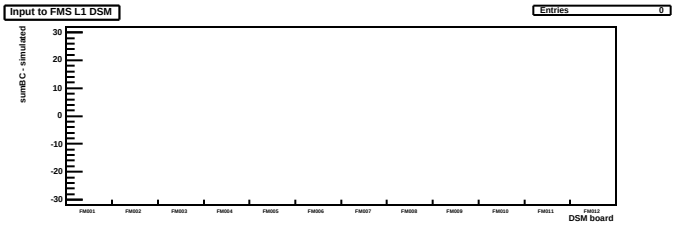

Input to FMS L1 DSM
Entries 640428
sumD
DSM board

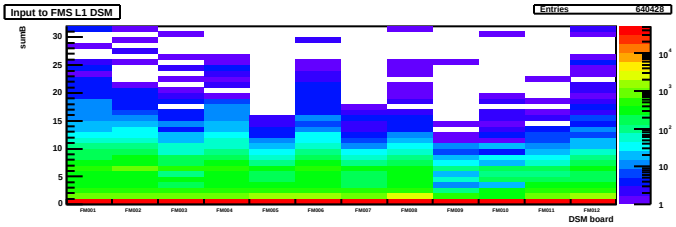

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

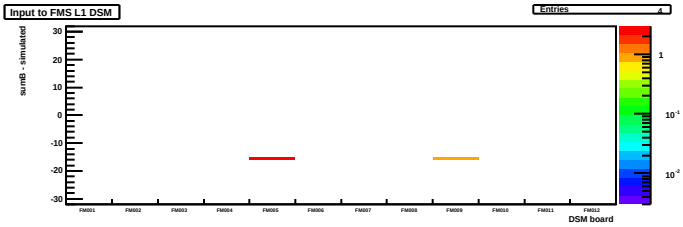

Input to FMS L1 DSM
Entries 640428
sumC
DSM board

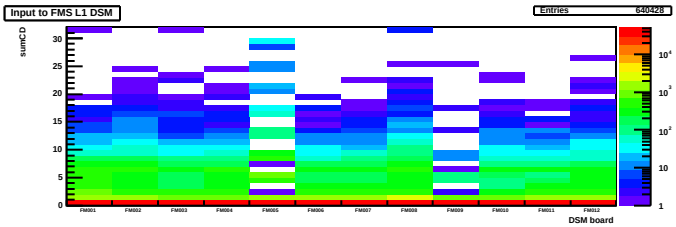

Input to FMS L1 DSM
Entries 52610
sumC - simulated
DSM board

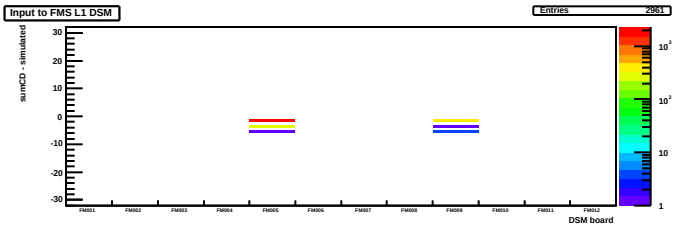

Input to FMS L1 DSM
Entries 640428
sumBC
DSM board

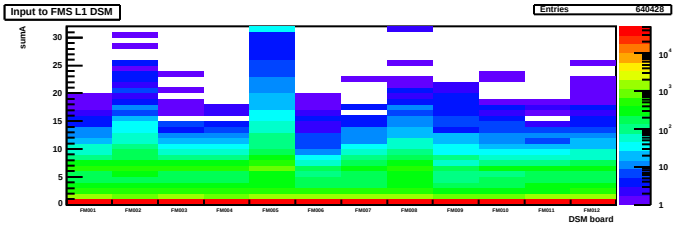

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

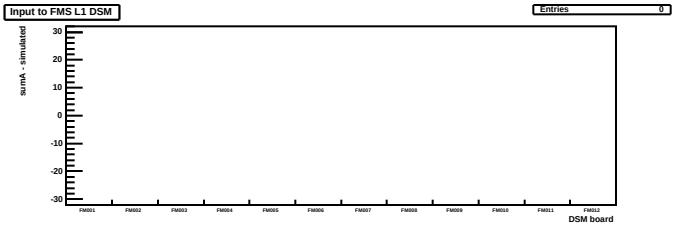

Input to FMS L1 DSM
Entries 640428
sumB
DSM board

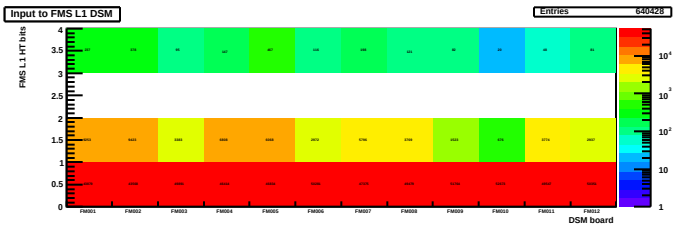

Input to FMS L1 DSM
Entries 4
sumB - simulated
DSM board

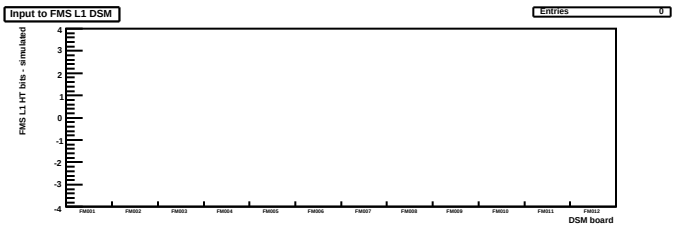

Input to FMS L1 DSM
Entries 640428
sumCD
DSM board

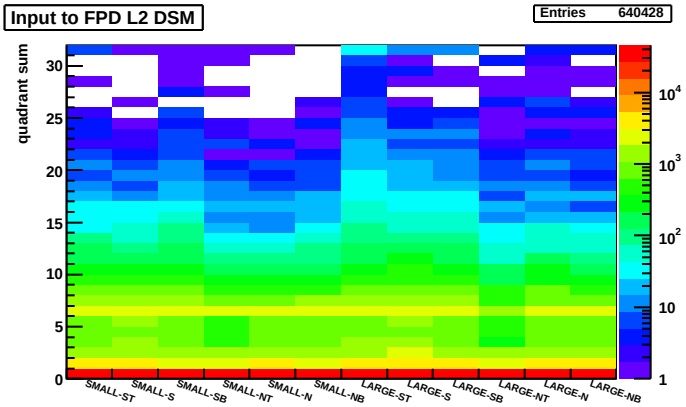

Input to FMS L1 DSM
Entries 2961
sumCD - simulated
DSM board

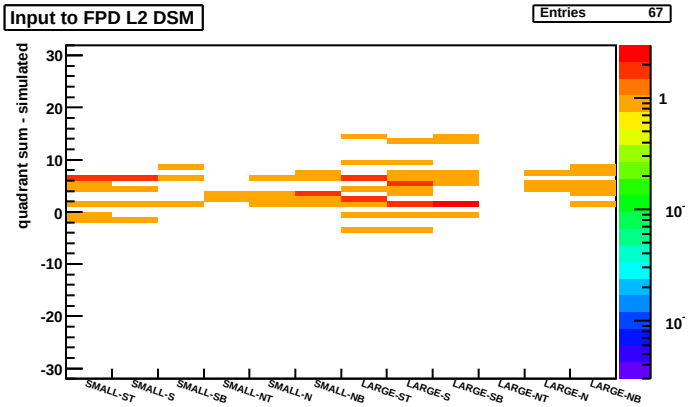

Input to FMS L1 DSM
Entries 640428
sumA
DSM board

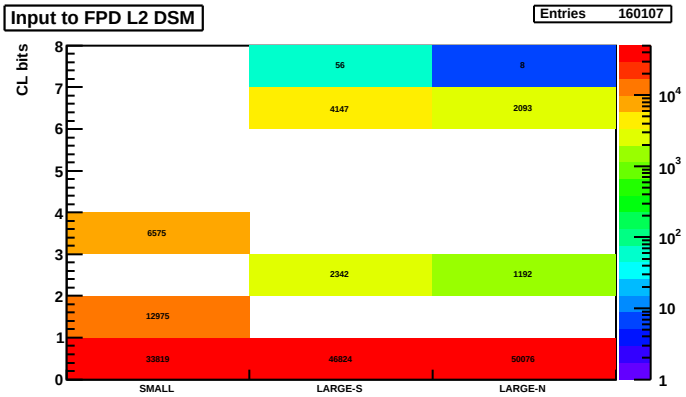

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

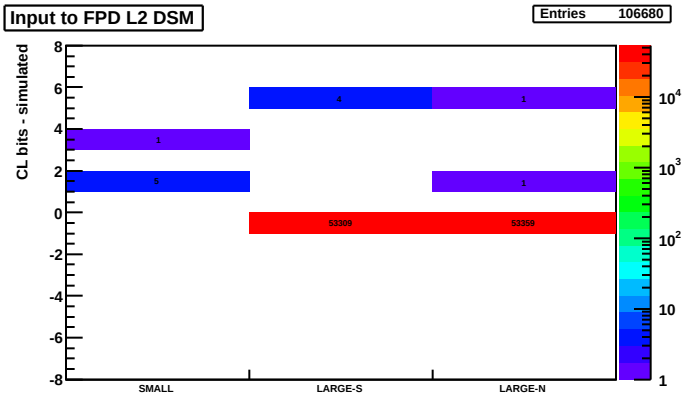

Input to FMS L1 DSM
Entries 640428
FMS L1 HT bits
DSM board

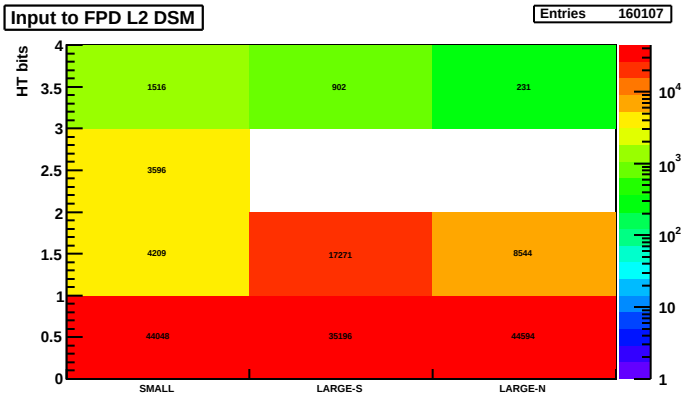

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

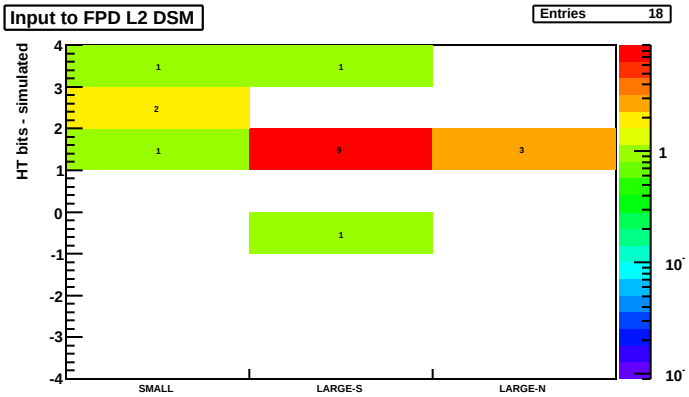

Input to FPD L2 DSM
Entries 640428
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

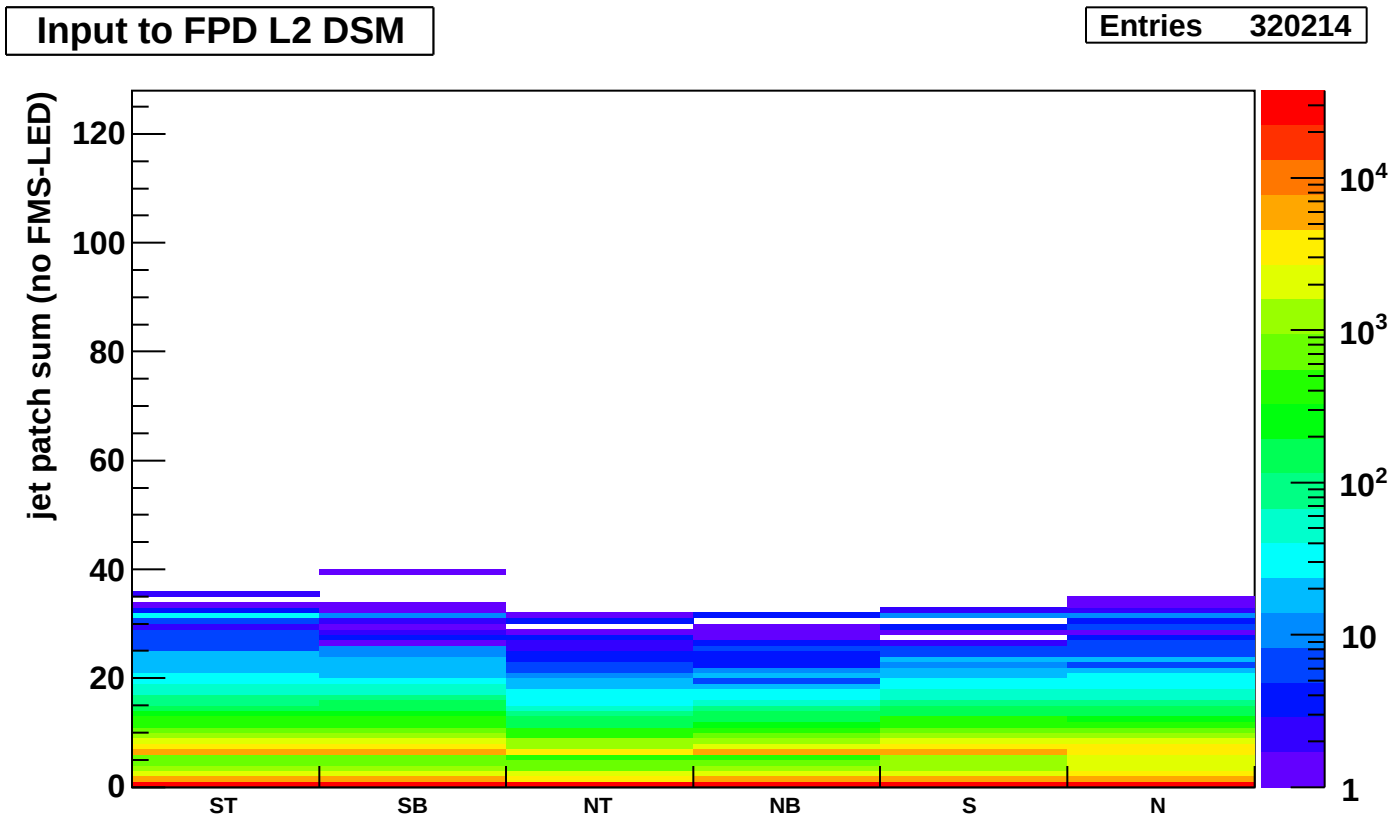

Input to FPD L2 DSM
Entries 67
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

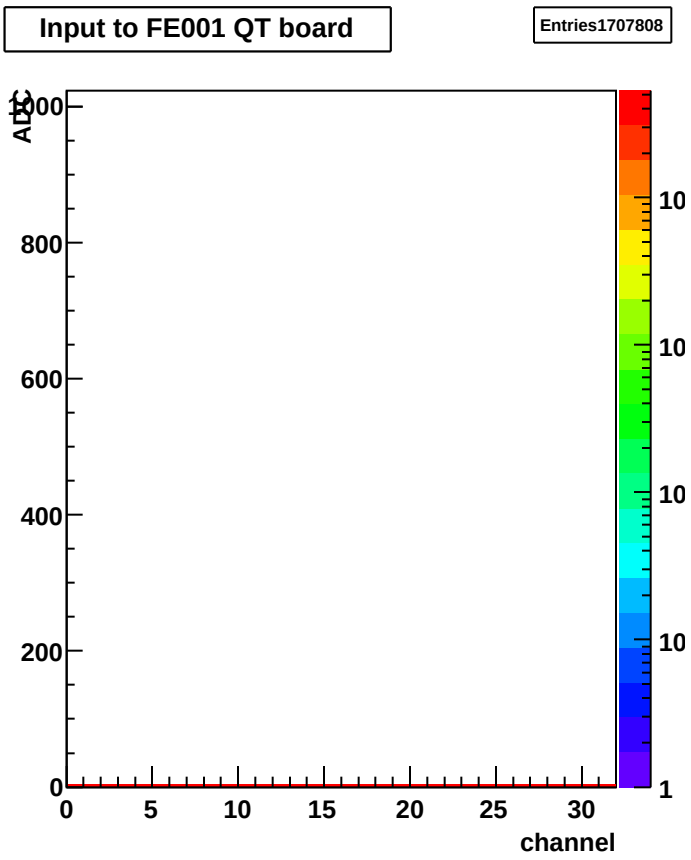

Input to FPD L2 DSM
Entries 160107
CL bits
56
8
4147
2093
6575
2342
1192
12975
33819
46824
50076
SMALL
LARGE-S
LARGE-N

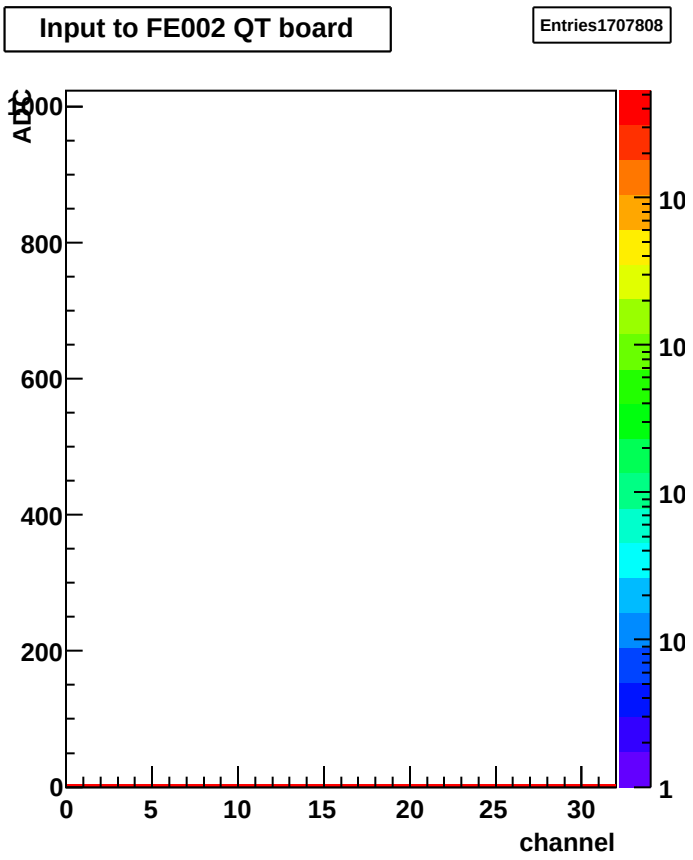

Input to FPD L2 DSM
Entries 106680
CL bits - simulated
4
1
1
5
1
53309
53359
SMALL
LARGE-S
LARGE-N

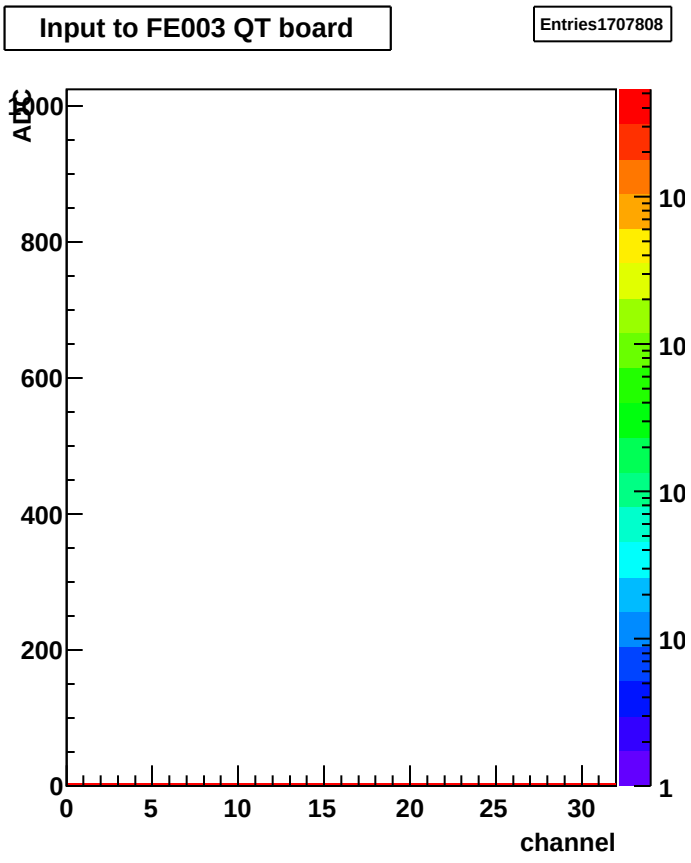

Input to FPD L2 DSM
Entries 160107
HT bits
1516
902
231
3596
4209
17271
8544
44048
35196
44594
SMALL
LARGE-S
LARGE-N

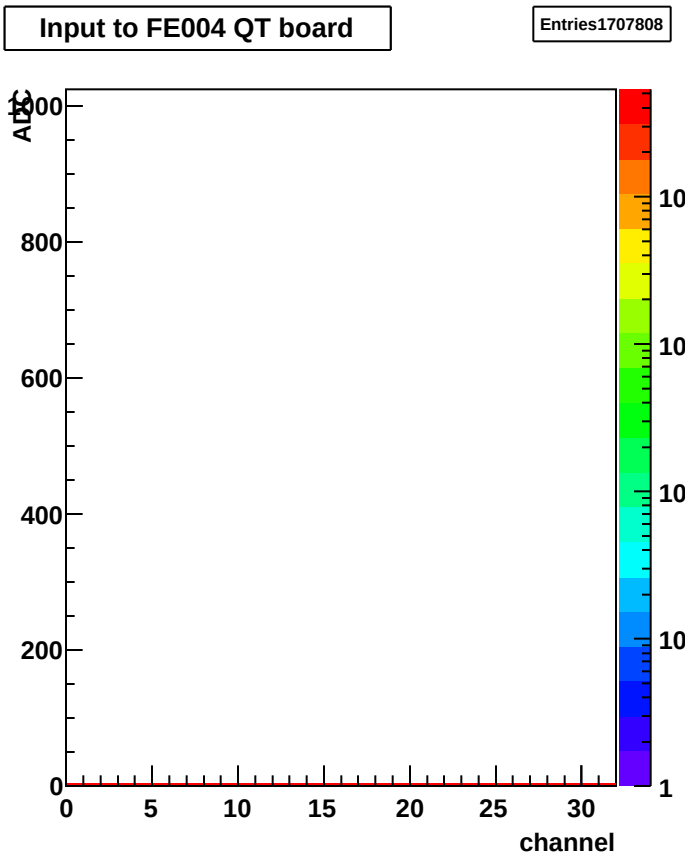

Input to FPD L2 DSM
Entries 18
HT bits - simulated
1
1
2
1
9
3
1
SMALL
LARGE-S
LARGE-N

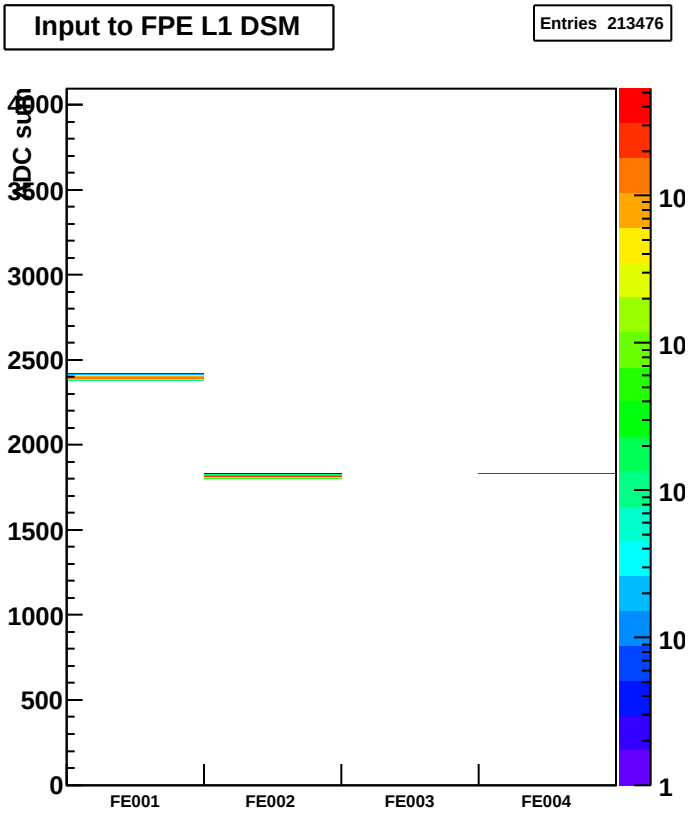

Input to FPD L2 DSM
Entries 320214
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

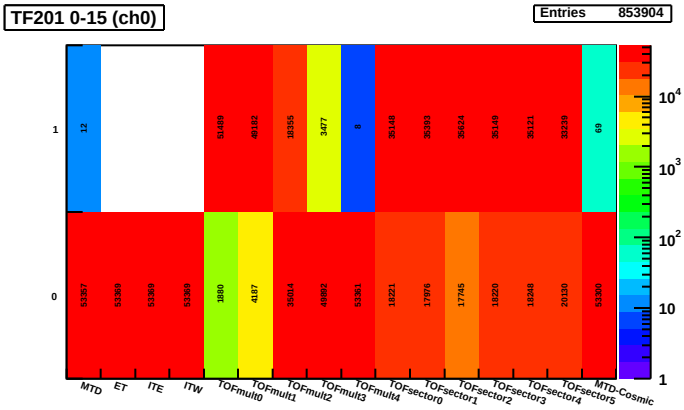

Input to FE001 QT board
Entries1707808
ADC
channel

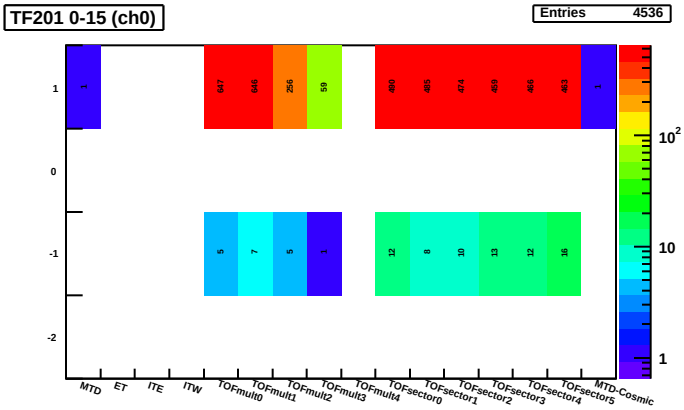

Input to FE002 QT board
Entries1707808
ADC
channel

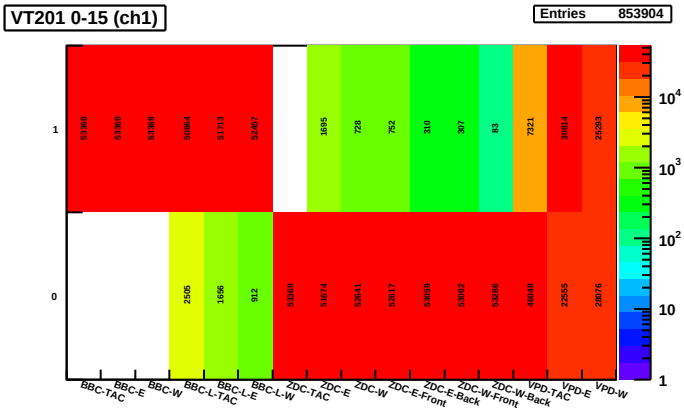

Input to FE003 QT board
Entries1707808
ADC
channel

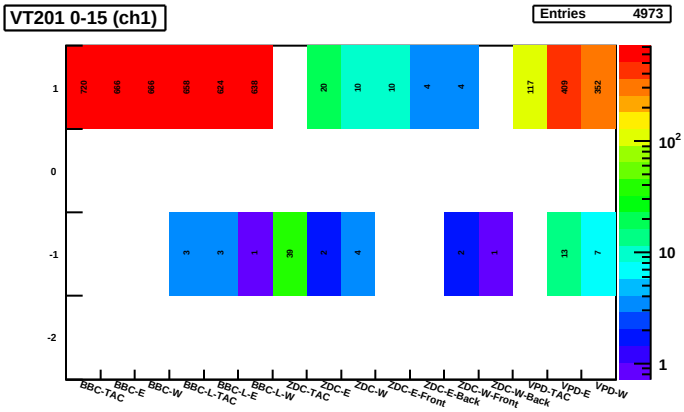

Input to FE004 QT board
Entries1707808
ADC
channel

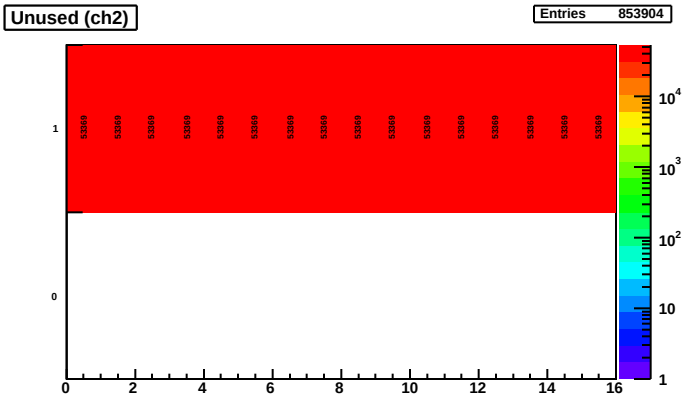

Input to FPE L1 DSM
Entries 213476
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

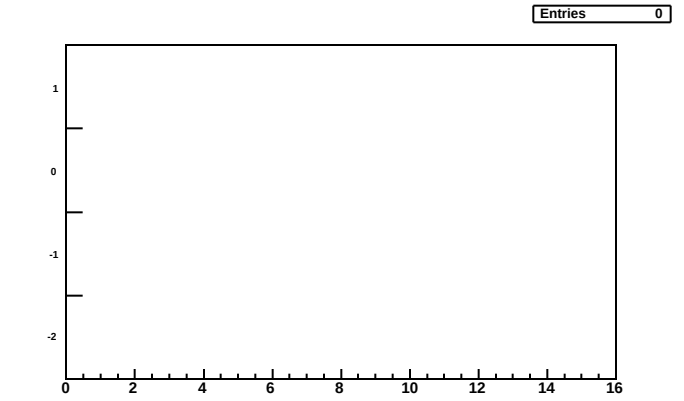

TF201 0-15 (ch0)
Entries 853904
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

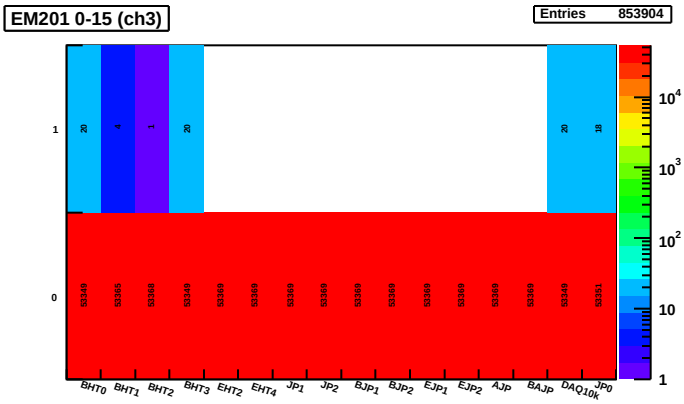

TF201 0-15 (ch0)
Entries 4536
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

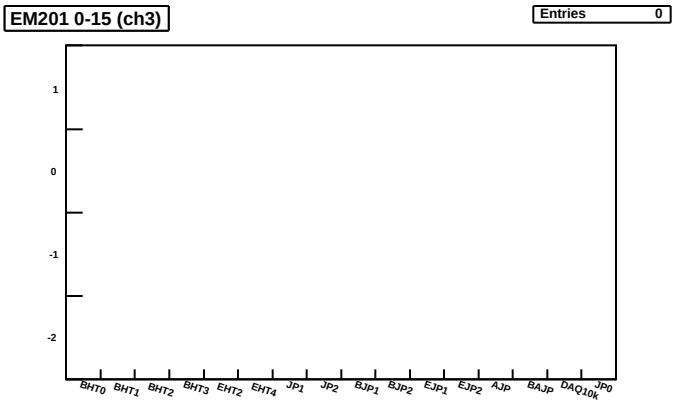

VT201 0-15 (ch1)
Entries 853904
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4973
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 853904


Entries 0


EM201 0-15 (ch3)
Entries 853904
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
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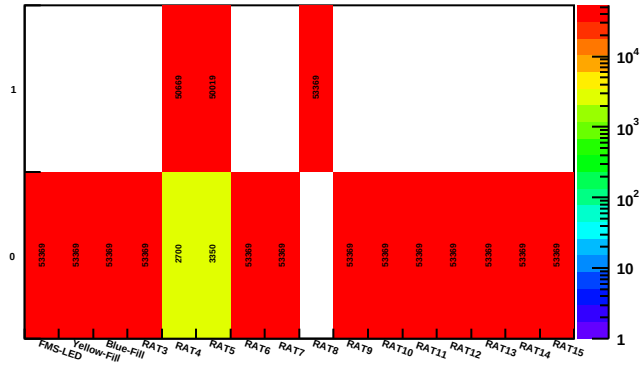
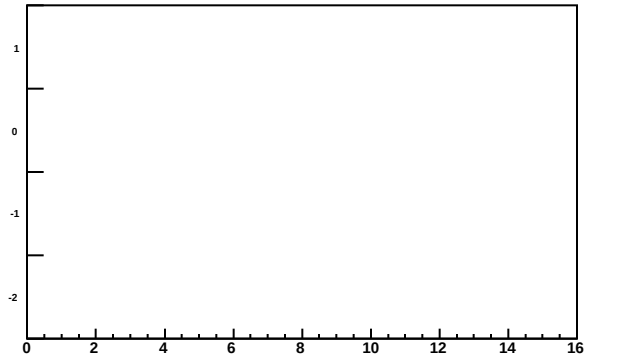
RAT board (ch4)





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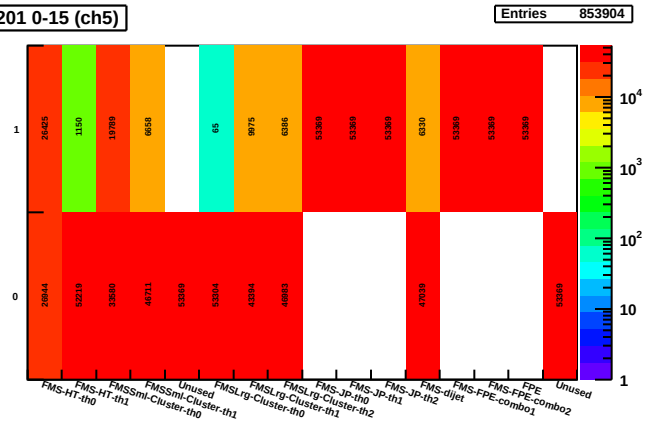

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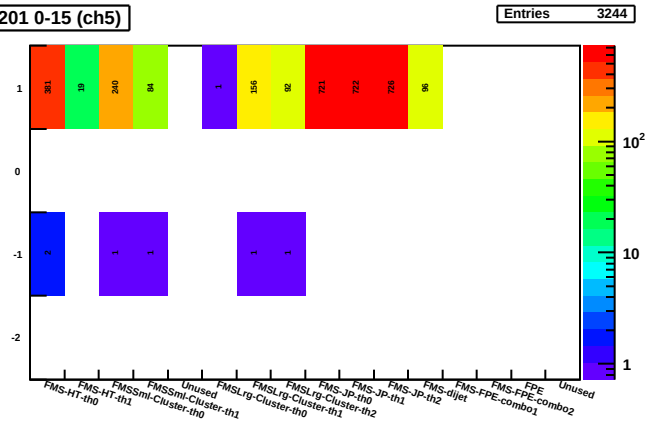

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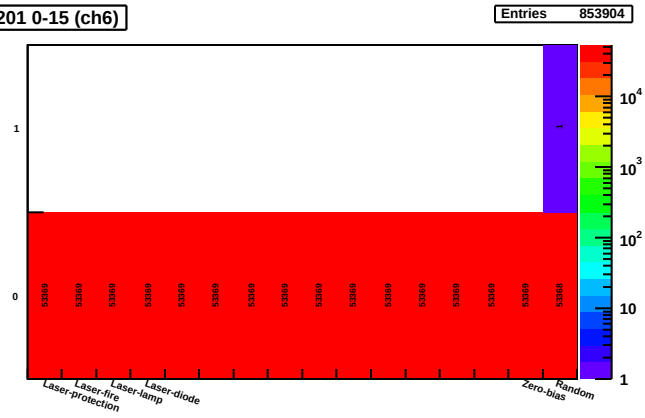

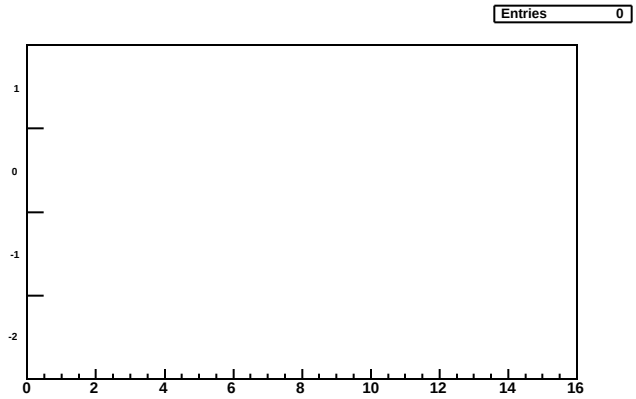
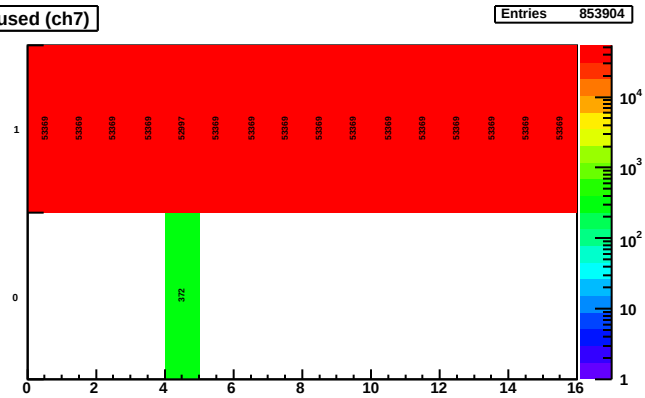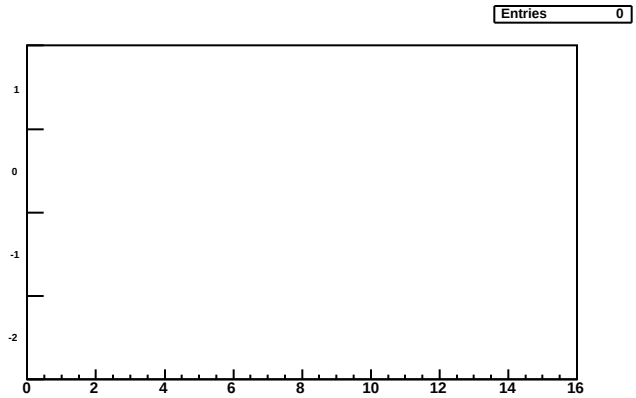
Unused (ch7)