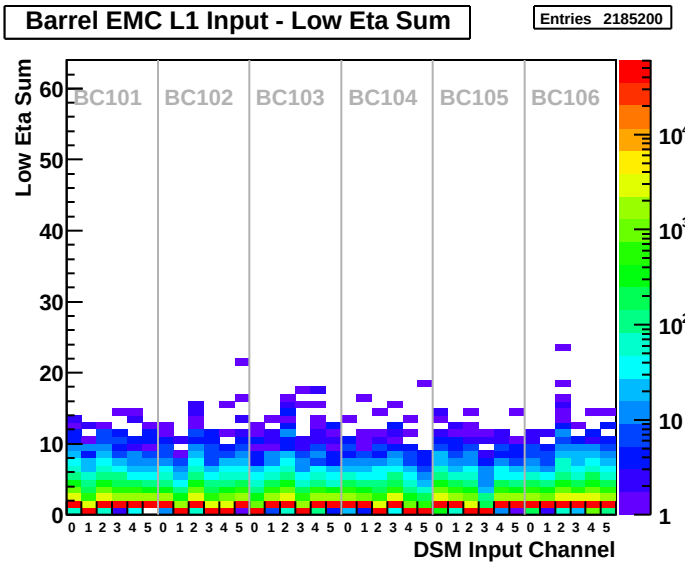
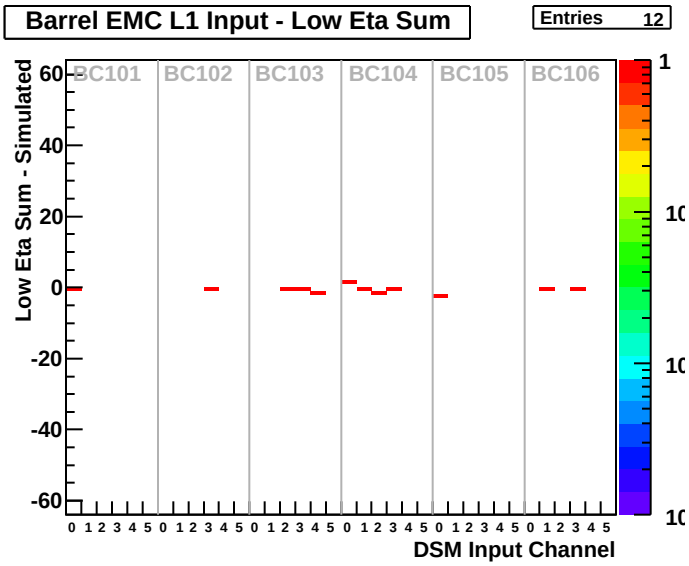
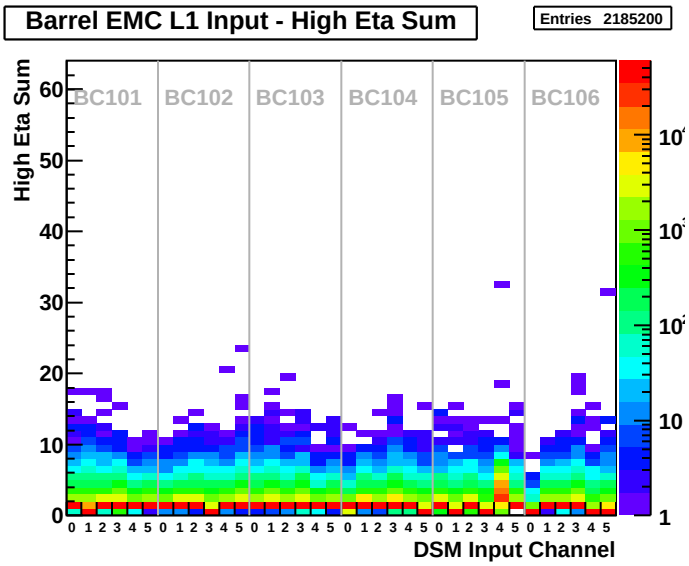
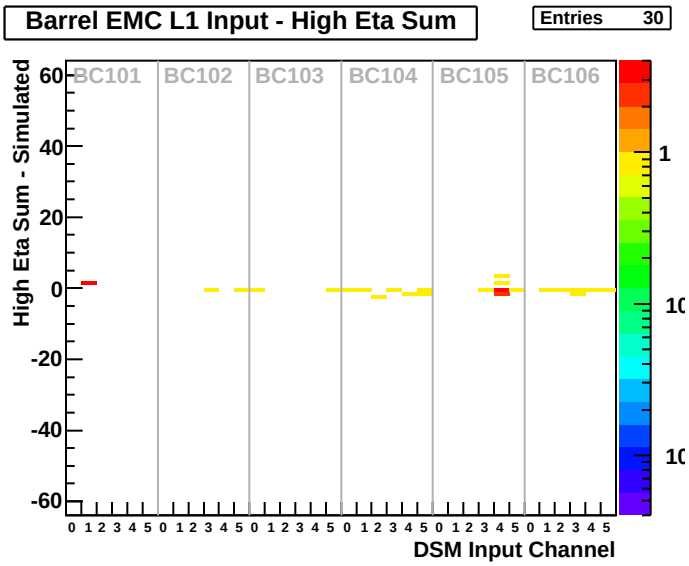
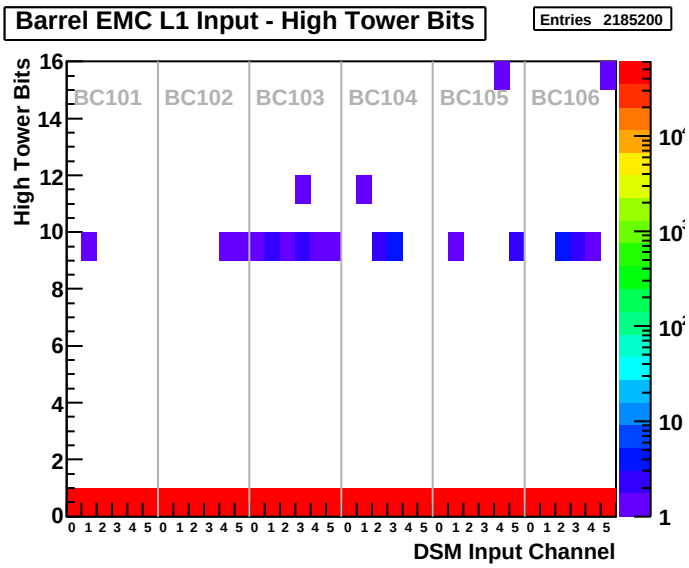
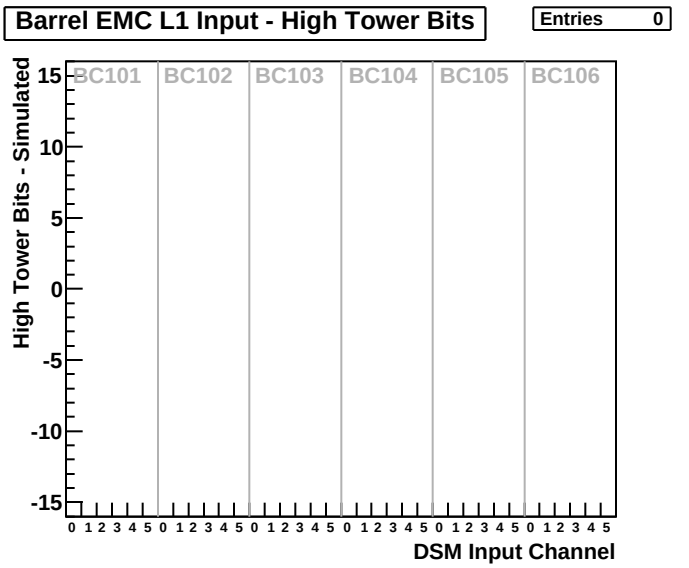

Barrel EMC L1 Input - Low Eta Sum
Entries 2185200
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

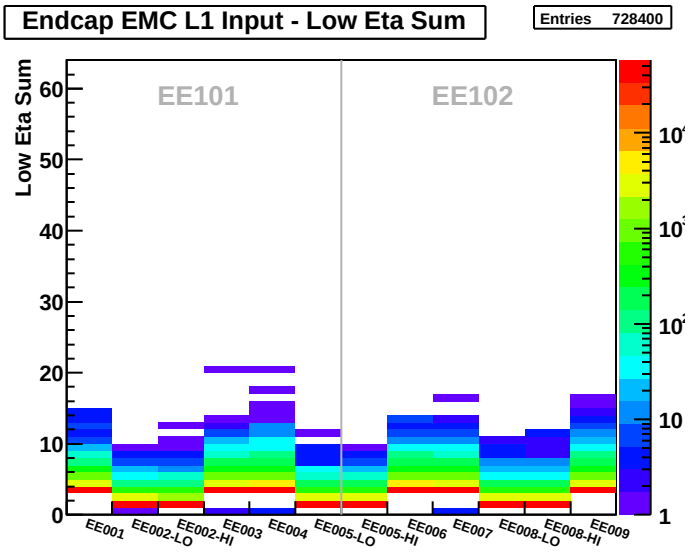

Barrel EMC L1 Input - Low Eta Sum
Entries 12
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

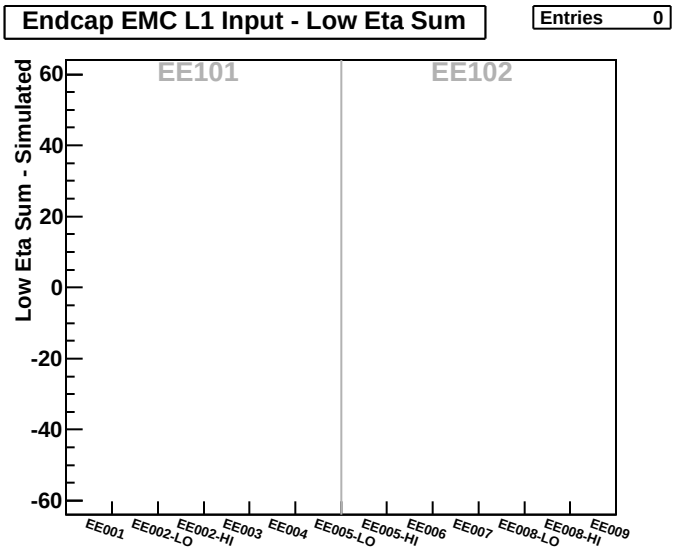

Barrel EMC L1 Input - High Eta Sum
Entries 2185200
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

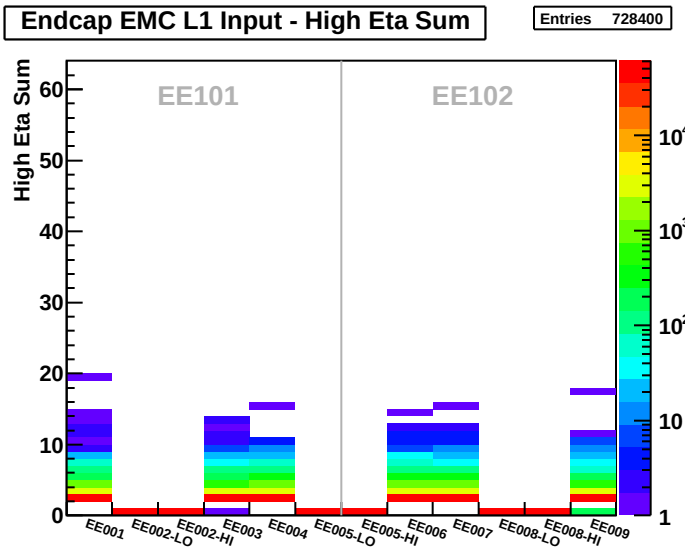

Barrel EMC L1 Input - High Eta Sum
Entries 30
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

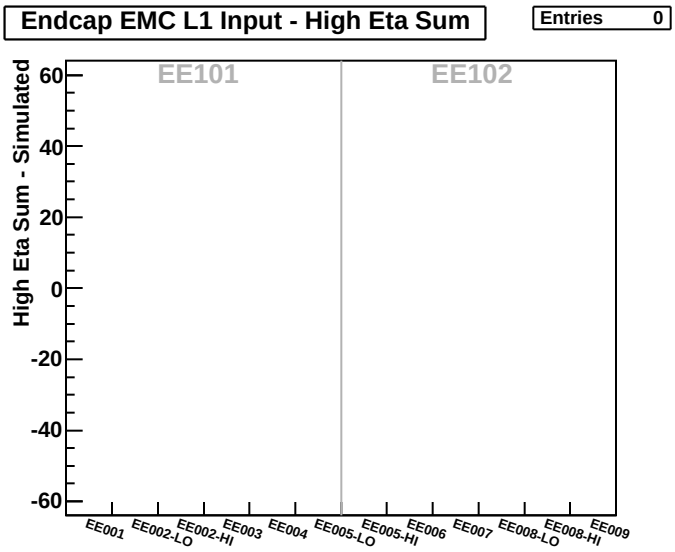

Barrel EMC L1 Input - High Tower Bits
Entries 2185200
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

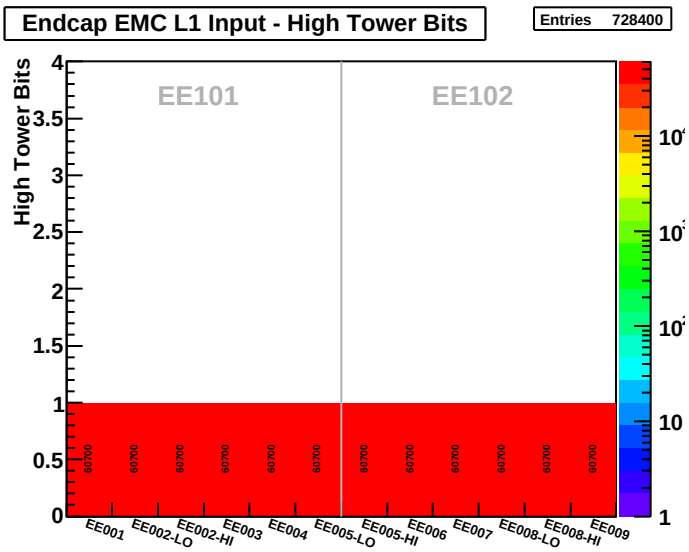

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

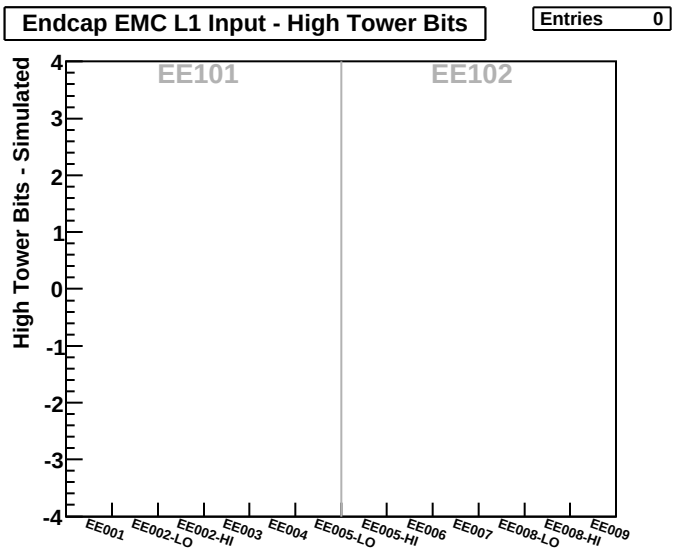

Endcap EMC L1 Input - Low Eta Sum
Entries 728400
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

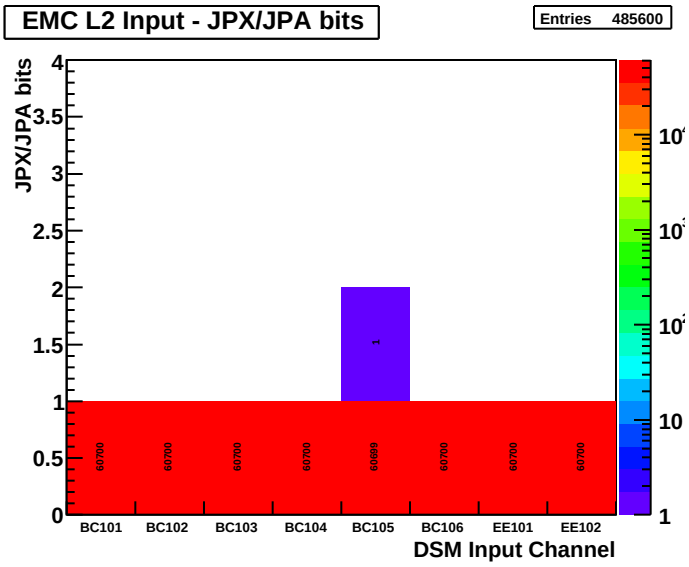

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

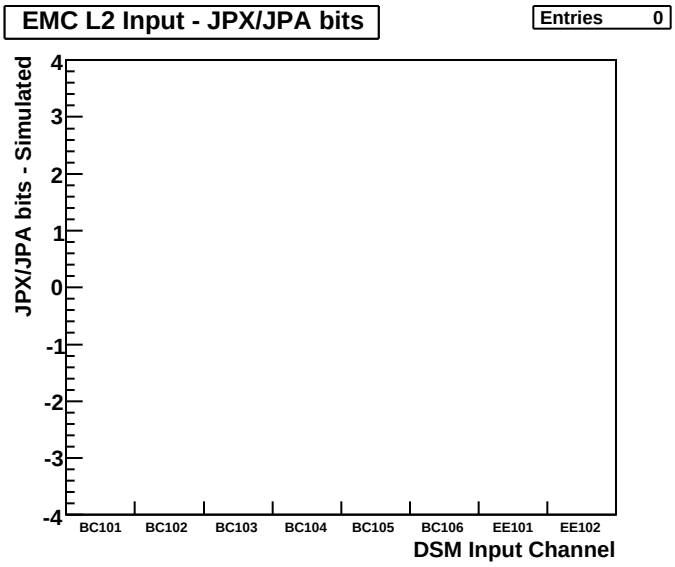

Endcap EMC L1 Input - High Eta Sum
Entries 728400
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

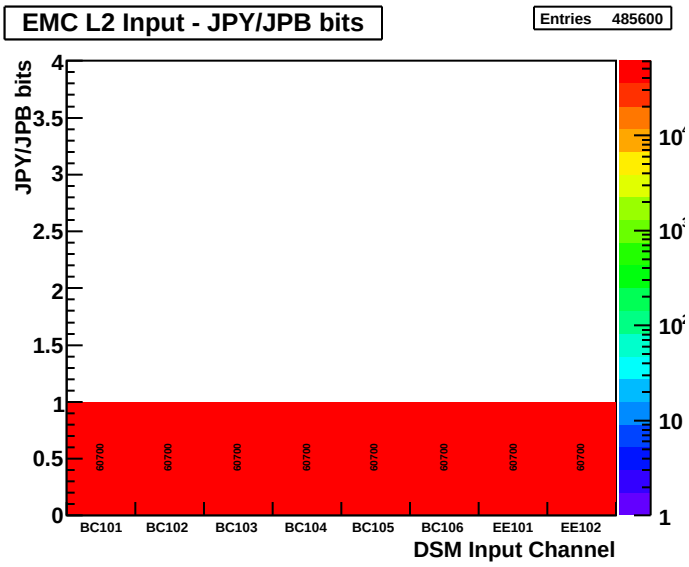

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

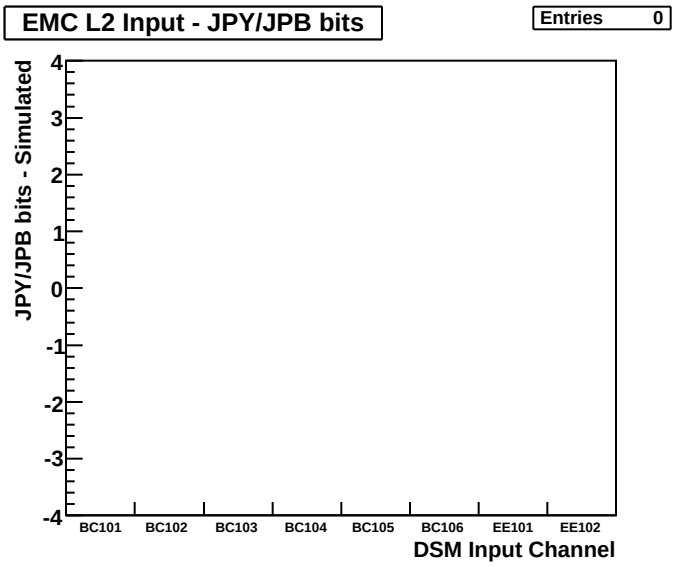

Endcap EMC L1 Input - High Tower Bits
Entries 728400
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

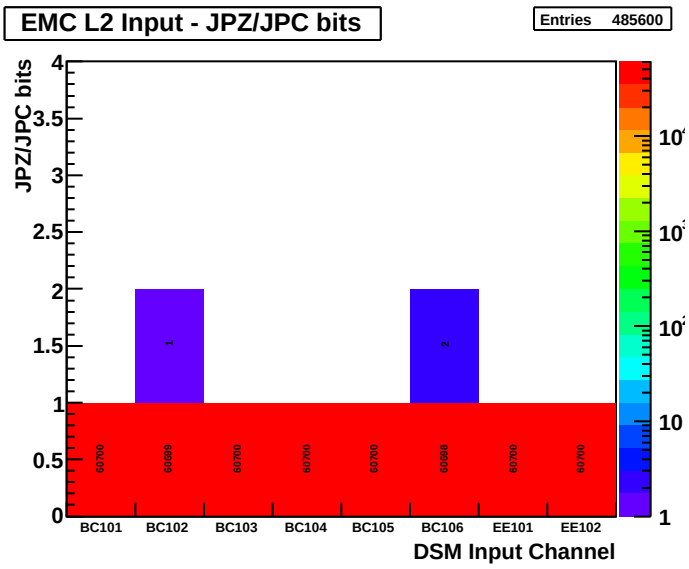

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

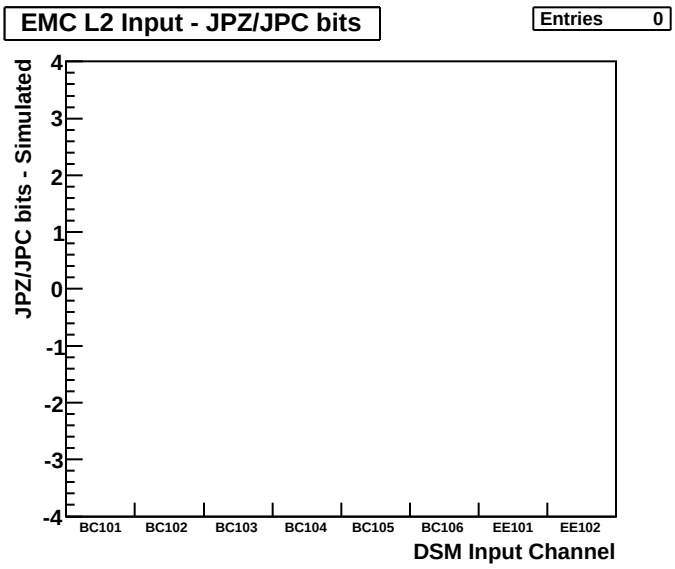

EMC L2 Input - JPX/JPA bits
Entries 485600
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

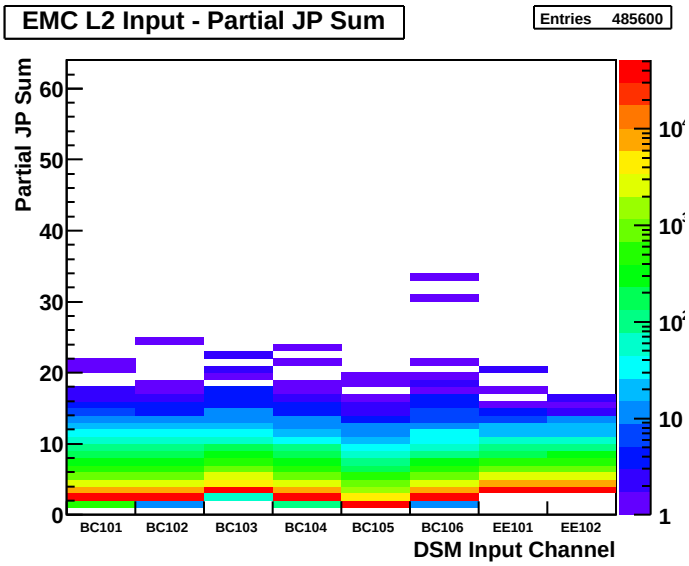

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

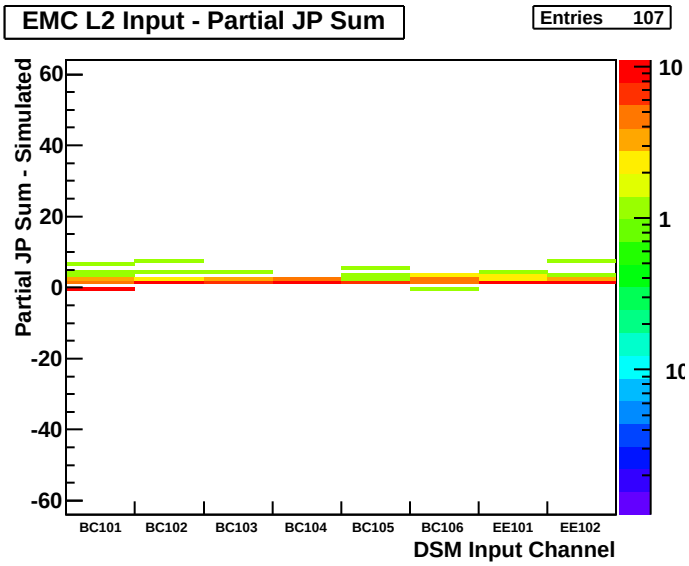

EMC L2 Input - JPY/JPB bits
Entries 485600
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

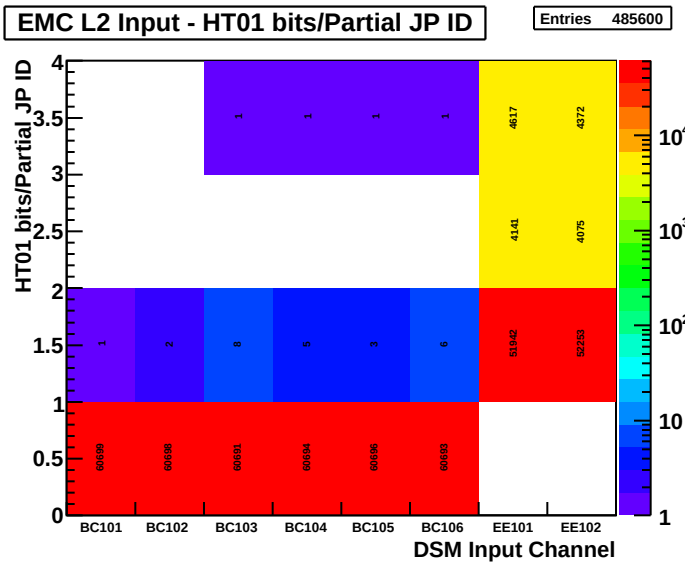

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

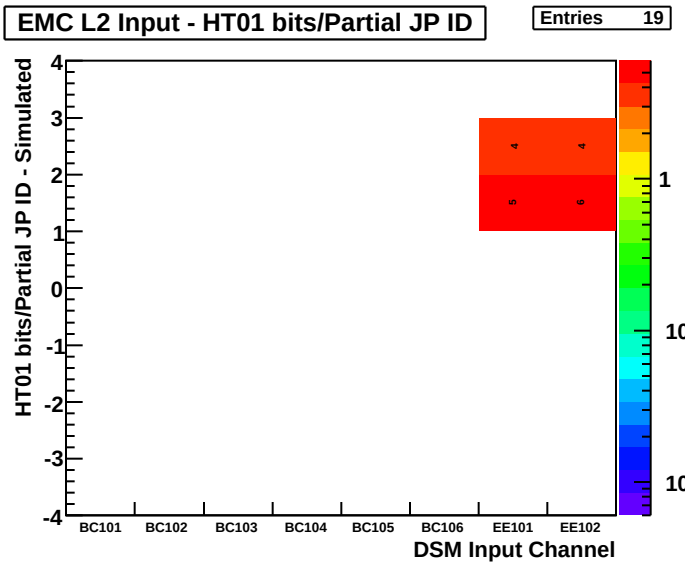

EMC L2 Input - JPZ/JPC bits
Entries 485600
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

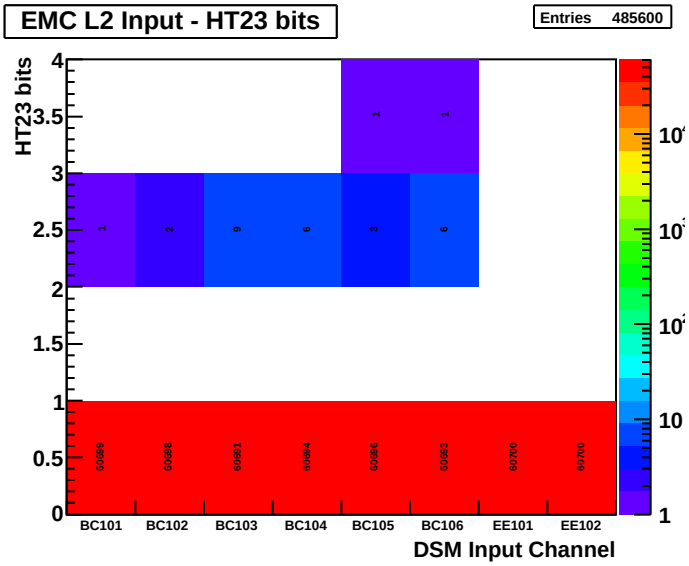

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

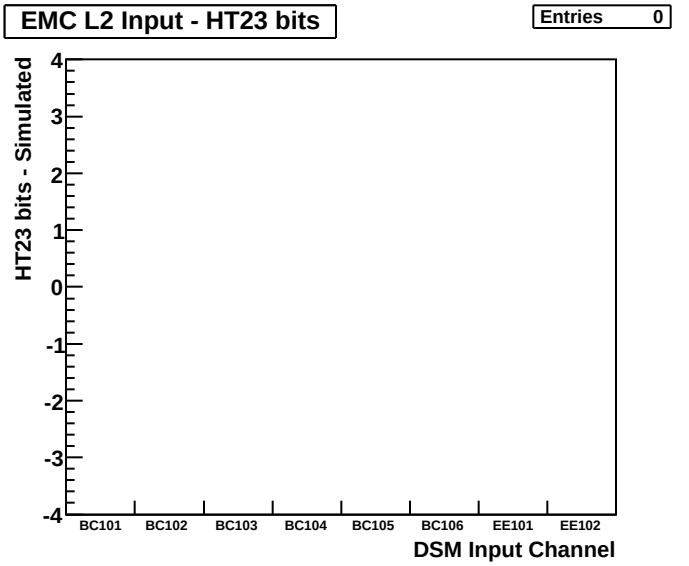

EMC L2 Input - Partial JP Sum
Entries 485600
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

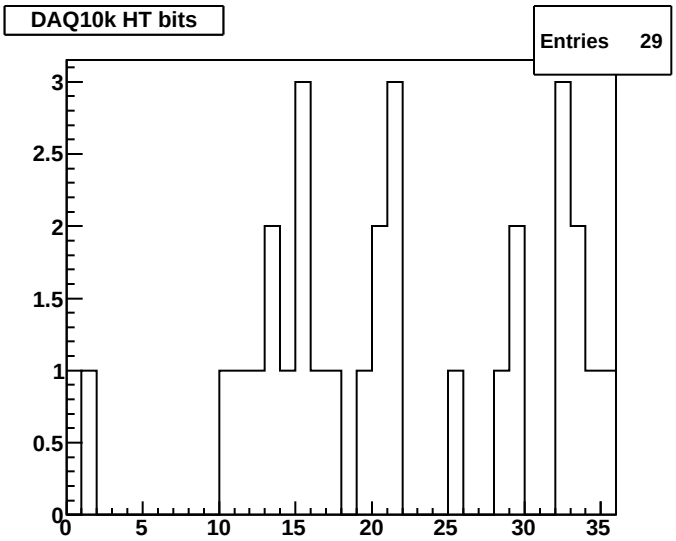

EMC L2 Input - Partial JP Sum
Entries 107
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

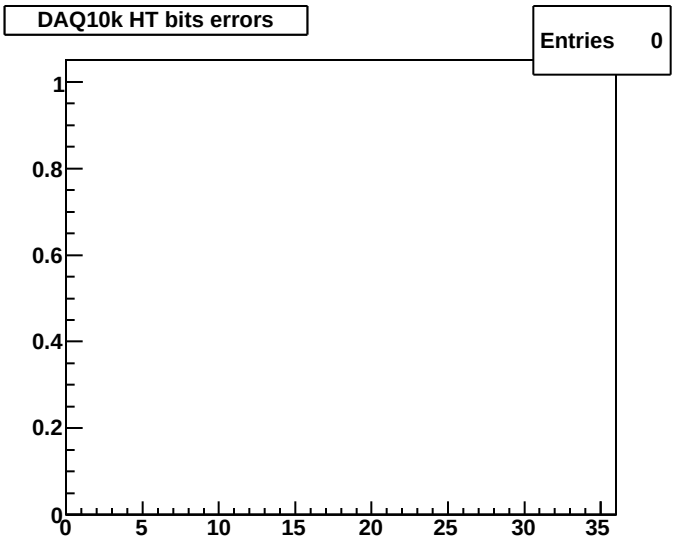

EMC L2 Input - HT01 bits/Partial JP ID
Entries 485600
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

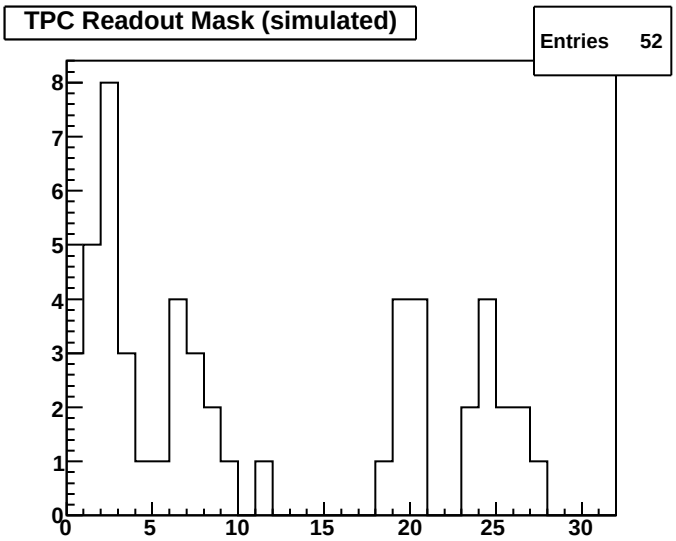

EMC L2 Input - HT01 bits/Partial JP ID
Entries 19
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

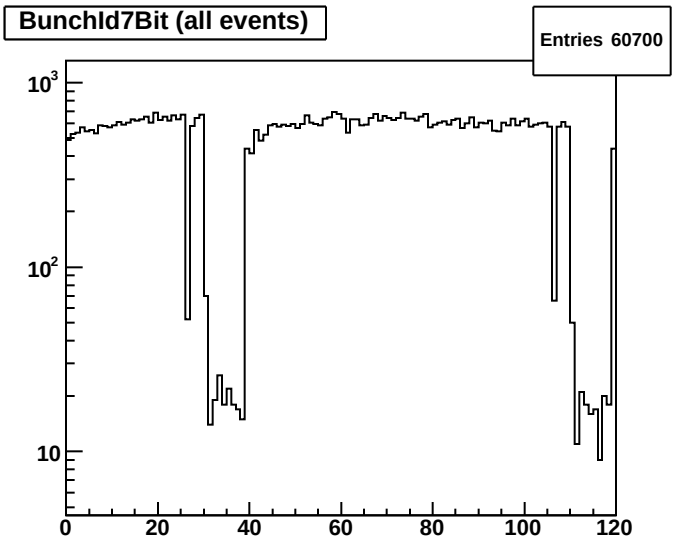

EMC L2 Input - HT23 bits
Entries 485600
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

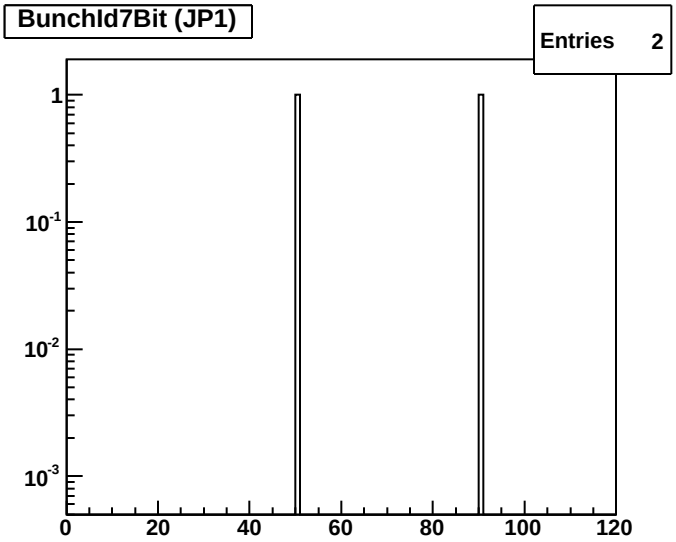

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

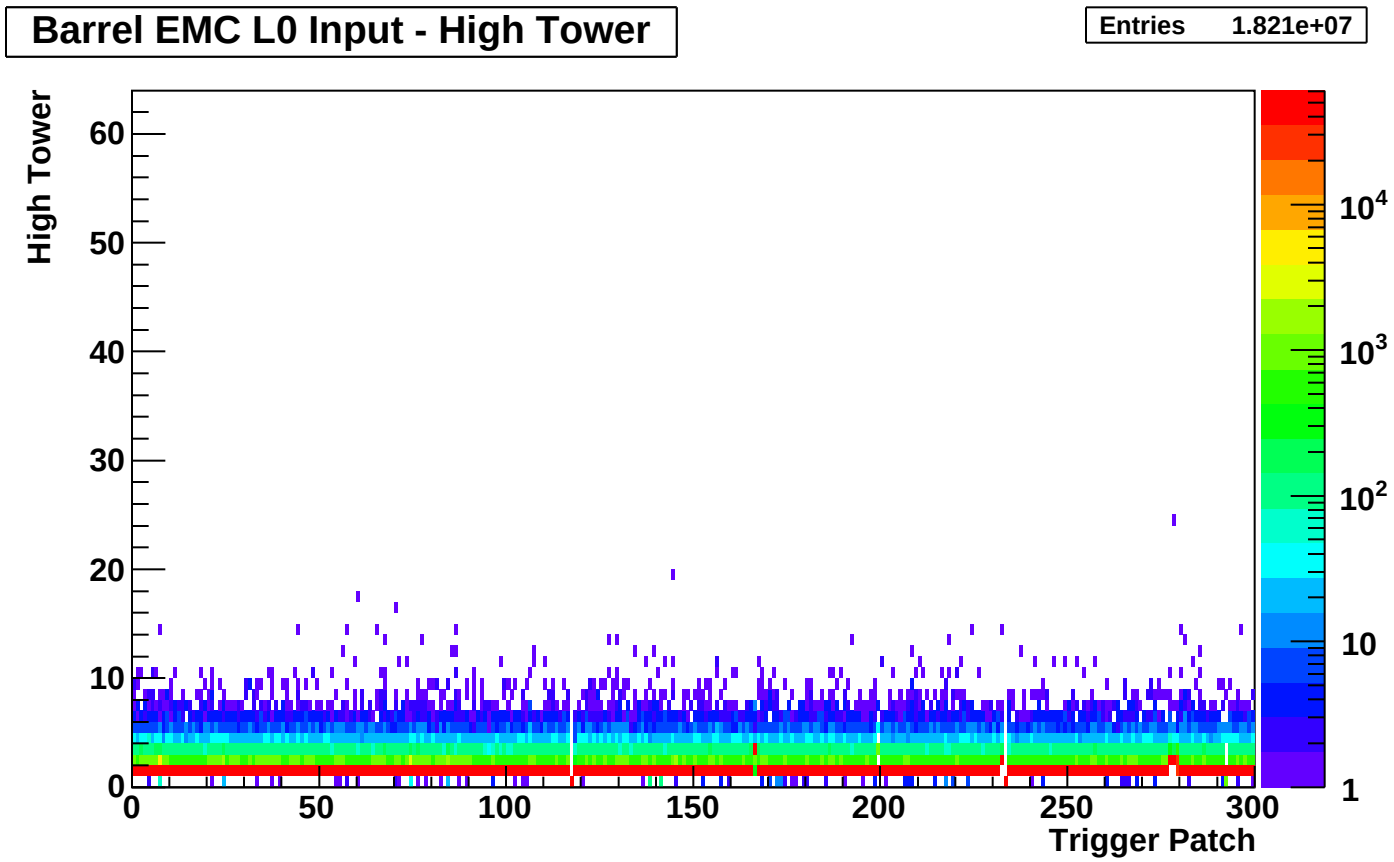

DAQ10k HT bits
Entries 29

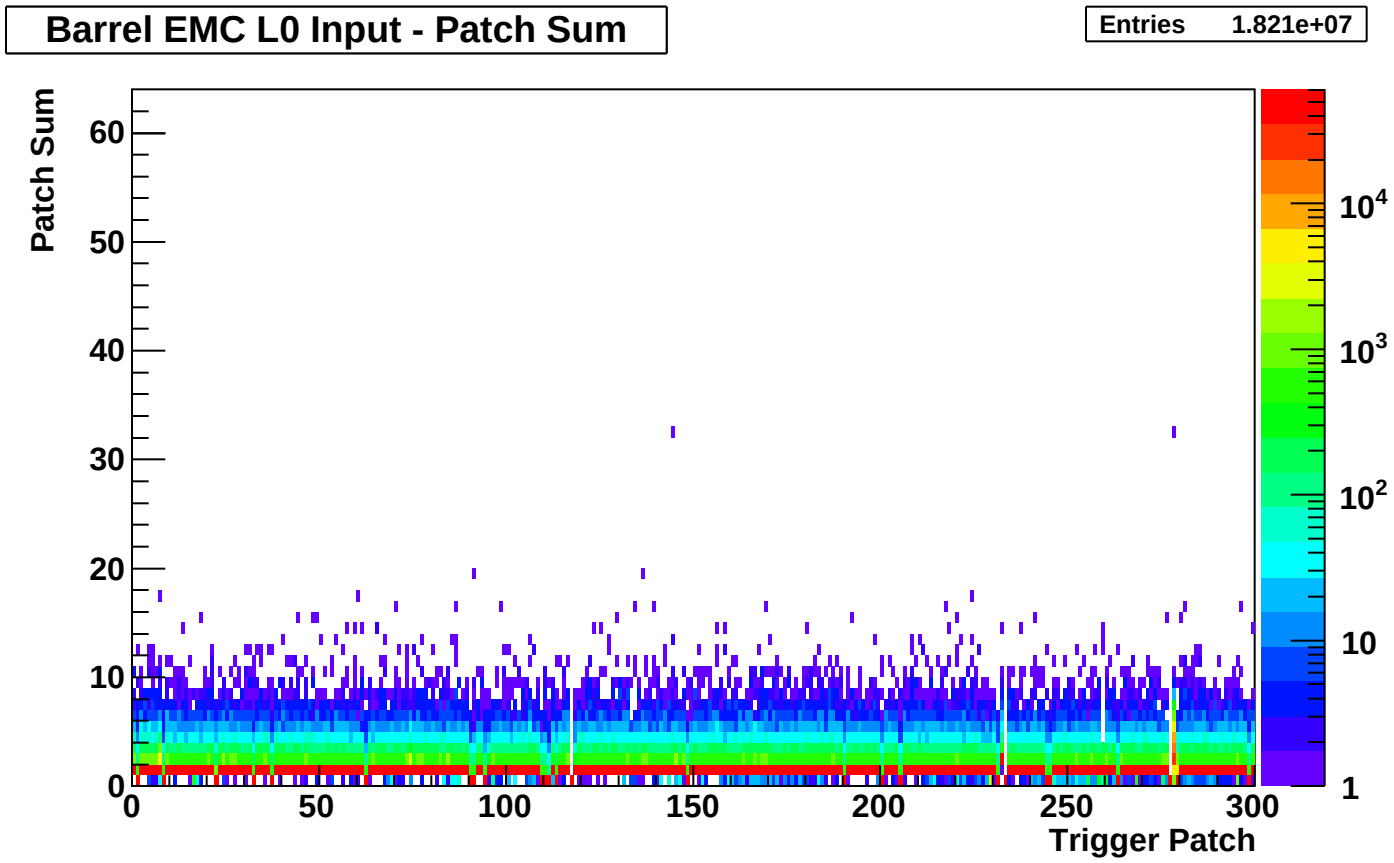

DAQ10k HT bits errors
Entries 0

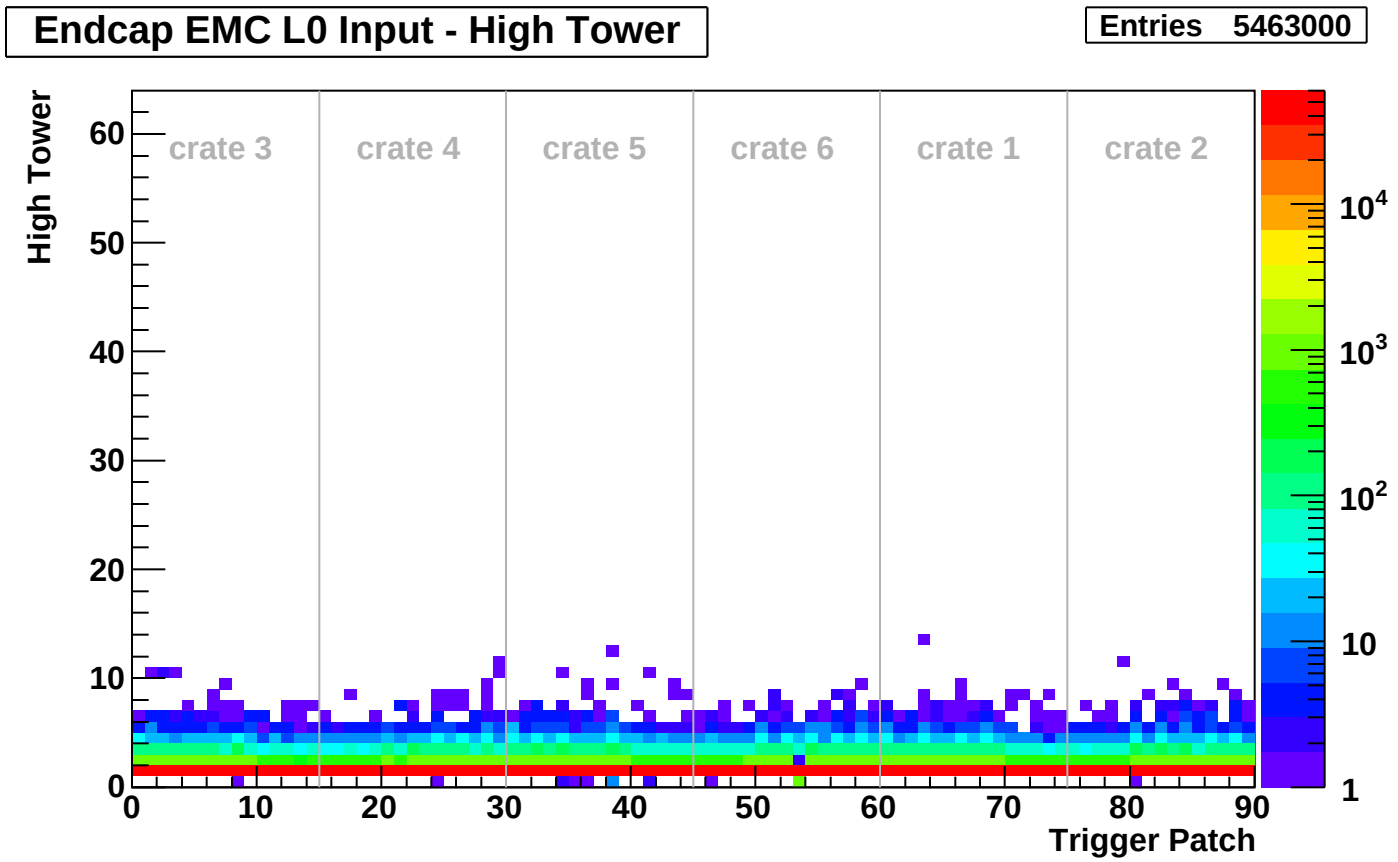

TPC Readout Mask (simulated)
Entries 52

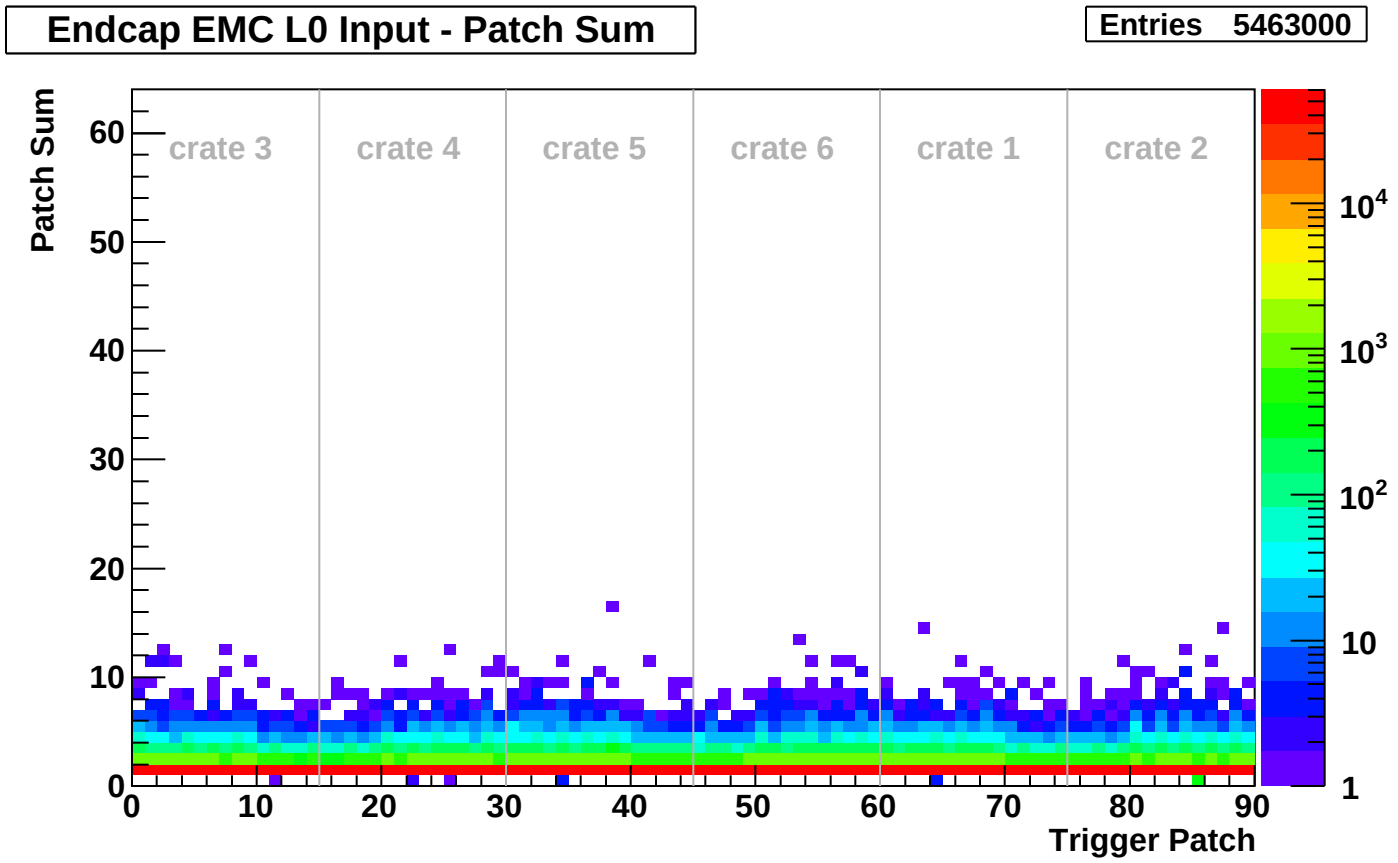

BunchId7Bit (all events)
Entries 60700

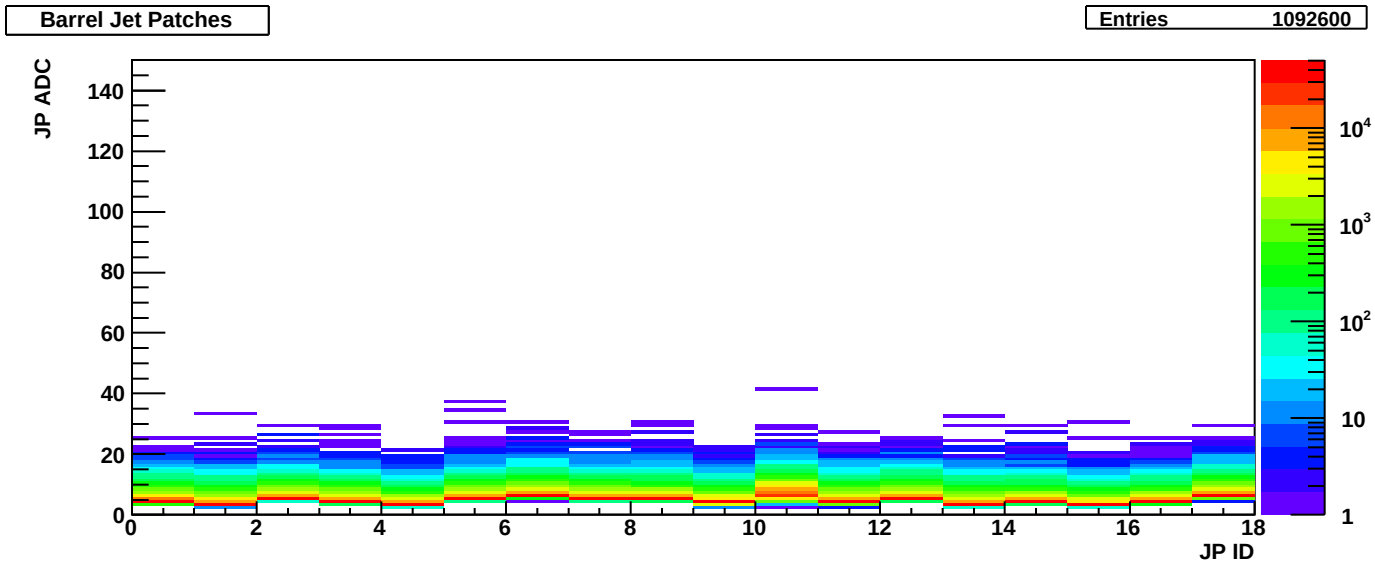

BunchId7Bit (JP1)
Entries 2

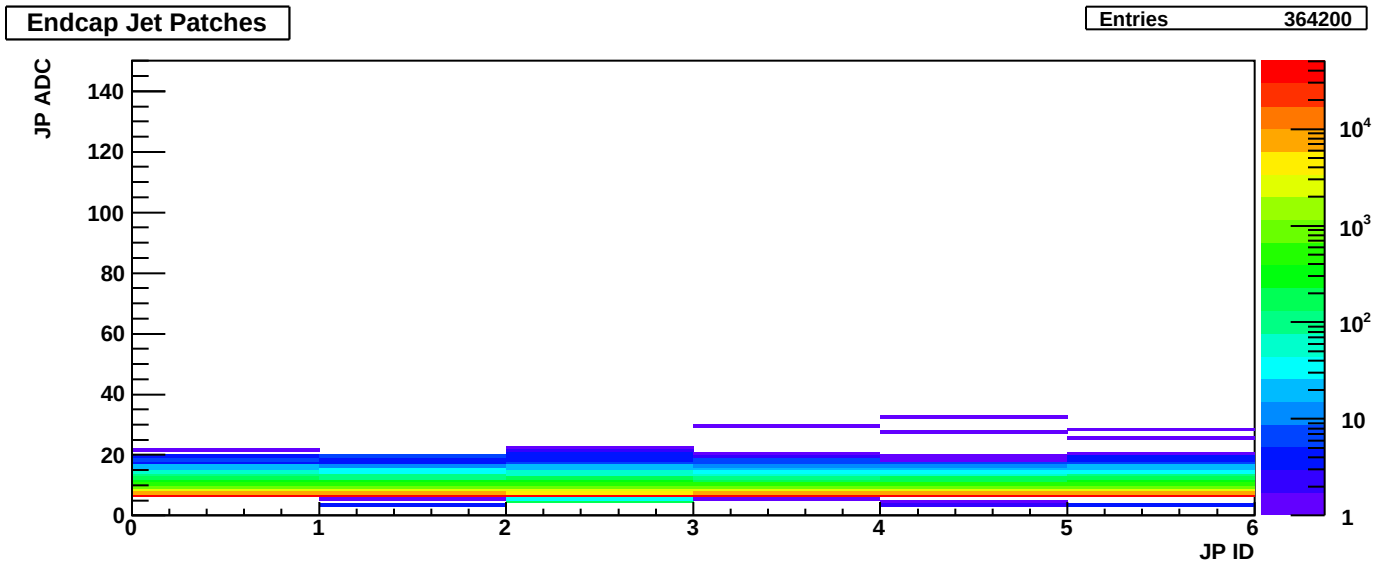

Barrel EMC L0 Input - High Tower
Entries 1.821e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

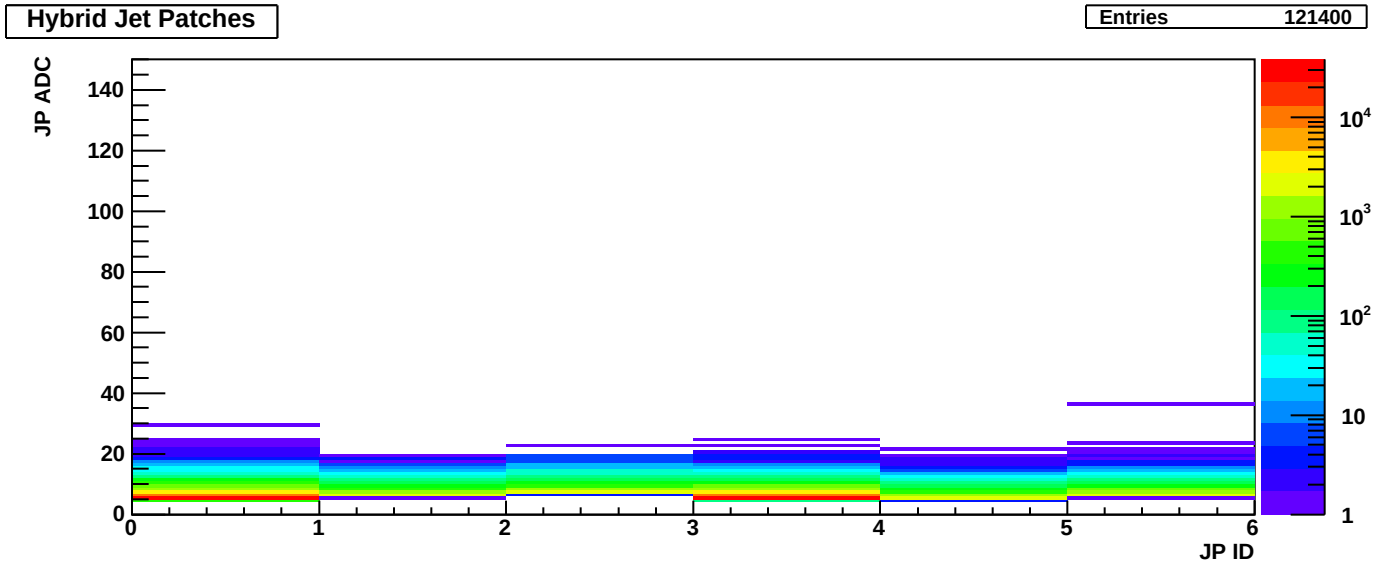

Barrel EMC L0 Input - Patch Sum
Entries 1.821e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

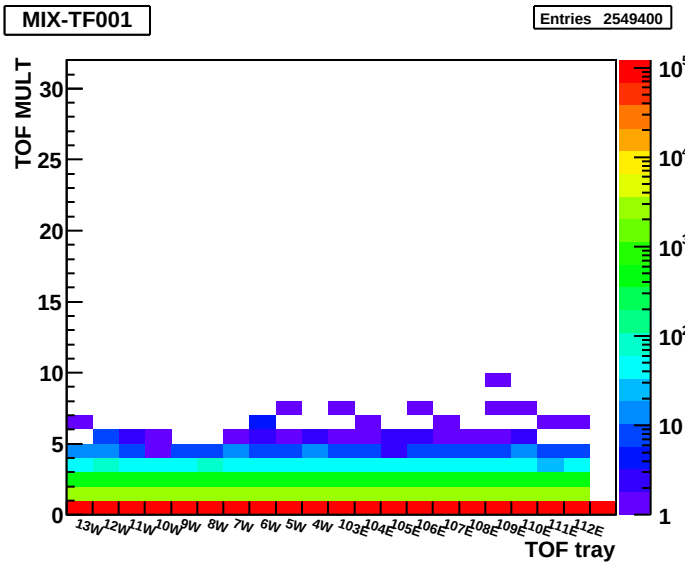

Endcap EMC L0 Input - High Tower
Entries 5463000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

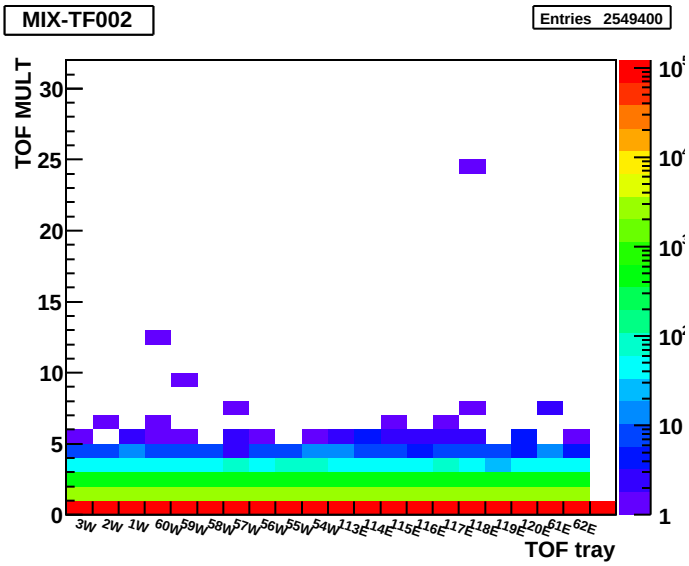

Endcap EMC L0 Input - Patch Sum
Entries 5463000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

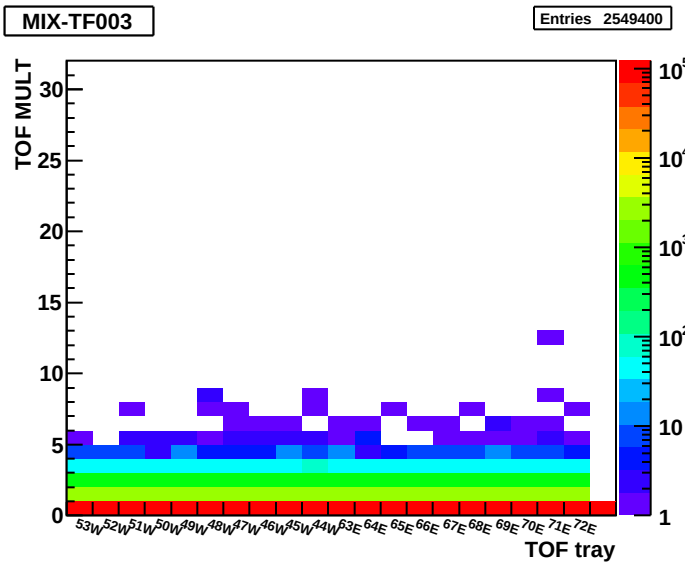

Barrel Jet Patches
Entries 1092600
JP ADC
JP ID

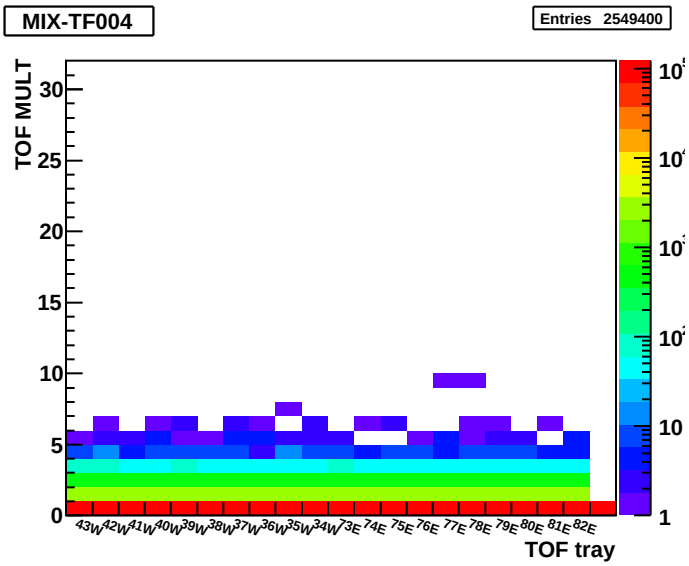

Endcap Jet Patches
Entries 364200
JP ADC
JP ID

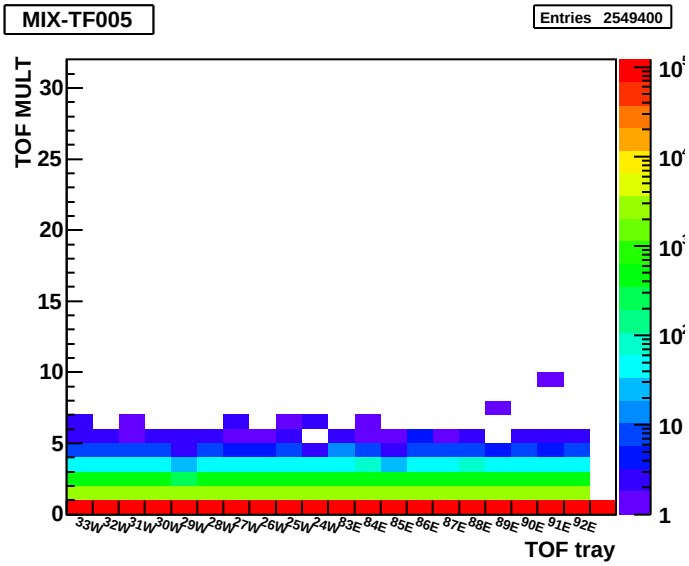

Hybrid Jet Patches
Entries 121400
JP ADC
JP ID

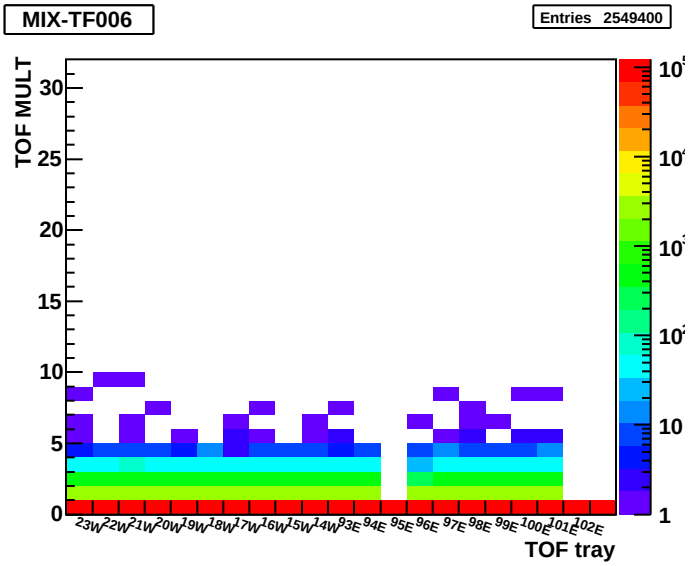

MIX-TF001
Entries 2549400
TOF MULT
TOF tray

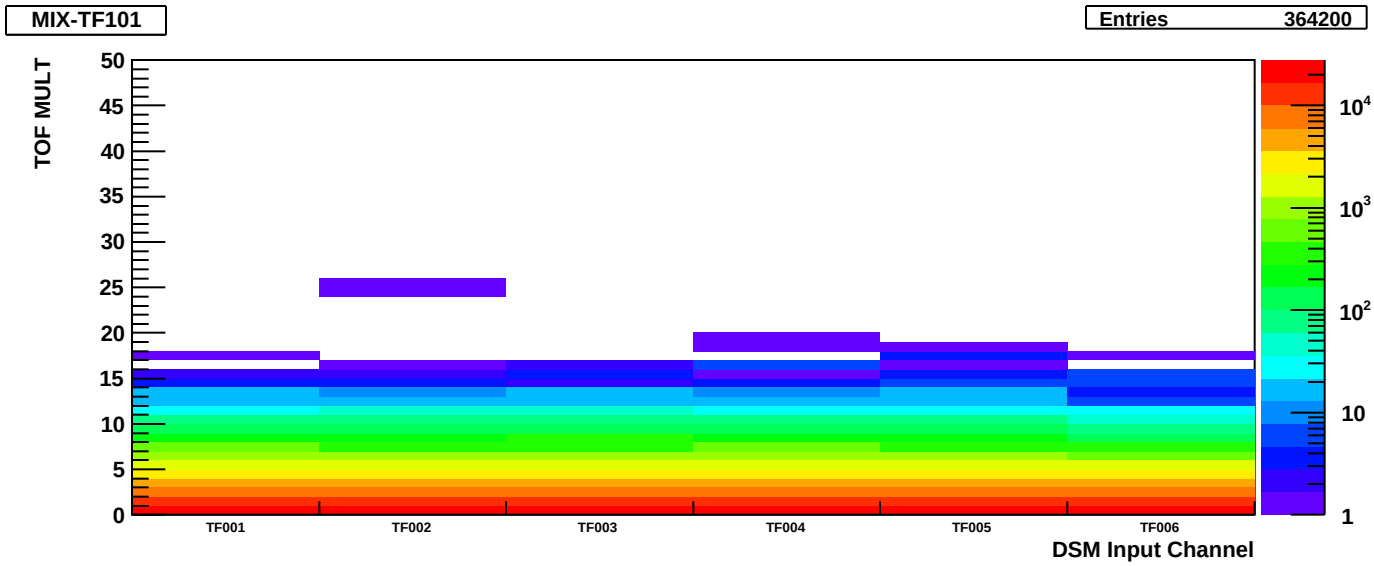

MIX-TF002
Entries 2549400
TOF MULT
TOF tray

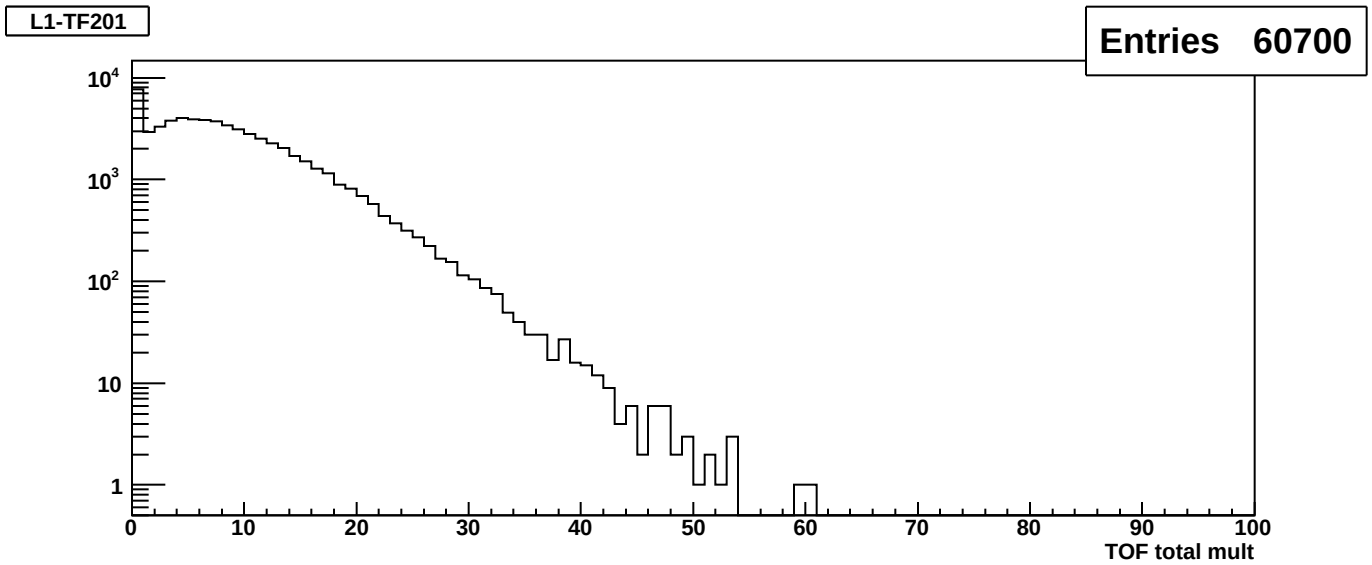

MIX-TF003
Entries 2549400
TOF MULT
TOF tray

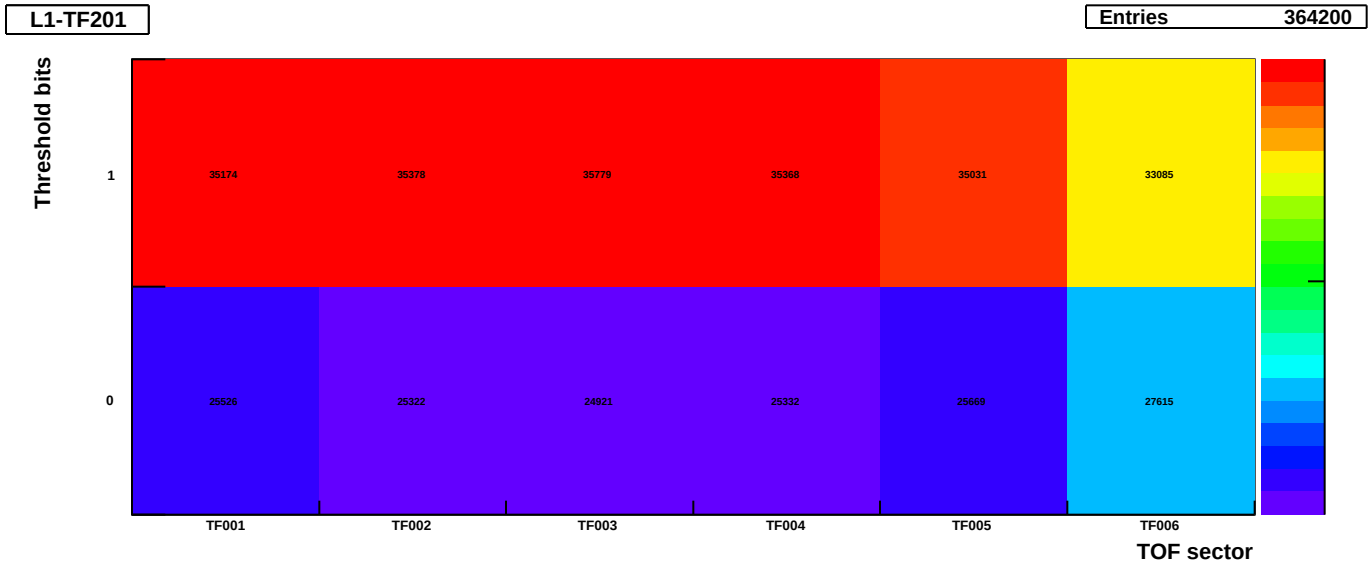

MIX-TF004
Entries 2549400
TOF MULT
TOF tray

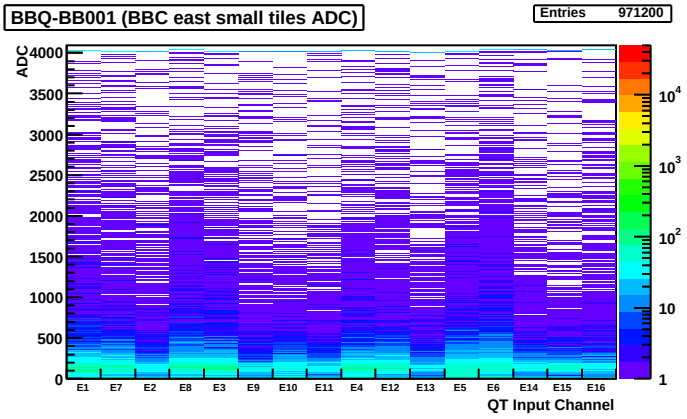

MIX-TF005
Entries 2549400
TOF MULT
TOF tray

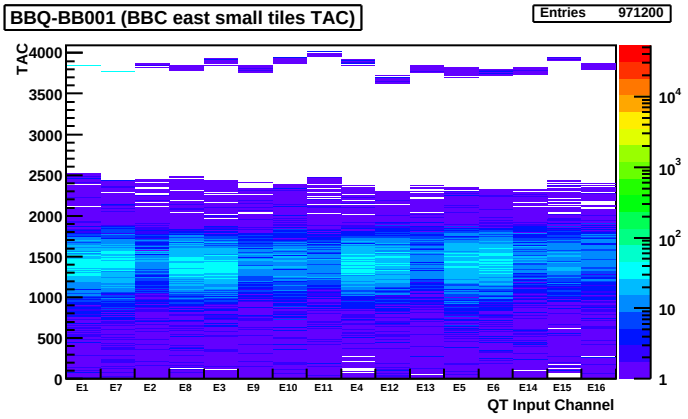

MIX-TF006
Entries 2549400
TOF MULT
TOF tray

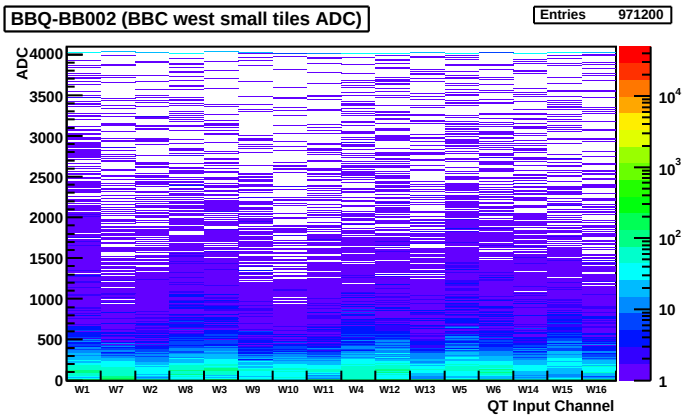

MIX-TF101
Entries 364200
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

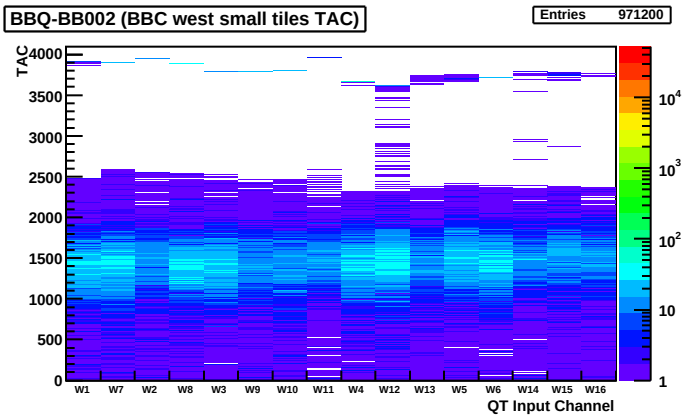

L1-TF201
Entries 60700
TOF total mult

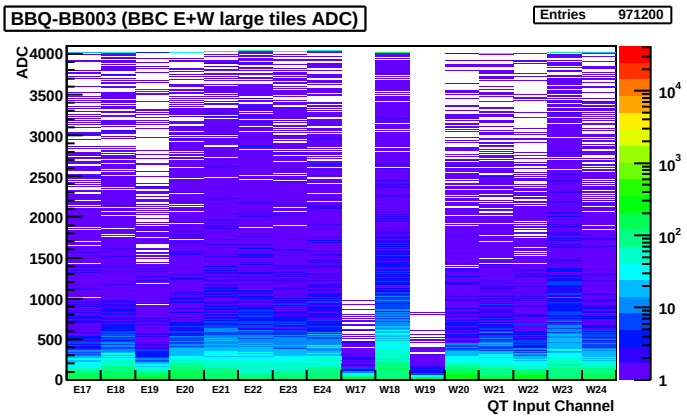

L1-TF201
Entries 364200
Threshold bits
35174
35378
35779
35368
35031
33085
25526
25322
24921
25332
25669
27615
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

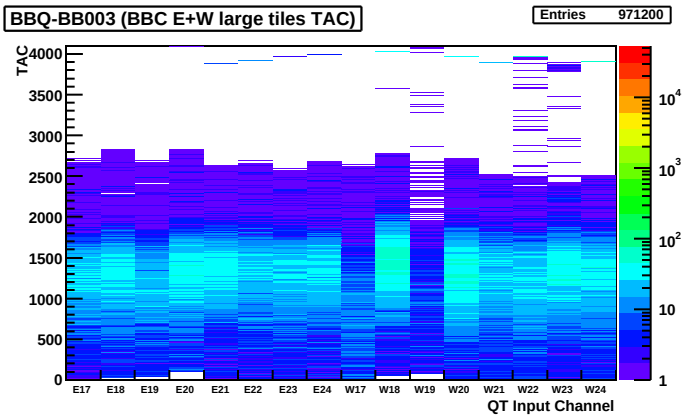

BBQ-BB001 (BBC east small tiles ADC)
Entries 971200
ADC
QT Input Channel

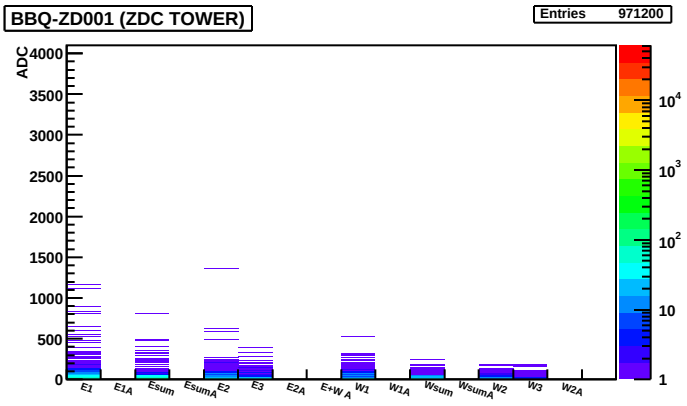

BBQ-BB001 (BBC east small tiles TAC)
Entries 971200
TAC
QT Input Channel

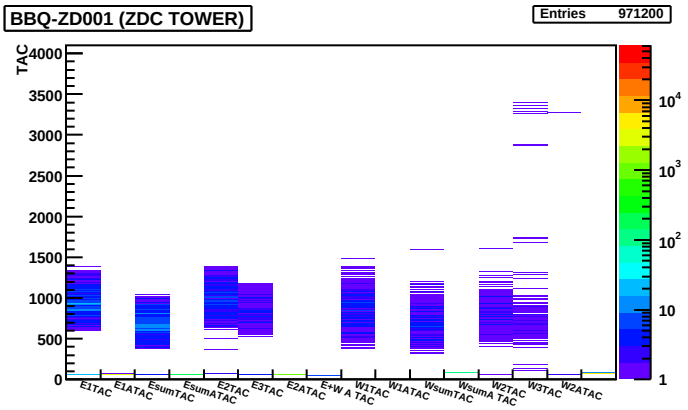

BBQ-BB002 (BBC west small tiles ADC)
Entries 971200
ADC
QT Input Channel

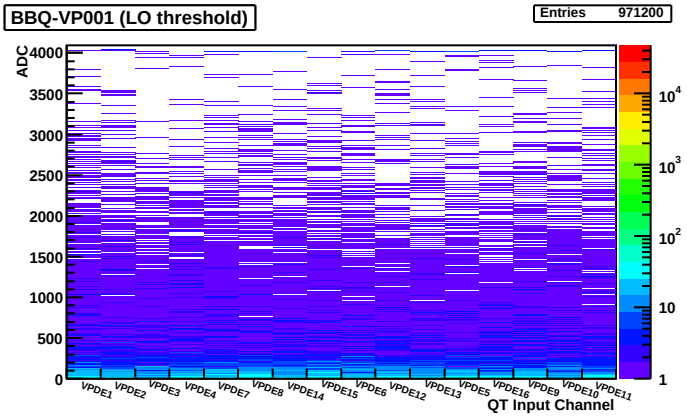

BBQ-BB002 (BBC west small tiles TAC)
Entries 971200
TAC
QT Input Channel

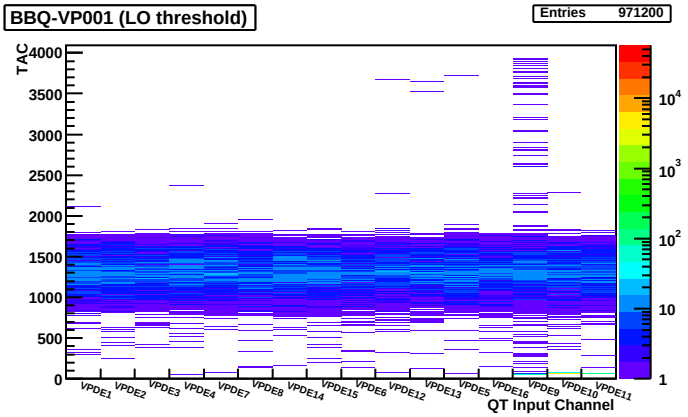

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 971200
ADC
QT Input Channel

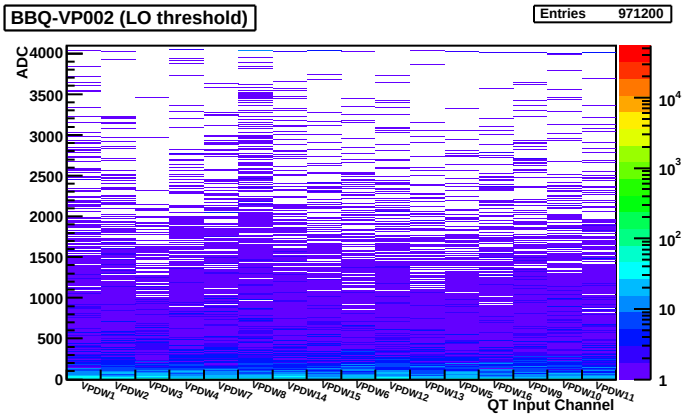

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 971200
TAC
QT Input Channel

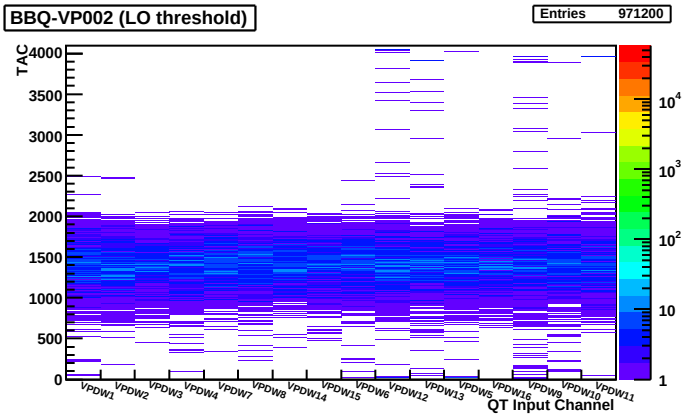

BBQ-ZD001 (ZDC TOWER)
Entries 971200
ADC

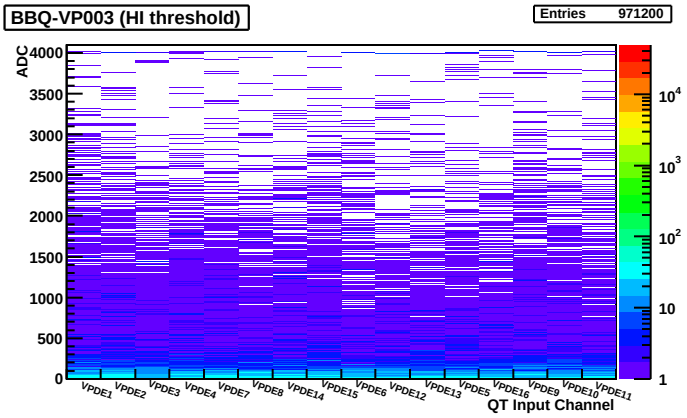

BBQ-ZD001 (ZDC TOWER)
Entries 971200
TAC

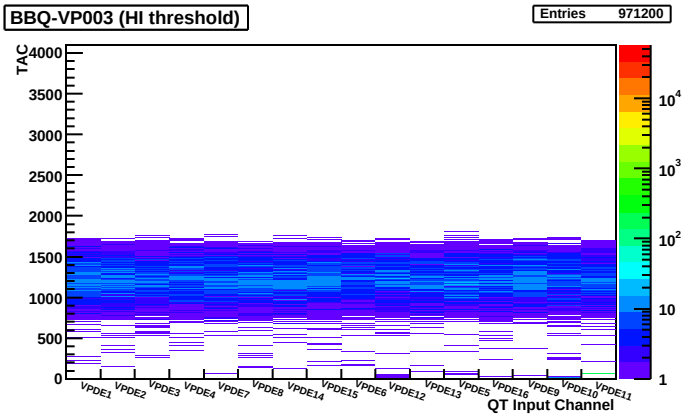

BBQ-VP001 (LO threshold)
Entries 971200
ADC
QT Input Channel

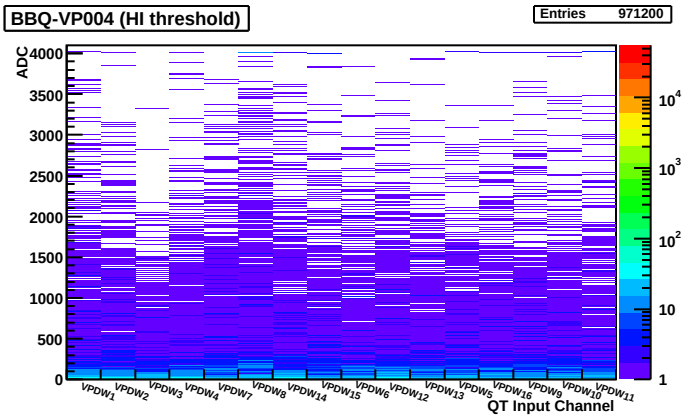

BBQ-VP001 (LO threshold)
Entries 971200
TAC
QT Input Channel

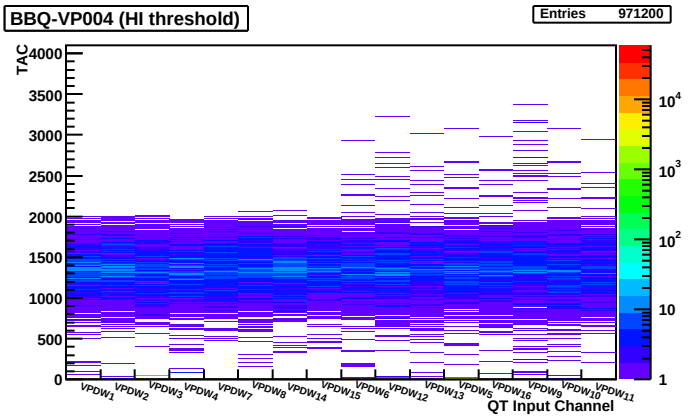

BBQ-VP002 (LO threshold)
Entries 971200
ADC
QT Input Channel

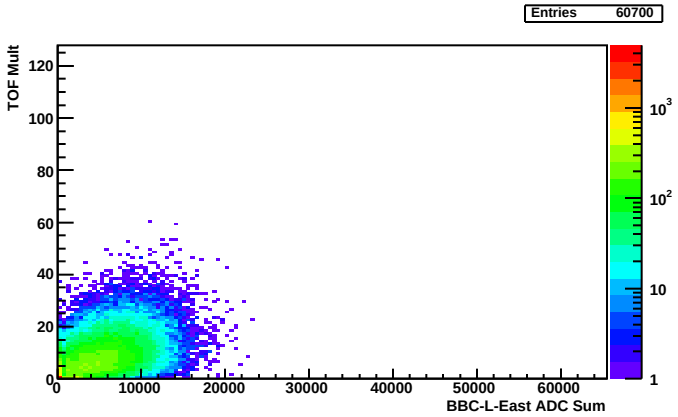

BBQ-VP002 (LO threshold)
Entries 971200
TAC
QT Input Channel

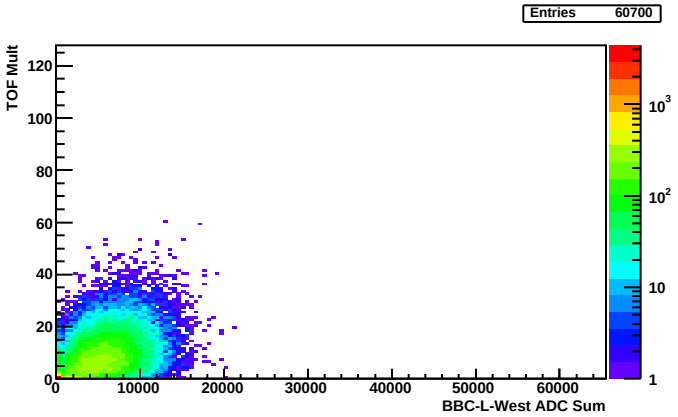

BBQ-VP003 (HI threshold)
Entries 971200
ADC
QT Input Channel

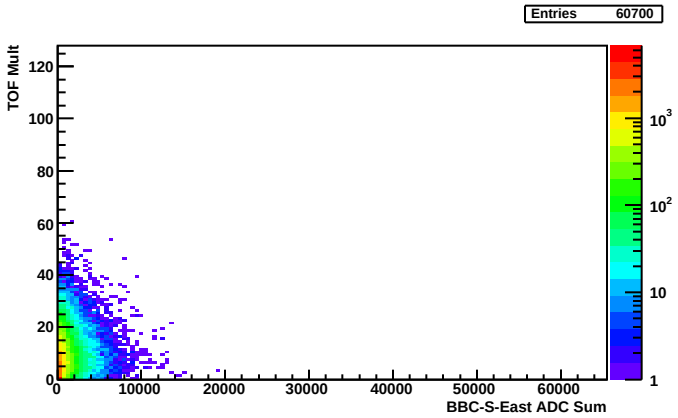

BBQ-VP003 (HI threshold)
Entries 971200
TAC
QT Input Channel

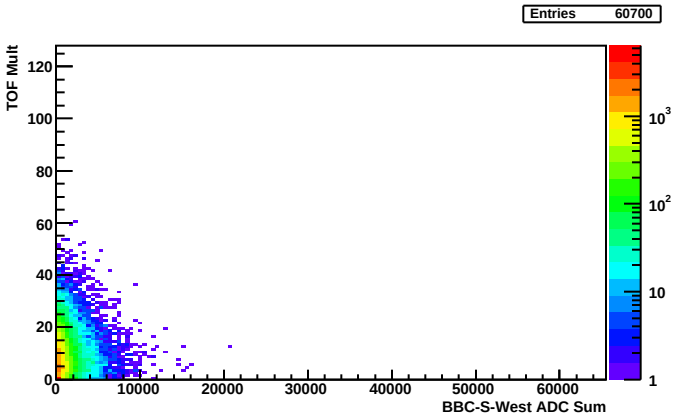

BBQ-VP004 (HI threshold)
Entries 971200
ADC
QT Input Channel

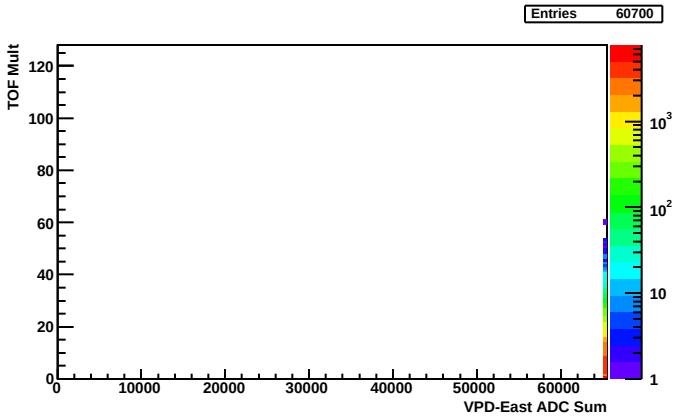

BBQ-VP004 (HI threshold)
Entries 971200
TAC
QT Input Channel

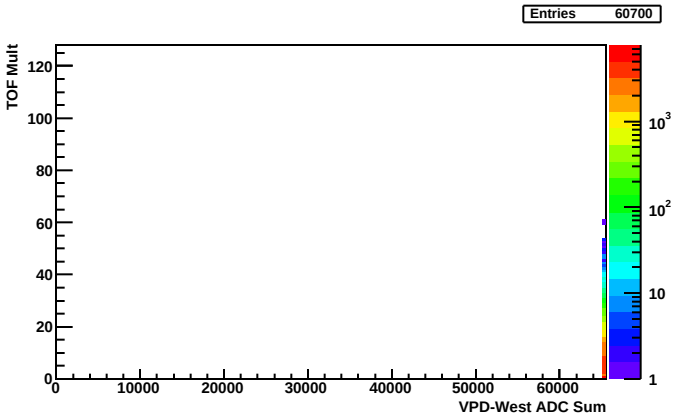

Entries 60700
TOF Mult
BBC-L-East ADC Sum

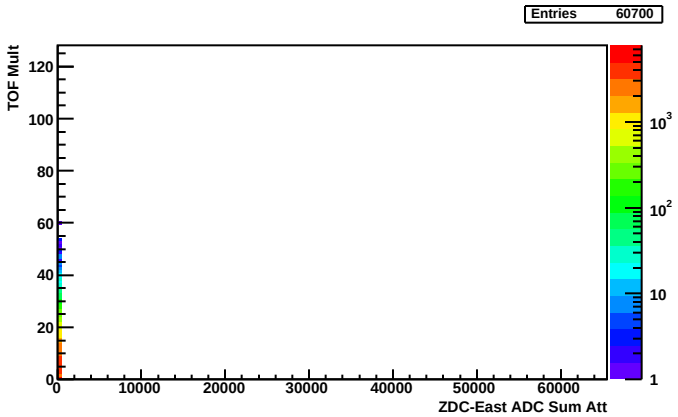

Entries 60700
TOF Mult
BBC-L-West ADC Sum

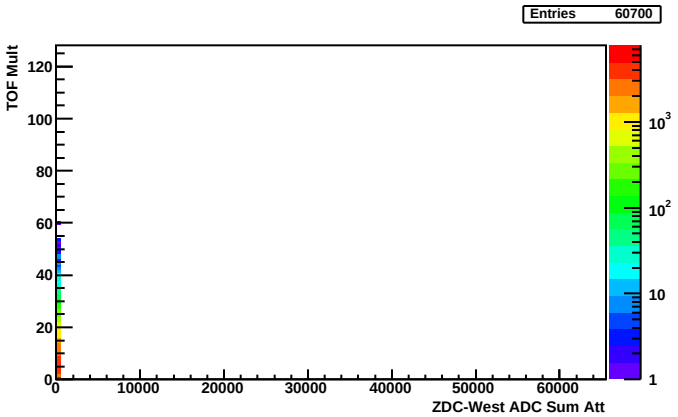

Entries 60700
TOF Mult
BBC-S-East ADC Sum

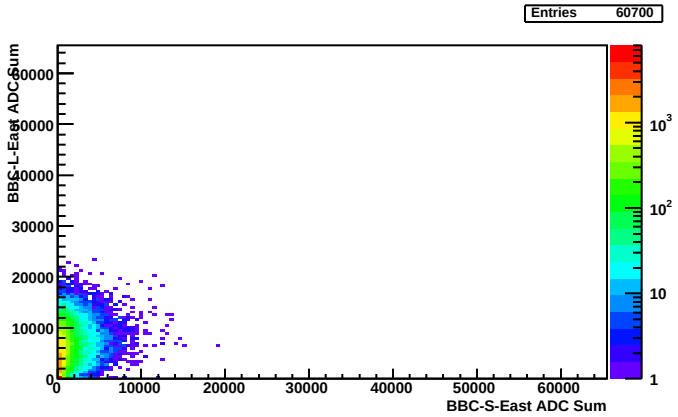

Entries 60700
TOF Mult
BBC-S-West ADC Sum

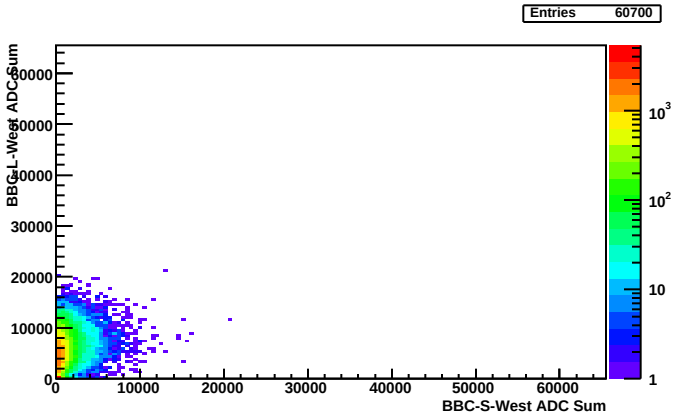

Entries 60700
TOF Mult
VPD-East ADC Sum

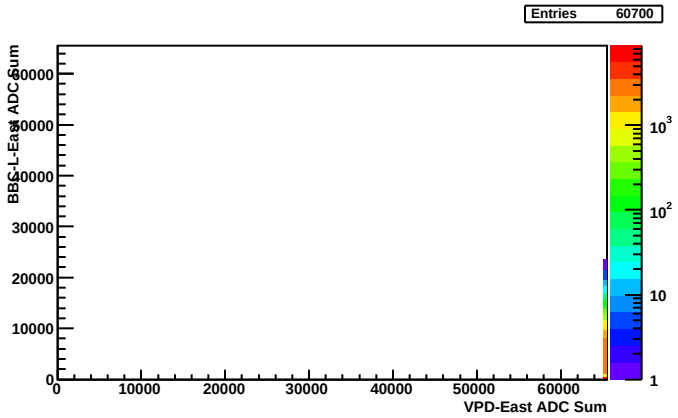

Entries 60700
TOF Mult
VPD-West ADC Sum

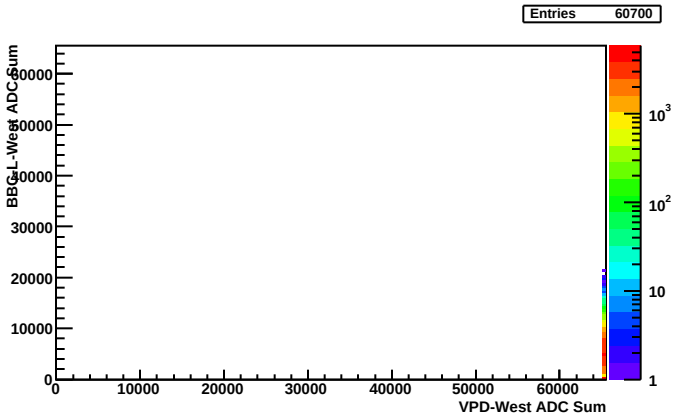

Entries 60700
TOF Mult
ZDC-East ADC Sum Att

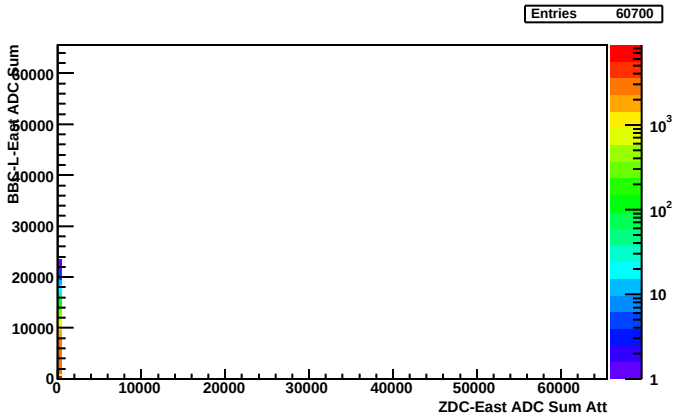

Entries 60700
TOF Mult
ZDC-West ADC Sum Att

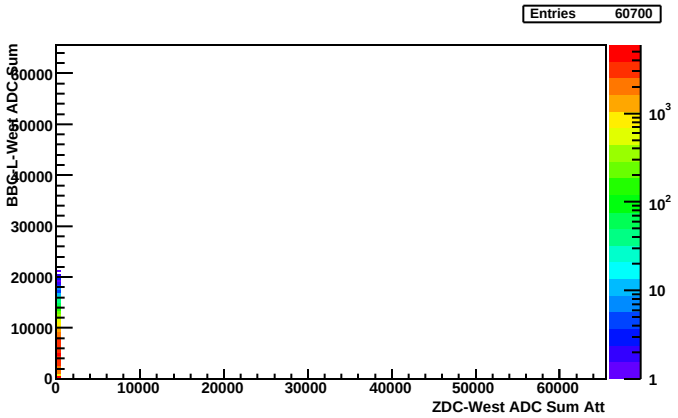

Entries 60700
BBC-L-East ADC Sum
BBC-S-East ADC Sum

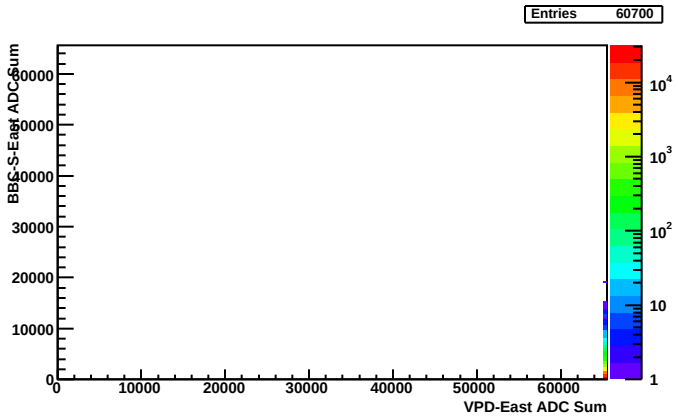

Entries 60700
BBC-L-West ADC Sum
BBC-S-West ADC Sum

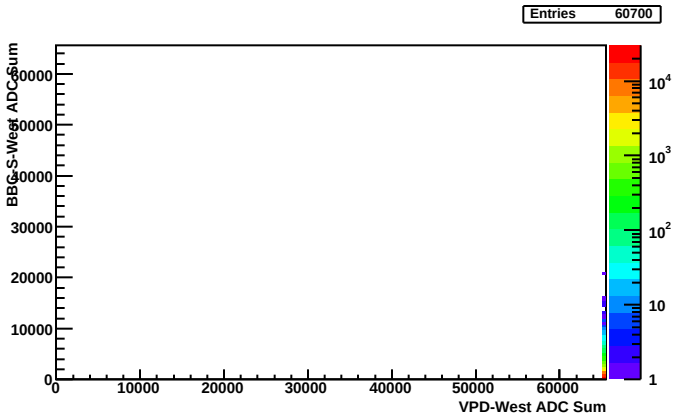

Entries 60700
BBC-L-East ADC Sum
VPD-East ADC Sum

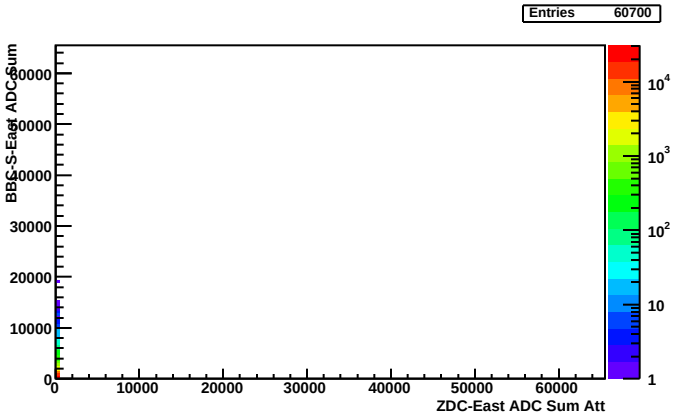

Entries 60700
BBC-L-West ADC Sum
VPD-West ADC Sum

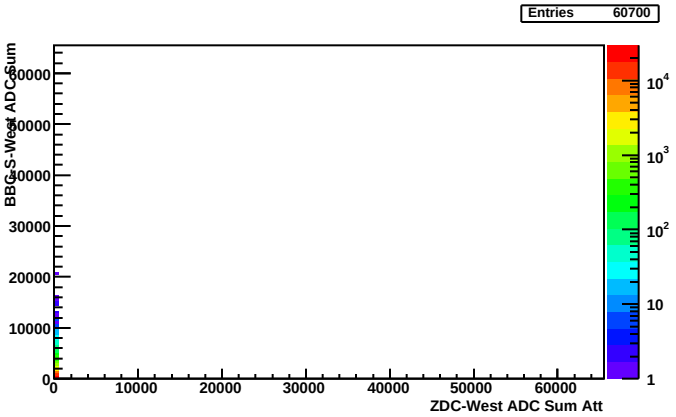

Entries 60700
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

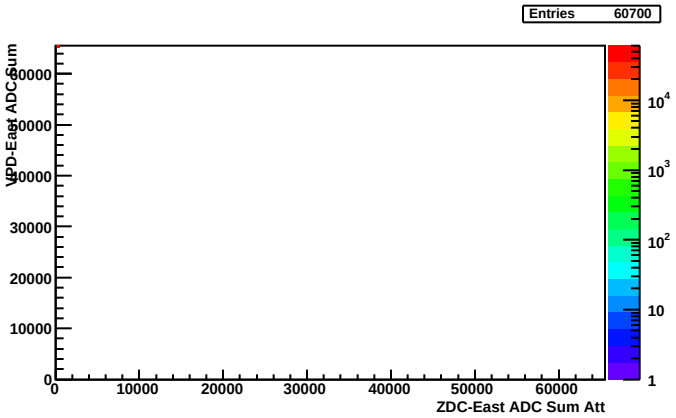

Entries 60700
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

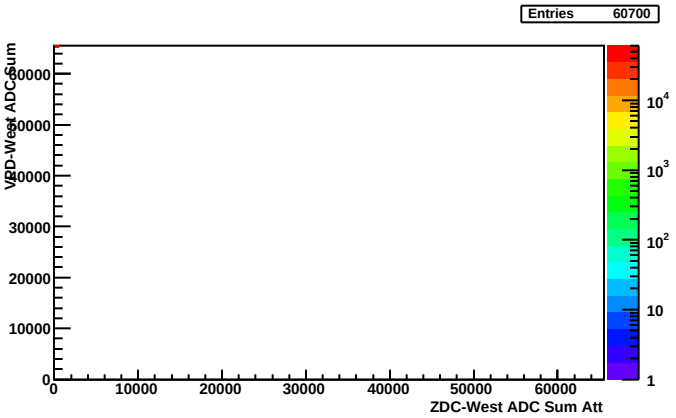

Entries 60700
BBC-S-East ADC Sum
VPD-East ADC Sum

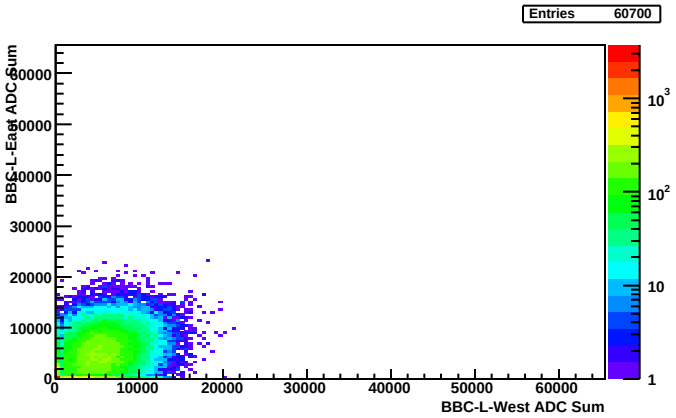

Entries 60700
BBC-S-West ADC Sum
VPD-West ADC Sum

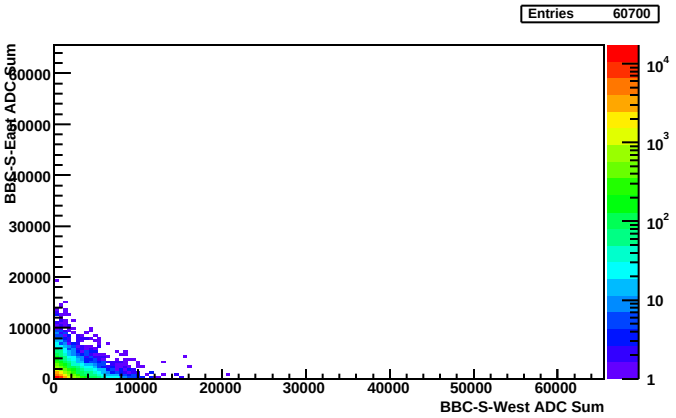

Entries 60700
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

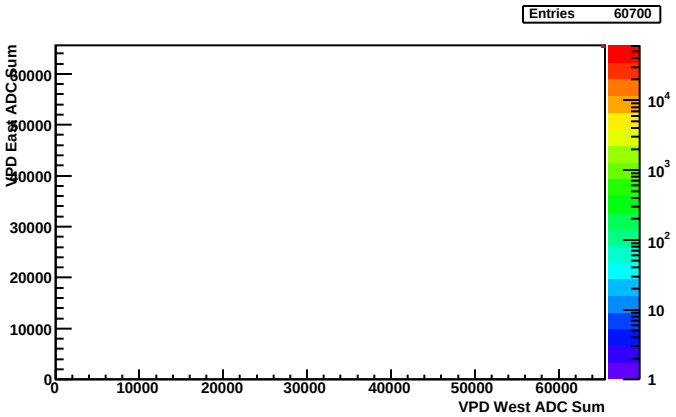

Entries 60700
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

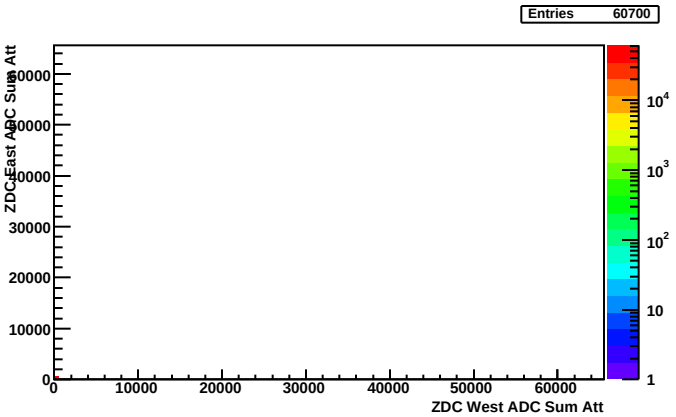

Entries 60700
VPD-East ADC Sum
ZDC-East ADC Sum Att

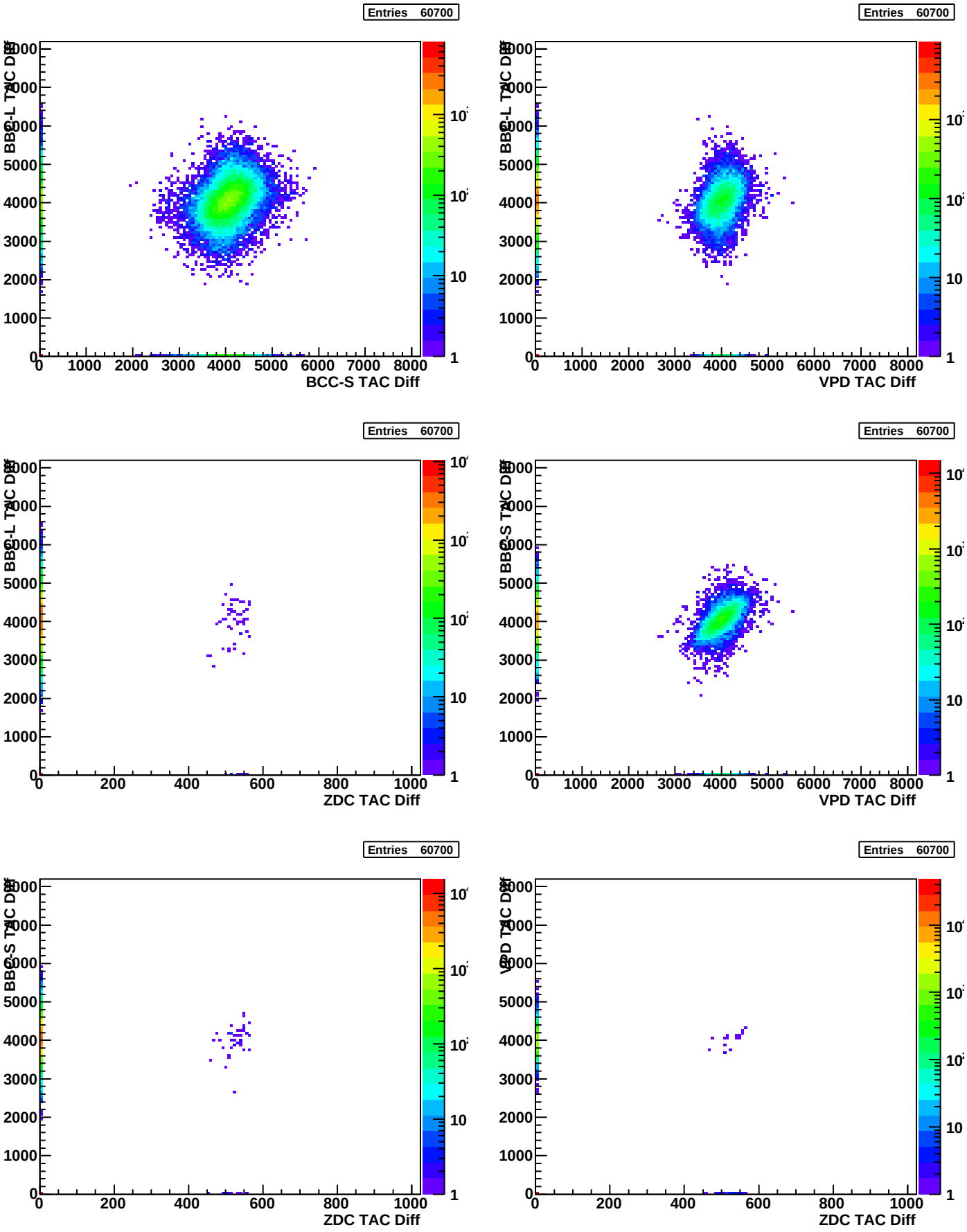

Entries 60700
VPD-West ADC Sum
ZDC-West ADC Sum Att

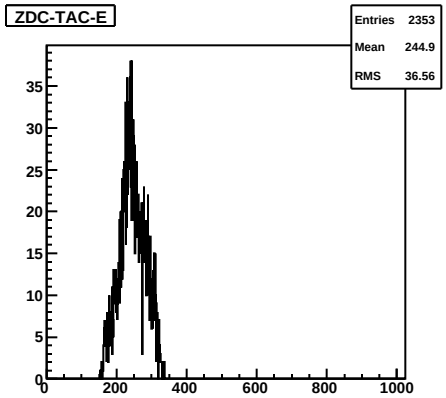

Entries 60700
BBC-L-East ADC Sum
BBC-L-West ADC Sum

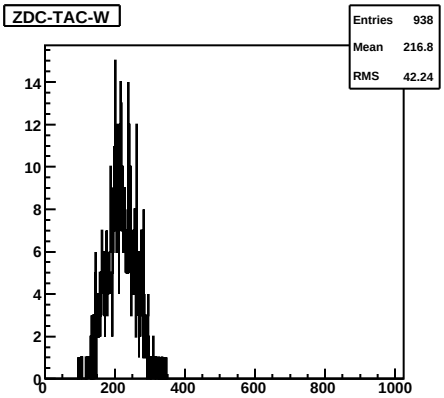

Entries 60700
BBC-S-East ADC Sum
BBC-S-West ADC Sum

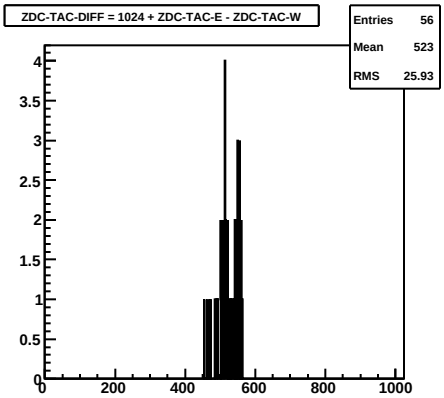

Entries 60700
VPD East ADC Sum
VPD West ADC Sum

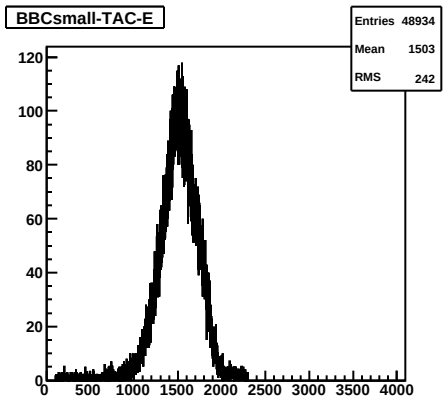

Entries 60700
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

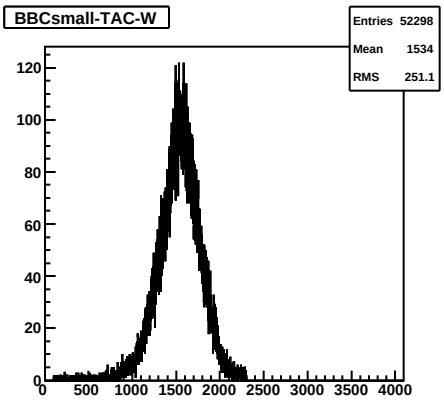

Entries 60700
BBC-L TAC Diff
BCC-S TAC Diff
Entries 60700
BBC-L TAC Diff
VPD TAC Diff
Entries 60700
BBC-L TAC Diff
ZDC TAC Diff
Entries 60700
BBC-S TAC Diff
VPD TAC Diff
Entries 60700
BBC-S TAC Diff
ZDC TAC Diff
Entries 60700
VPD TAC Diff
ZDC TAC Diff

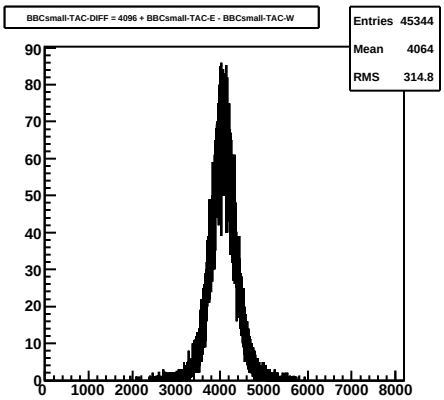

ZDC-TAC-E
Entries 2353
Mean 244.9
RMS 36.56

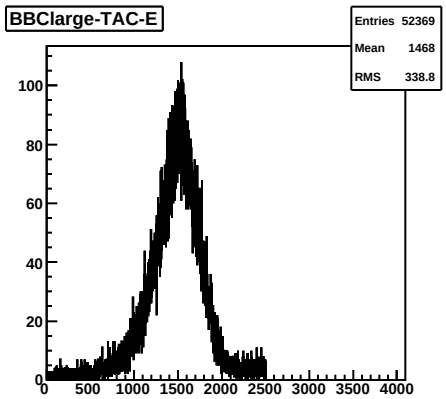

ZDC-TAC-W
Entries 938
Mean 216.8
RMS 42.24

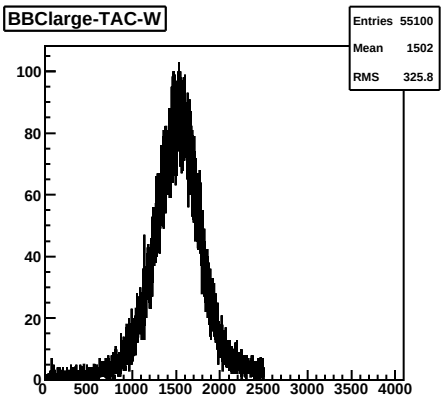

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 56
Mean 523
RMS 25.93

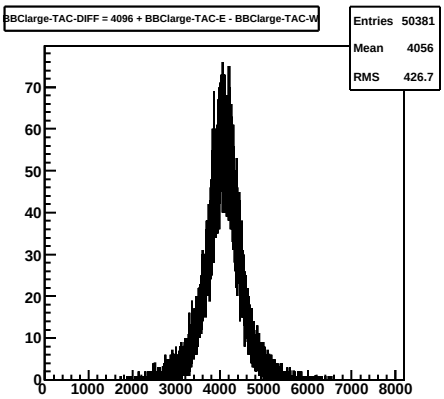

BBCsmall-TAC-E
Entries 48934
Mean 1503
RMS 242

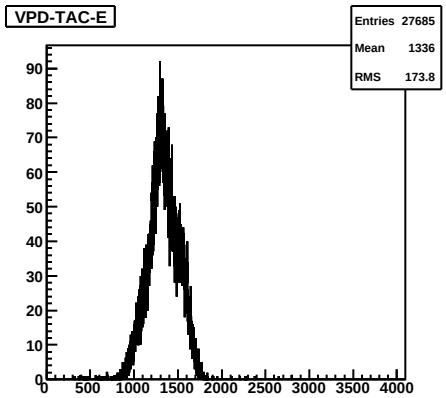

BBCsmall-TAC-W
Entries 52298
Mean 1534
RMS 251.1

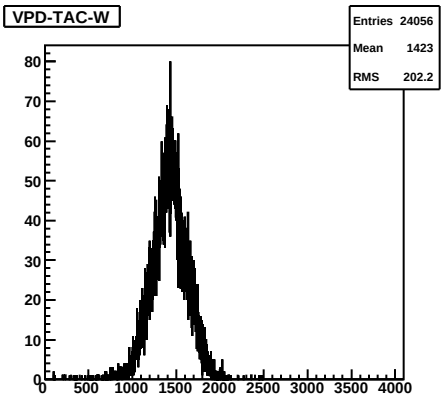

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 45344
Mean 4064
RMS 314.8

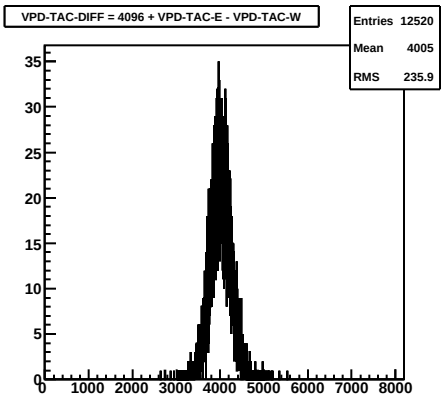

BBClarge-TAC-E
Entries 52369
Mean 1468
RMS 338.8

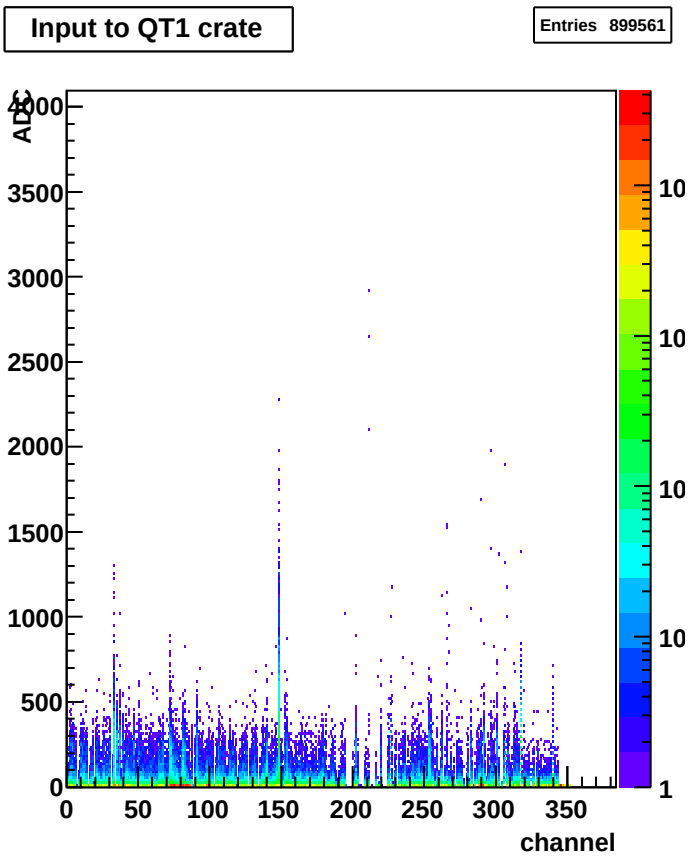

BBClarge-TAC-W
Entries 55100
Mean 1502
RMS 325.8

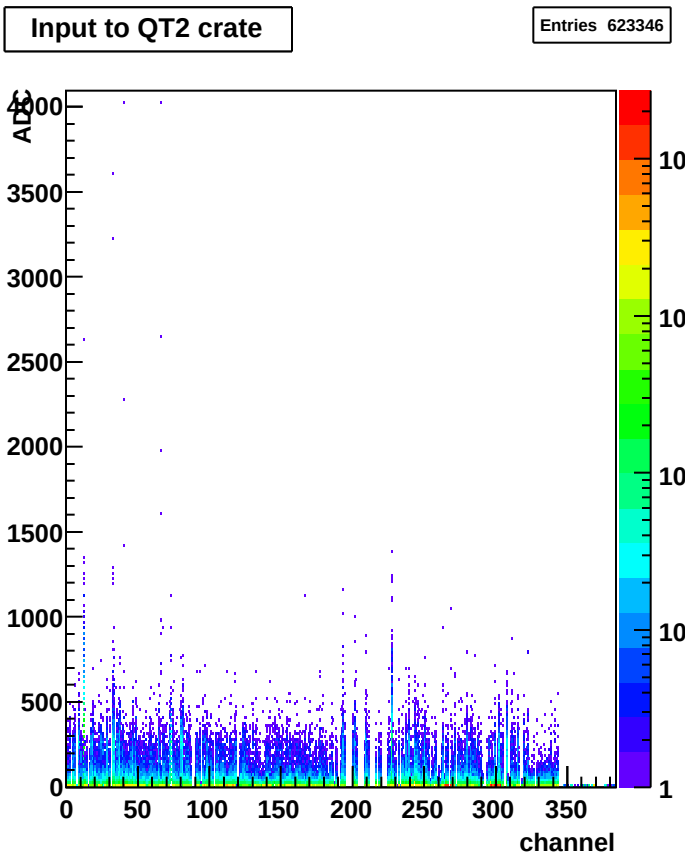

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 50381
Mean 4056
RMS 426.7

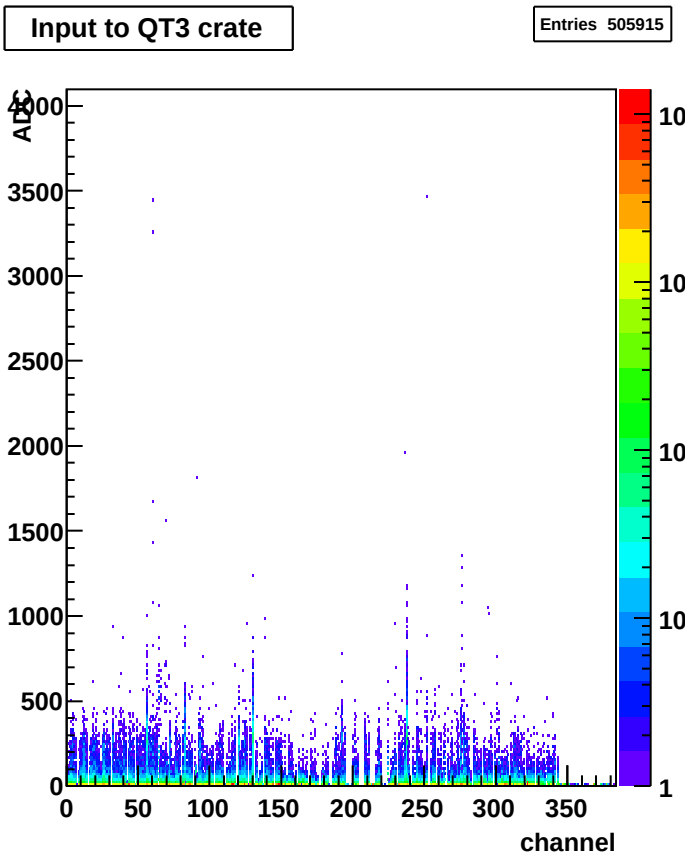

VPD-TAC-E
Entries 27685
Mean 1336
RMS 173.8

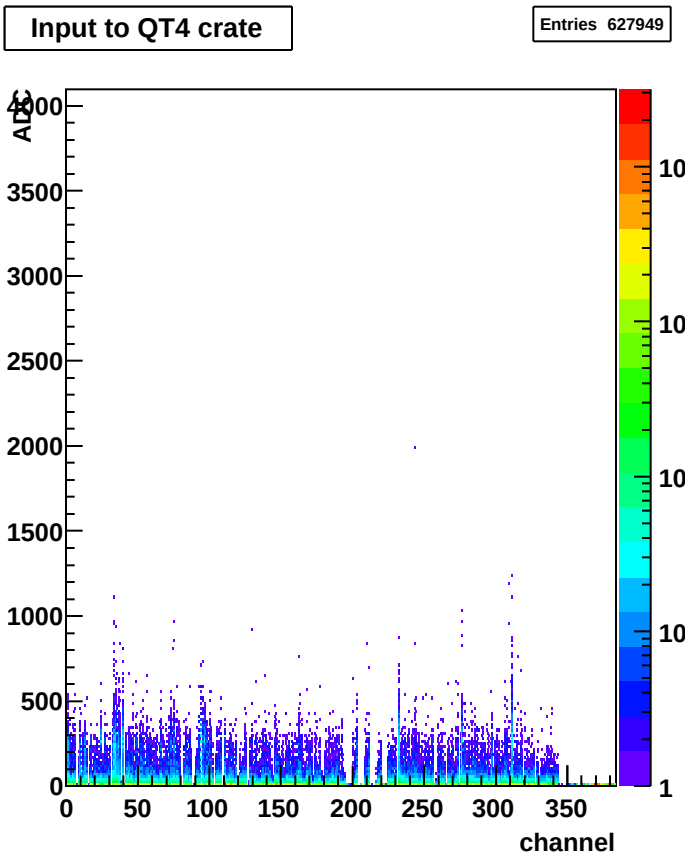

VPD-TAC-W
Entries 24056
Mean 1423
RMS 202.2

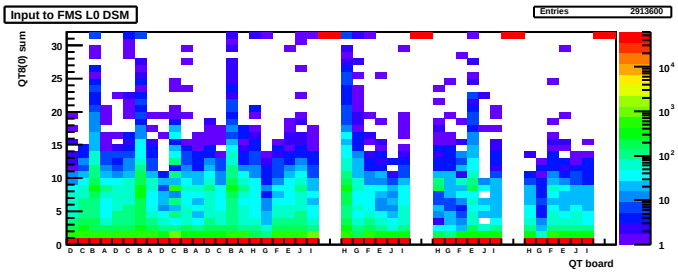

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 12520
Mean 4005
RMS 235.9

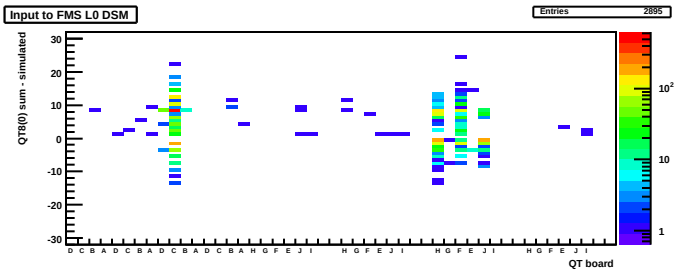

Input to QT1 crate
Entries 899561
ADC
channel

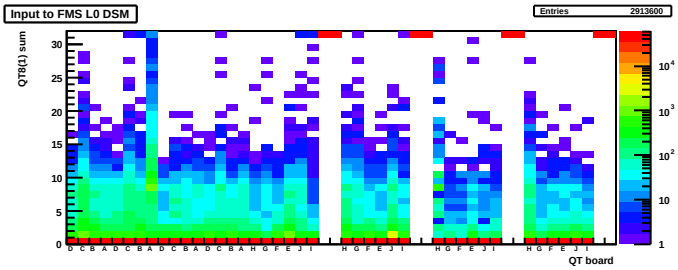

Input to QT2 crate
Entries 623346
ADC
channel

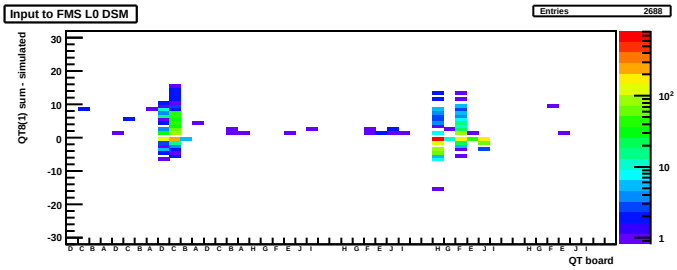

Input to QT3 crate
Entries 505915
ADC
channel

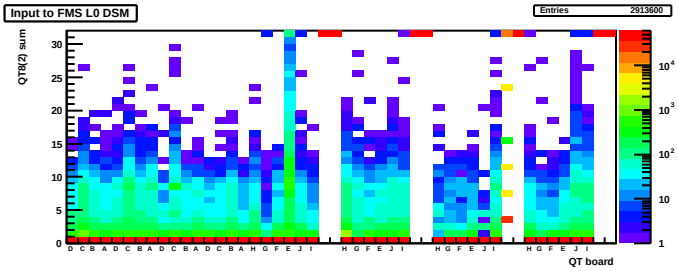

Input to QT4 crate
Entries 627949
ADC
channel

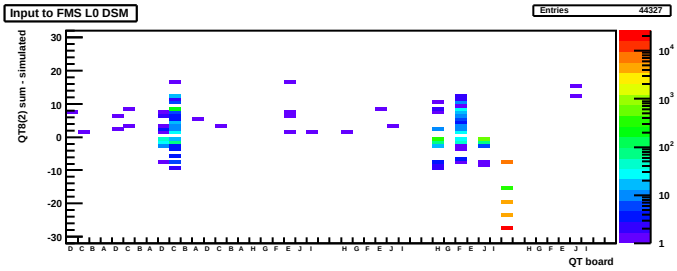

Input to FMS L0 DSM
Entries 2913600
QT8(0) sum
QT board

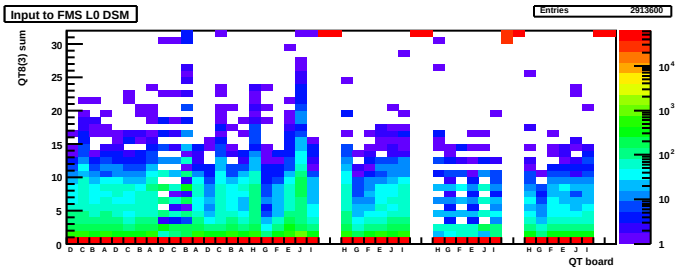

Input to FMS L0 DSM
Entries 2895
QT8(0) sum - simulated
QT board

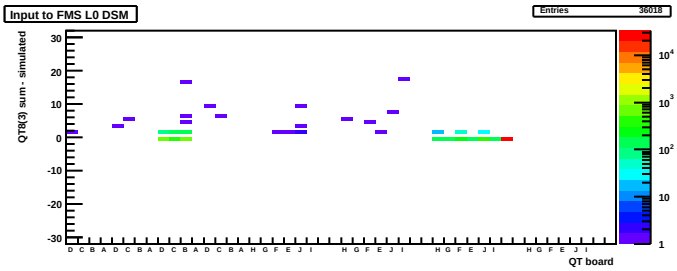

Input to FMS L0 DSM
Entries 2913600
QT8(1) sum
QT board

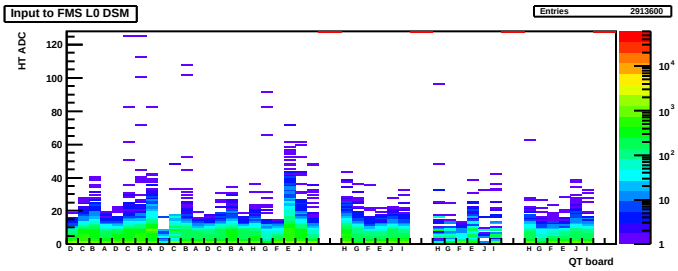

Input to FMS L0 DSM
Entries 2688
QT8(1) sum - simulated
QT board

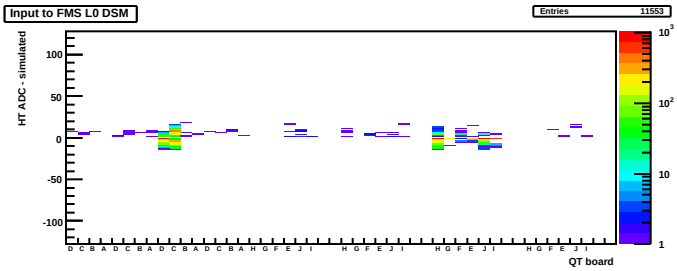

Input to FMS L0 DSM
Entries 2913600
QT8(2) sum
QT board

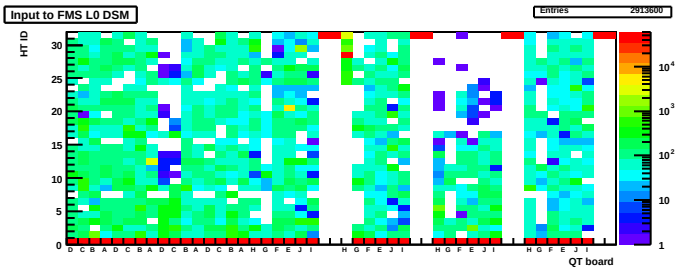

Input to FMS L0 DSM
Entries 44327
QT8(2) sum - simulated
QT board

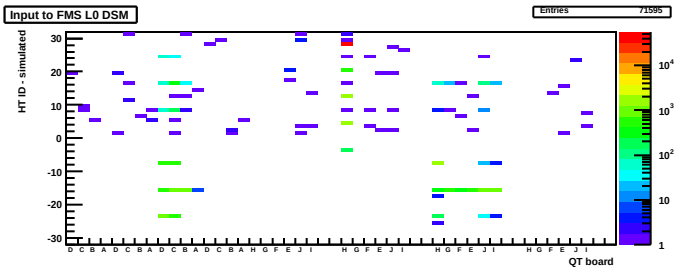

Input to FMS L0 DSM
Entries 2913600
QT8(3) sum
QT board

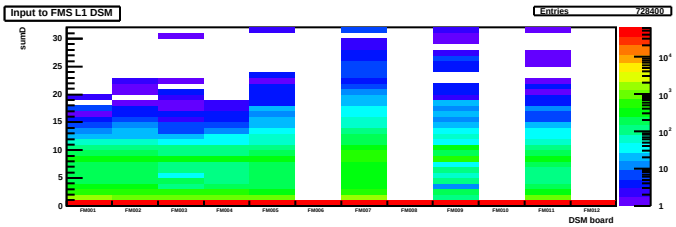

Input to FMS L0 DSM
Entries 36018
QT8(3) sum - simulated
QT board

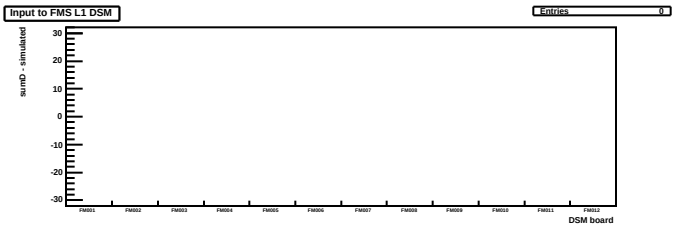

Input to FMS L0 DSM
Entries 2913600
HT ADC
QT board

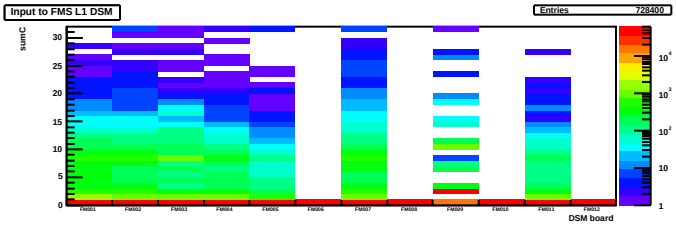

Input to FMS L0 DSM
Entries 11553
HT ADC - simulated
QT board

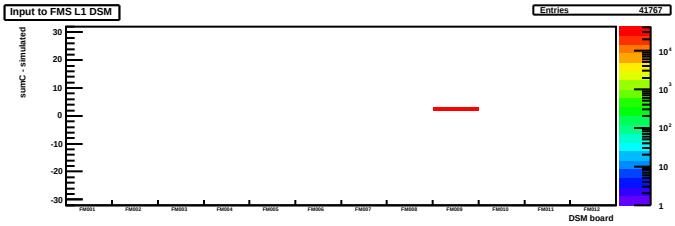

Input to FMS L0 DSM
Entries 2913600
HT ID
QT board

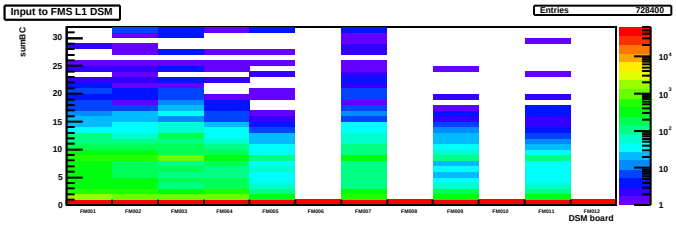

Input to FMS L0 DSM
Entries 71595
HT ID - simulated
QT board

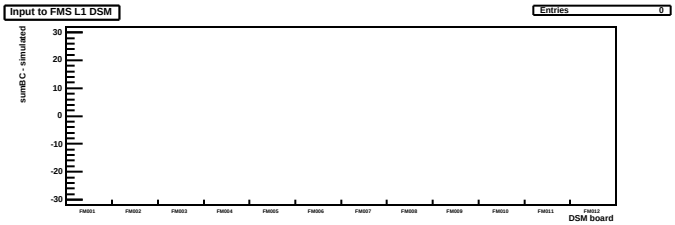

Input to FMS L1 DSM
Entries 728400
sumD
DSM board

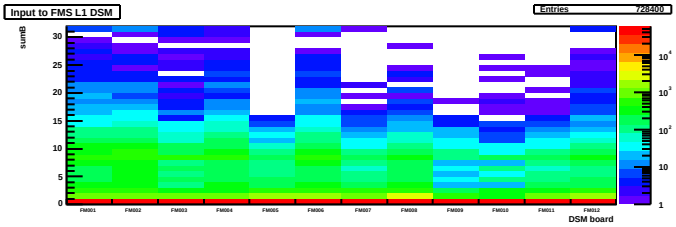

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

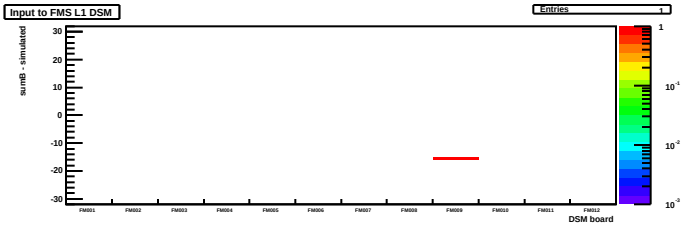

Input to FMS L1 DSM
Entries 728400
sumC
DSM board

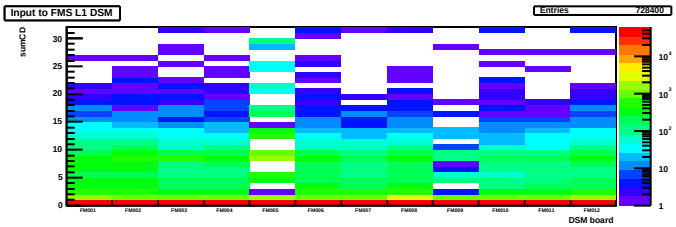

Input to FMS L1 DSM
Entries 41767
sumC - simulated
DSM board

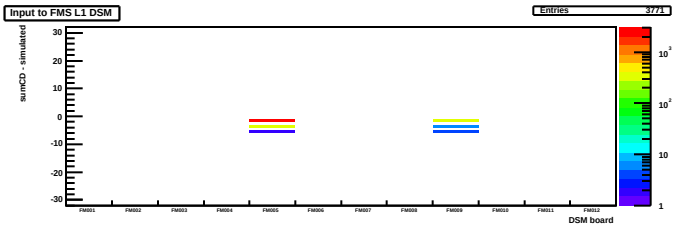

Input to FMS L1 DSM
Entries 728400
sumBC
DSM board

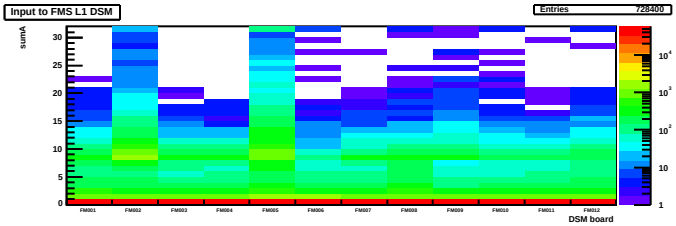

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

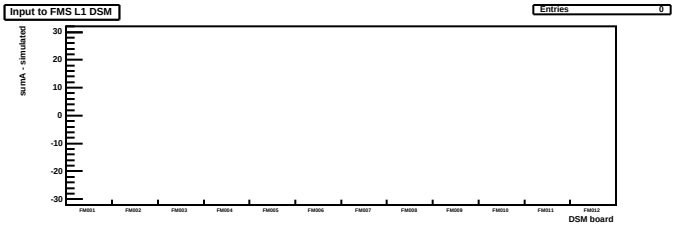

Input to FMS L1 DSM
Entries 728400
sumB
DSM board

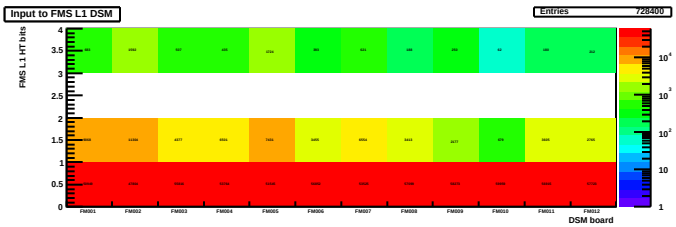

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

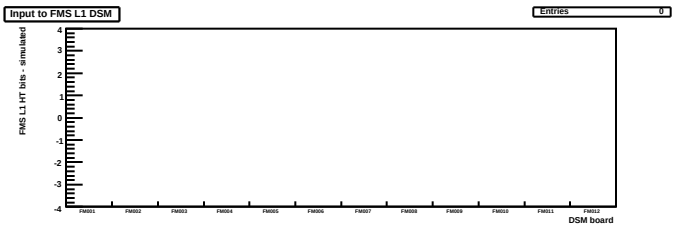

Input to FMS L1 DSM
Entries 728400
sumCD
DSM board

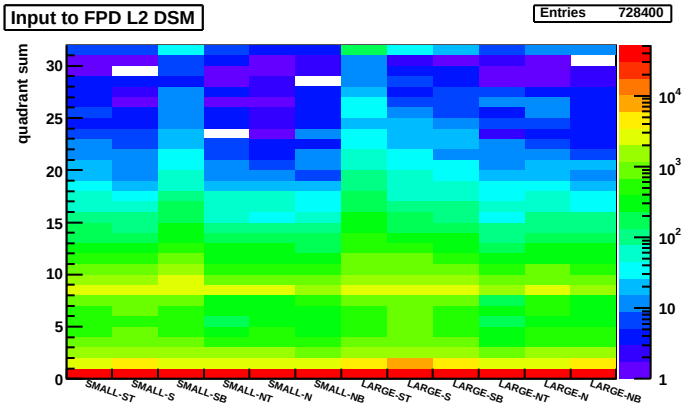

Input to FMS L1 DSM
Entries 3771
sumCD - simulated
DSM board

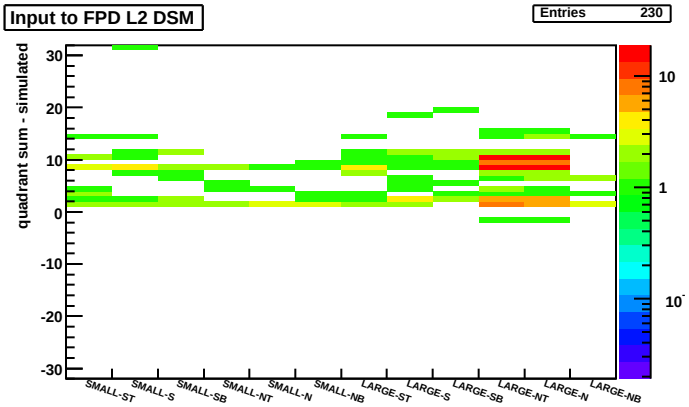

Input to FMS L1 DSM
Entries 728400
sumA
DSM board

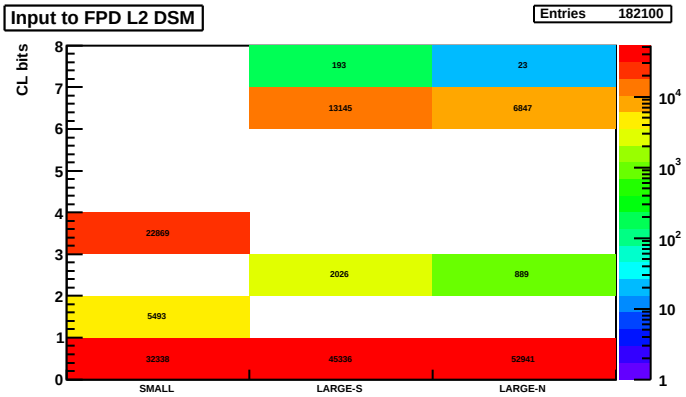

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

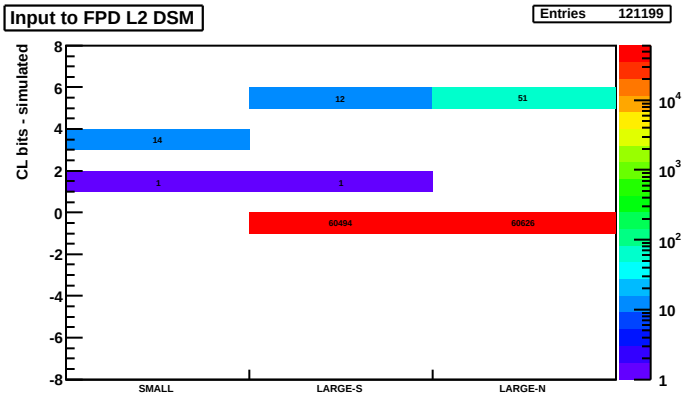

Input to FMS L1 DSM
Entries 728400
FMS L1 HT bits
DSM board

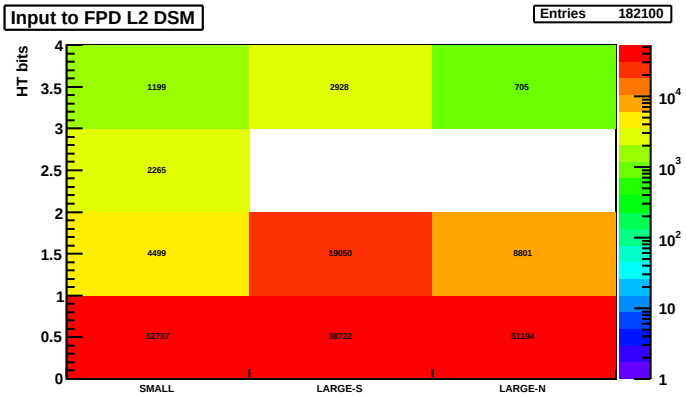

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

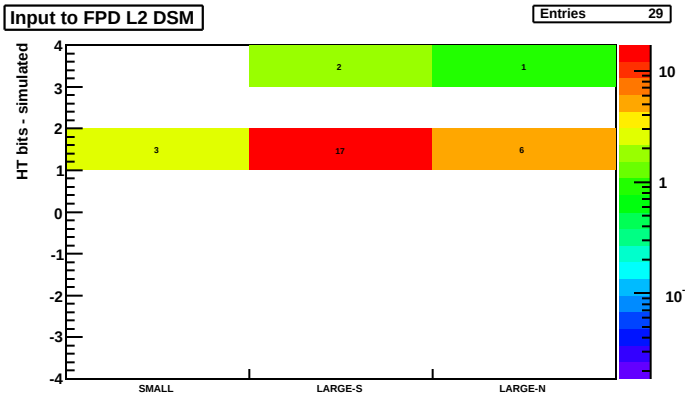

Input to FPD L2 DSM
Entries 728400
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

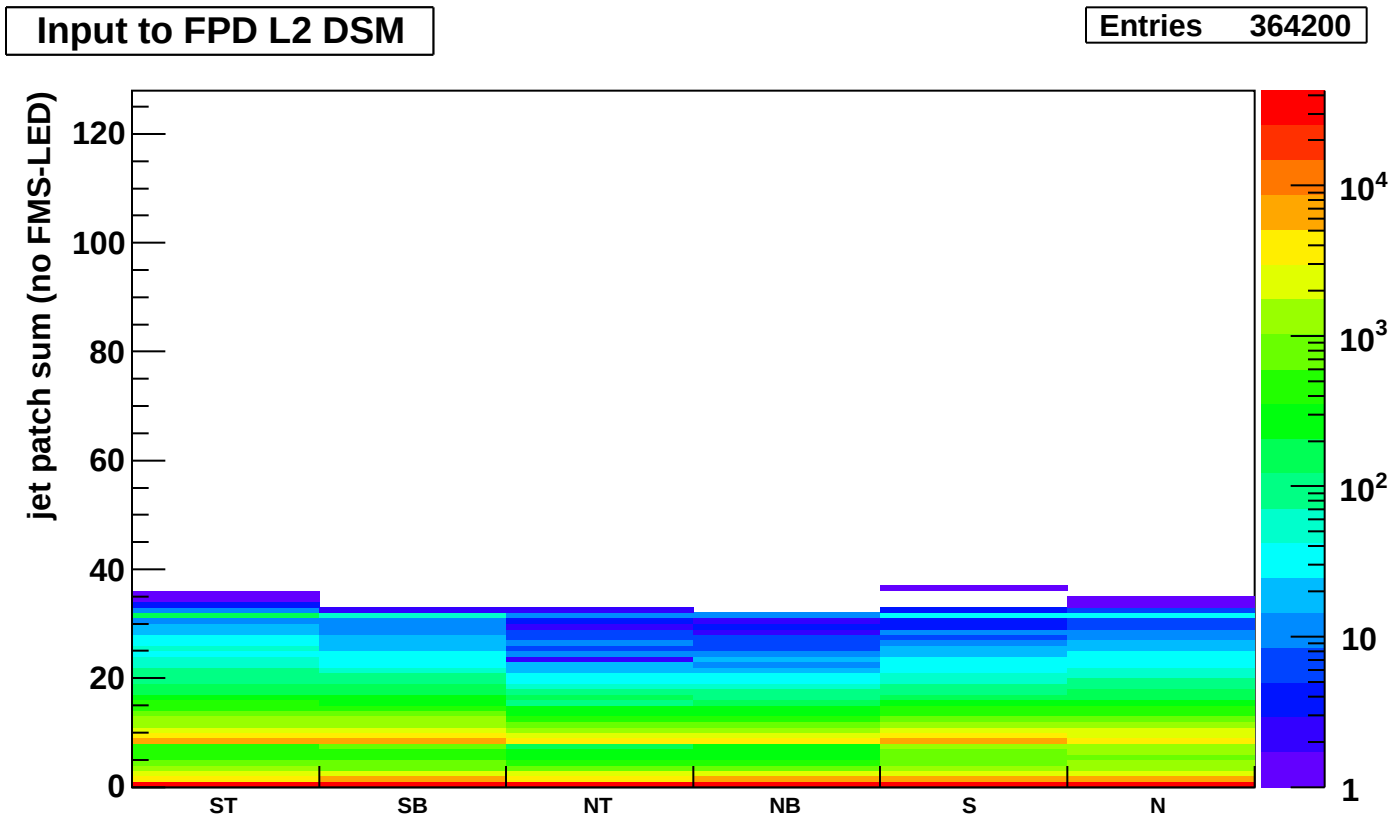

Input to FPD L2 DSM
Entries 230
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

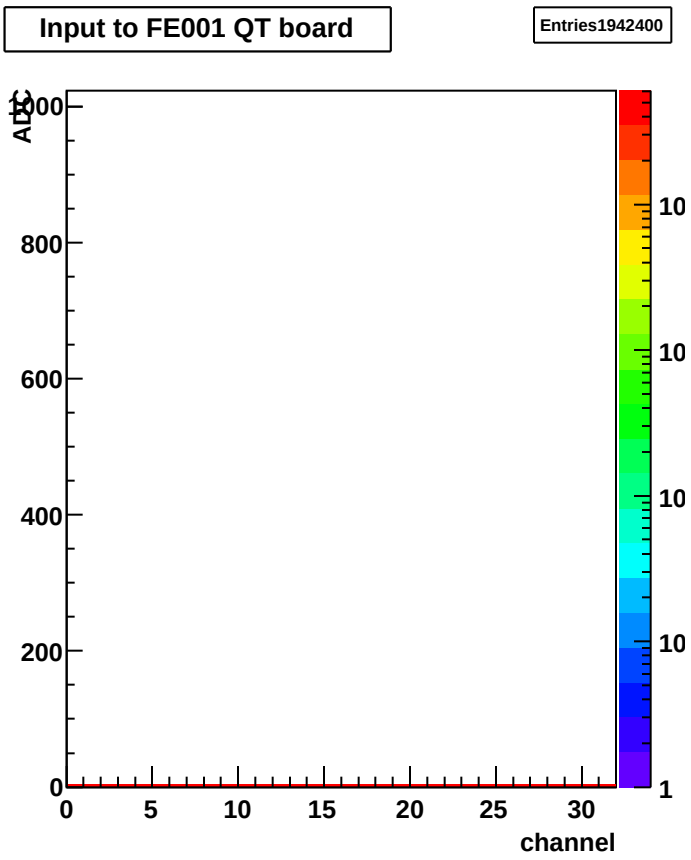

Input to FPD L2 DSM
Entries 182100
CL bits
193
23
13145
6847
22869
2026
889
5493
32338
45336
52941
SMALL
LARGE-S
LARGE-N

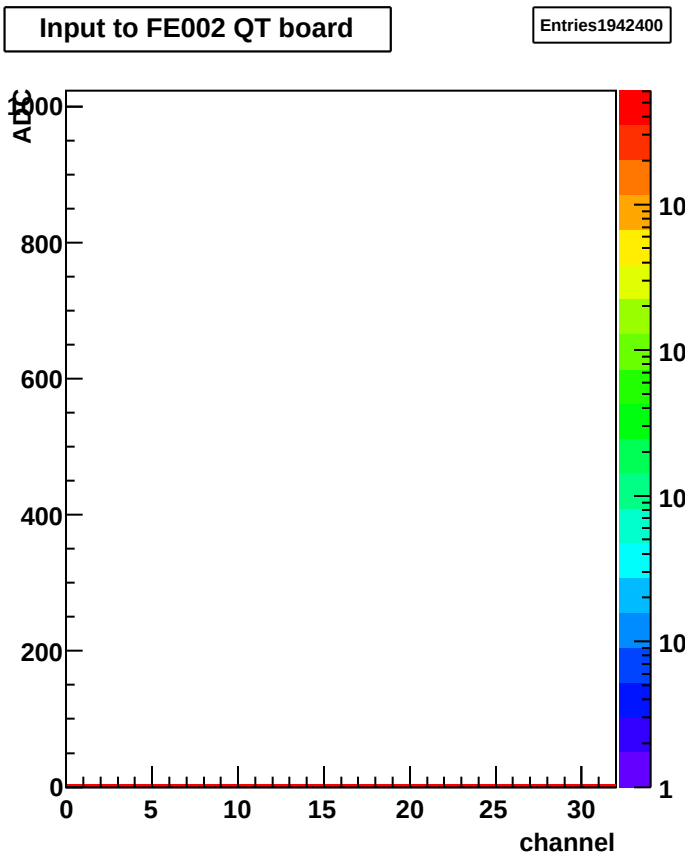

Input to FPD L2 DSM
Entries 121199
CL bits - simulated
12
51
14
1
1
60494
60626
SMALL
LARGE-S
LARGE-N

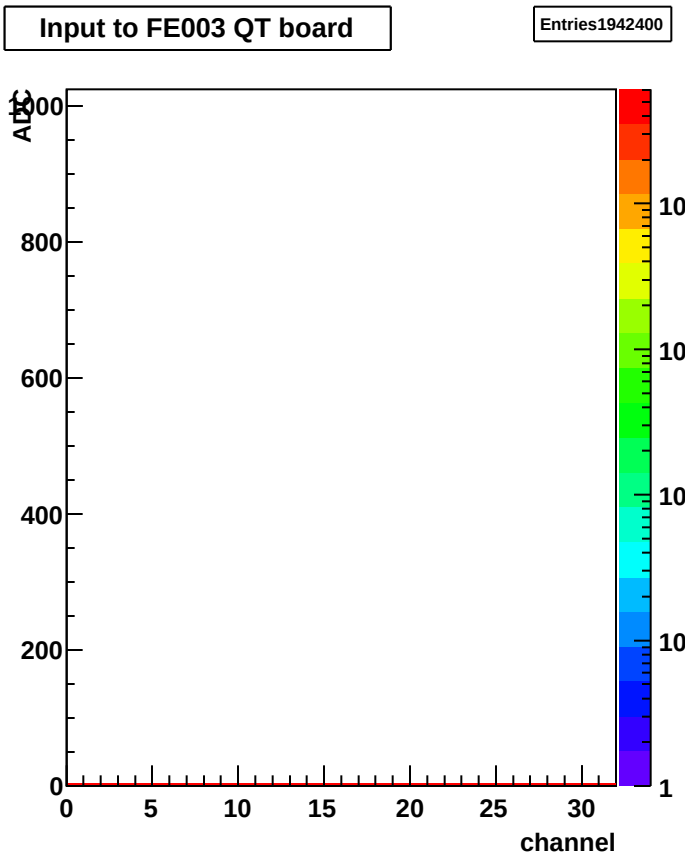

Input to FPD L2 DSM
Entries 182100
HT bits
1199
2928
705
2265
4499
19050
8801
52737
38722
51194
SMALL
LARGE-S
LARGE-N

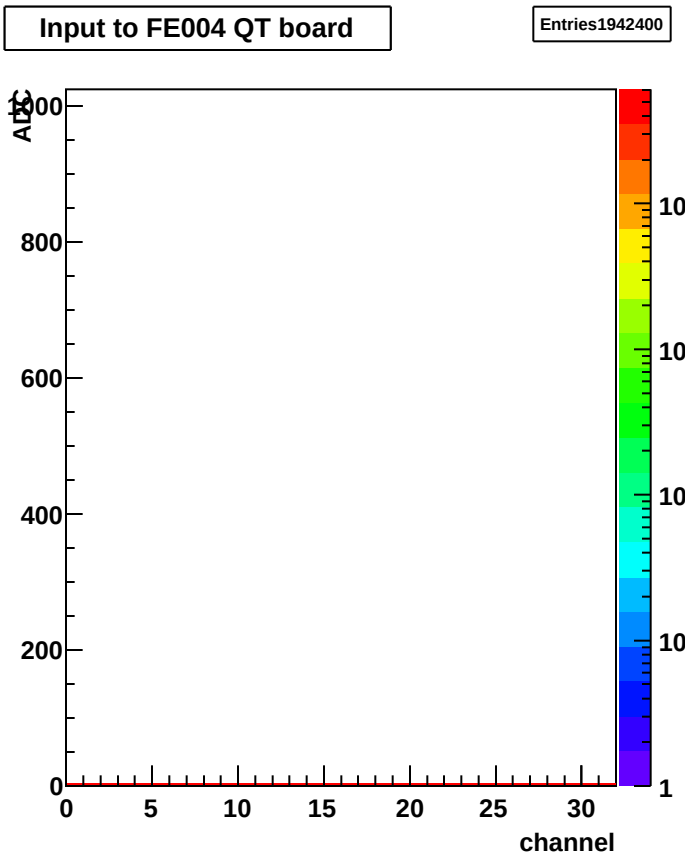

Input to FPD L2 DSM
Entries 29
HT bits - simulated
2
1
3
17
6
SMALL
LARGE-S
LARGE-N

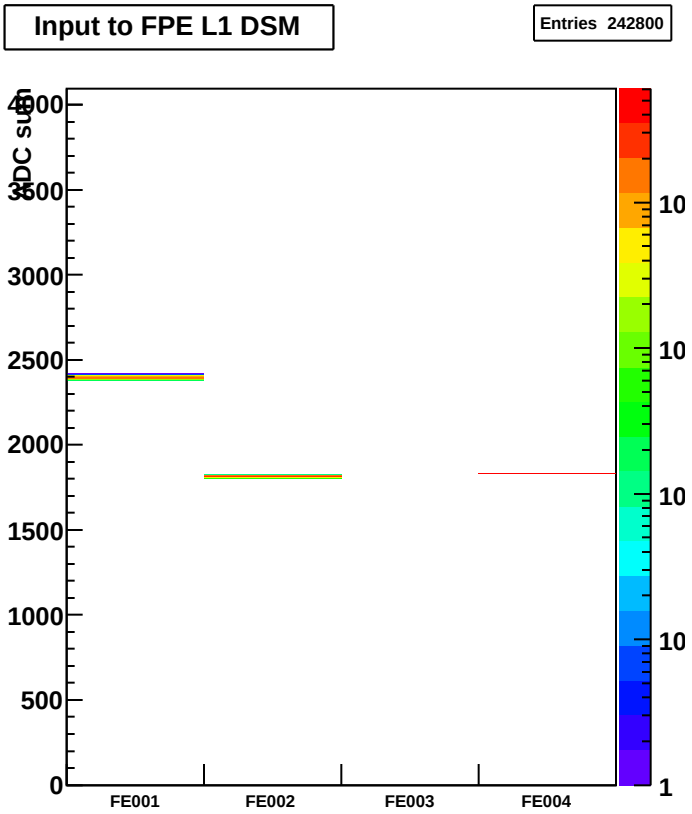

Input to FPD L2 DSM
Entries 364200
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

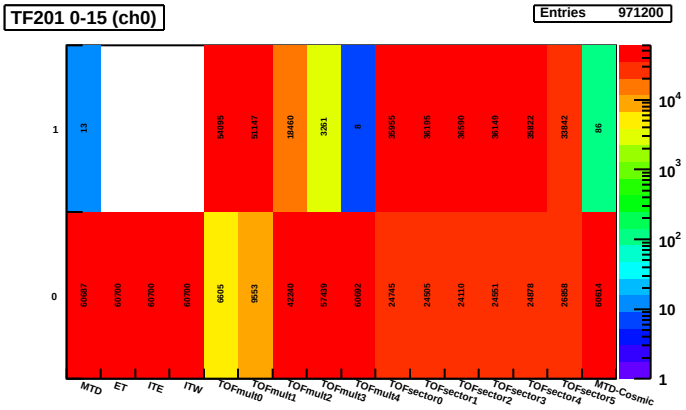

Input to FE001 QT board
Entries1942400
ADC
channel

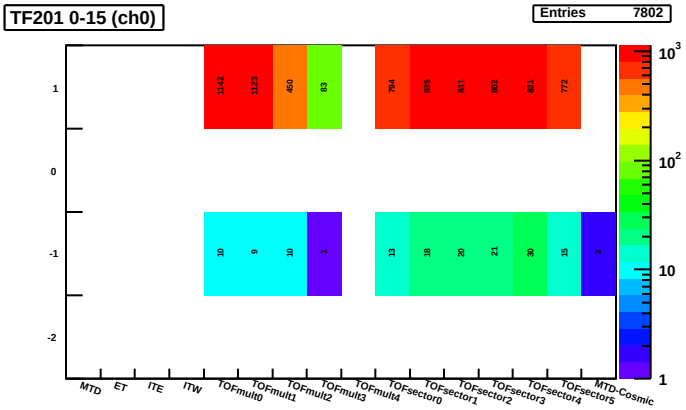

Input to FE002 QT board
Entries1942400
ADC
channel

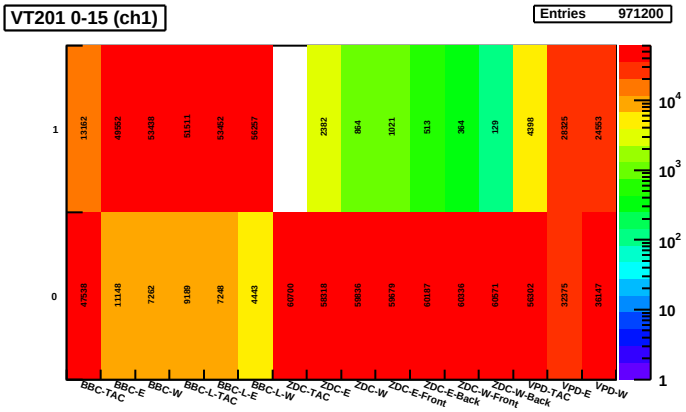

Input to FE003 QT board
Entries1942400
ADC
channel

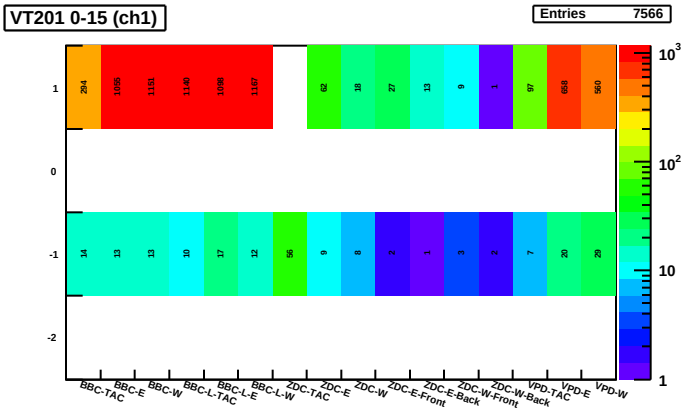

Input to FE004 QT board
Entries1942400
ADC
channel

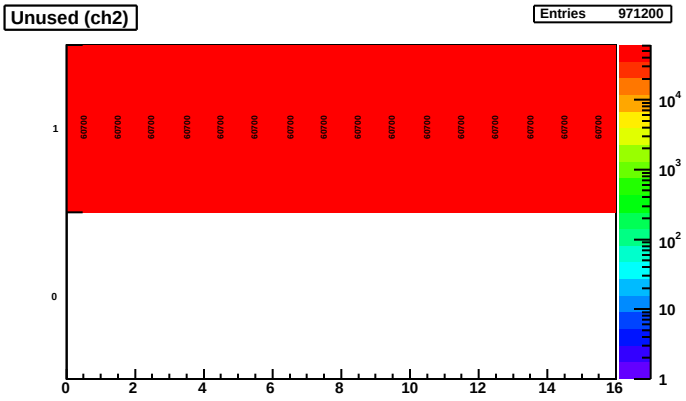

Input to FPE L1 DSM
Entries 242800
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

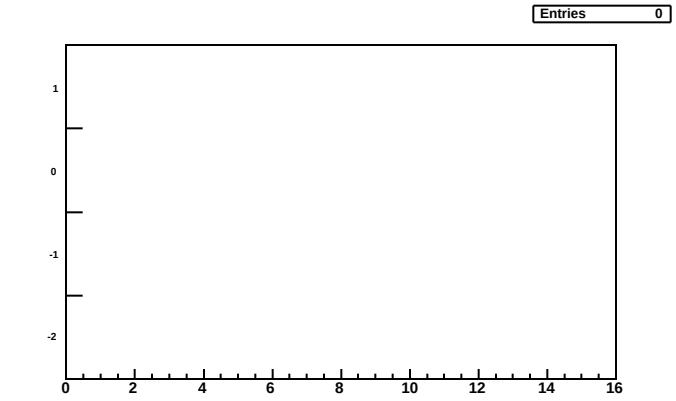

TF201 0-15 (ch0)
Entries 971200
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

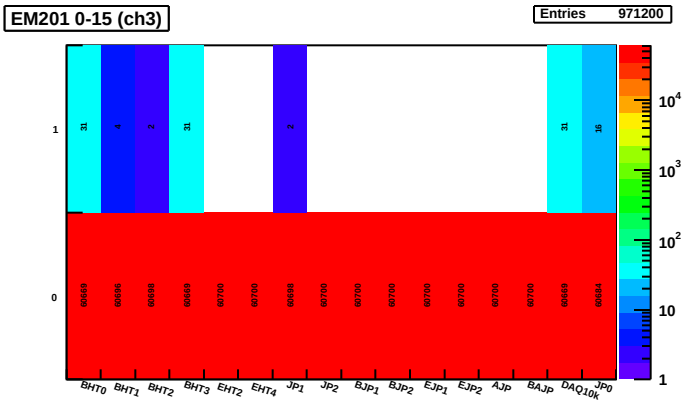

TF201 0-15 (ch0)
Entries 7802
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

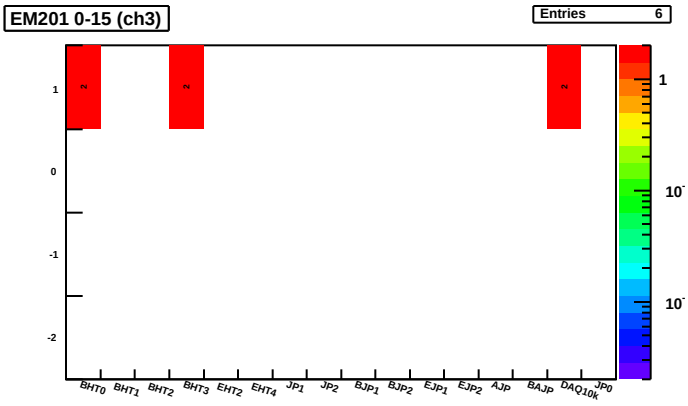

VT201 0-15 (ch1)
Entries 971200
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 7566
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

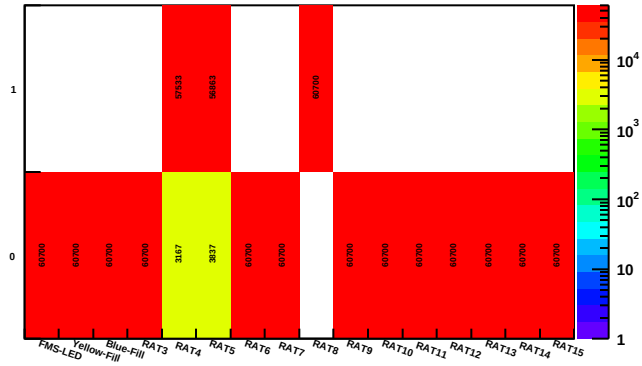

Unused (ch2)
Entries 971200

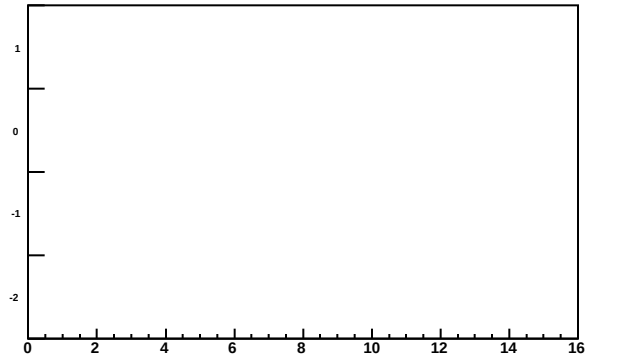

Entries 0


EM201 0-15 (ch3)
Entries 971200
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

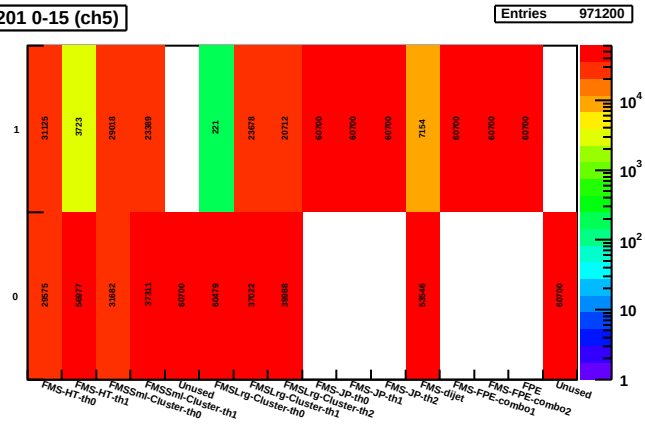

EM201 0-15 (ch3)
Entries 6
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

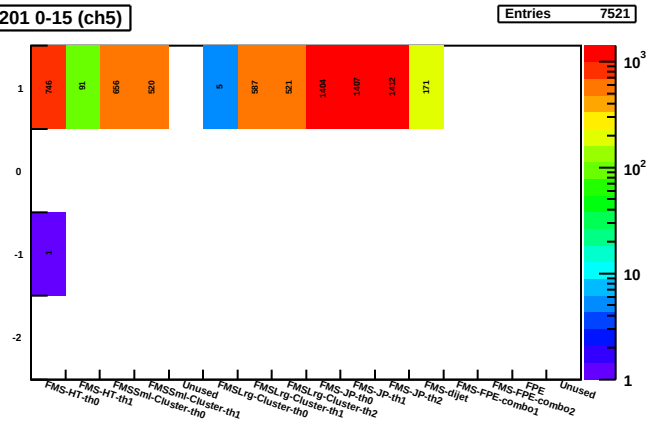




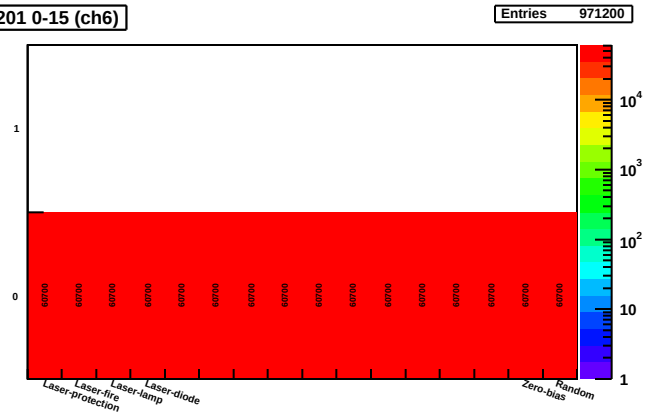
FP201 0-15 (ch5)

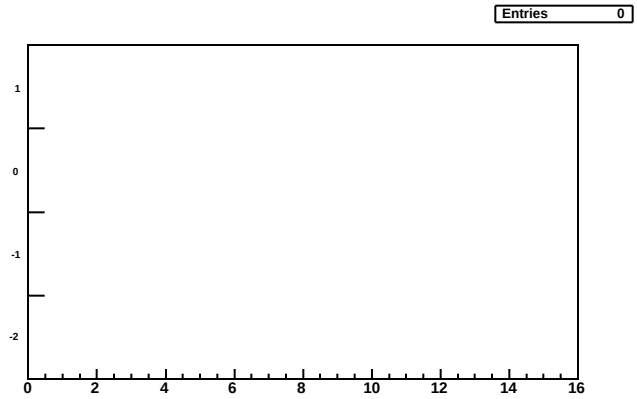
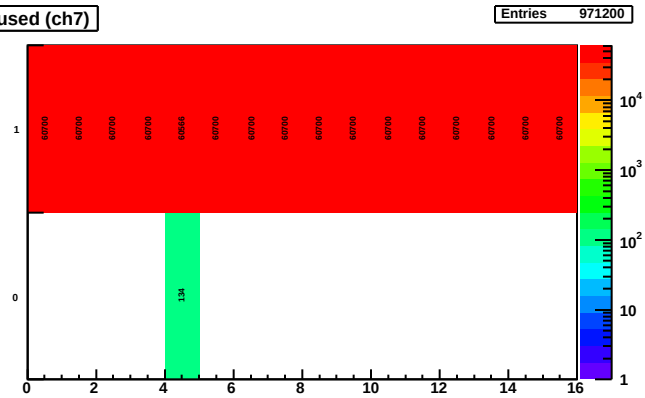
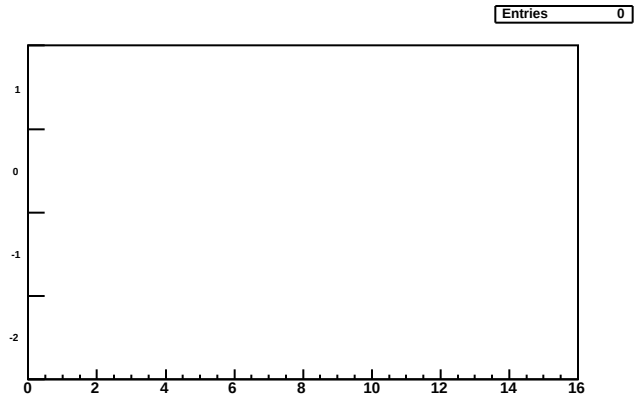


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)