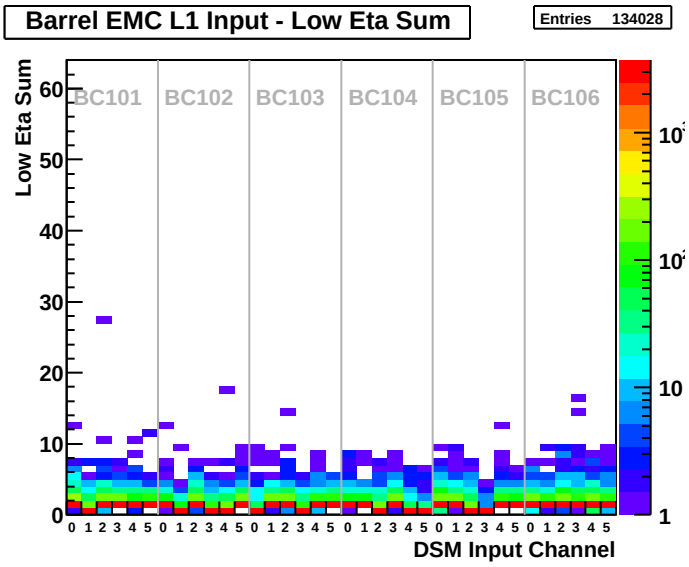
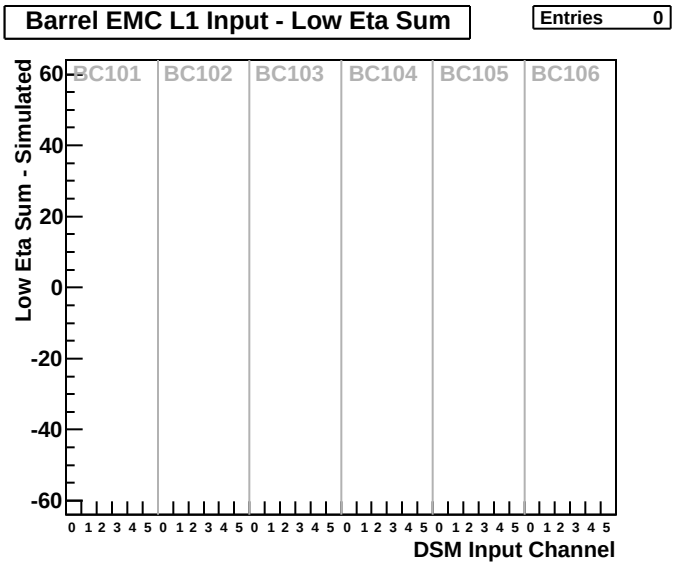
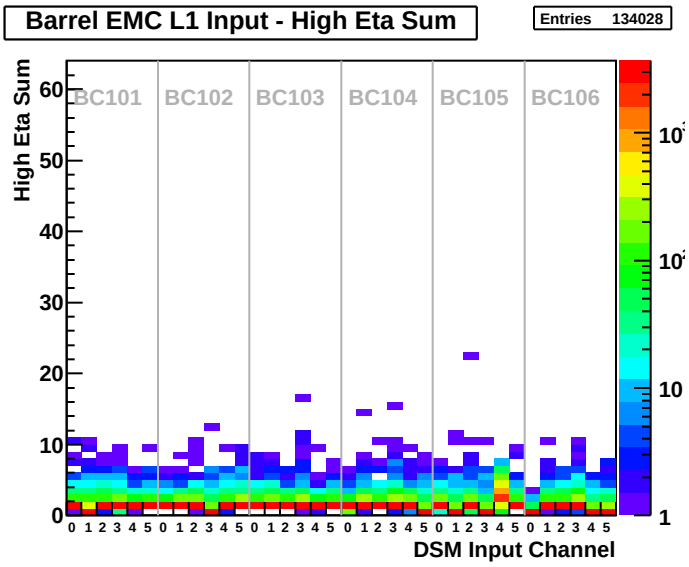
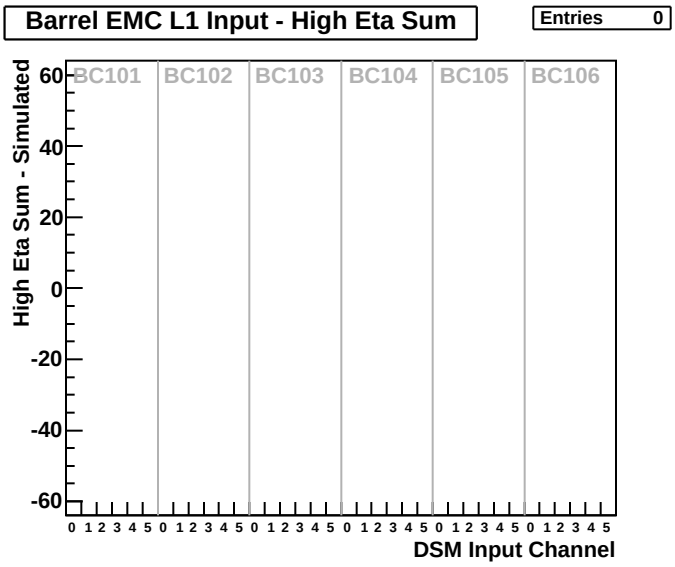
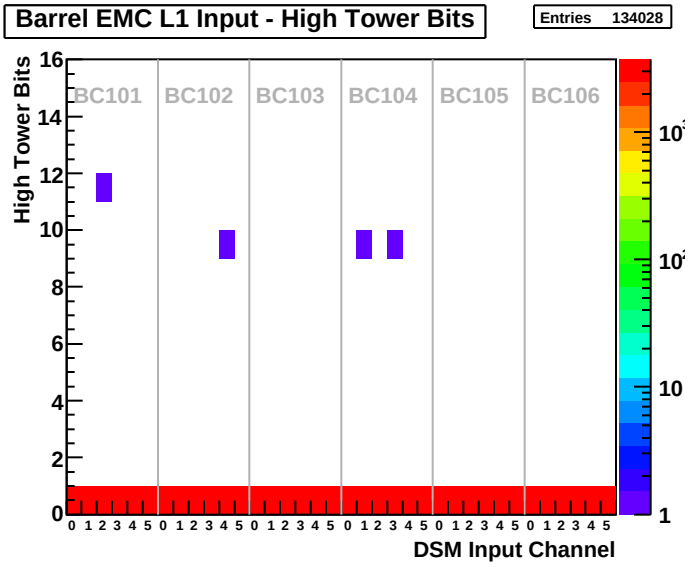
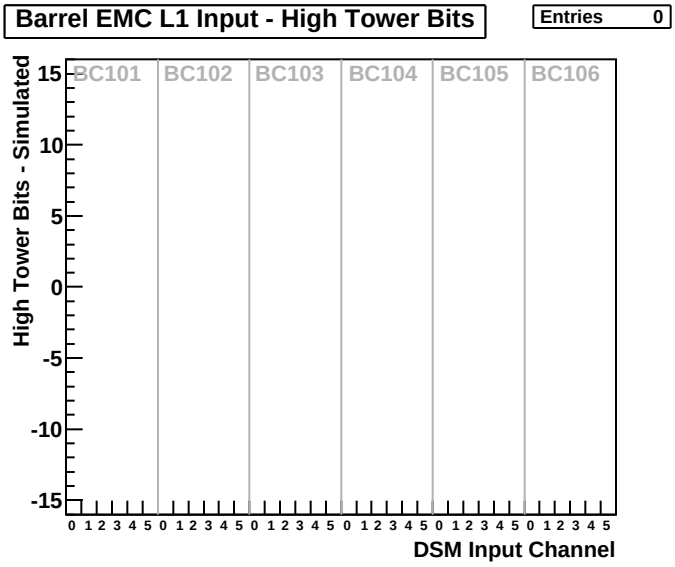

Barrel EMC L1 Input - Low Eta Sum
Entries 134028
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

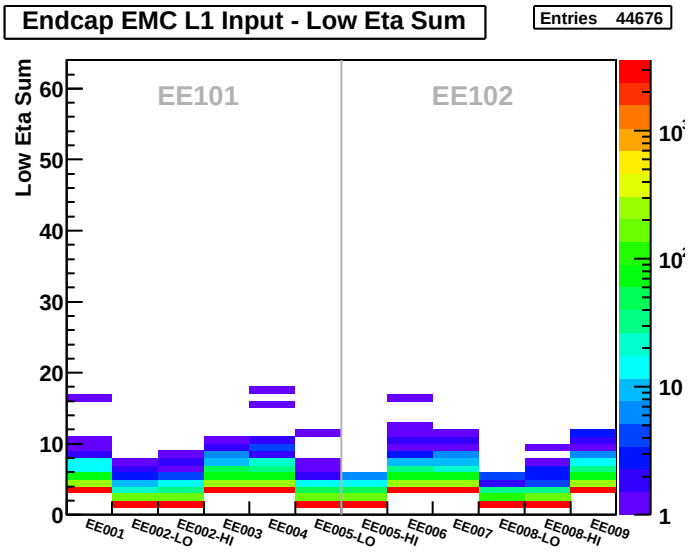

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

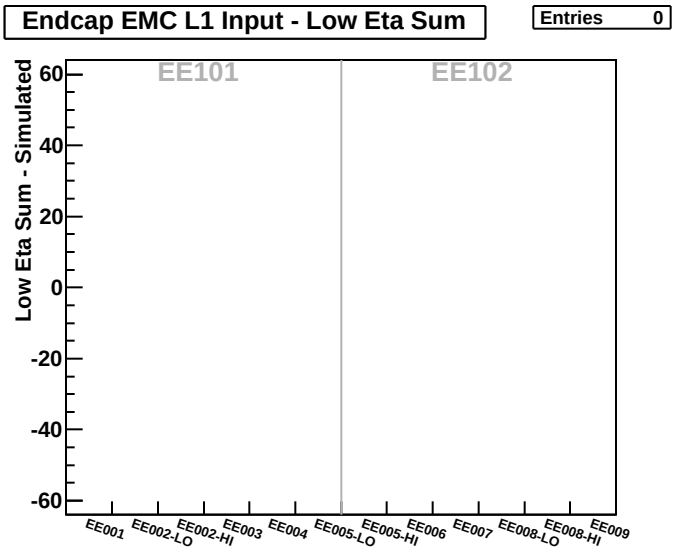

Barrel EMC L1 Input - High Eta Sum
Entries 134028
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

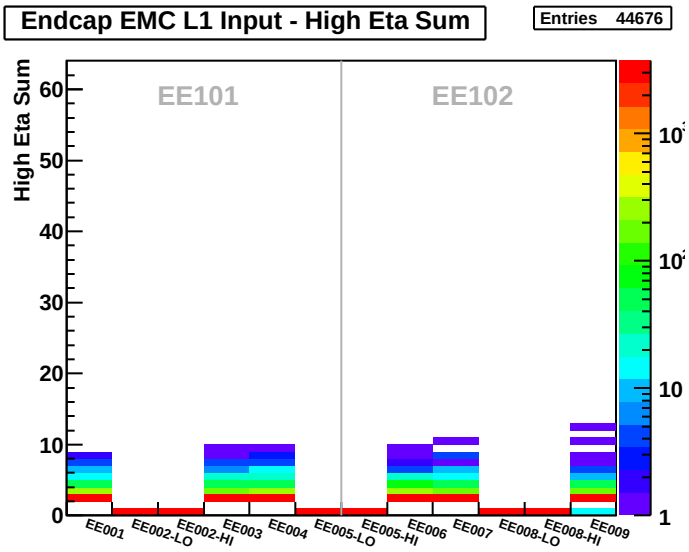

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

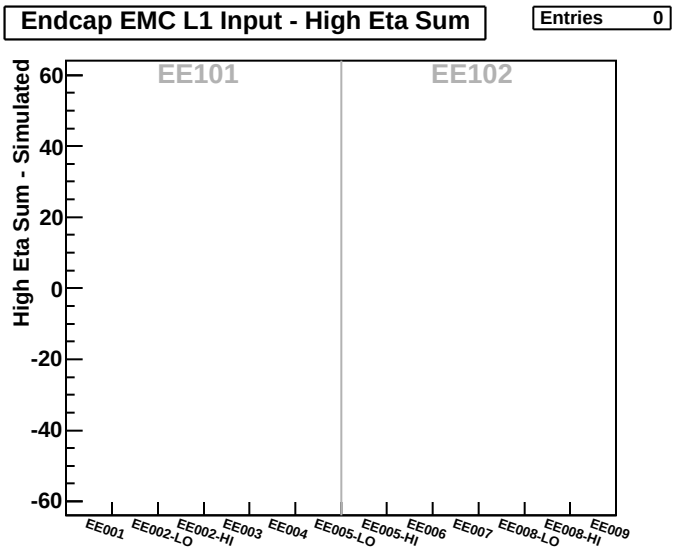

Barrel EMC L1 Input - High Tower Bits
Entries 134028
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

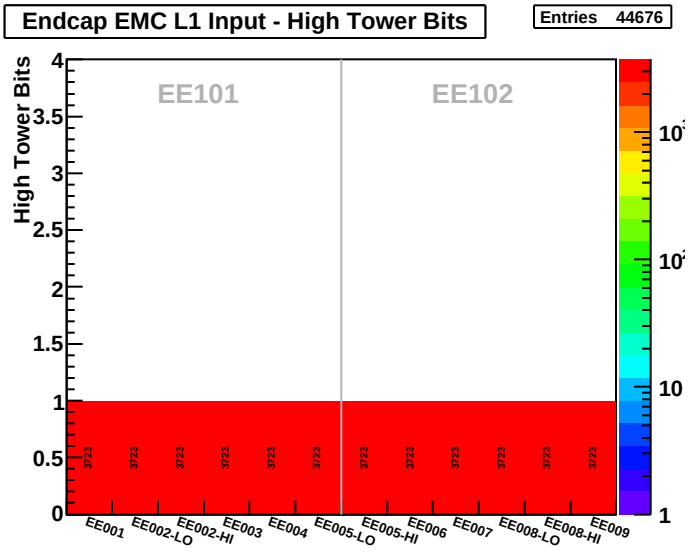

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

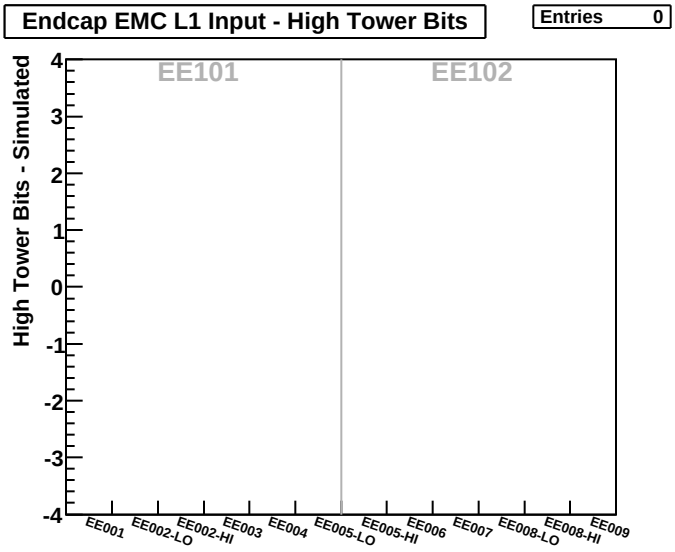

Endcap EMC L1 Input - Low Eta Sum
Entries 44676
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

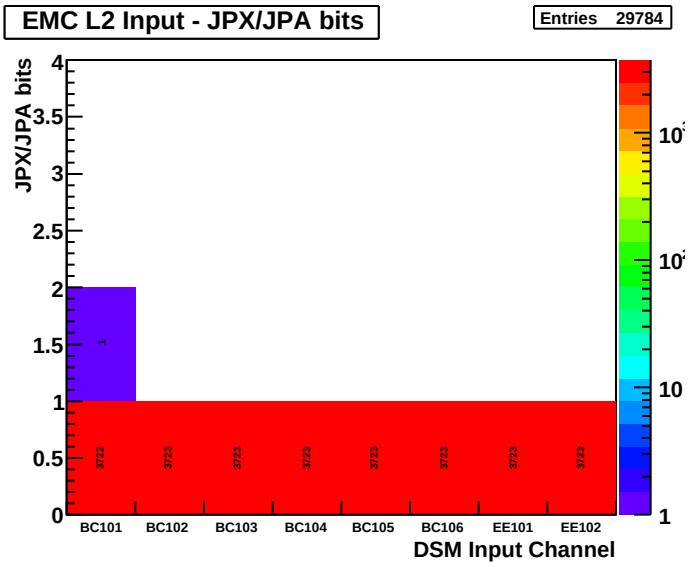

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

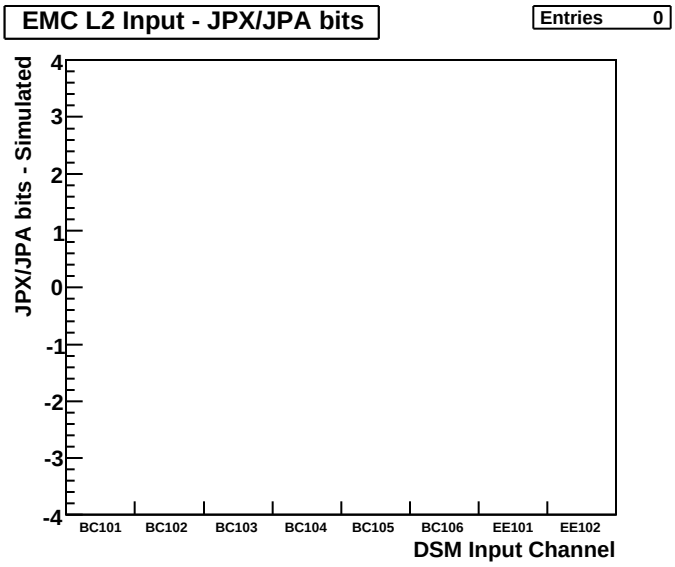

Endcap EMC L1 Input - High Eta Sum
Entries 44676
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

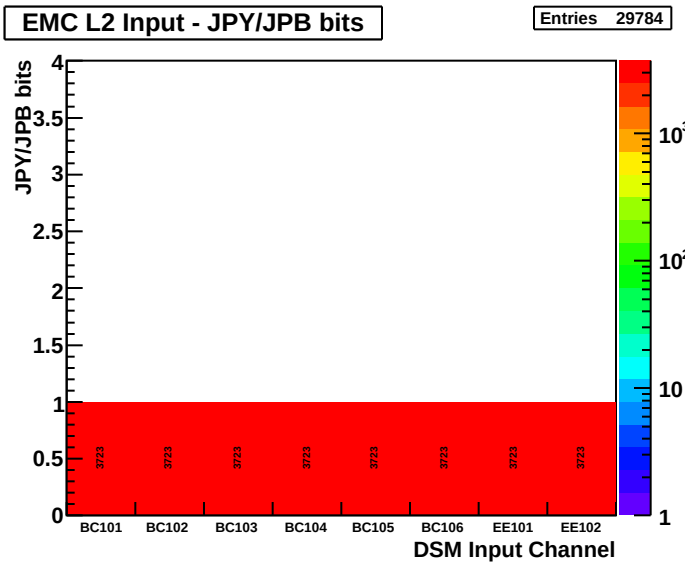

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

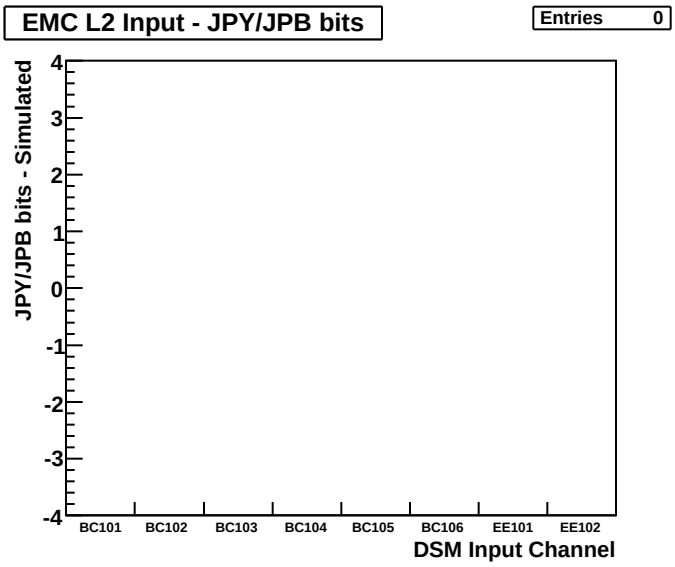

Endcap EMC L1 Input - High Tower Bits
Entries 44676
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

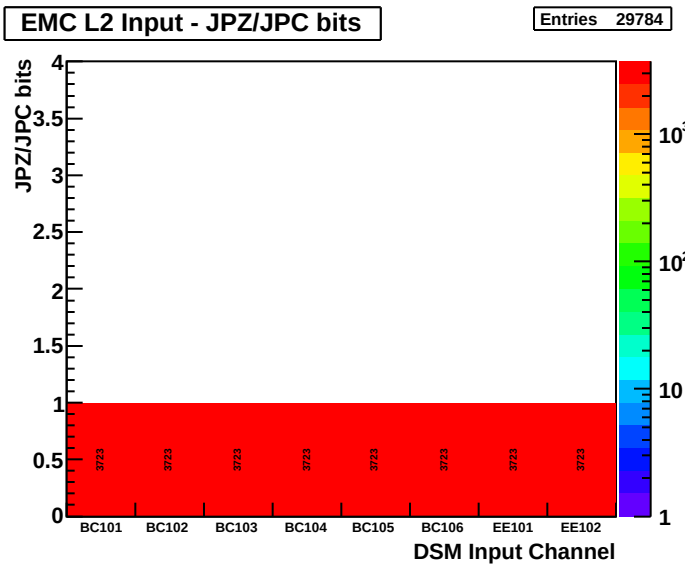

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

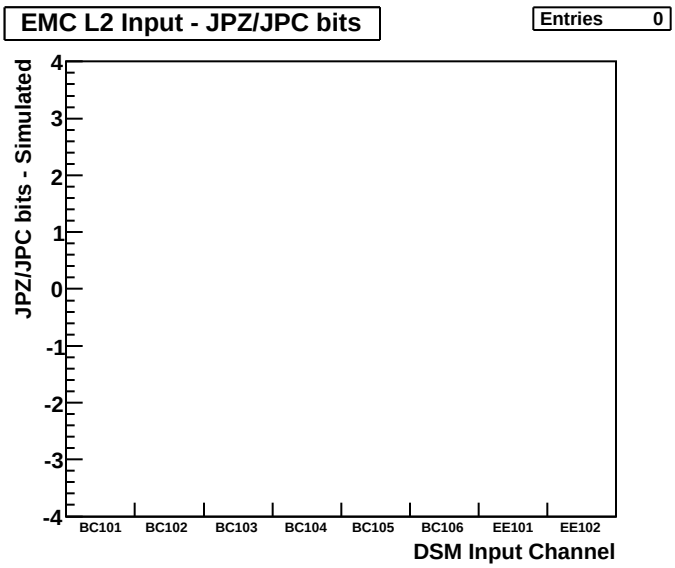

EMC L2 Input - JPX/JPA bits
Entries 29784
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

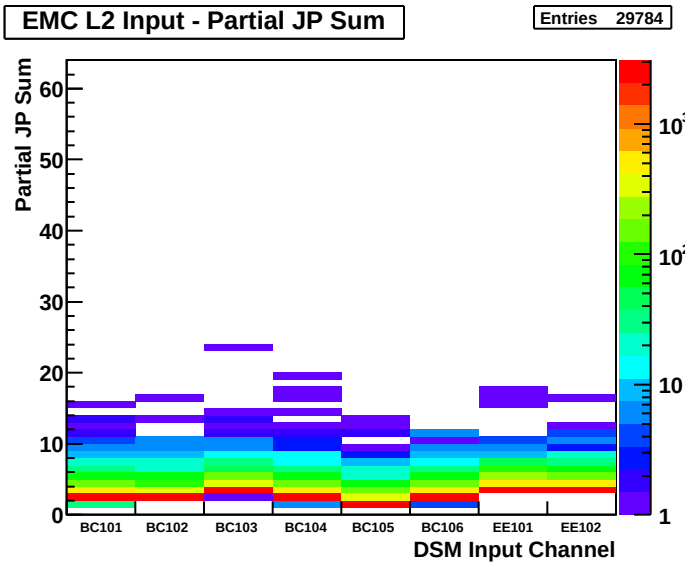

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

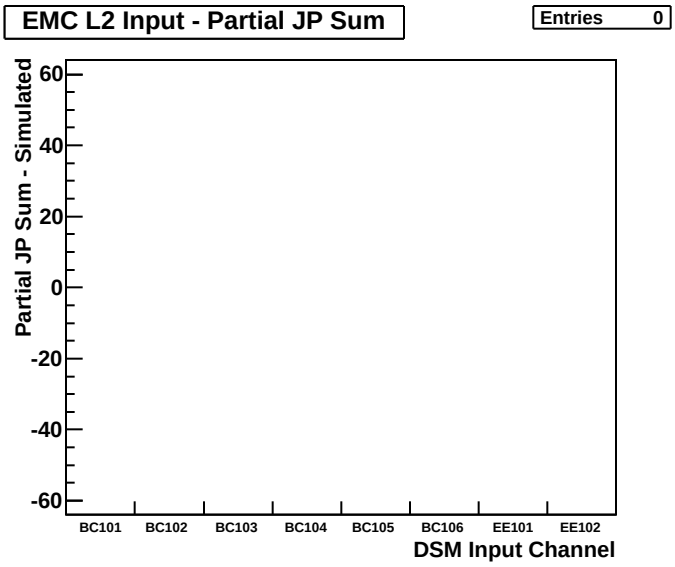

EMC L2 Input - JPY/JPB bits
Entries 29784
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

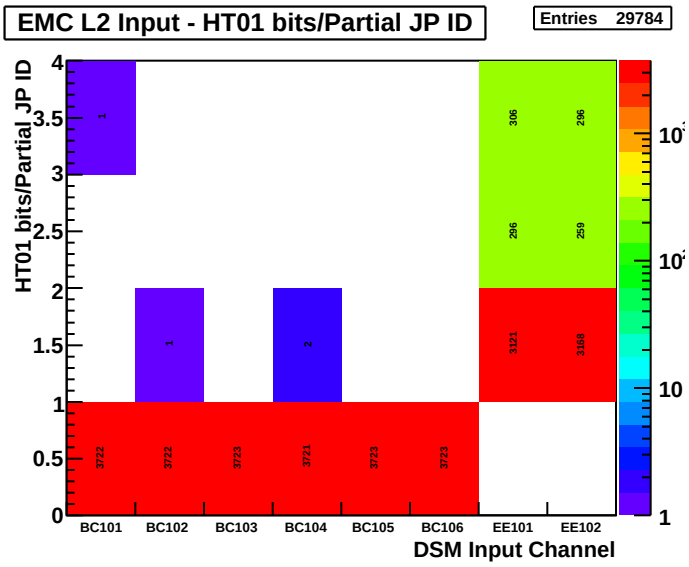

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

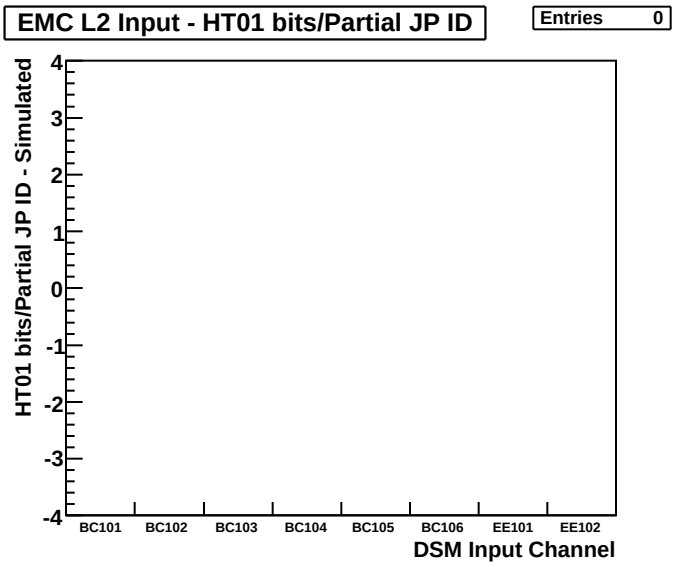

EMC L2 Input - JPZ/JPC bits
Entries 29784
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

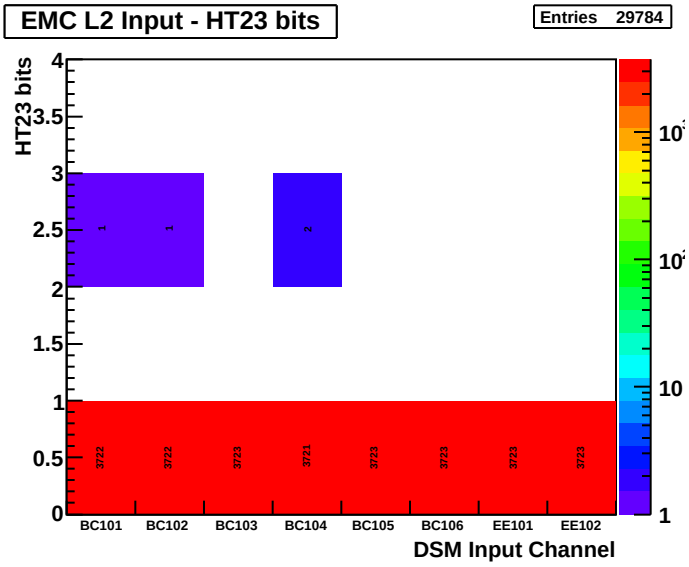

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

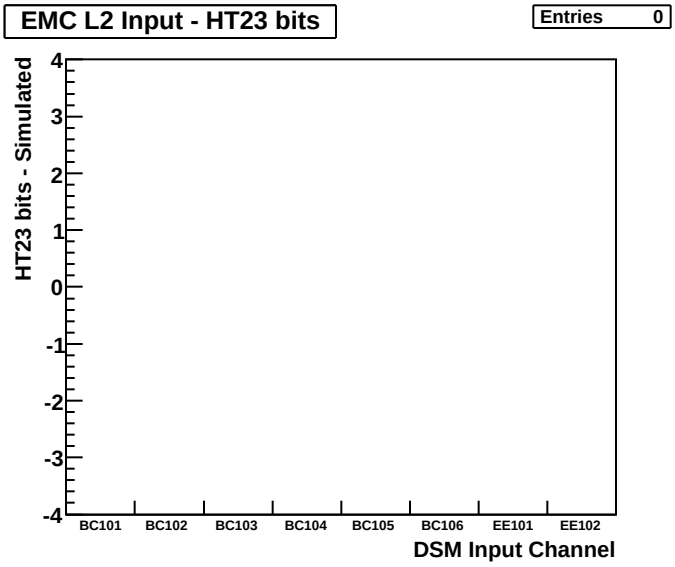

EMC L2 Input - Partial JP Sum
Entries 29784
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

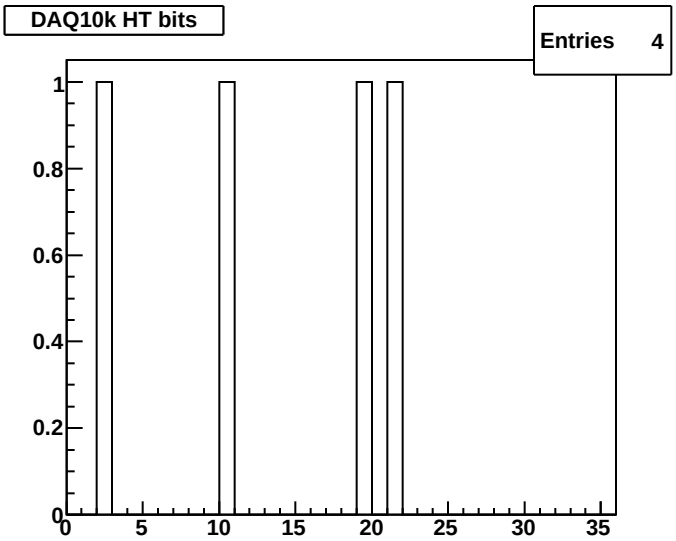

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

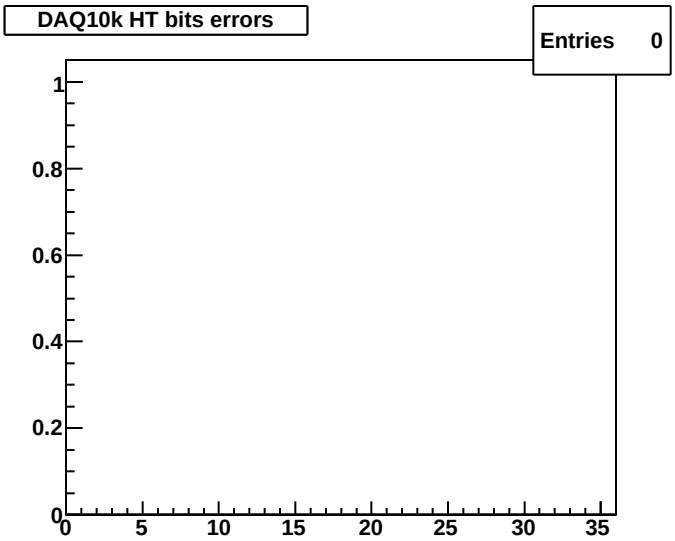

EMC L2 Input - HT01 bits/Partial JP ID
Entries 29784
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

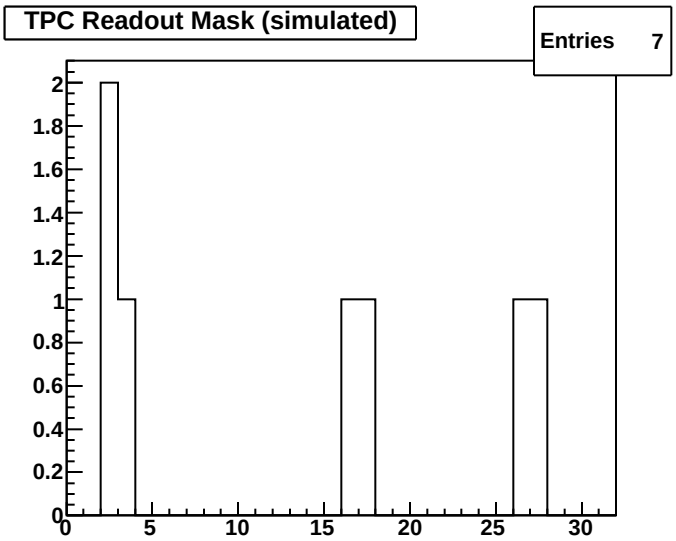

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

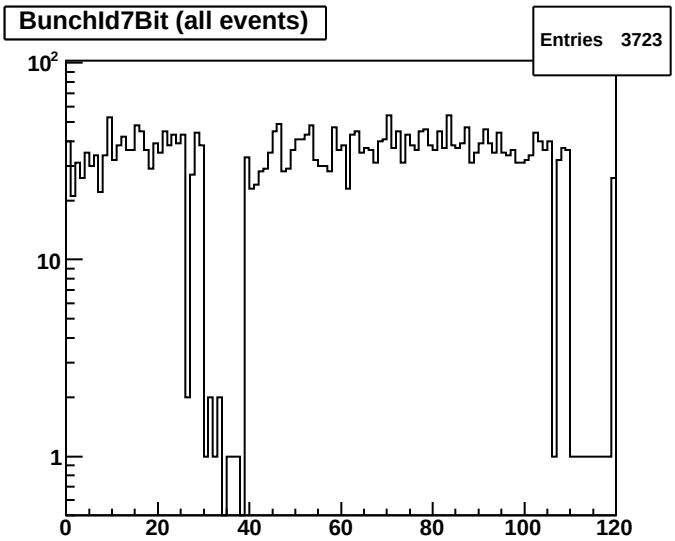

EMC L2 Input - HT23 bits
Entries 29784
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

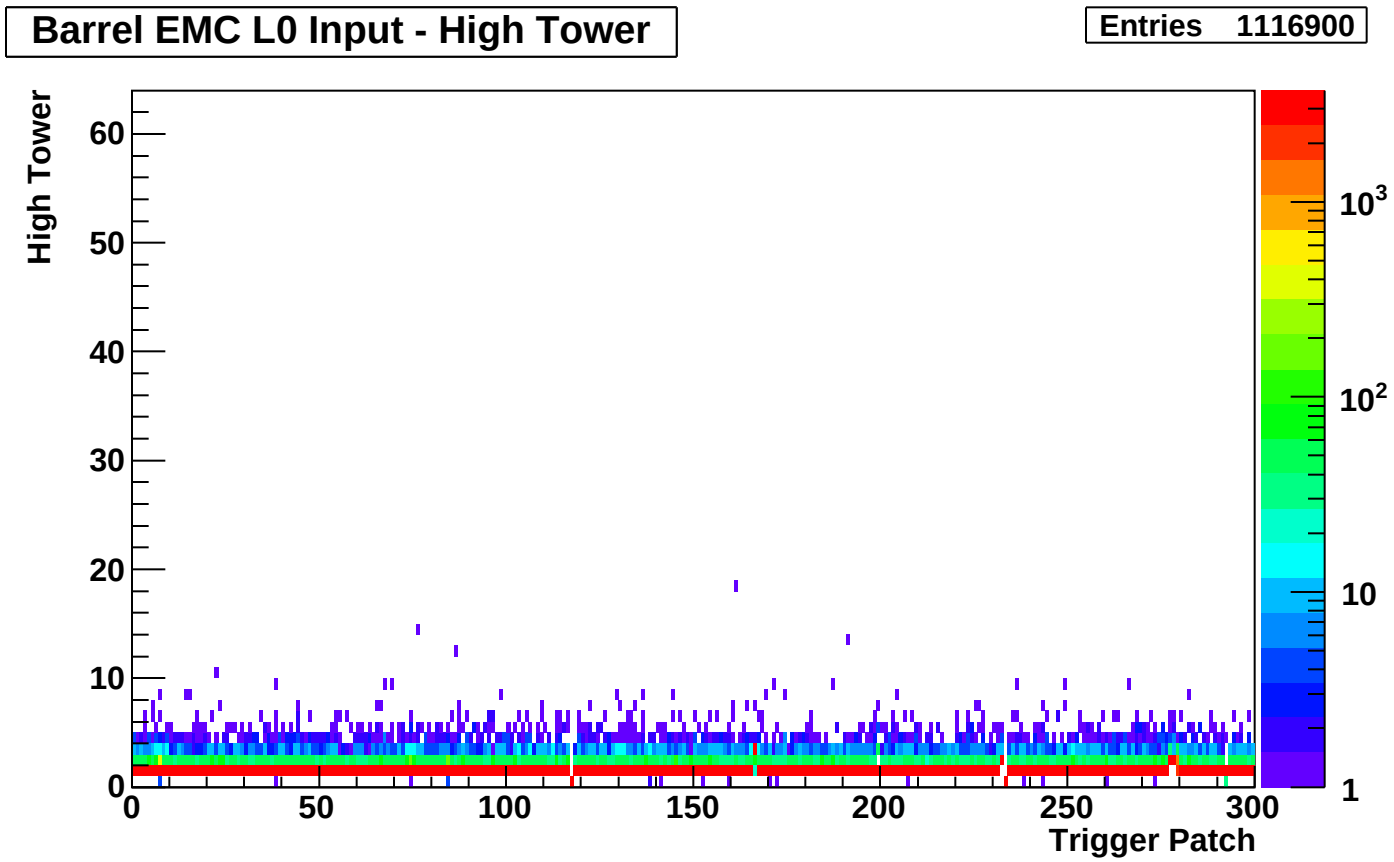

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

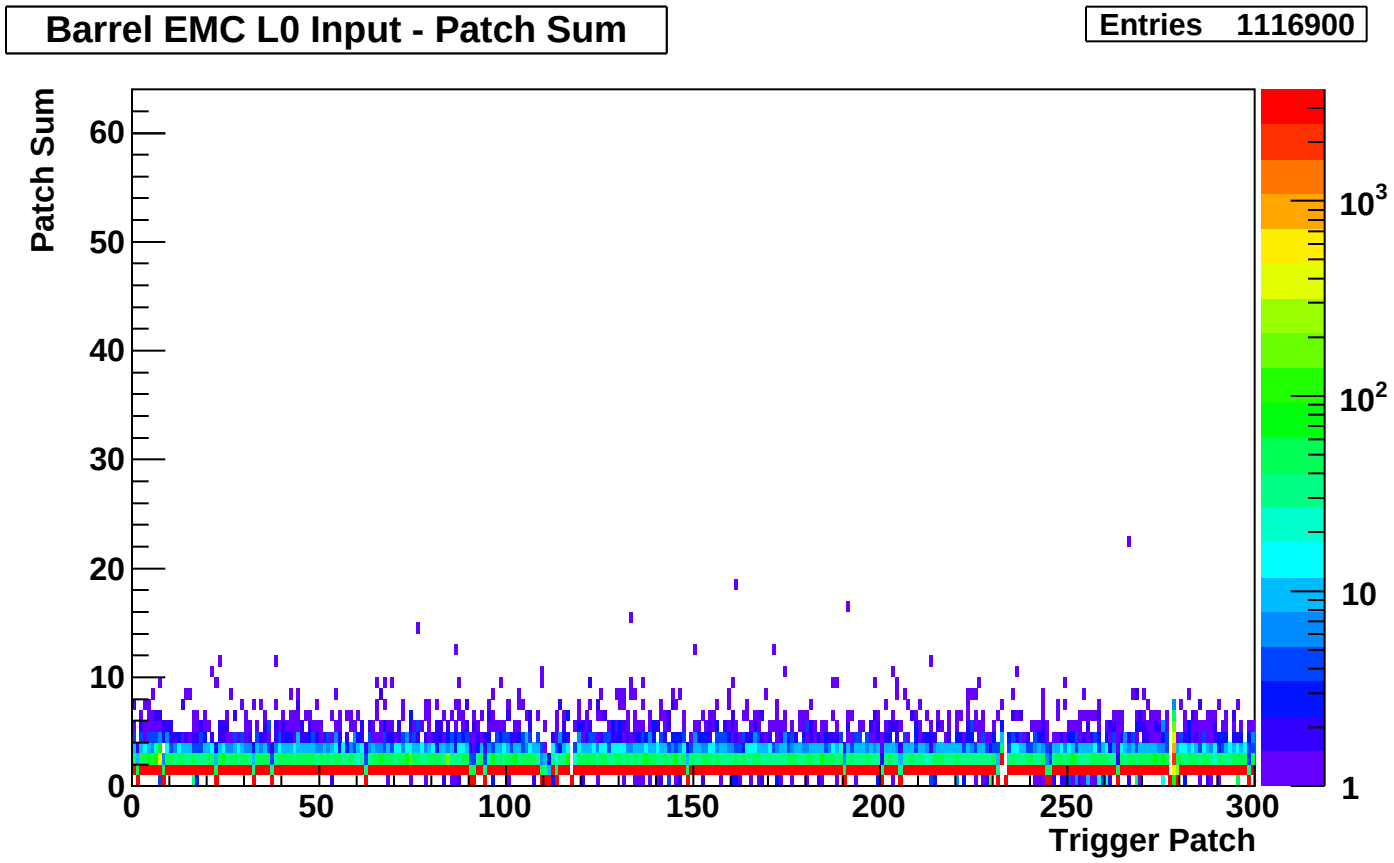

DAQ10k HT bits
Entries 4

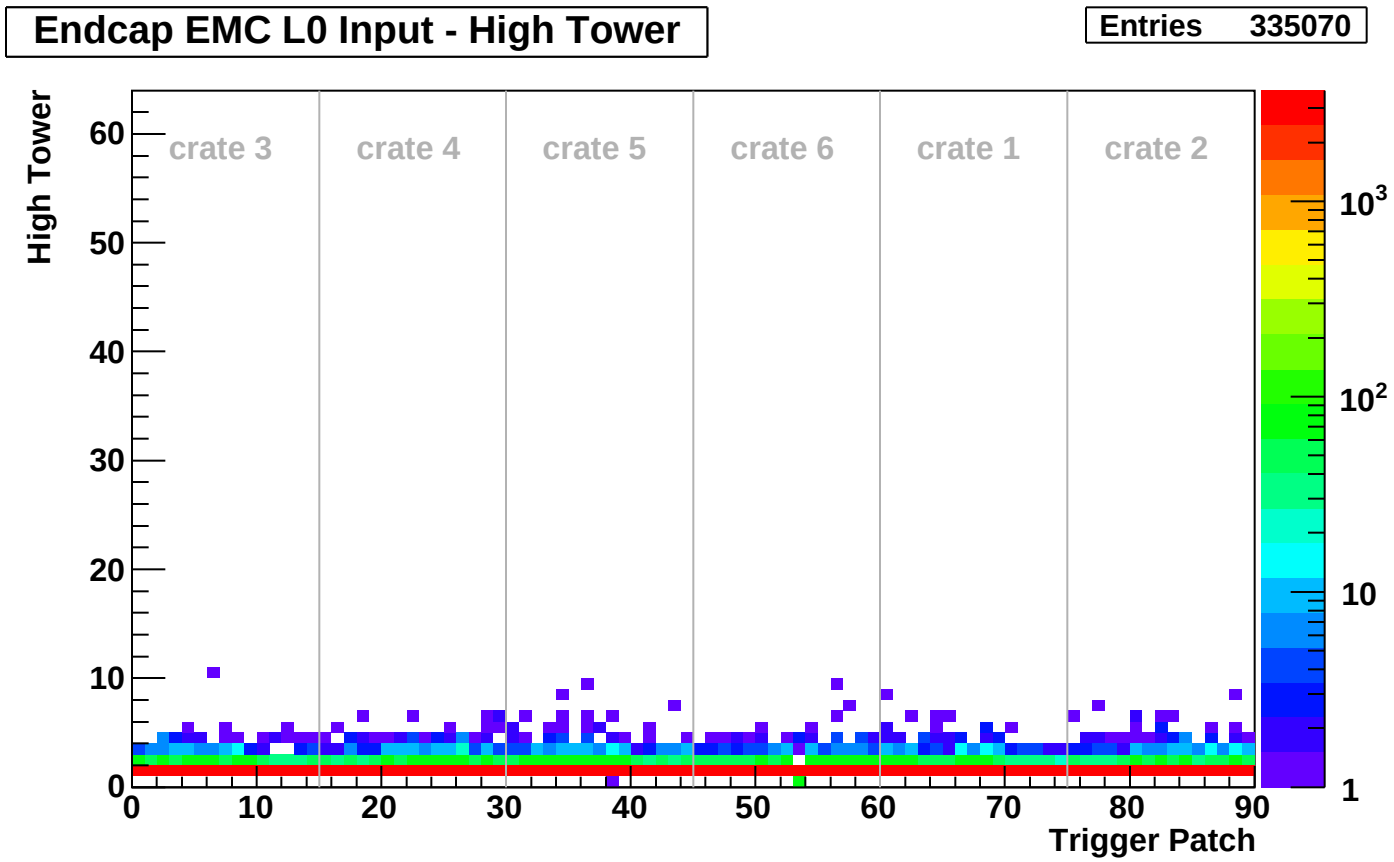

DAQ10k HT bits errors
Entries 0

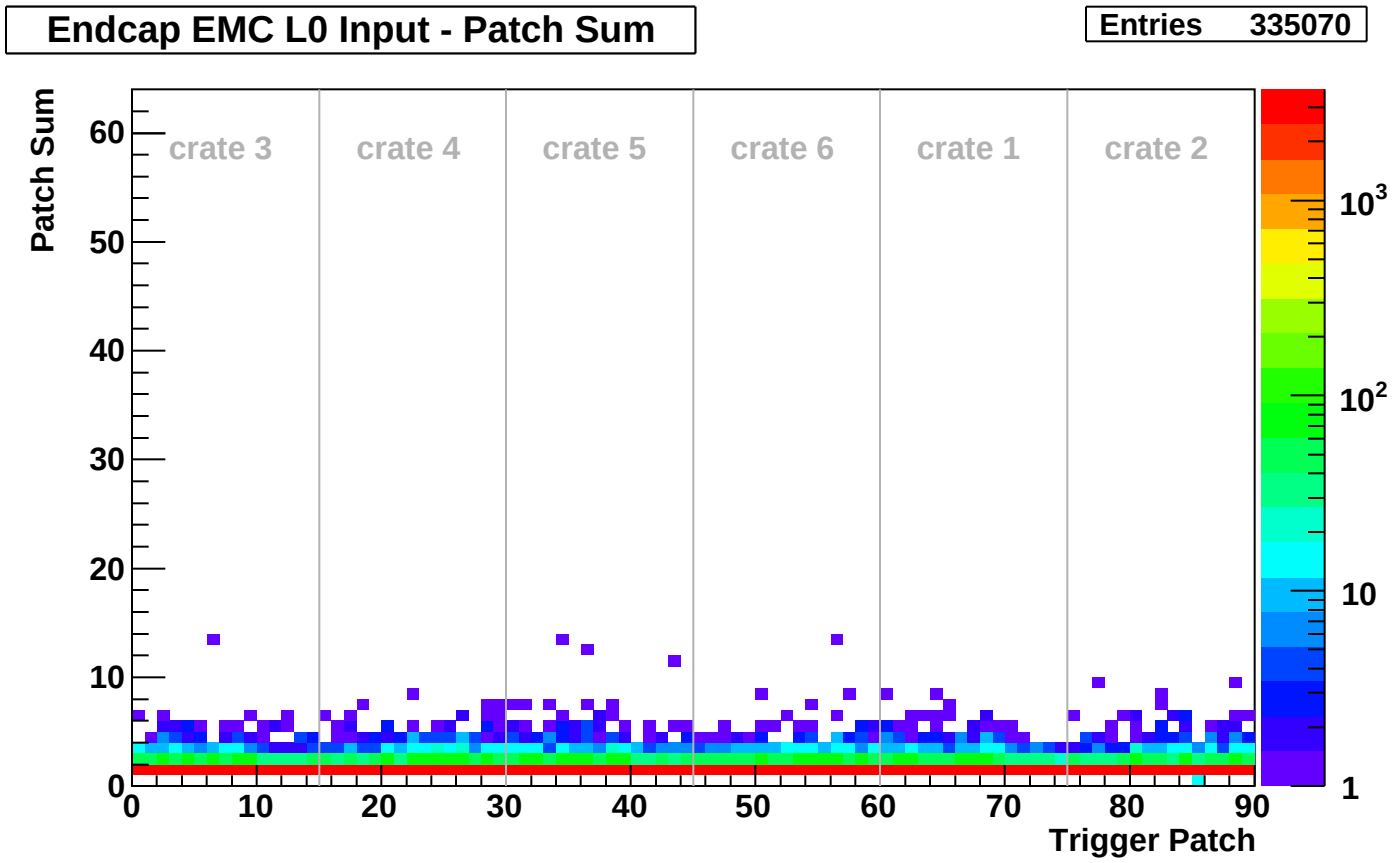

TPC Readout Mask (simulated)
Entries 7

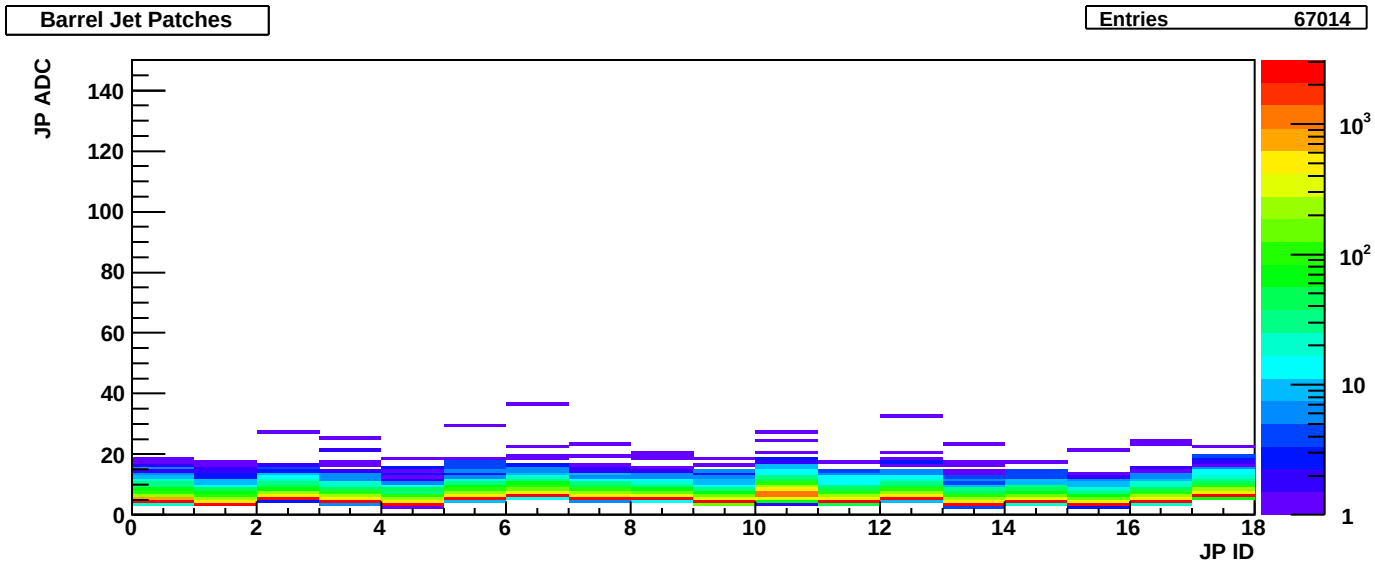

BunchId7Bit (all events)
Entries 3723

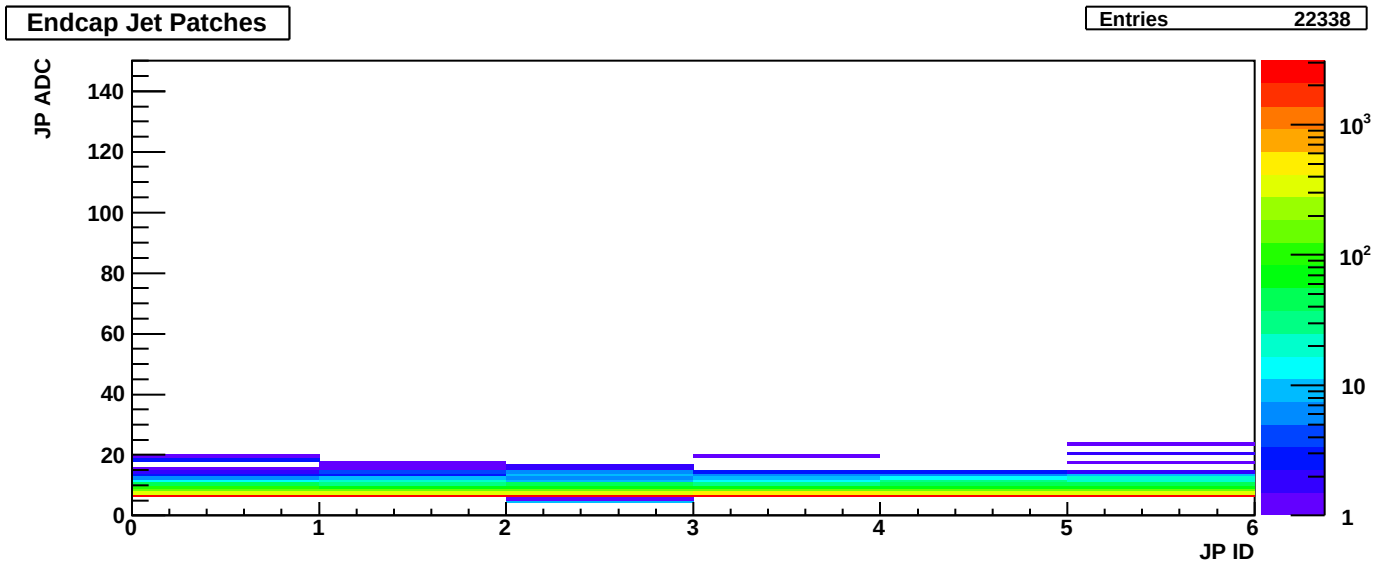

Barrel EMC L0 Input - High Tower
Entries 1116900
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

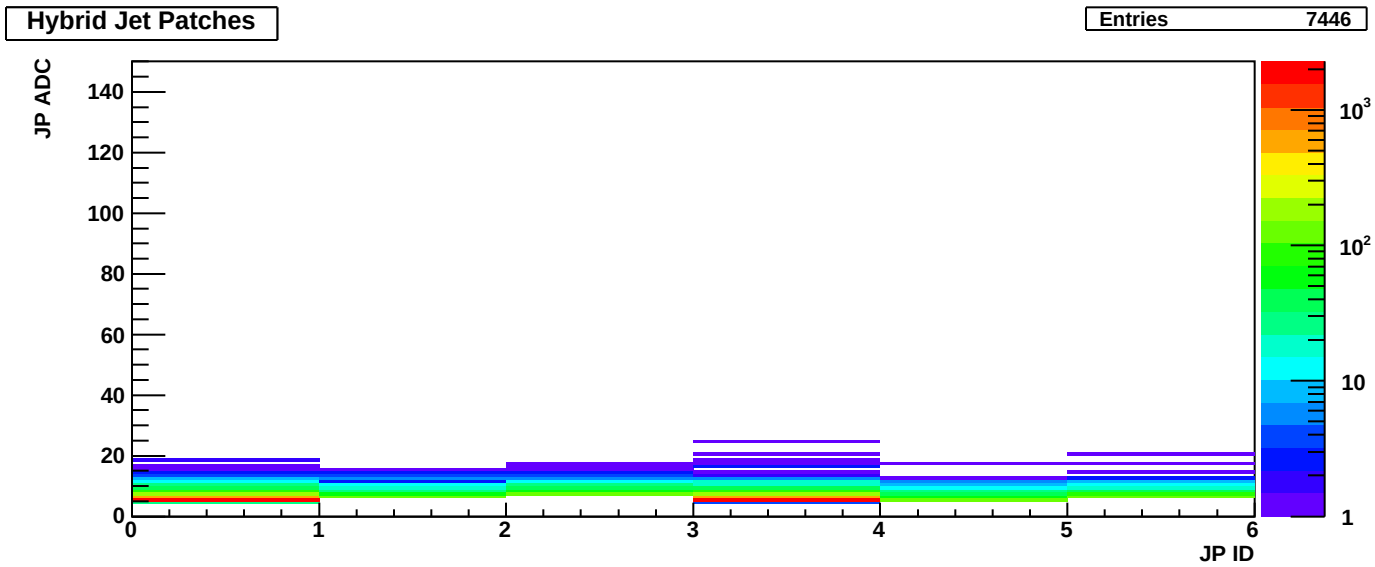

Barrel EMC L0 Input - Patch Sum
Entries 1116900
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

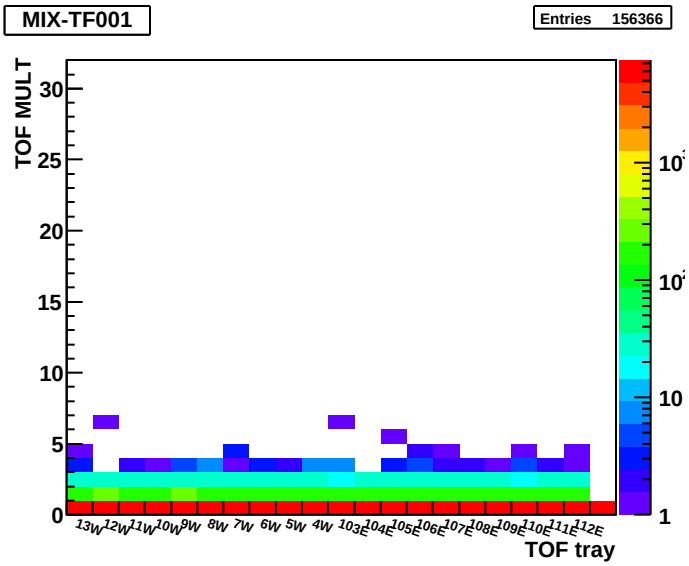

Endcap EMC L0 Input - High Tower
Entries 335070
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

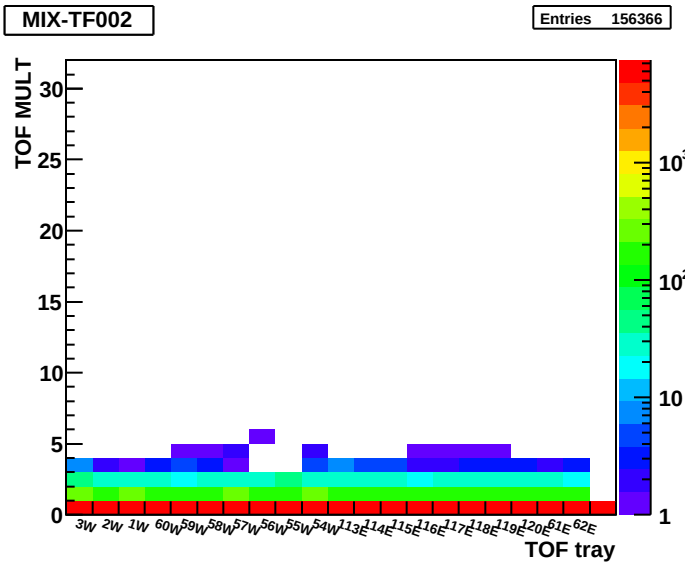

Endcap EMC L0 Input - Patch Sum
Entries 335070
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

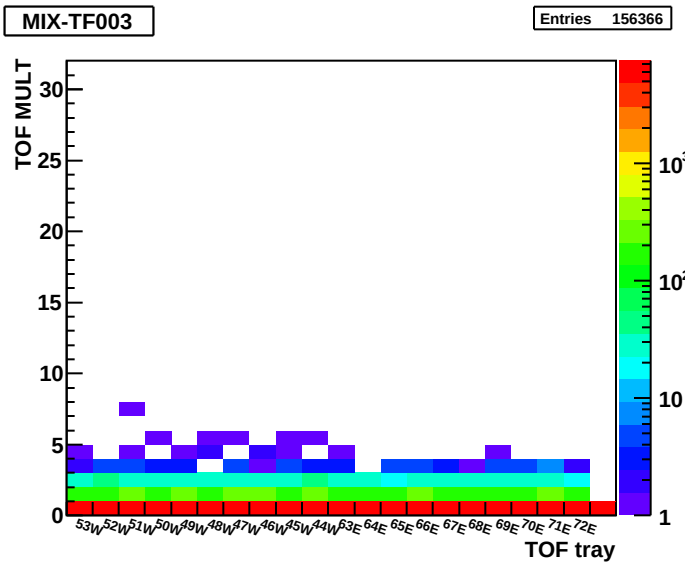

Barrel Jet Patches
Entries 67014
JP ADC
JP ID

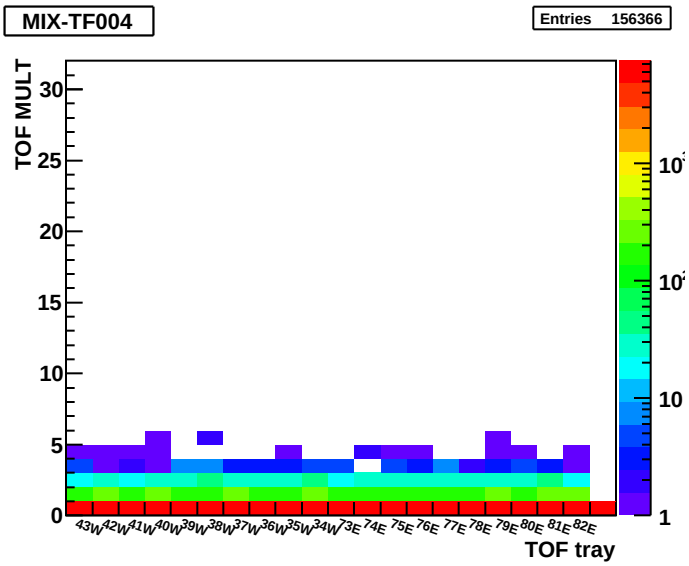

Endcap Jet Patches
Entries 22338
JP ADC
JP ID

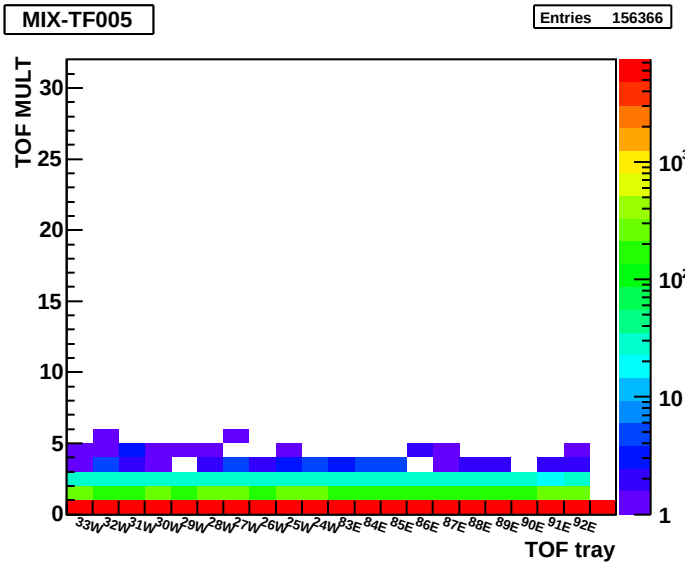

Hybrid Jet Patches
Entries 7446
JP ADC
JP ID

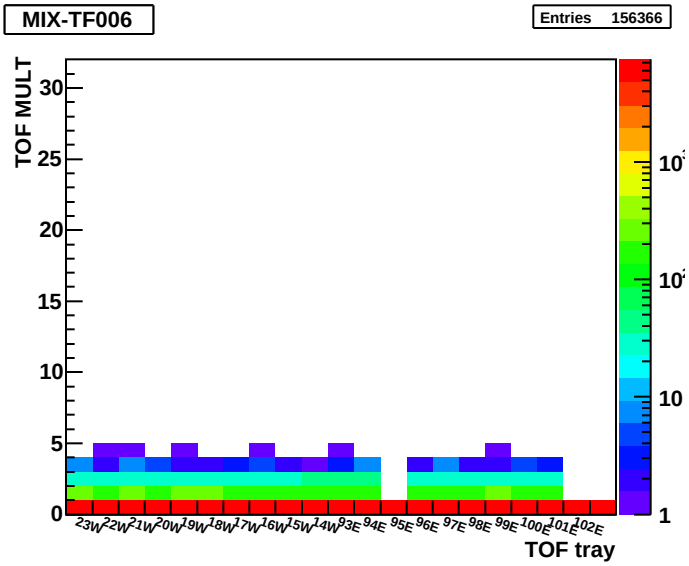

MIX-TF001
Entries 156366
TOF MULT
TOF tray

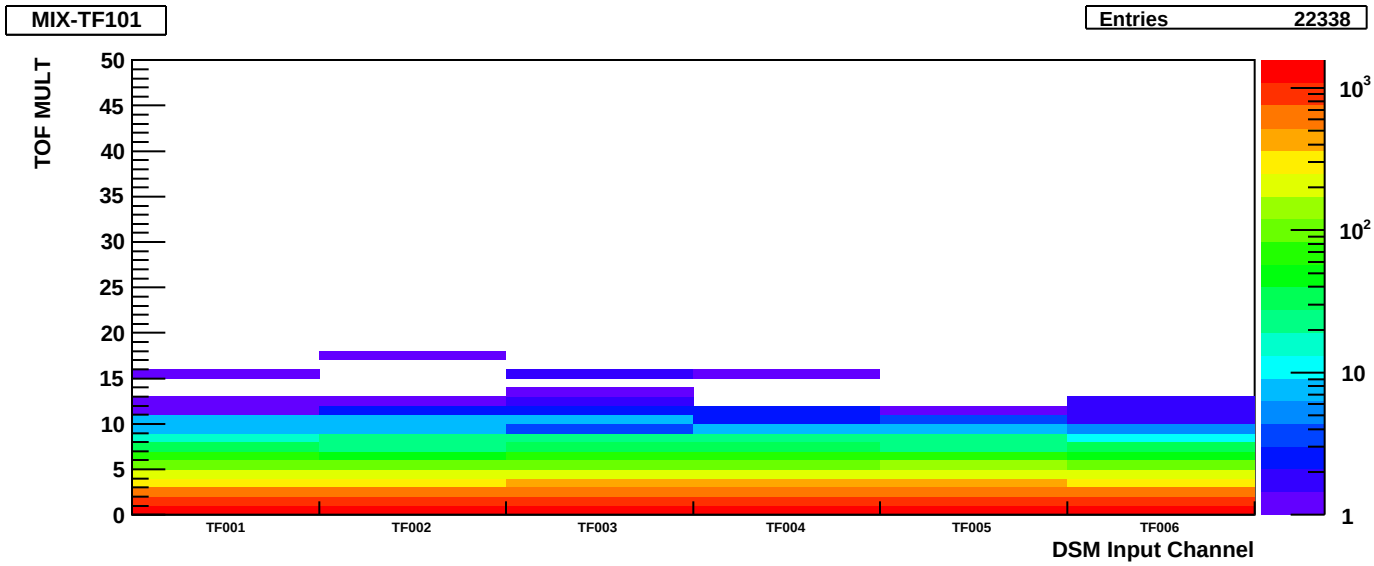

MIX-TF002
Entries 156366
TOF MULT
TOF tray

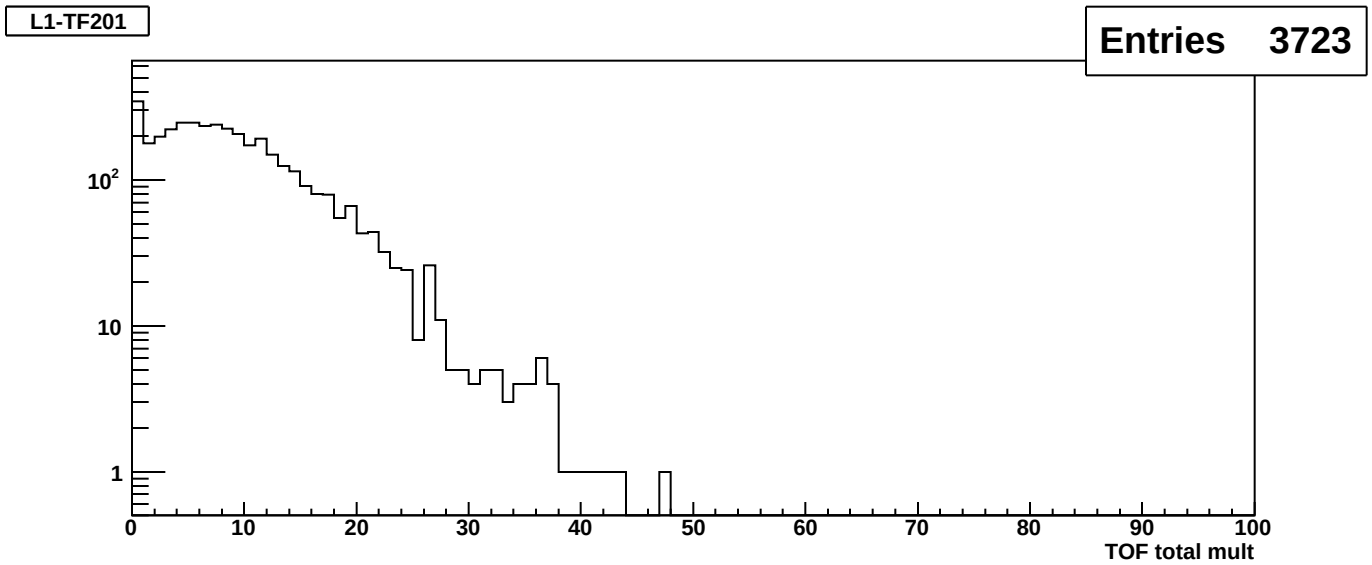

MIX-TF003
Entries 156366
TOF MULT
TOF tray

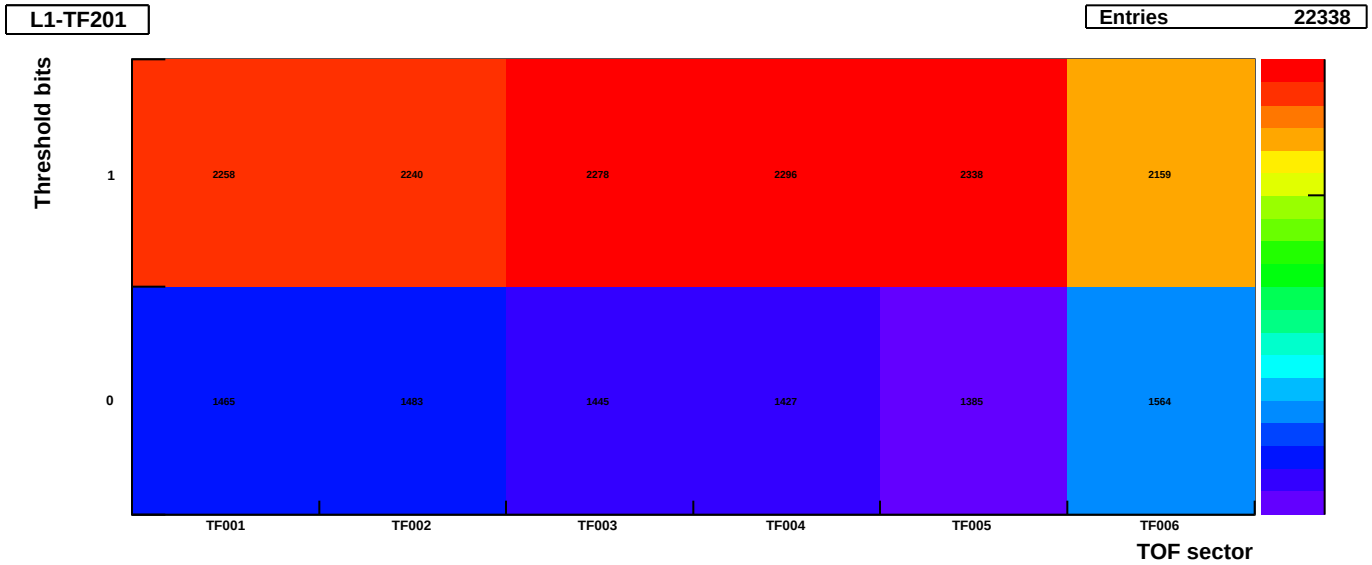

MIX-TF004
Entries 156366
TOF MULT
TOF tray

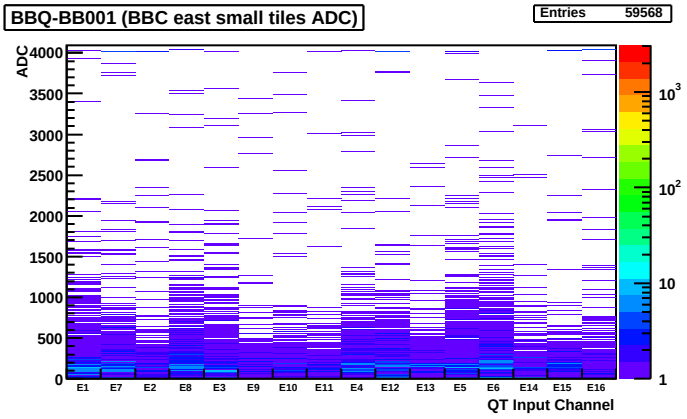

MIX-TF005
Entries 156366
TOF MULT
TOF tray

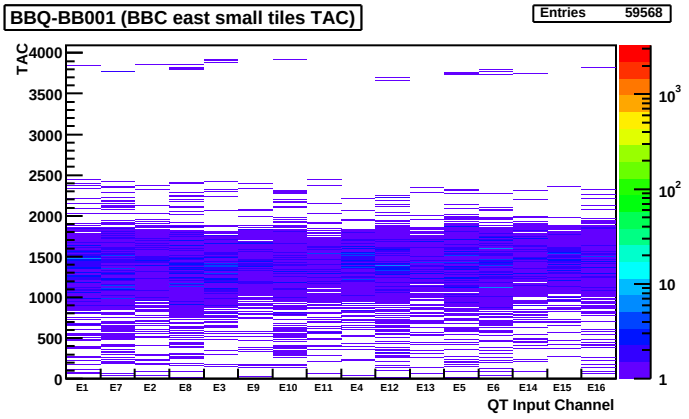

MIX-TF006
Entries 156366
TOF MULT
TOF tray

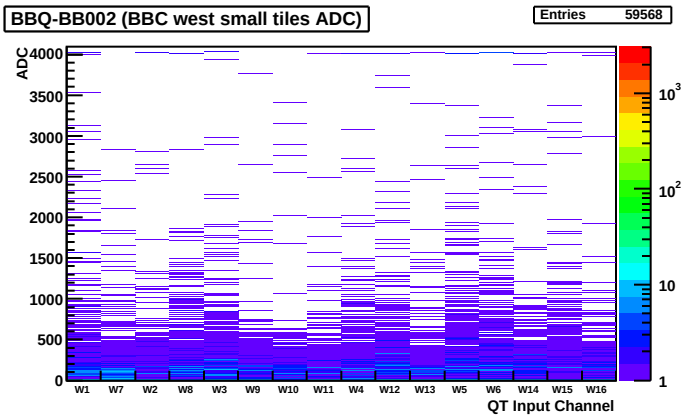

MIX-TF101
Entries 22338
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

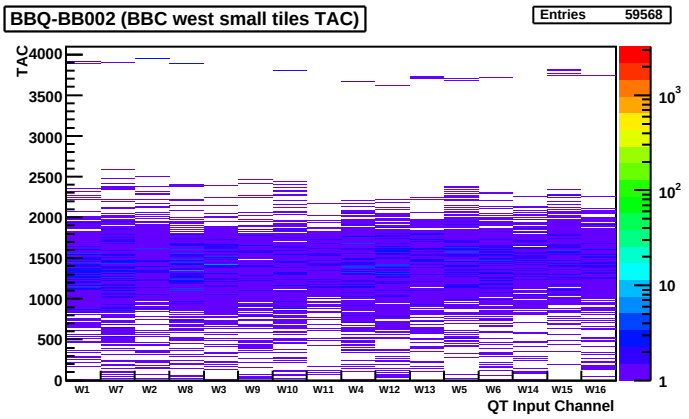

L1-TF201
Entries 3723
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

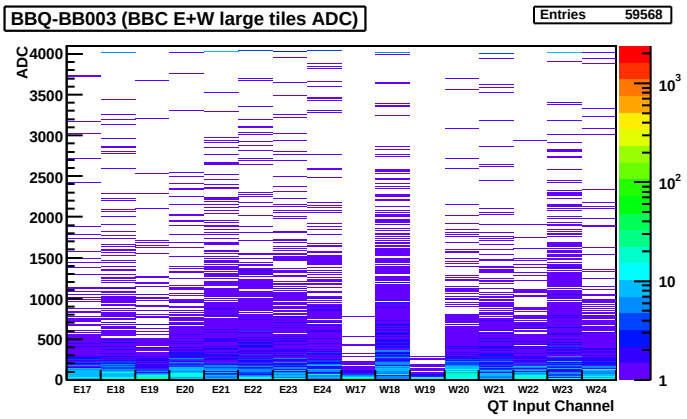

L1-TF201
Entries 22338
Threshold bits
1
0
2258
2240
2278
2296
2338
2159
1465
1483
1445
1427
1385
1564
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

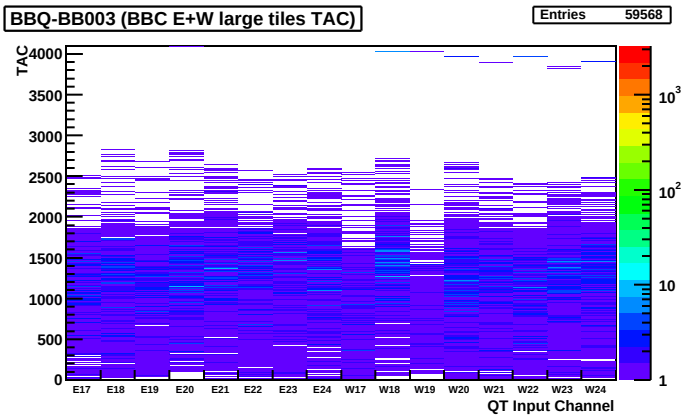

BBQ-BB001 (BBC east small tiles ADC)
Entries 59568
ADC
QT Input Channel

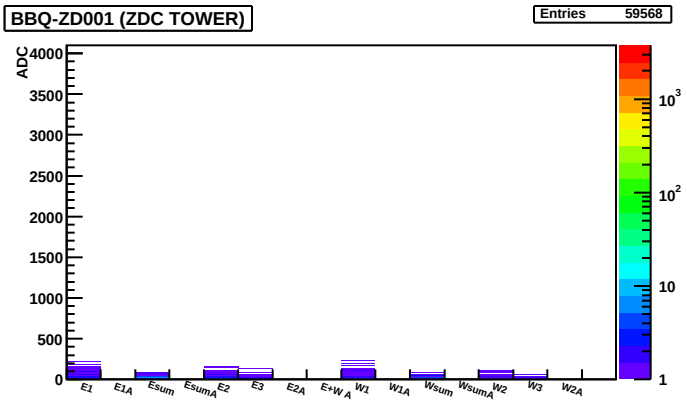

BBQ-BB001 (BBC east small tiles TAC)
Entries 59568
TAC
QT Input Channel

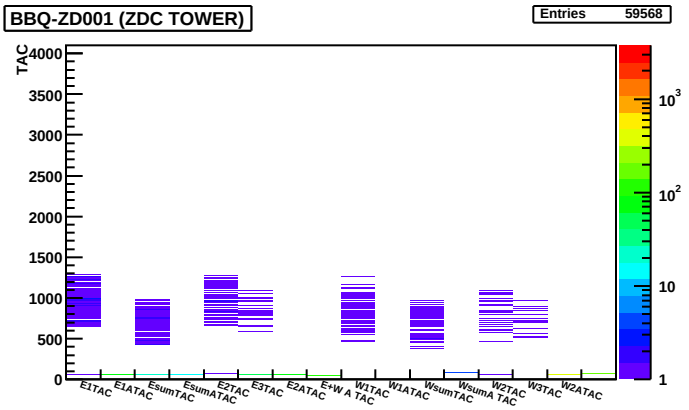

BBQ-BB002 (BBC west small tiles ADC)
Entries 59568
ADC
QT Input Channel

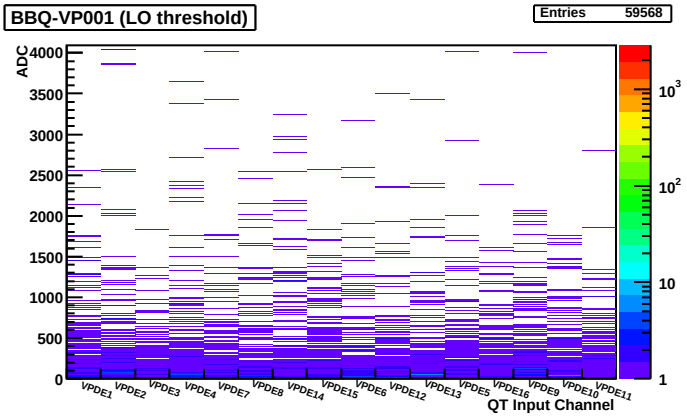

BBQ-BB002 (BBC west small tiles TAC)
Entries 59568
TAC
QT Input Channel

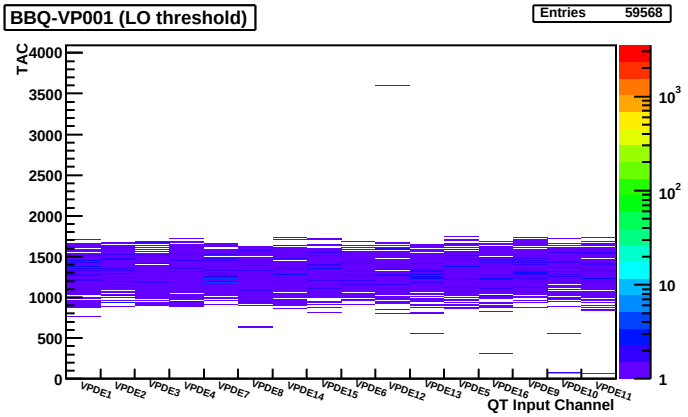

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 59568
ADC
QT Input Channel

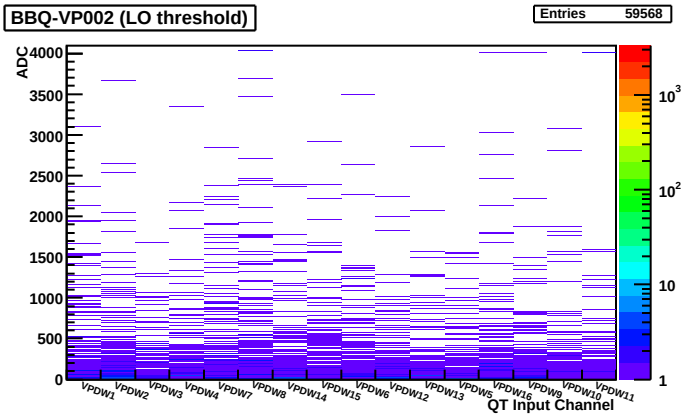

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 59568
TAC
QT Input Channel

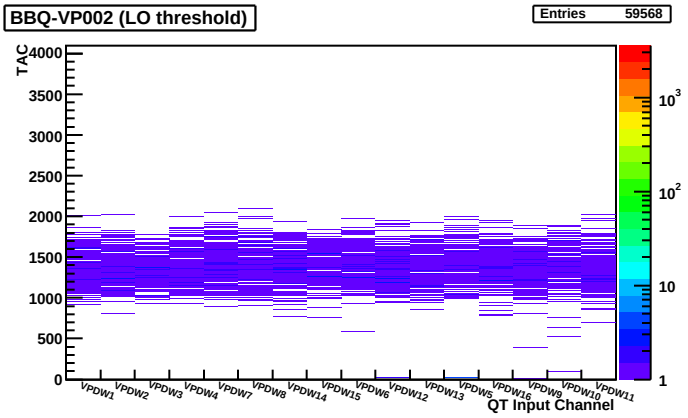

BBQ-ZD001 (ZDC TOWER)
Entries 59568
ADC

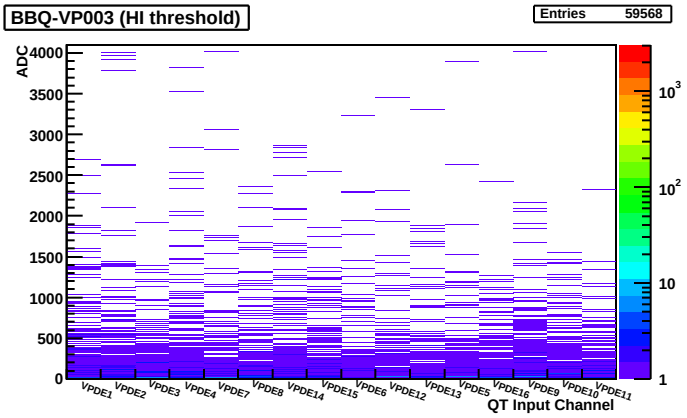

BBQ-ZD001 (ZDC TOWER)
Entries 59568
TAC

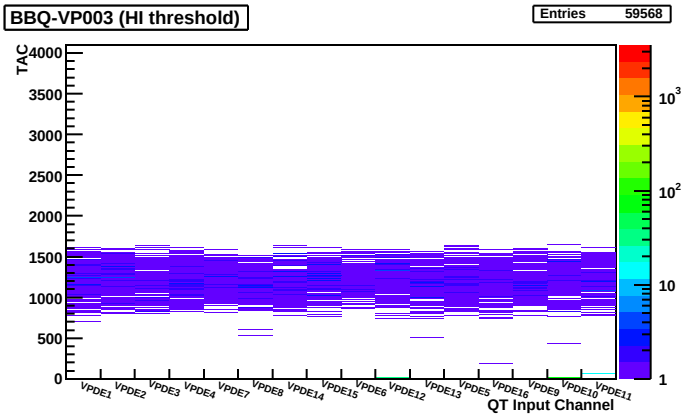

BBQ-VP001 (LO threshold)
Entries 59568
ADC
QT Input Channel

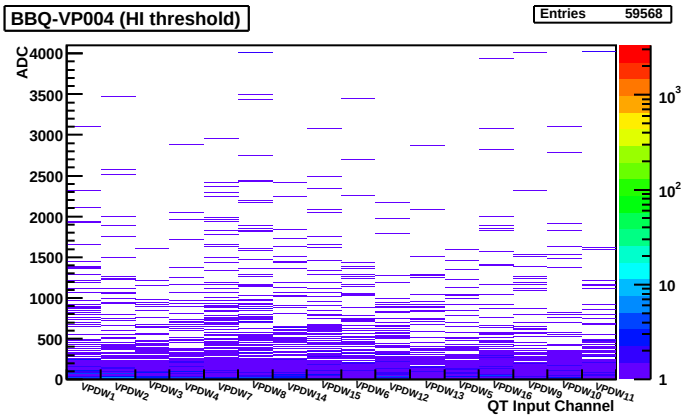

BBQ-VP001 (LO threshold)
Entries 59568
TAC
QT Input Channel

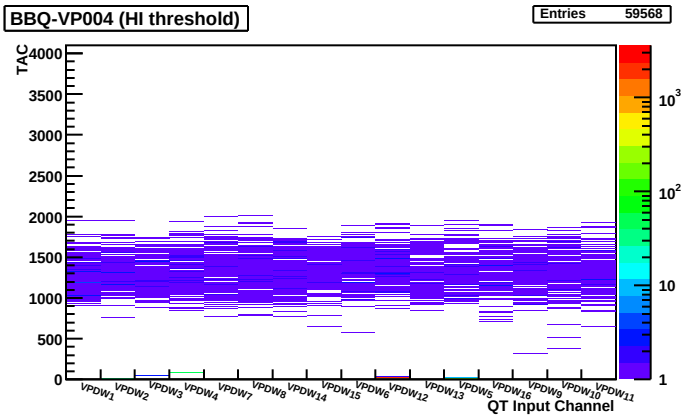

BBQ-VP002 (LO threshold)
Entries 59568
ADC
QT Input Channel

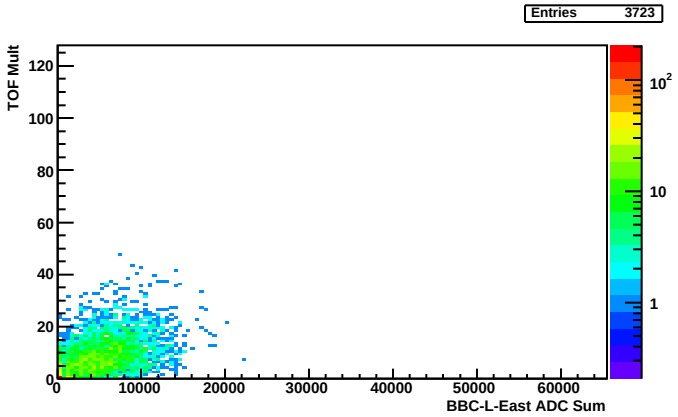

BBQ-VP002 (LO threshold)
Entries 59568
TAC
QT Input Channel

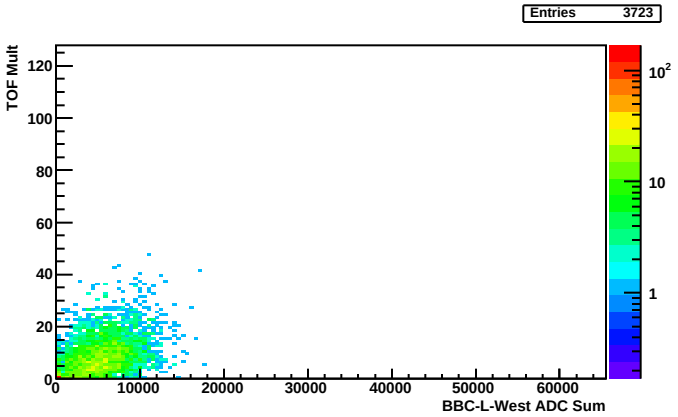

BBQ-VP003 (HI threshold)
Entries 59568
ADC
QT Input Channel

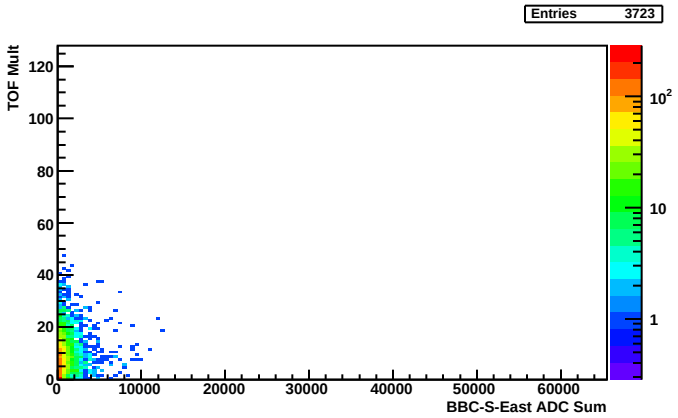

BBQ-VP003 (HI threshold)
Entries 59568
TAC
QT Input Channel

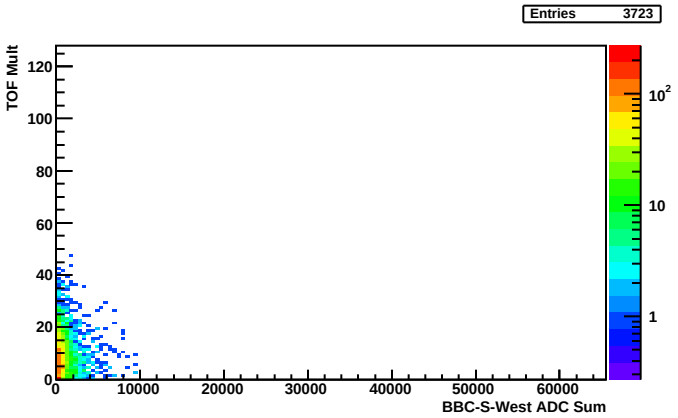

BBQ-VP004 (HI threshold)
Entries 59568
ADC
QT Input Channel

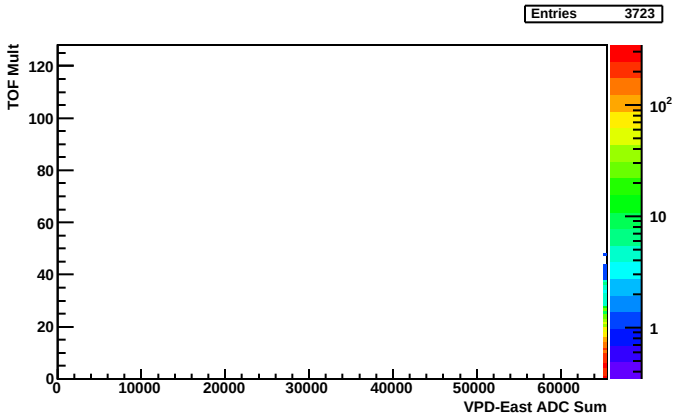

BBQ-VP004 (HI threshold)
Entries 59568
TAC
QT Input Channel

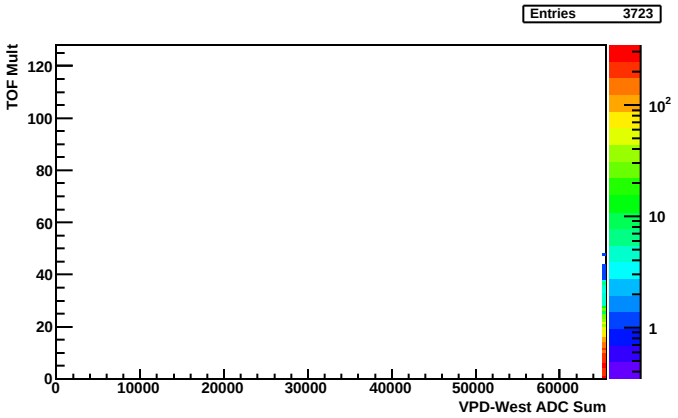

Entries 3723
TOF Mult
BBC-L-East ADC Sum

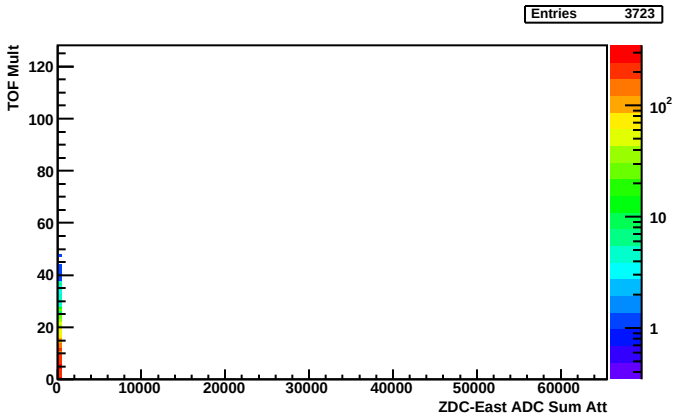

Entries 3723
TOF Mult
BBC-L-West ADC Sum

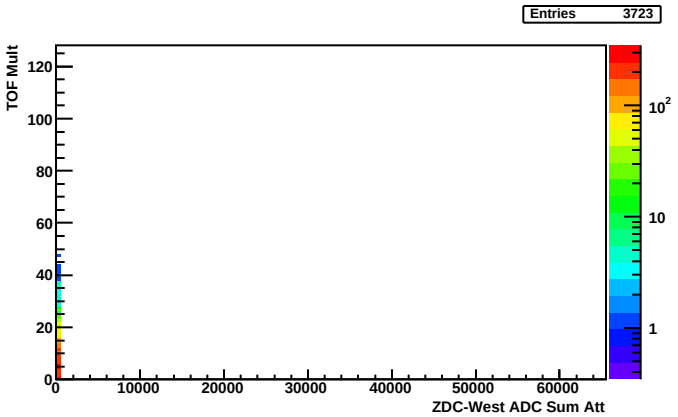

Entries 3723
TOF Mult
BBC-S-East ADC Sum

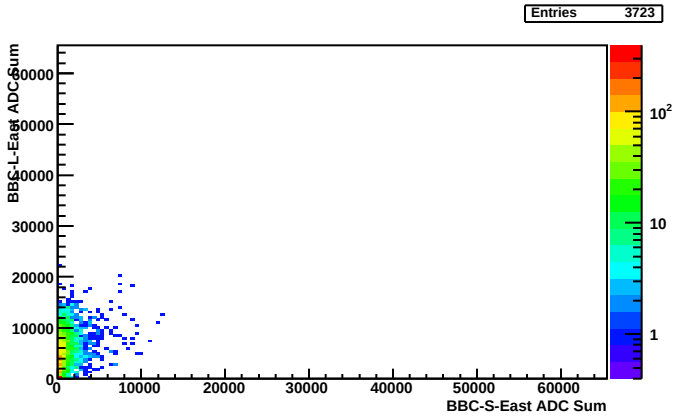

Entries 3723
TOF Mult
BBC-S-West ADC Sum

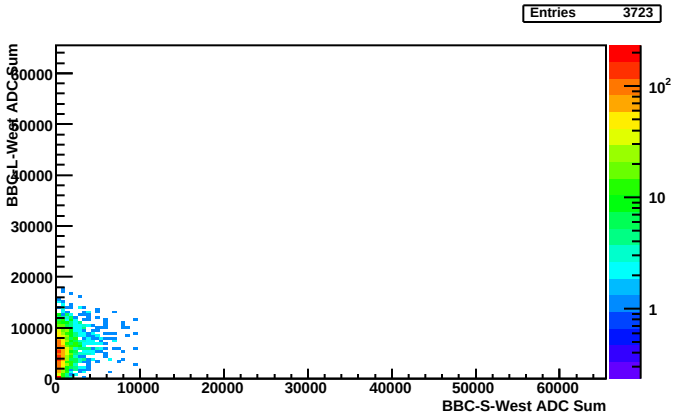

Entries 3723
TOF Mult
VPD-East ADC Sum

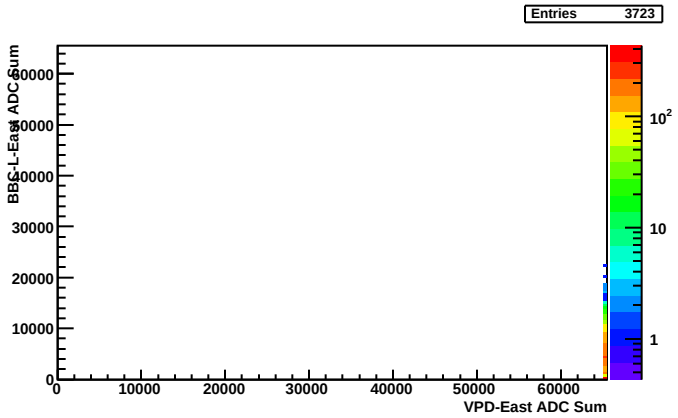

Entries 3723
TOF Mult
VPD-West ADC Sum

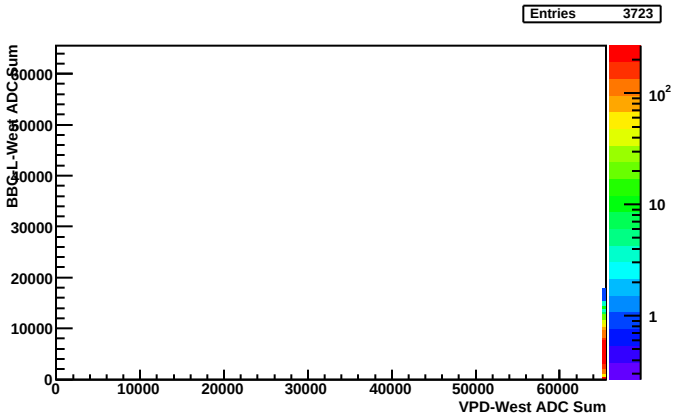

Entries 3723
TOF Mult
ZDC-East ADC Sum Att

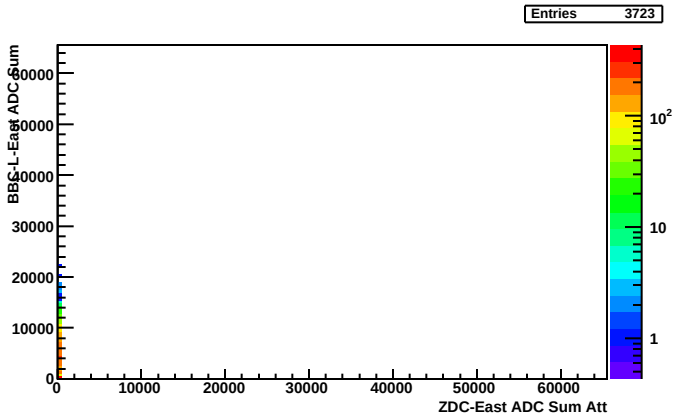

Entries 3723
TOF Mult
ZDC-West ADC Sum Att

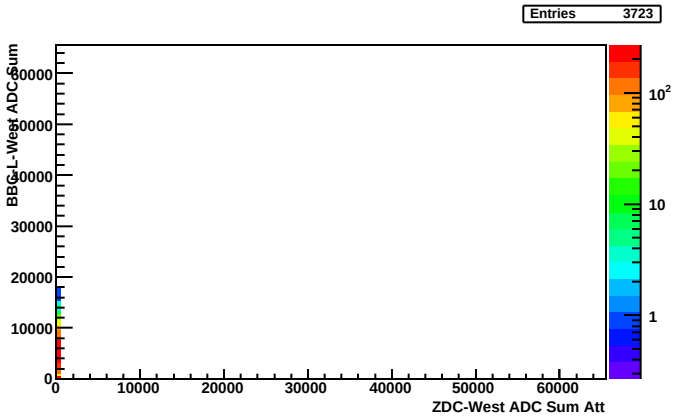

Entries 3723
BBC-L-East ADC Sum
BBC-S-East ADC Sum

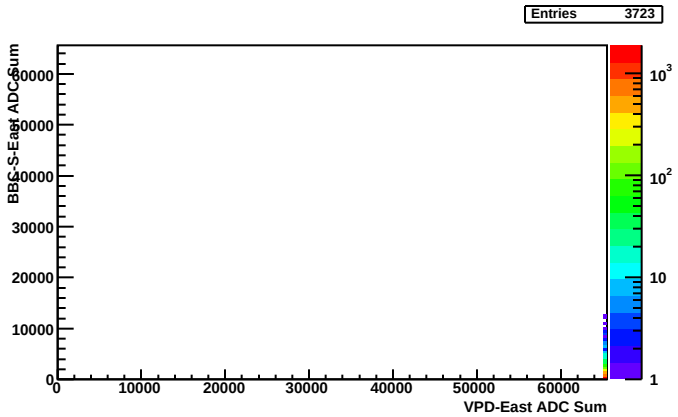

Entries 3723
BBC-L-West ADC Sum
BBC-S-West ADC Sum

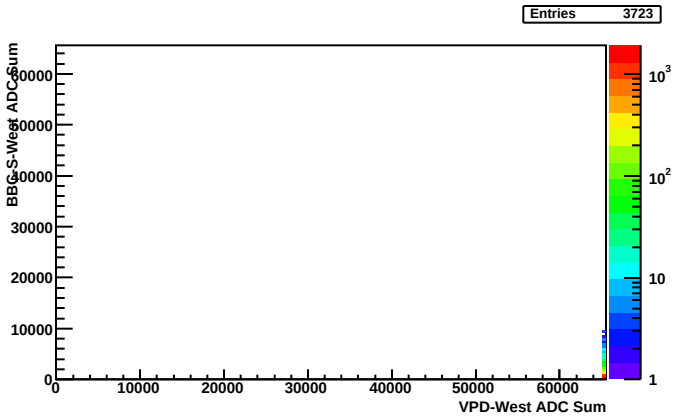

Entries 3723
BBC-L-East ADC Sum
VPD-East ADC Sum

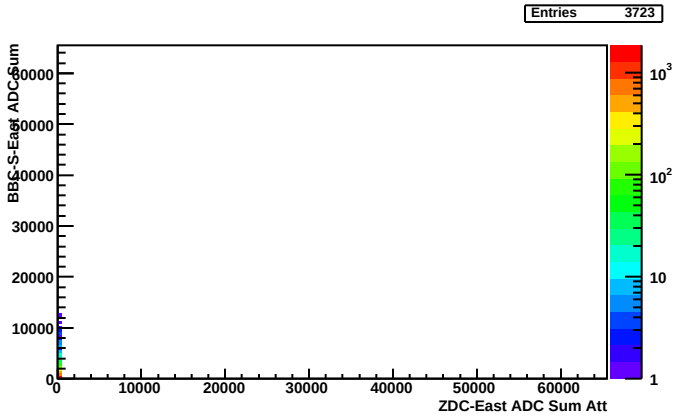

Entries 3723
BBC-L-West ADC Sum
VPD-West ADC Sum

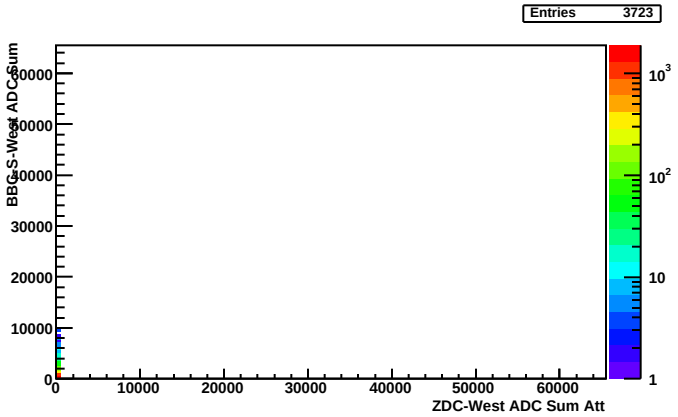

Entries 3723
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

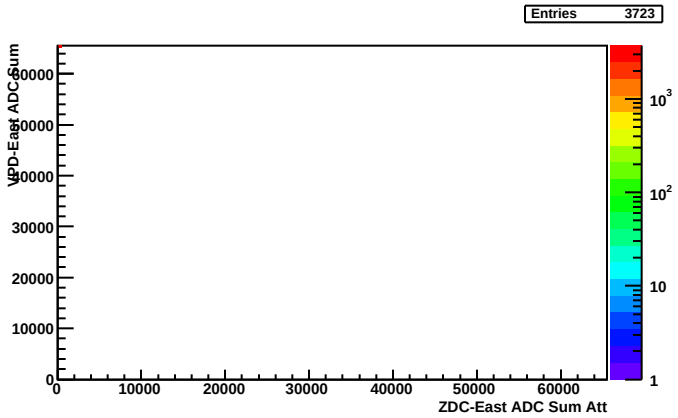

Entries 3723
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

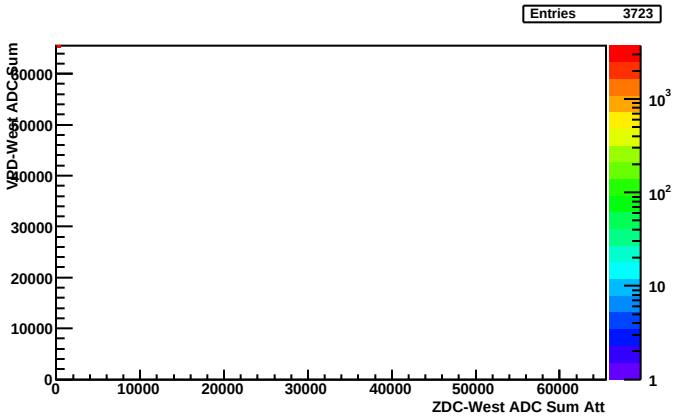

Entries 3723
BBC-S-East ADC Sum
VPD-East ADC Sum

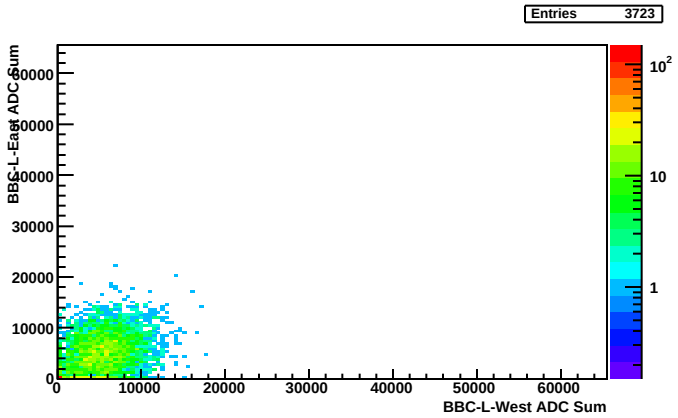

Entries 3723
BBC-S-West ADC Sum
VPD-West ADC Sum

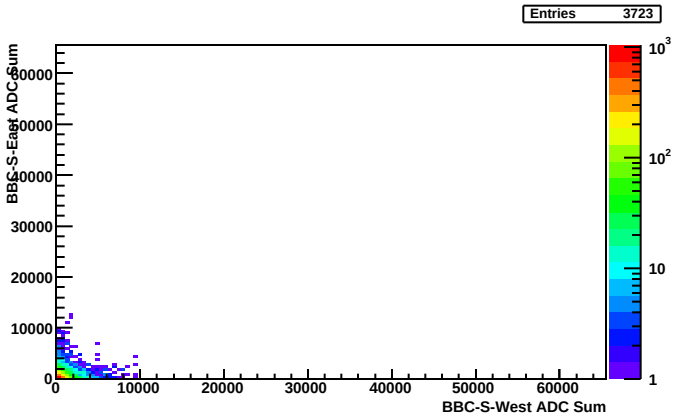

Entries 3723
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

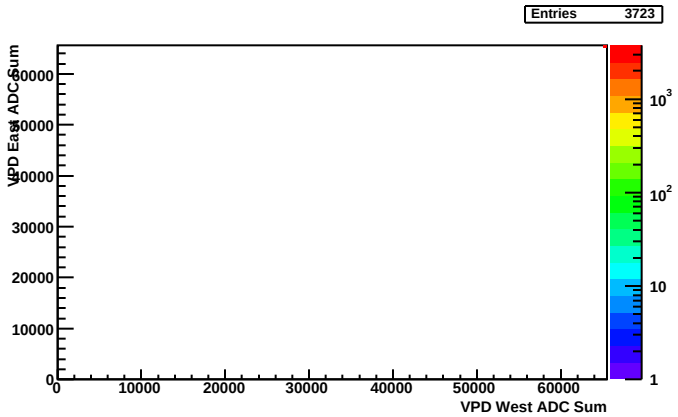

Entries 3723
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

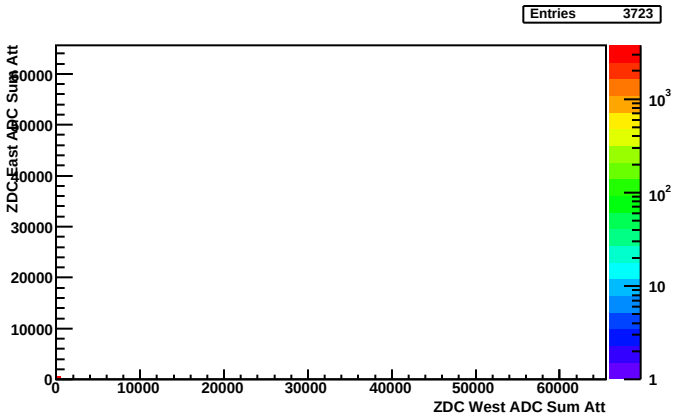

Entries 3723
VPD-East ADC Sum
ZDC-East ADC Sum Att

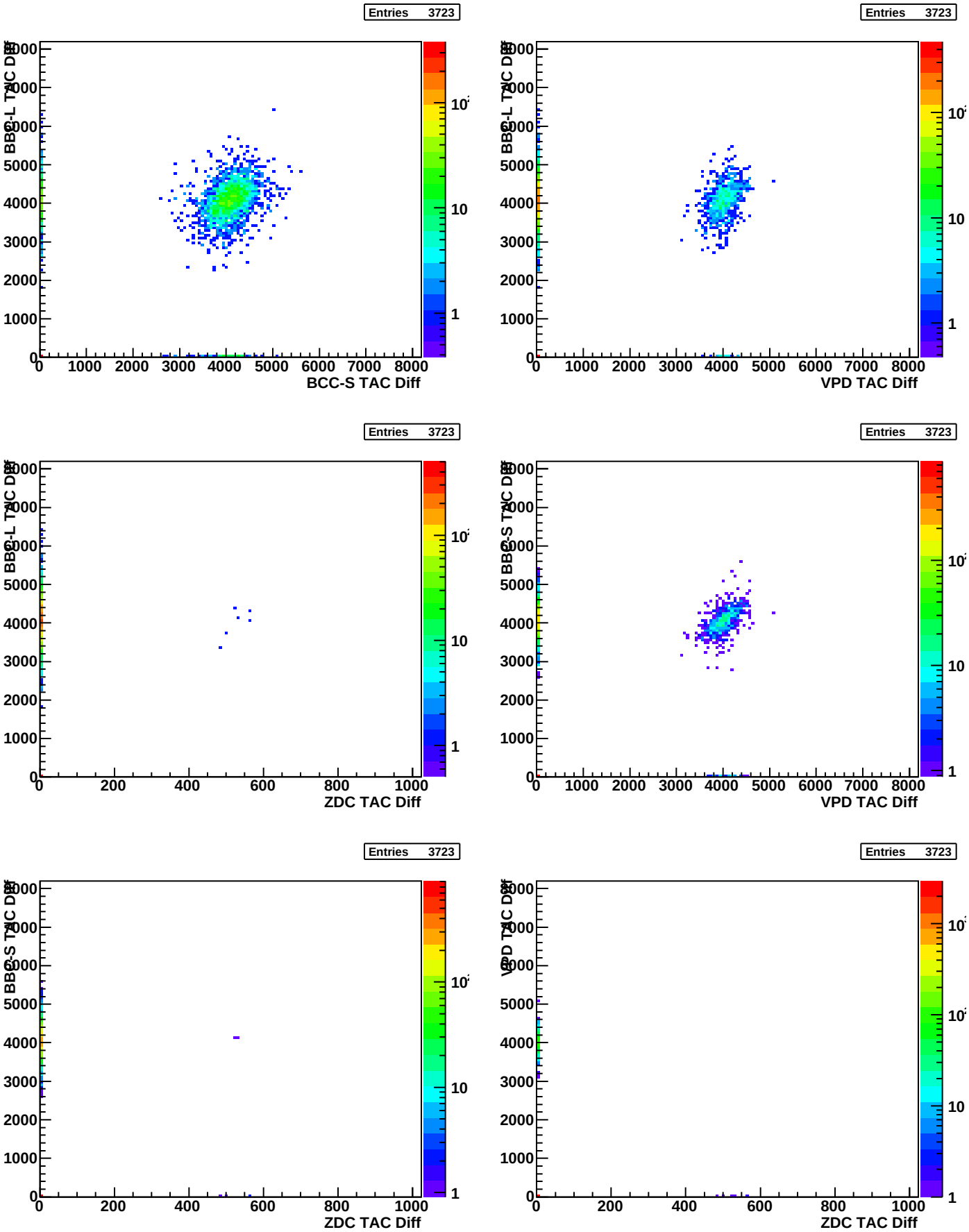

Entries 3723
VPD-West ADC Sum
ZDC-West ADC Sum Att

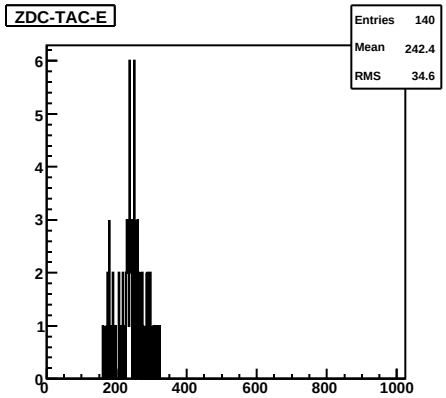

Entries 3723
BBC-L-East ADC Sum
BBC-L-West ADC Sum

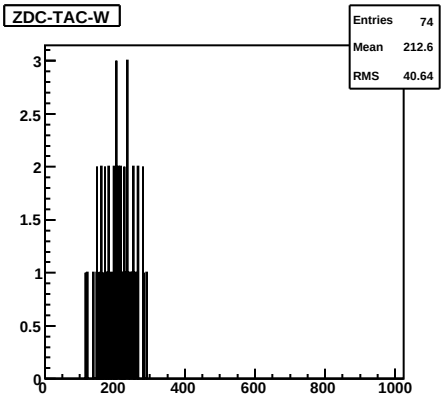

Entries 3723
BBC-S-East ADC Sum
BBC-S-West ADC Sum

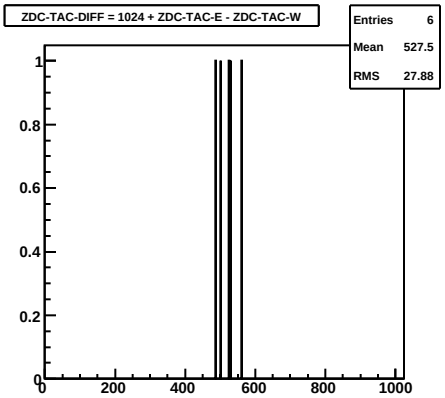

Entries 3723
VPD East ADC Sum
VPD West ADC Sum

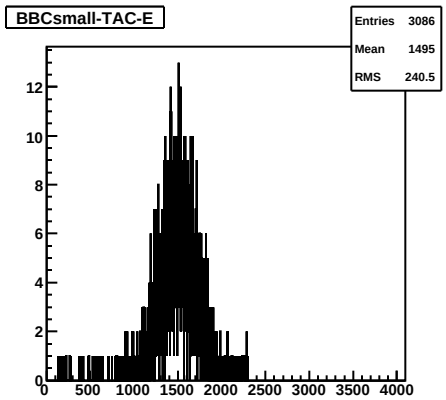

Entries 3723
ZDC East ADC Sum Att
ZDC West ADC Sum Att

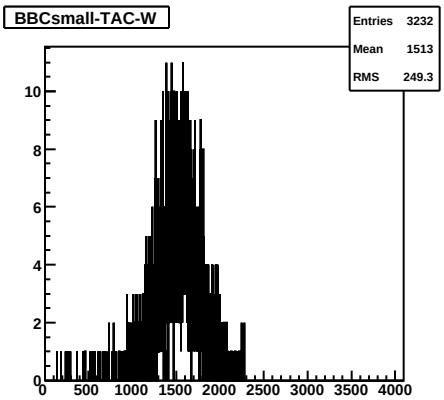

Entries 3723
BBC-L TAC Diff
BCC-S TAC Diff
Entries 3723
BBC-L TAC Diff
VPD TAC Diff
Entries 3723
BBC-L TAC Diff
ZDC TAC Diff
Entries 3723
BBC-S TAC Diff
VPD TAC Diff
Entries 3723
BBC-S TAC Diff
ZDC TAC Diff
Entries 3723
VPD TAC Diff
ZDC TAC Diff

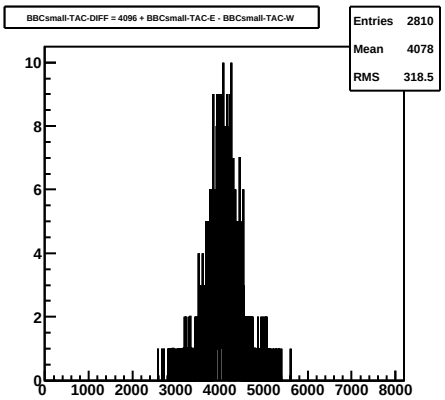

ZDC-TAC-E
Entries 140
Mean 242.4
RMS 34.6

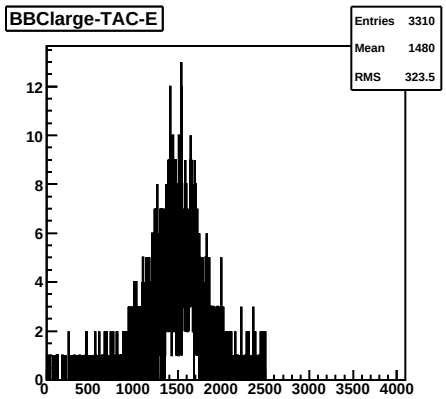

ZDC-TAC-W
Entries 74
Mean 212.6
RMS 40.64

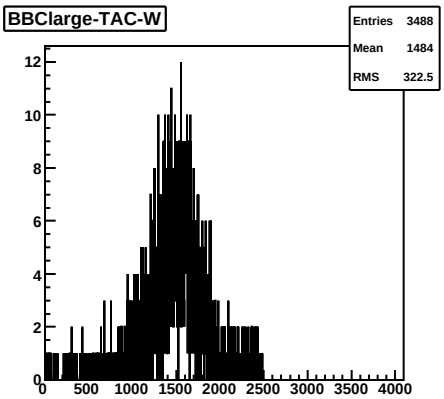

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 6
Mean 527.5
RMS 27.88

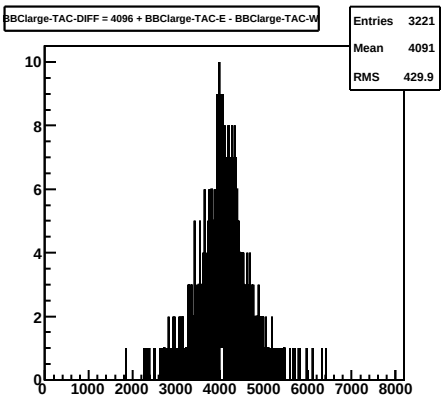

BBCsmall-TAC-E
Entries 3086
Mean 1495
RMS 240.5

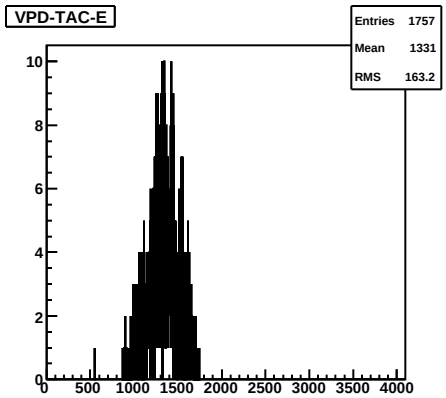

BBCsmall-TAC-W
Entries 3232
Mean 1513
RMS 249.3

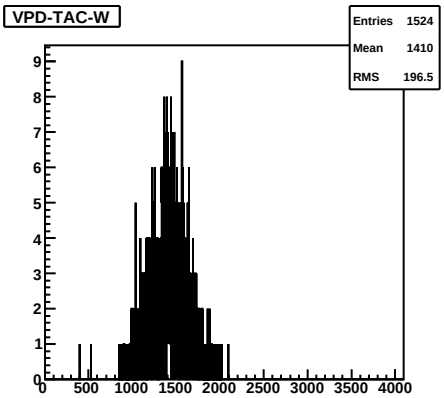

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2810
Mean 4078
RMS 318.5

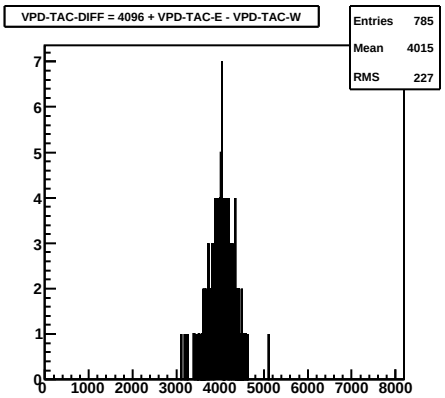

BBClarge-TAC-E
Entries 3310
Mean 1480
RMS 323.5

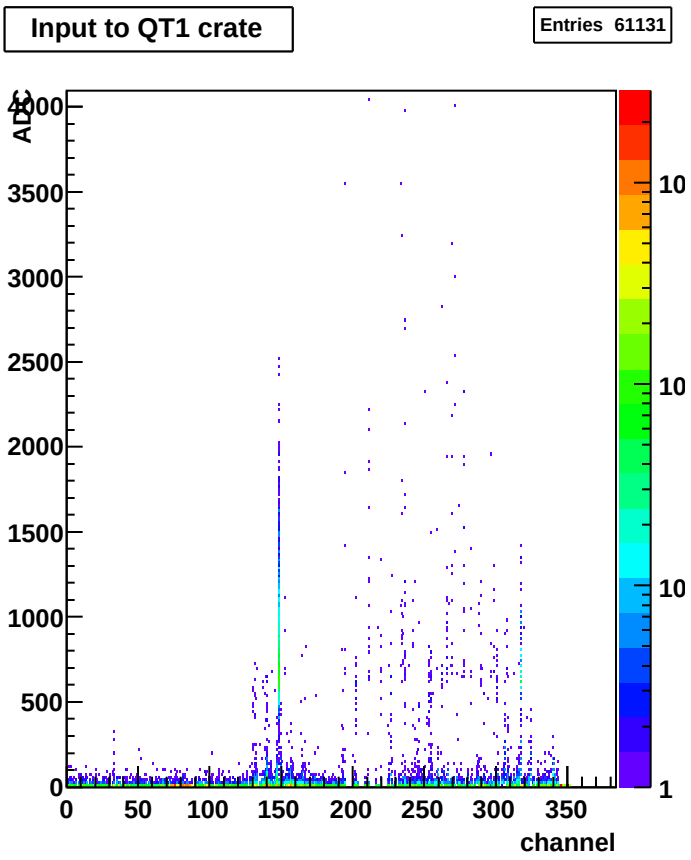

BBClarge-TAC-W
Entries 3488
Mean 1484
RMS 322.5

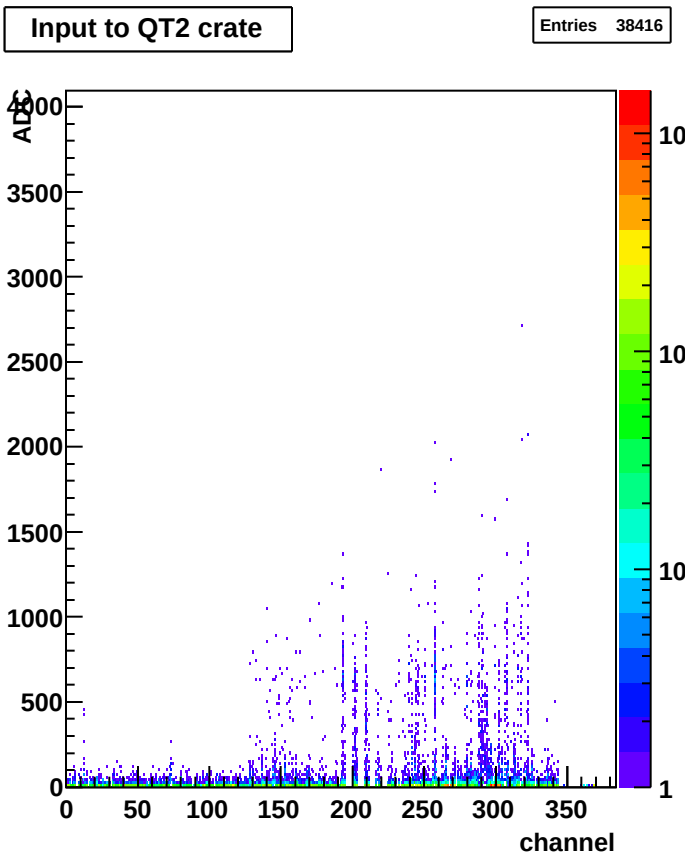

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 3221
Mean 4091
RMS 429.9

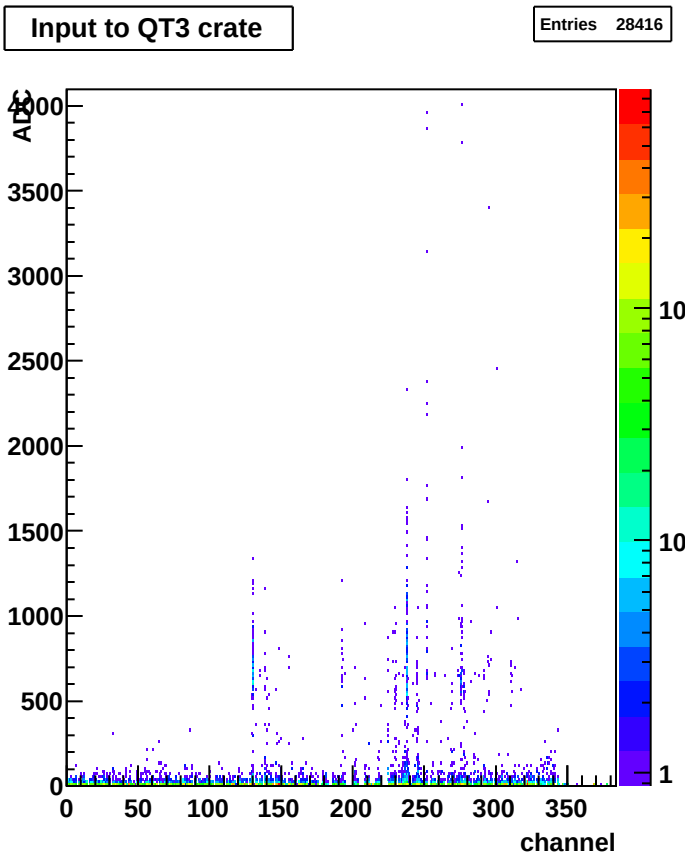

VPD-TAC-E
Entries 1757
Mean 1331
RMS 163.2

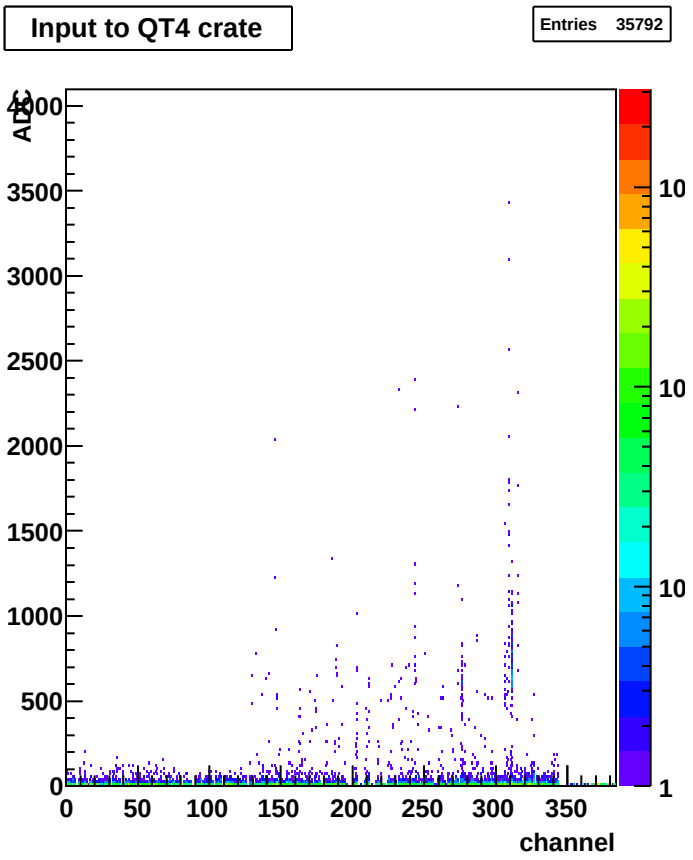

VPD-TAC-W
Entries 1524
Mean 1410
RMS 196.5

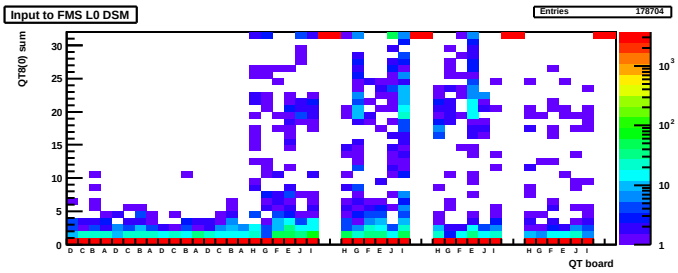

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 785
Mean 4015
RMS 227

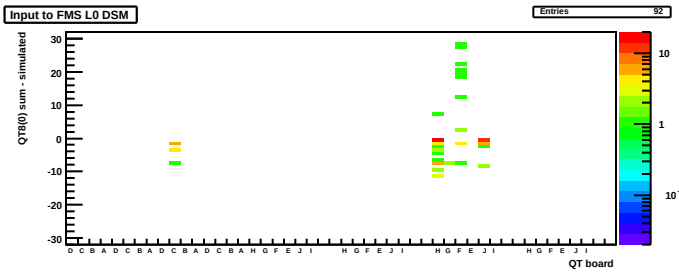

Input to QT1 crate
Entries 61131
ADC
channel

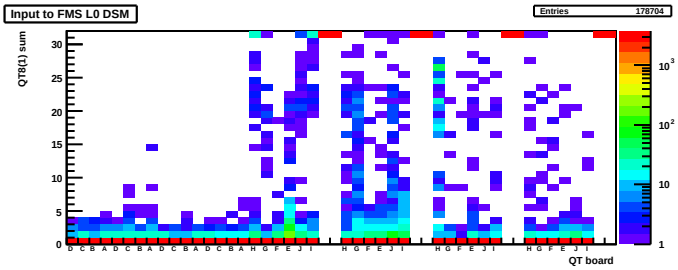

Input to QT2 crate
Entries 38416
ADC
channel

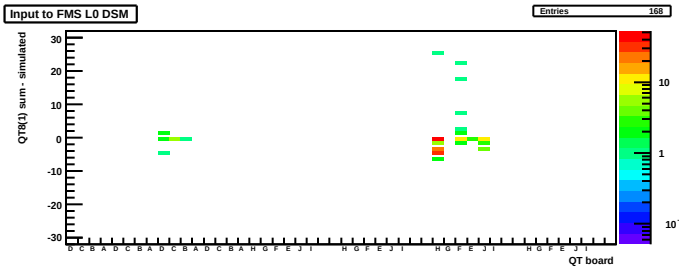

Input to QT3 crate
Entries 28416
ADC
channel

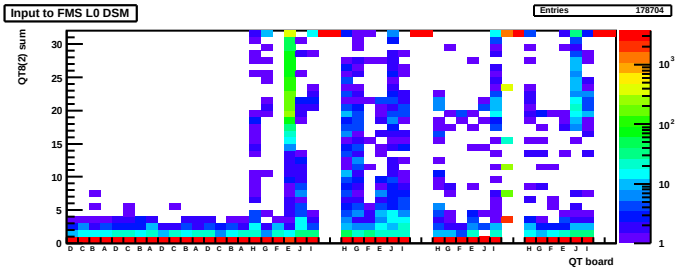

Input to QT4 crate
Entries 35792
ADC
channel

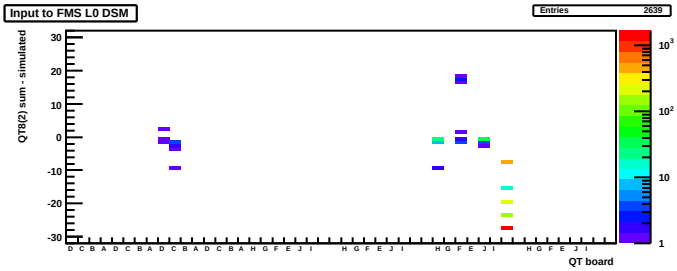

Input to FMS L0 DSM
Entries 178704
QT8(0) sum
QT board

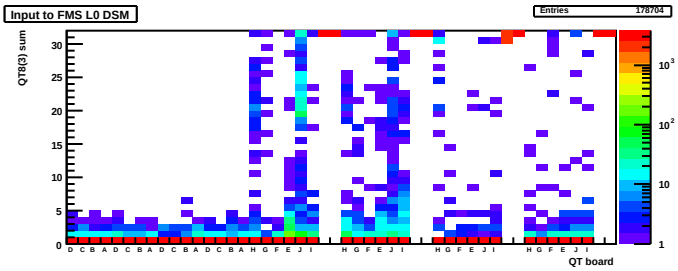

Input to FMS L0 DSM
Entries 92
QT8(0) sum - simulated
QT board

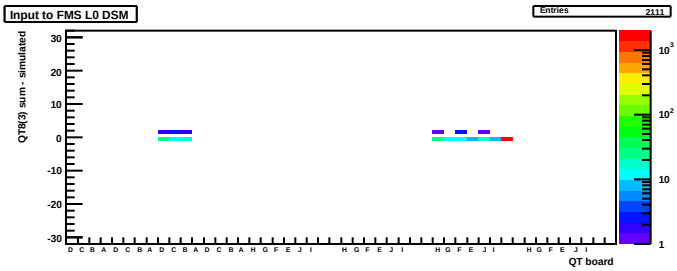

Input to FMS L0 DSM
Entries 178704
QT8(1) sum
QT board

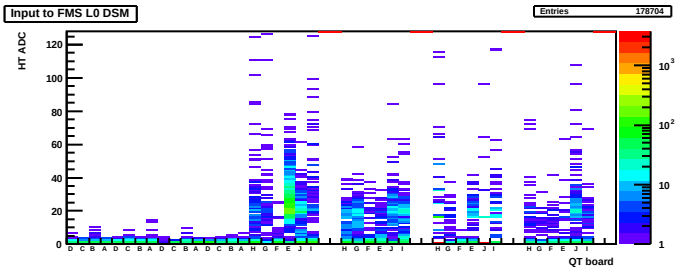

Input to FMS L0 DSM
Entries 168
QT8(1) sum - simulated
QT board

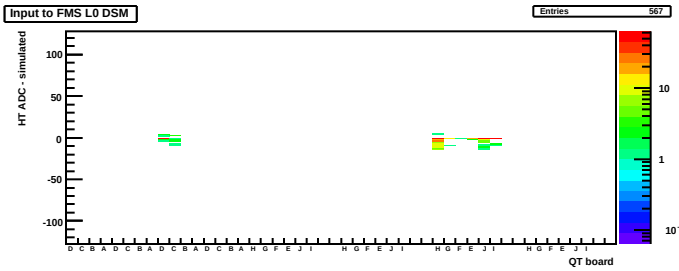

Input to FMS L0 DSM
Entries 178704
QT8(2) sum
QT board

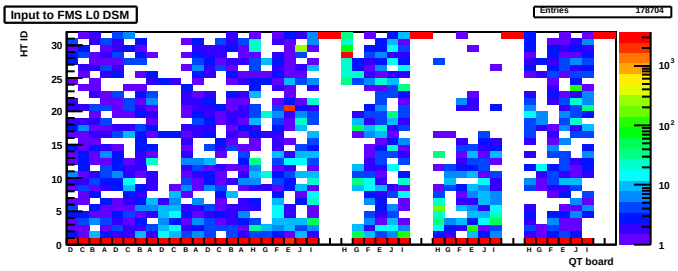

Input to FMS L0 DSM
Entries 2639
QT8(2) sum - simulated
QT board

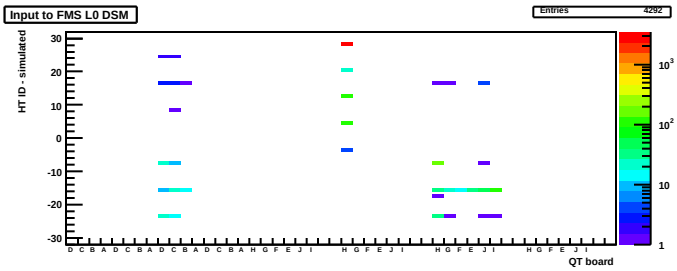

Input to FMS L0 DSM
Entries 178704
QT8(3) sum
QT board

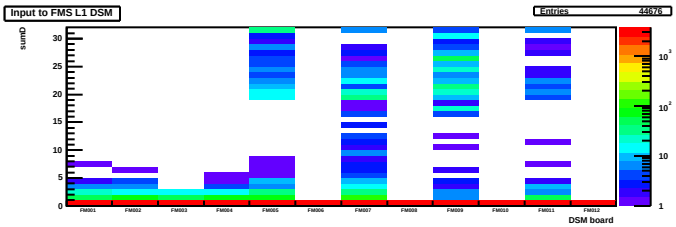

Input to FMS L0 DSM
Entries 2111
QT8(3) sum - simulated
QT board

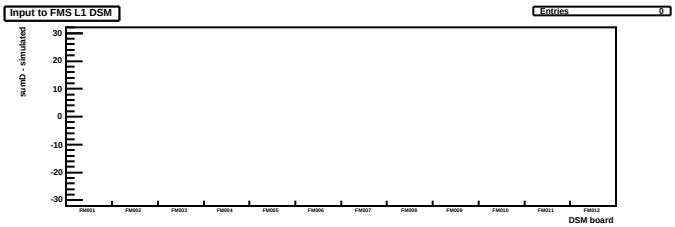

Input to FMS L0 DSM
Entries 178704
HT ADC
QT board

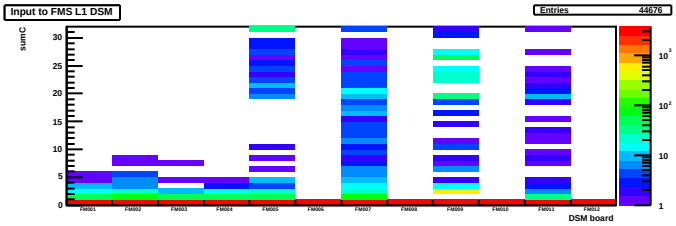

Input to FMS L0 DSM
Entries 567
HT ADC - simulated
QT board

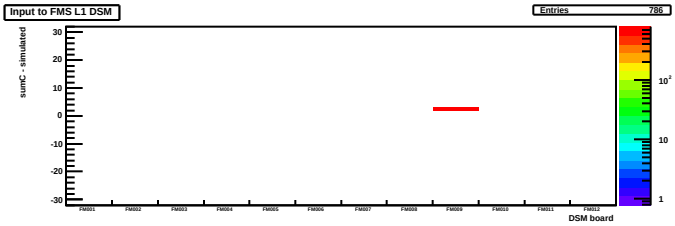

Input to FMS L0 DSM
Entries 178704
HT ID
QT board

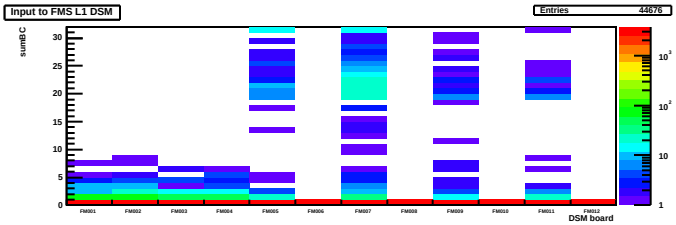

Input to FMS L0 DSM
Entries 4292
HT ID - simulated
QT board

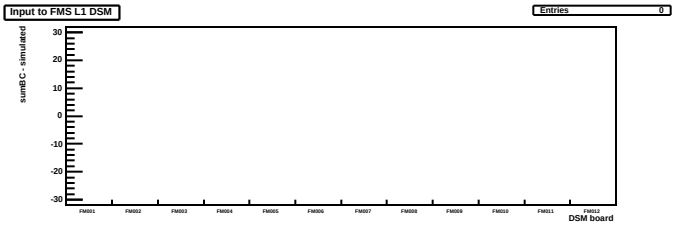

Input to FMS L1 DSM
Entries 44676
sumD
DSM board

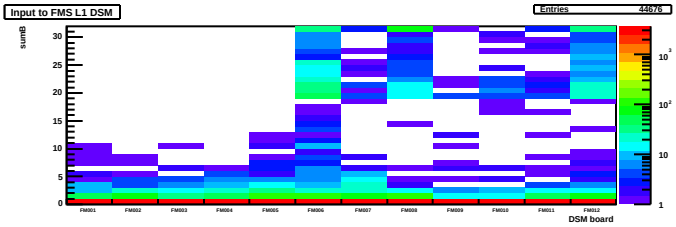

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

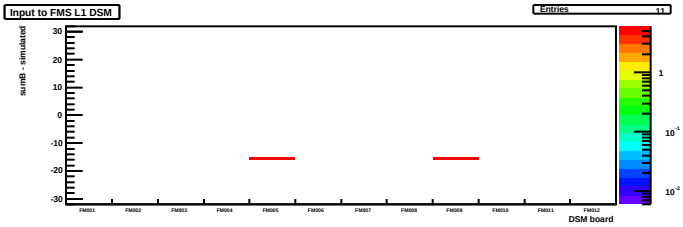

Input to FMS L1 DSM
Entries 44676
sumC
DSM board

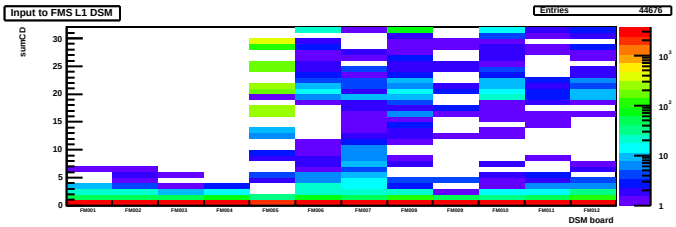

Input to FMS L1 DSM
Entries 786
sumC - simulated
DSM board

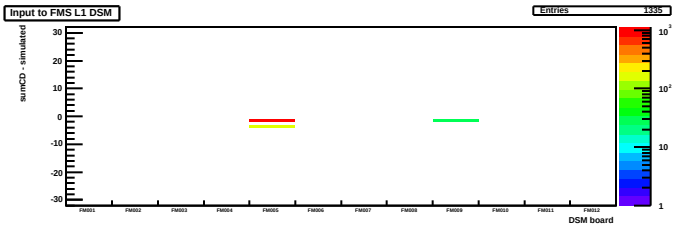

Input to FMS L1 DSM
Entries 44676
sumBC
DSM board

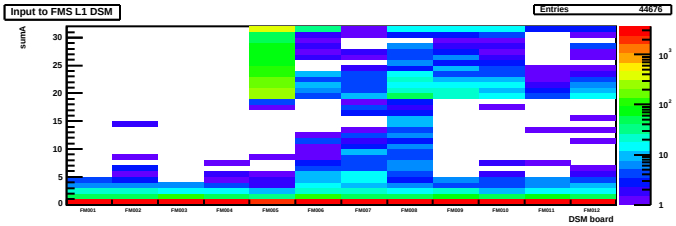

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

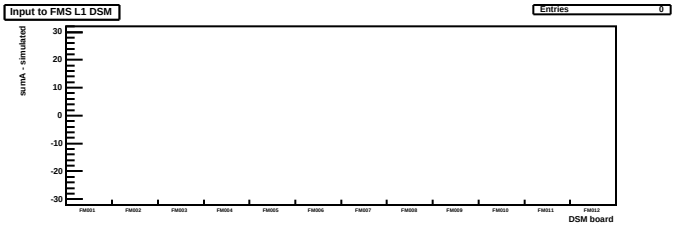

Input to FMS L1 DSM
Entries 44676
sumB
DSM board

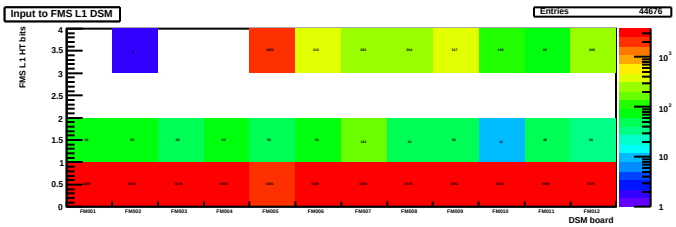

Input to FMS L1 DSM
Entries 11
sumB - simulated
DSM board

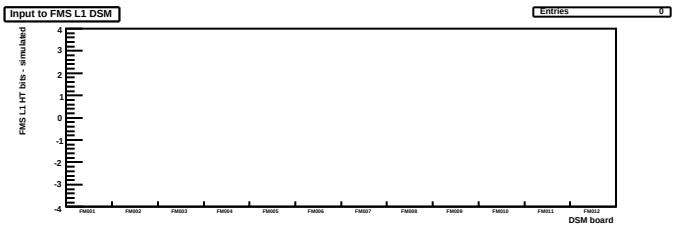

Input to FMS L1 DSM
Entries 44676
sumCD
DSM board

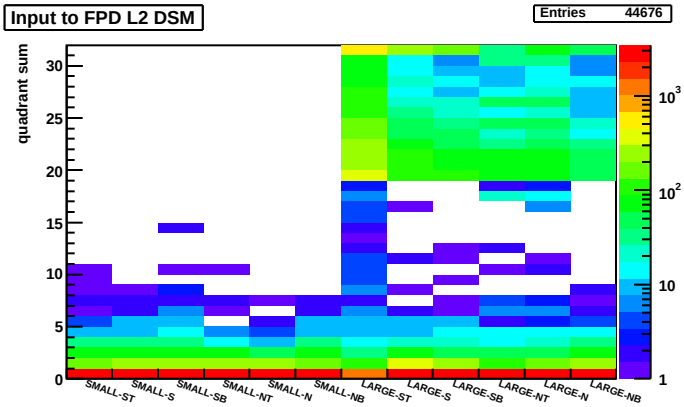

Input to FMS L1 DSM
Entries 1335
sumCD - simulated
DSM board

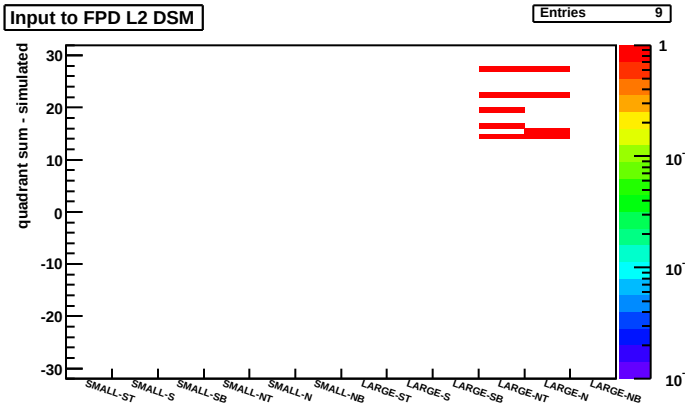

Input to FMS L1 DSM
Entries 44676
sumA
DSM board

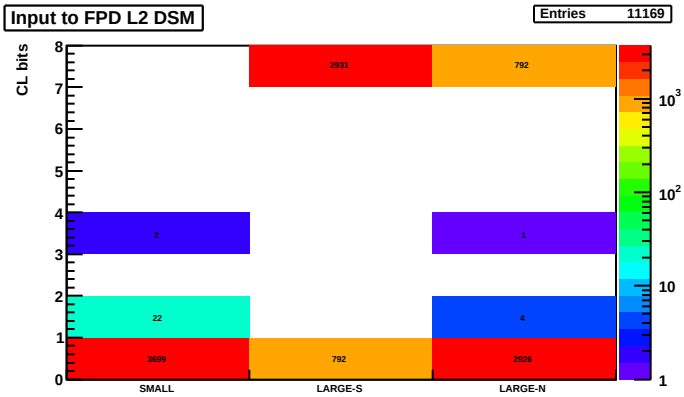

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

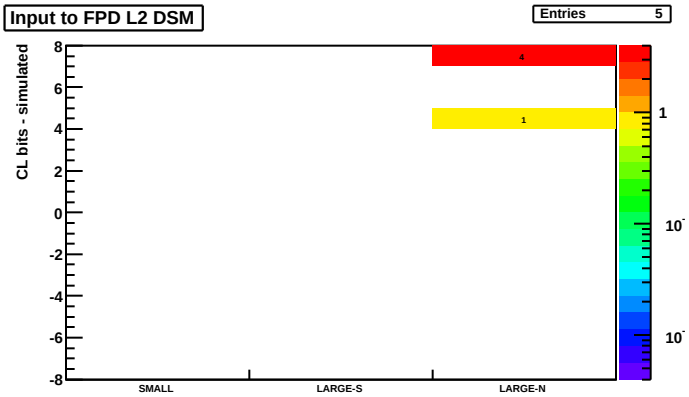

Input to FMS L1 DSM
Entries 44676
FMS L1 HT bits
DSM board

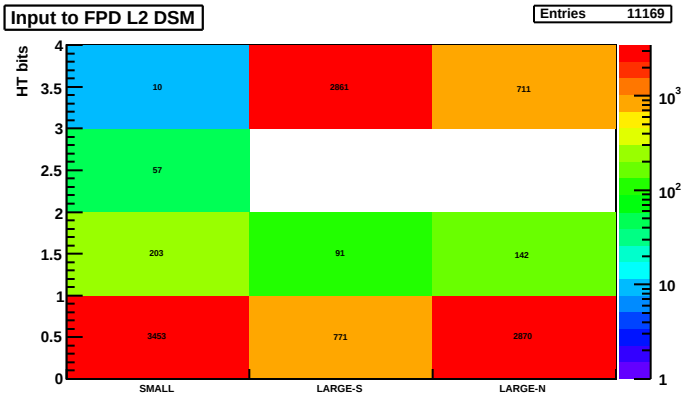

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

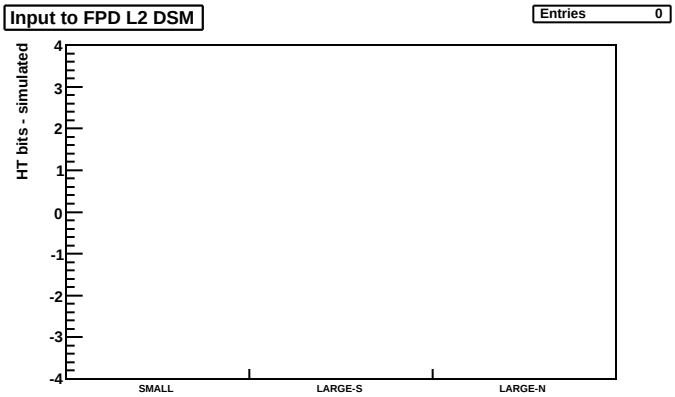

Input to FPD L2 DSM
Entries 44676
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

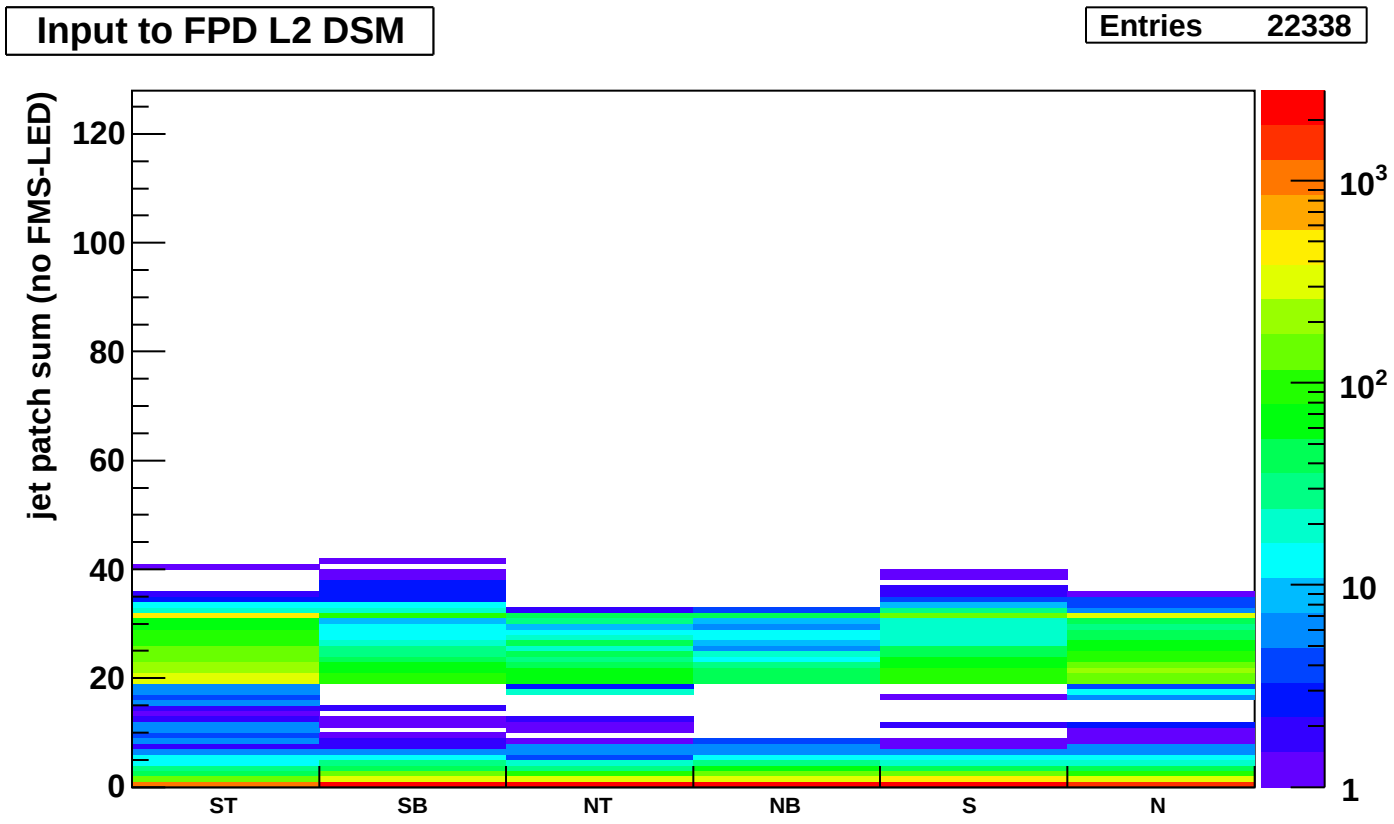

Input to FPD L2 DSM
Entries 9
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

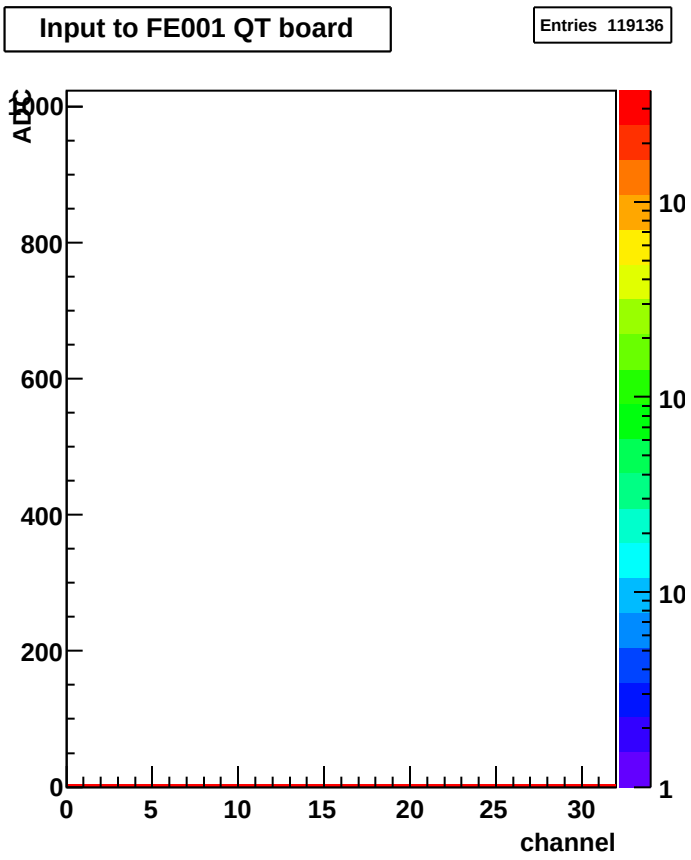

Input to FPD L2 DSM
Entries 11169
CL bits
2931
792
2
1
22
4
3699
792
2926
SMALL
LARGE-S
LARGE-N

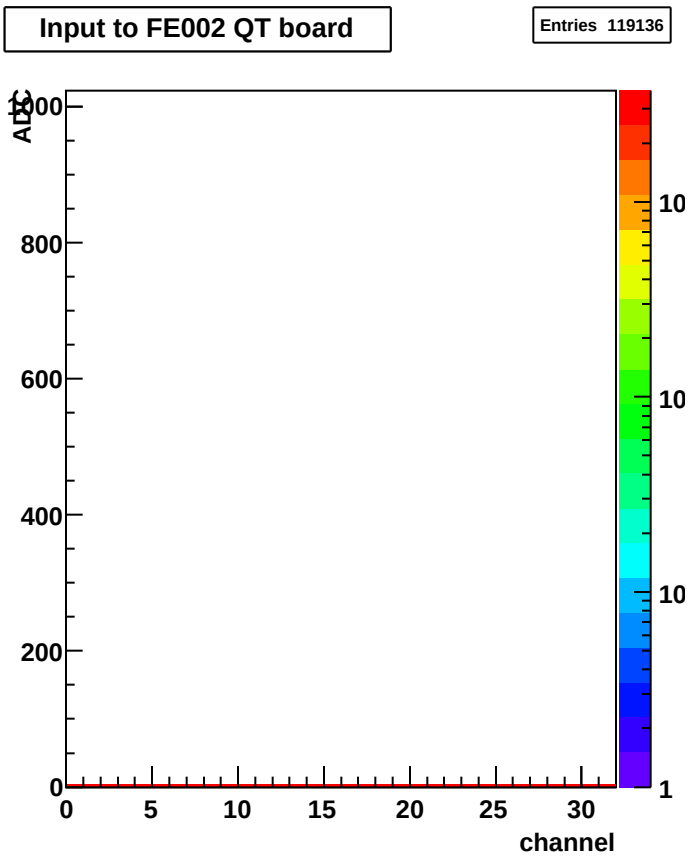

Input to FPD L2 DSM
Entries 5
CL bits - simulated
4
1
SMALL
LARGE-S
LARGE-N

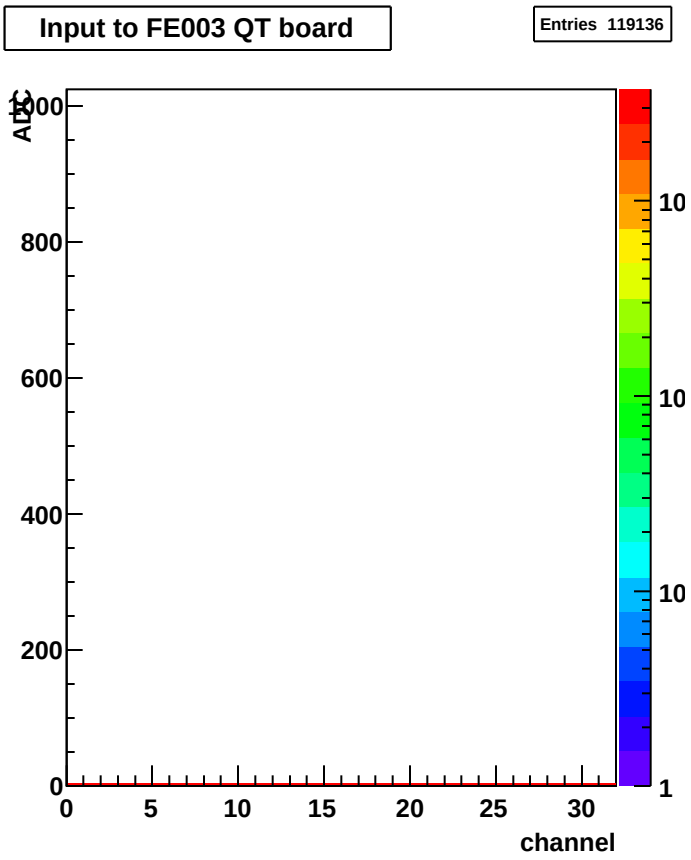

Input to FPD L2 DSM
Entries 11169
HT bits
10
2861
711
57
203
91
142
3453
771
2870
SMALL
LARGE-S
LARGE-N

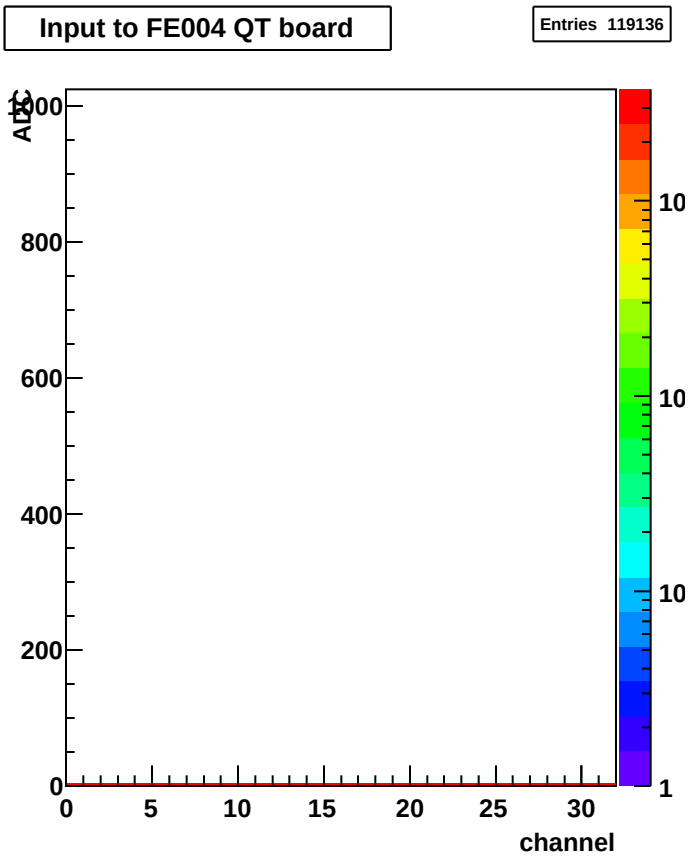

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

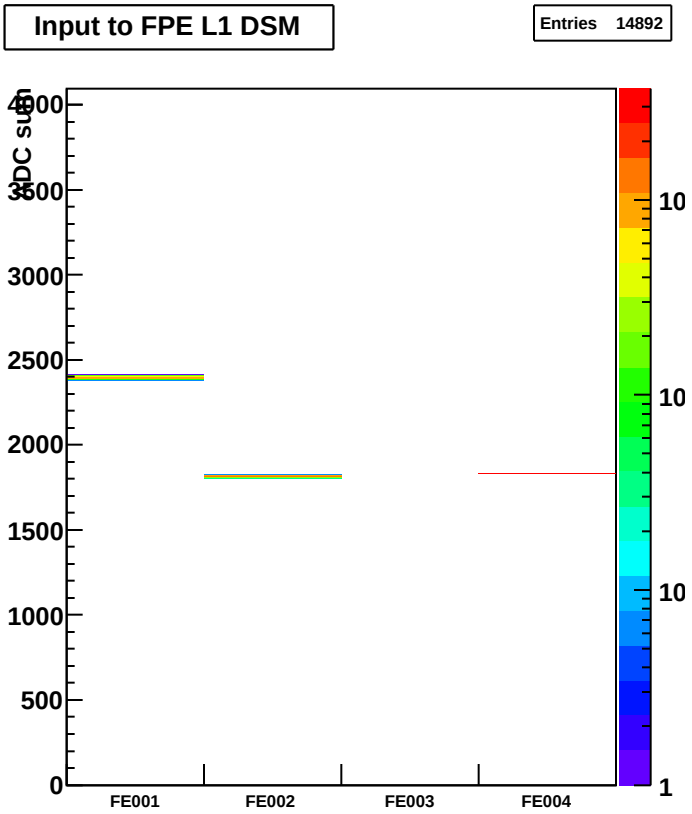

Input to FPD L2 DSM
Entries 22338
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3

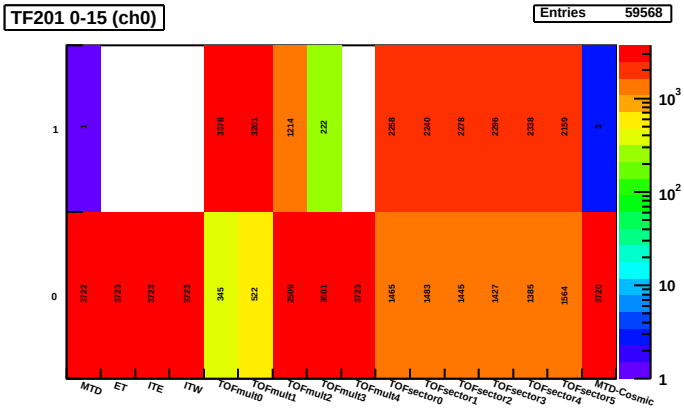

Input to FE001 QT board
Entries 119136
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
1

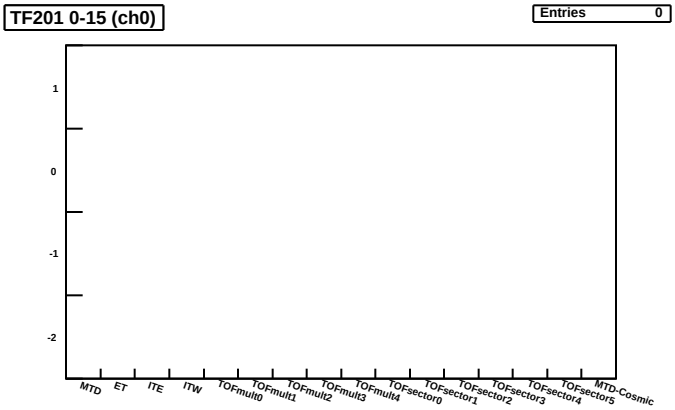

Input to FE002 QT board
Entries 119136
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
1

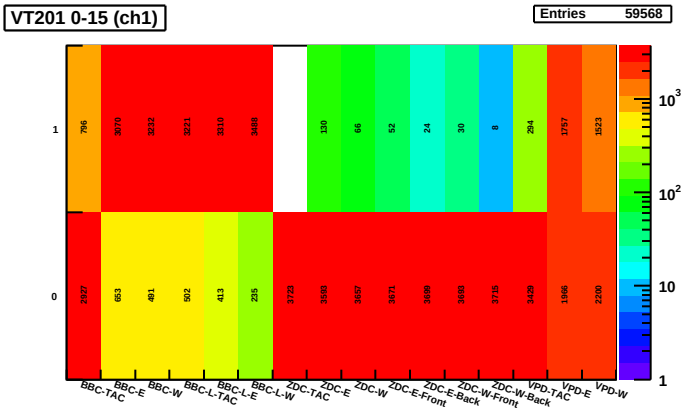

Input to FE003 QT board
Entries 119136
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
1

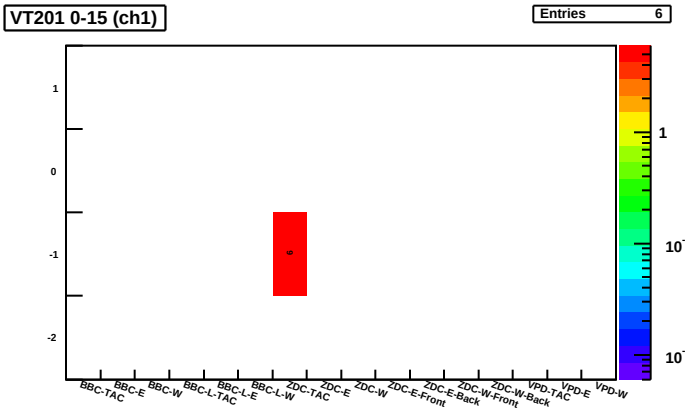

Input to FE004 QT board
Entries 119136
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
1

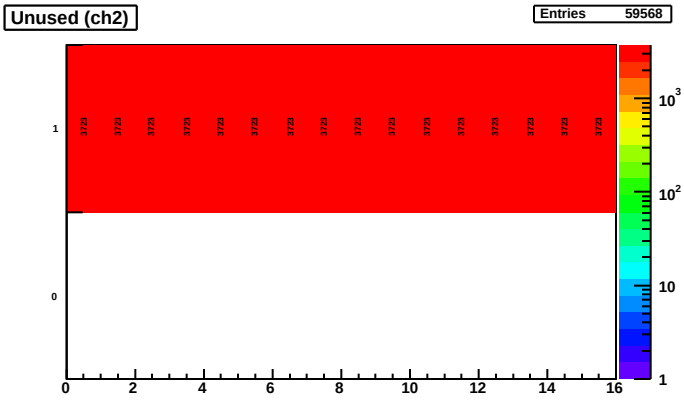

Input to FPE L1 DSM
Entries 14892
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
1

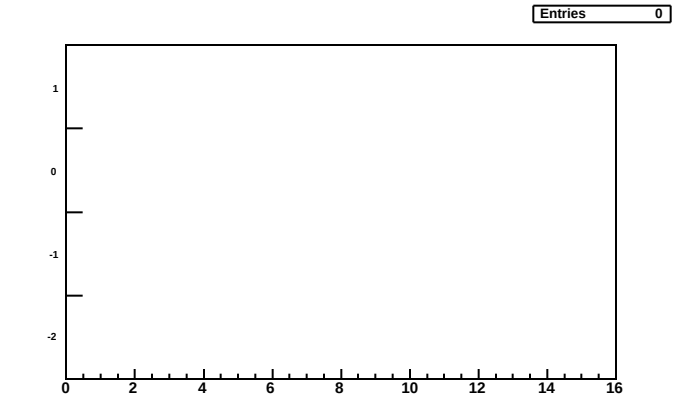

TF201 0-15 (ch0)
Entries 59568
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

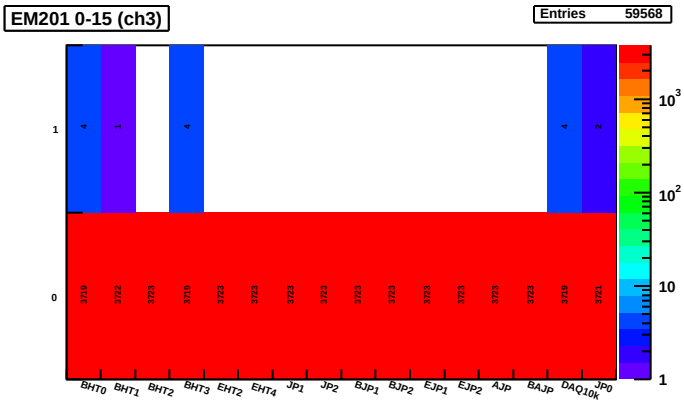

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

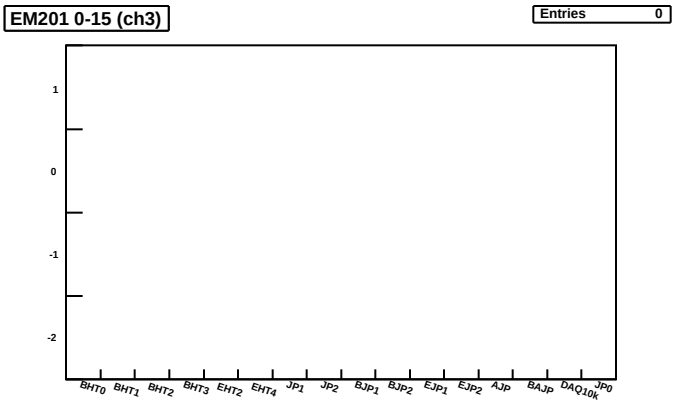

VT201 0-15 (ch1)
Entries 59568
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 6
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 59568


Entries 0


EM201 0-15 (ch3)
Entries 59568
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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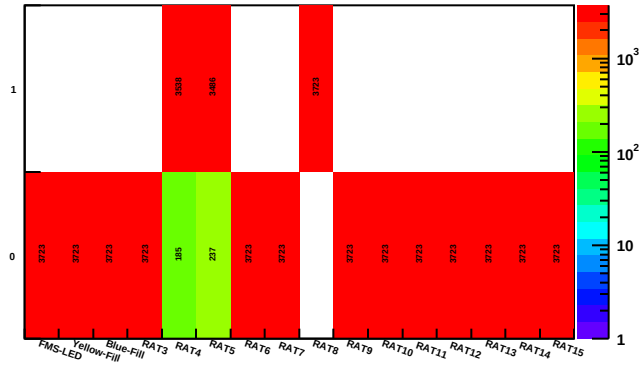

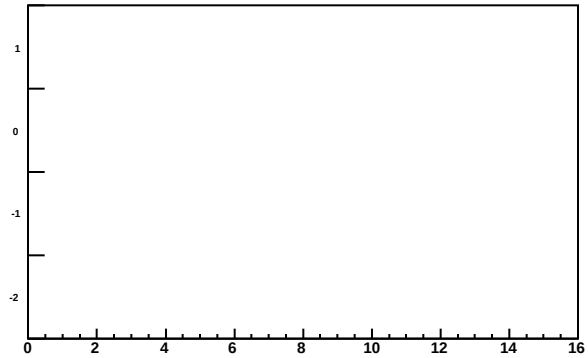

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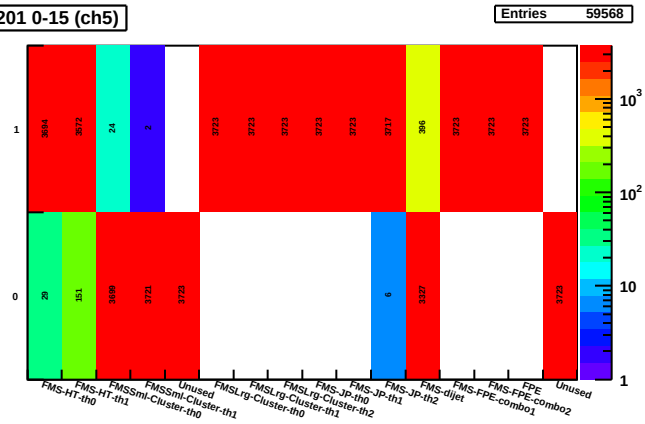

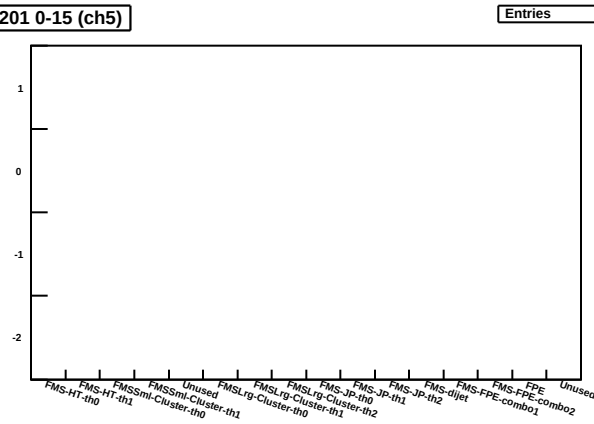

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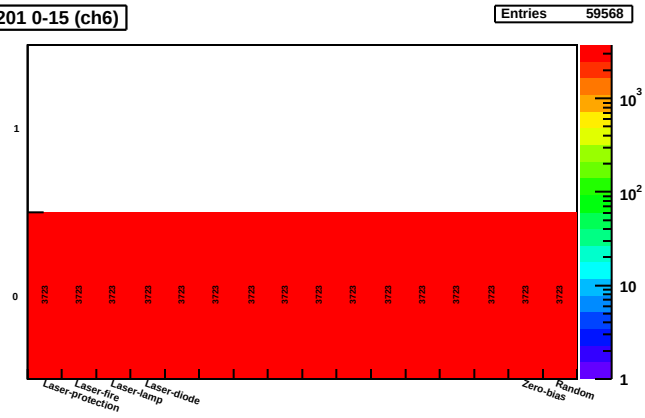

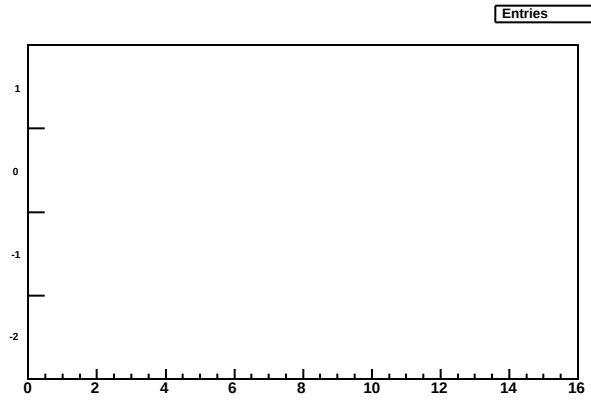
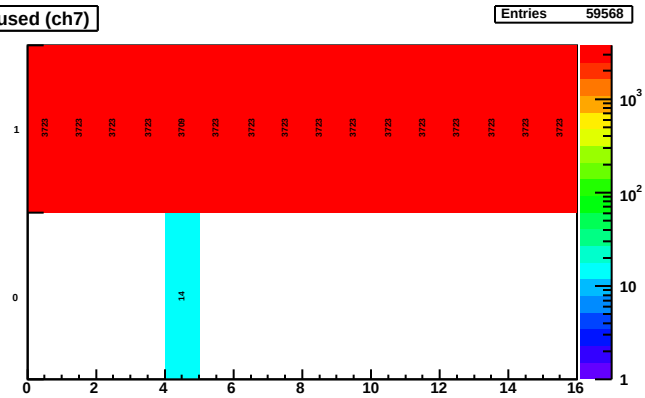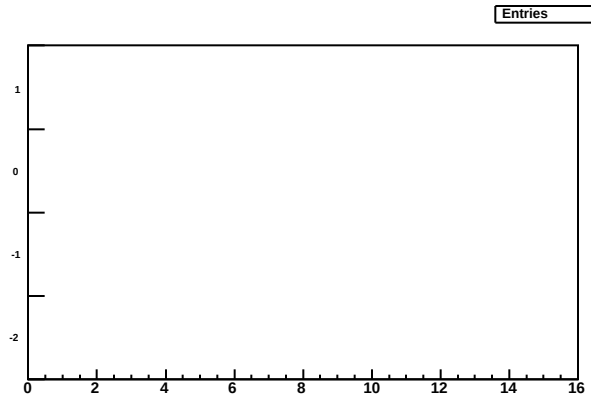
Unused (ch7)