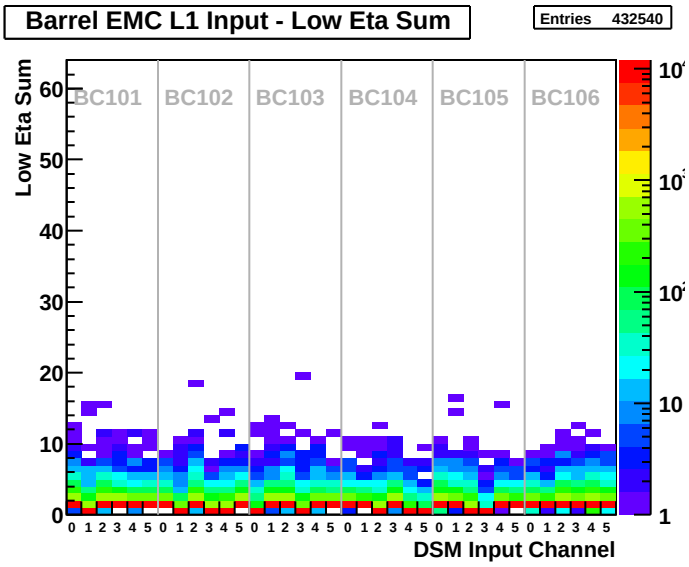
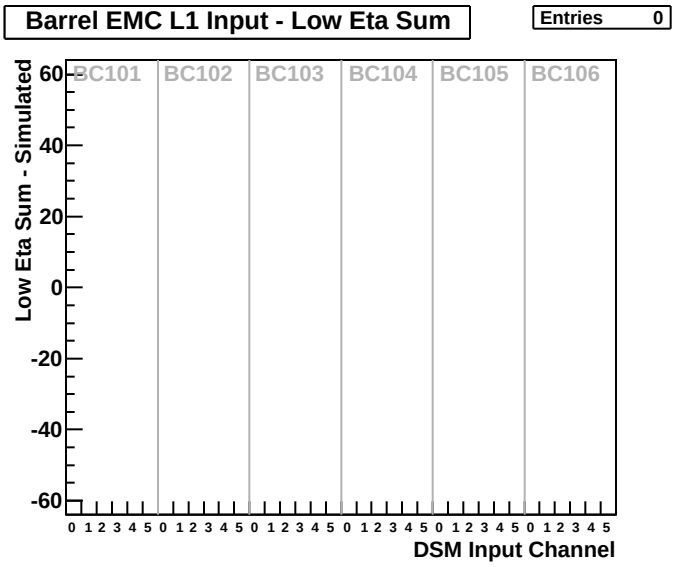
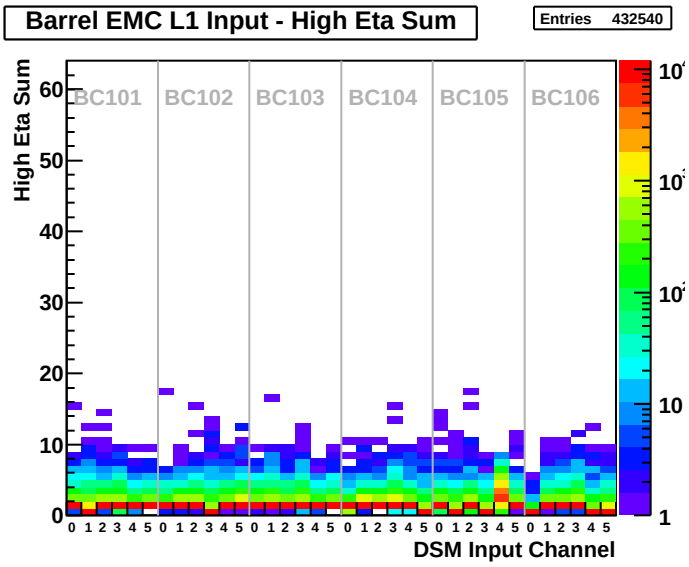
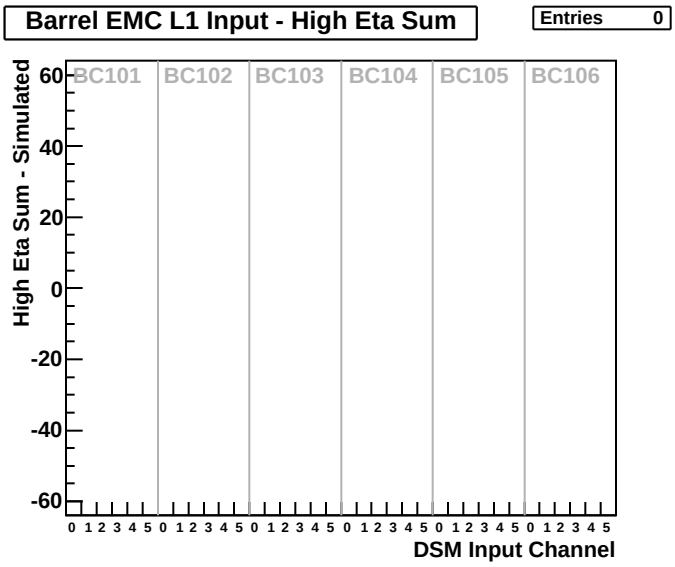
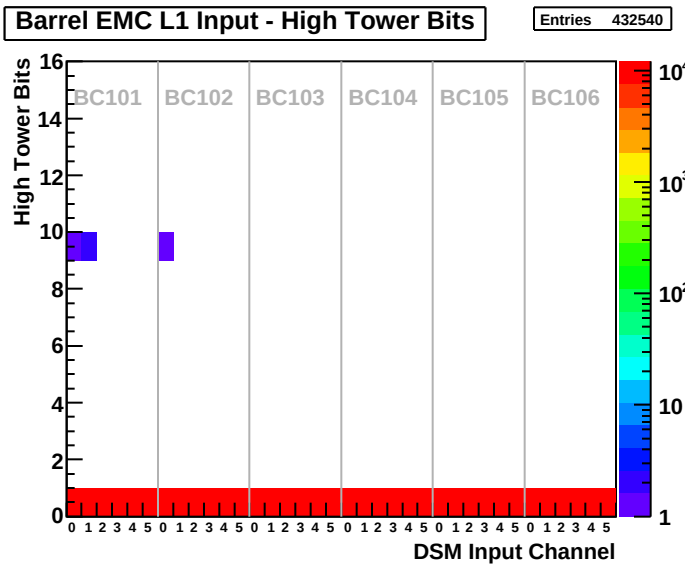
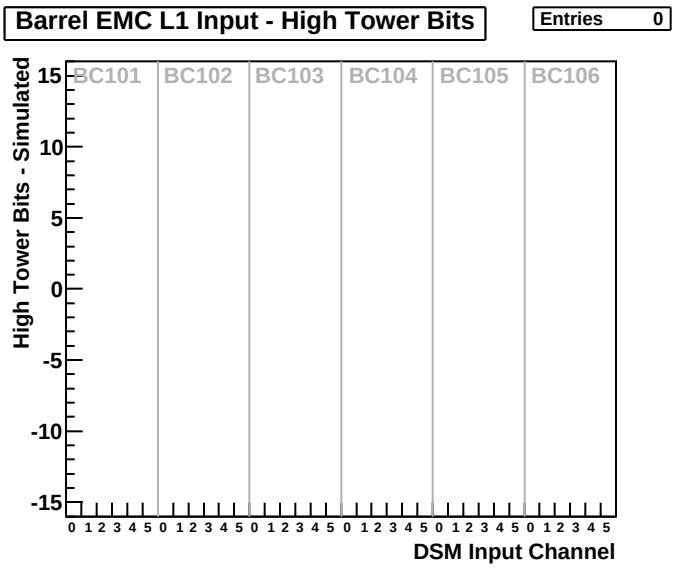

Barrel EMC L1 Input - Low Eta Sum
Entries 432540
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

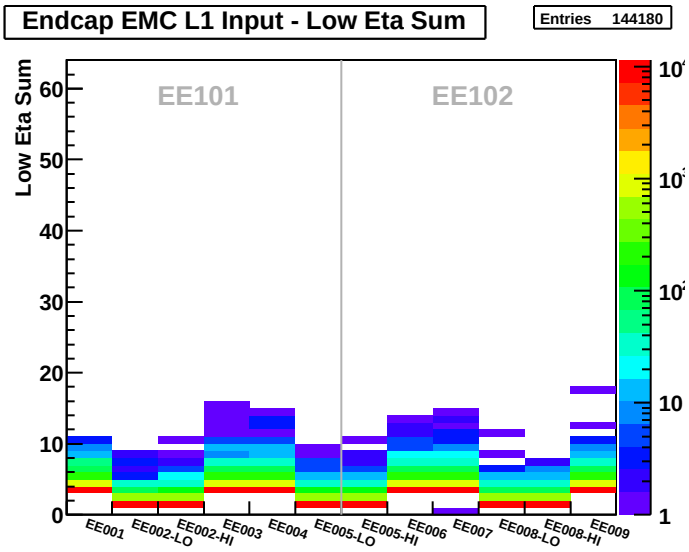

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

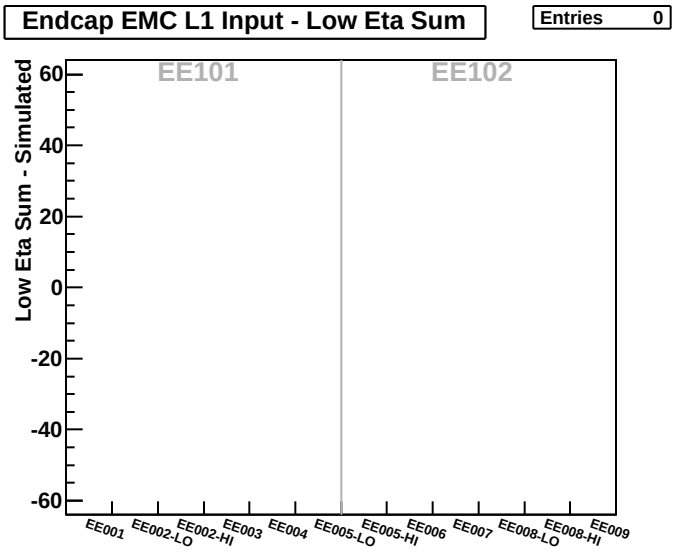

Barrel EMC L1 Input - High Eta Sum
Entries 432540
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

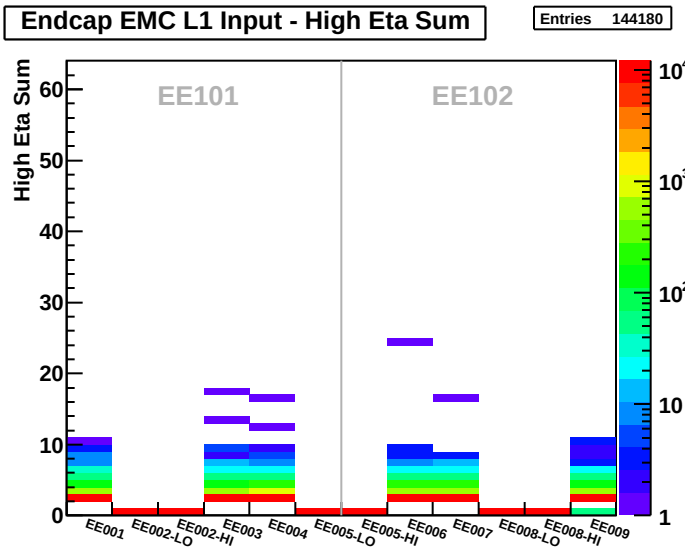

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

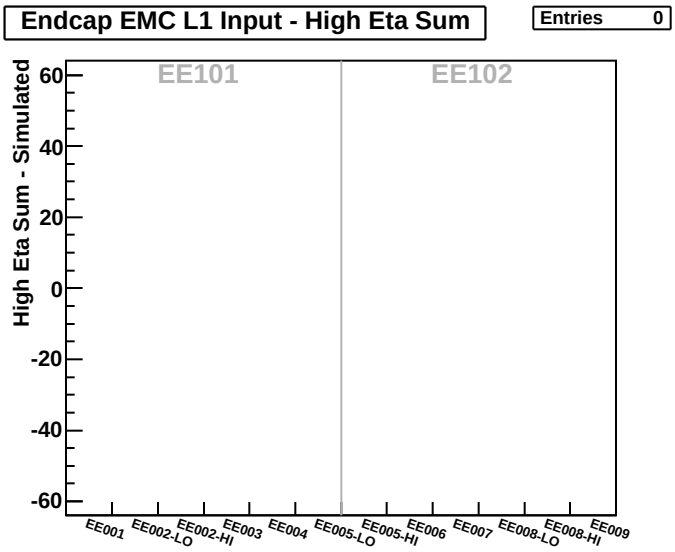

Barrel EMC L1 Input - High Tower Bits
Entries 432540
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

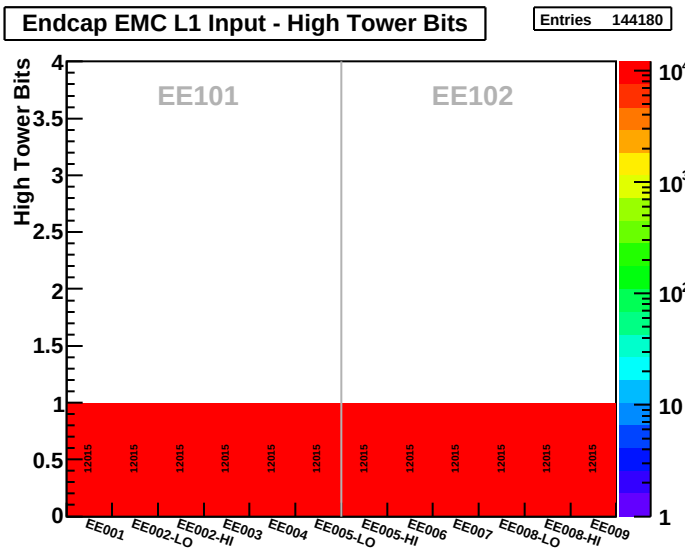

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

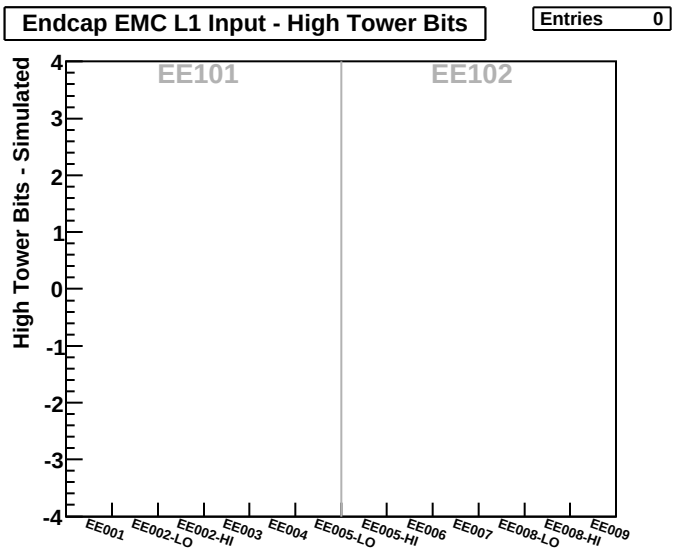

Endcap EMC L1 Input - Low Eta Sum
Entries 144180
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

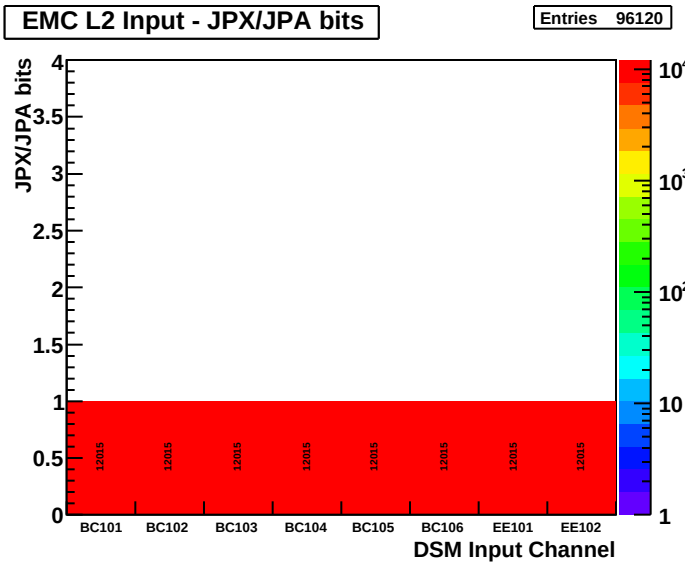

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

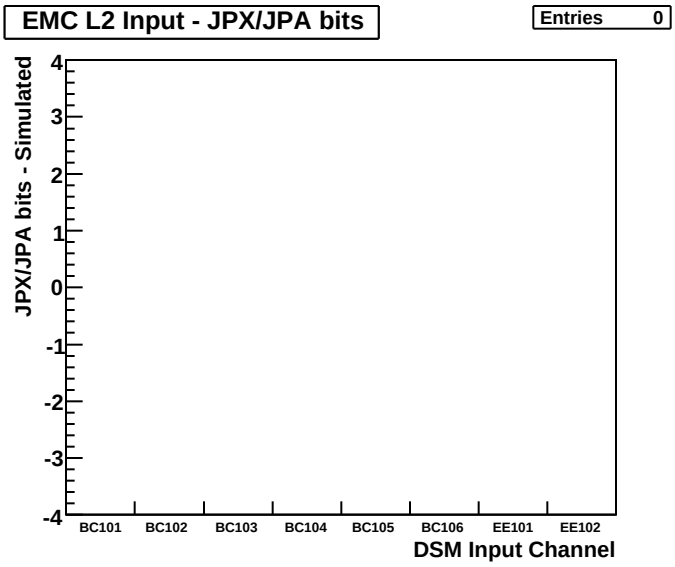

Endcap EMC L1 Input - High Eta Sum
Entries 144180
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

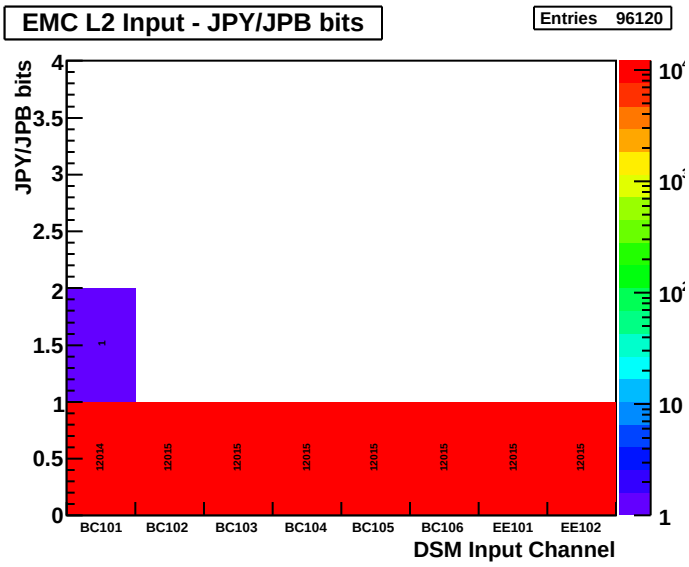

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

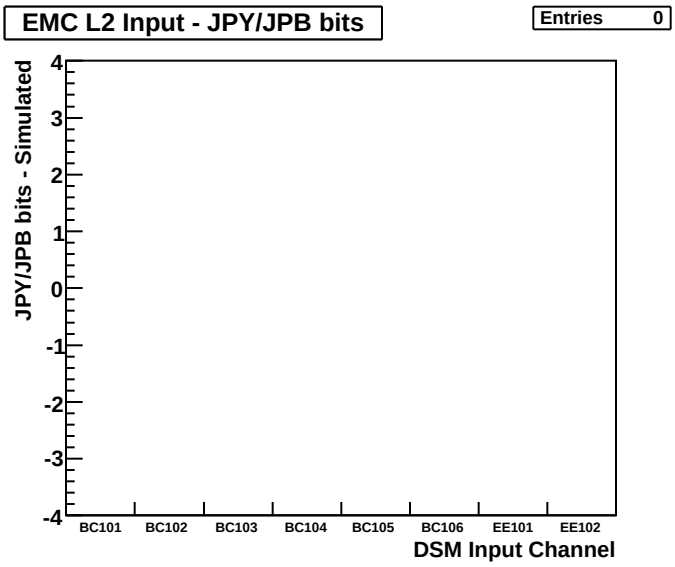

Endcap EMC L1 Input - High Tower Bits
Entries 144180
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

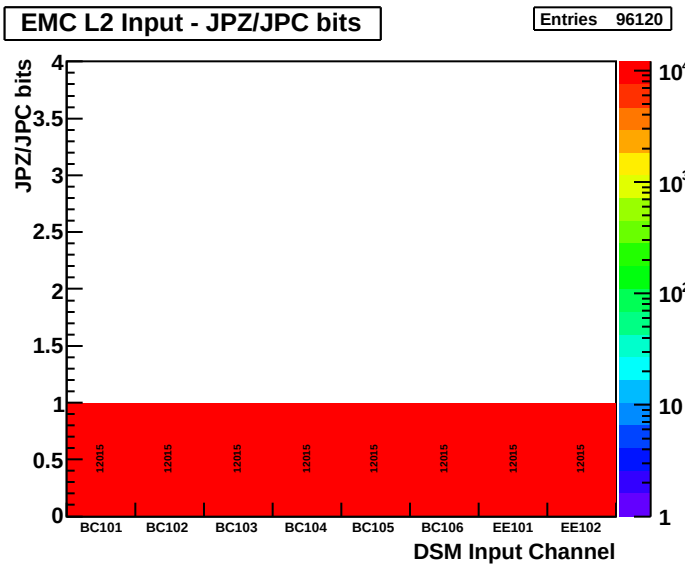

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

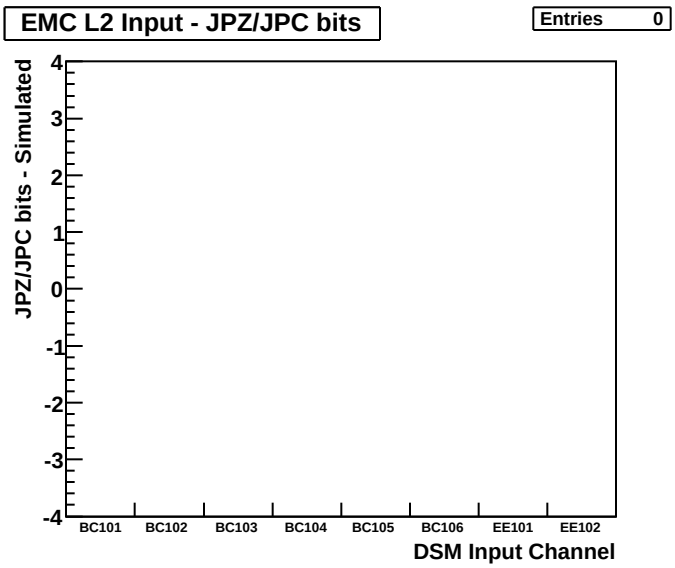

EMC L2 Input - JPX/JPA bits
Entries 96120
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

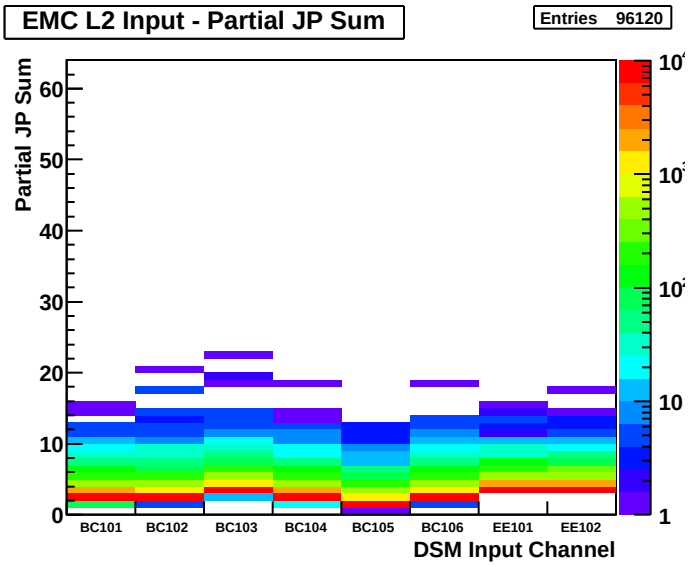

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

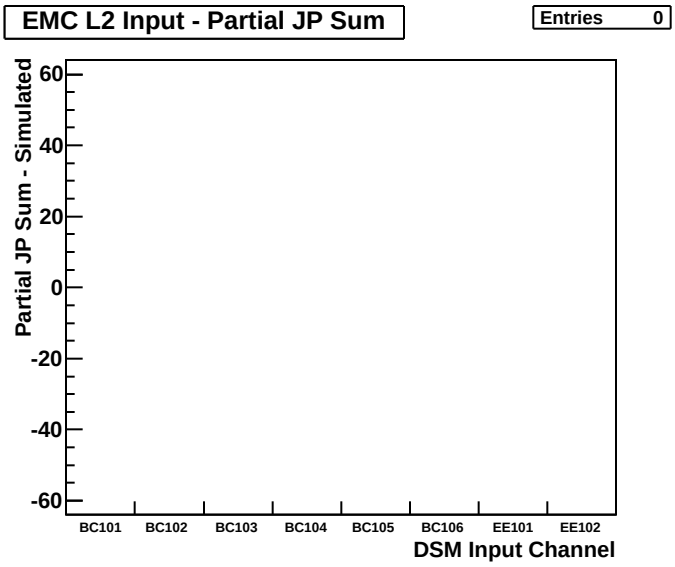

EMC L2 Input - JPY/JPB bits
Entries 96120
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

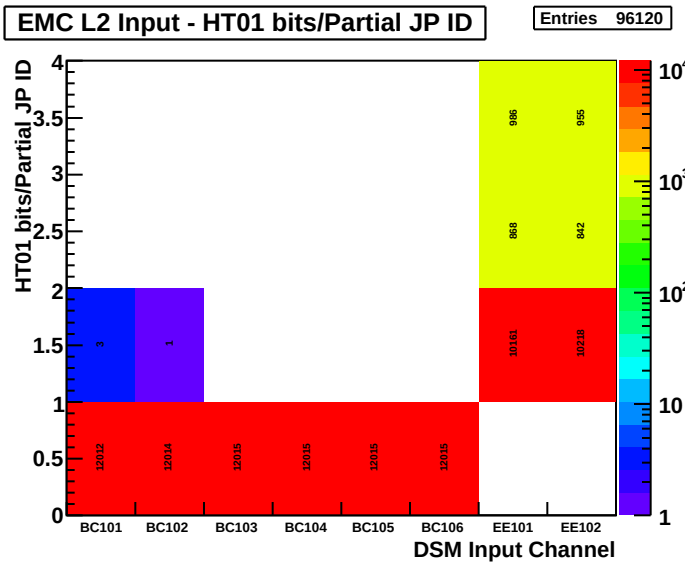

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

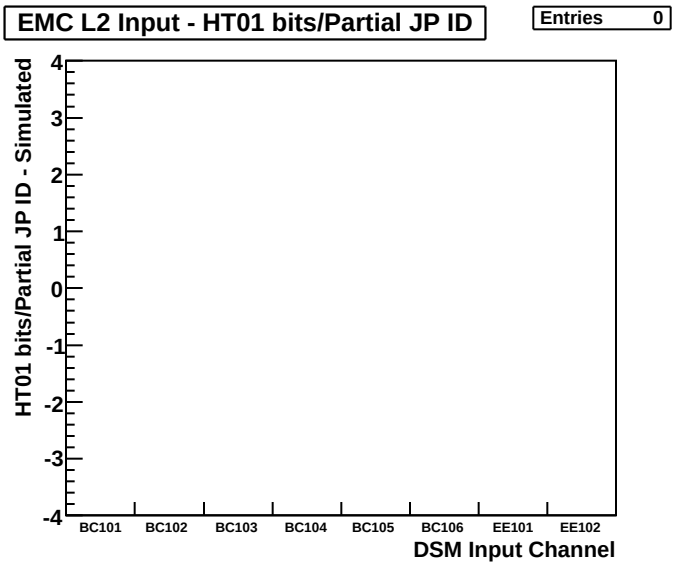

EMC L2 Input - JPZ/JPC bits
Entries 96120
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

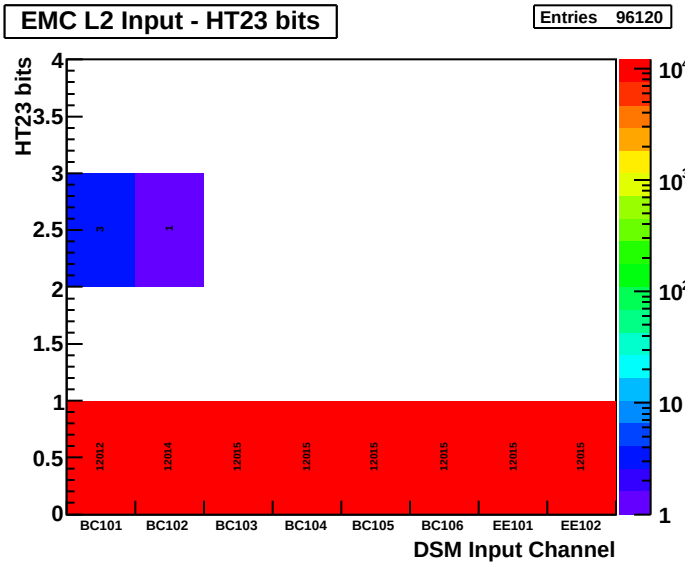

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

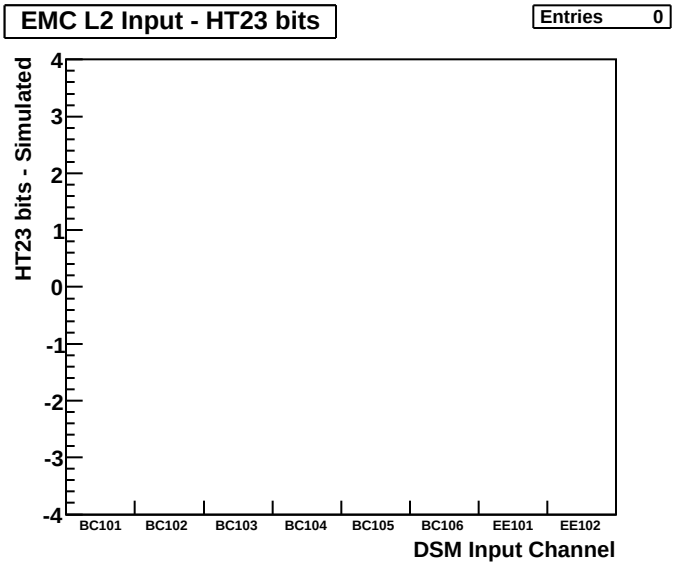

EMC L2 Input - Partial JP Sum
Entries 96120
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

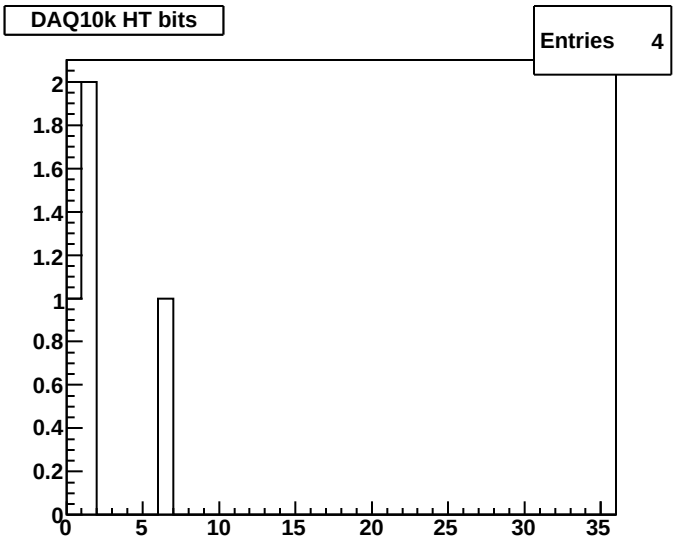

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

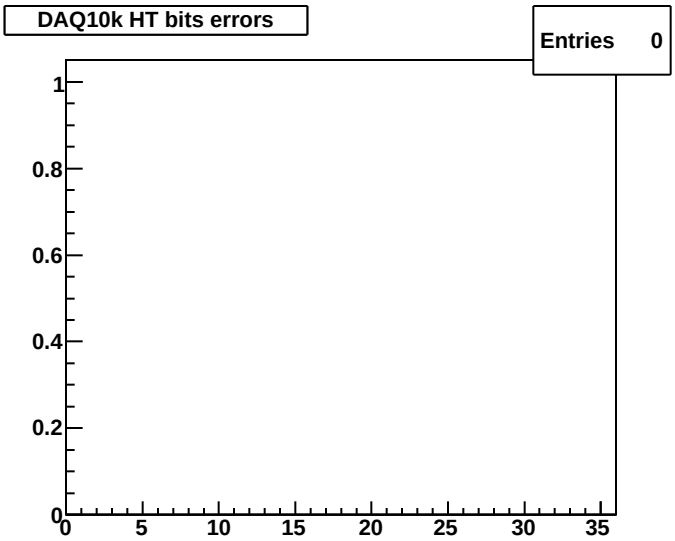

EMC L2 Input - HT01 bits/Partial JP ID
Entries 96120
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

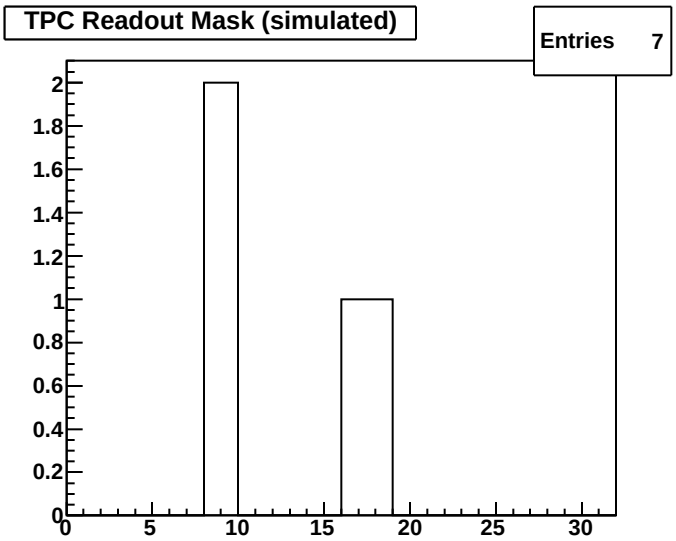

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

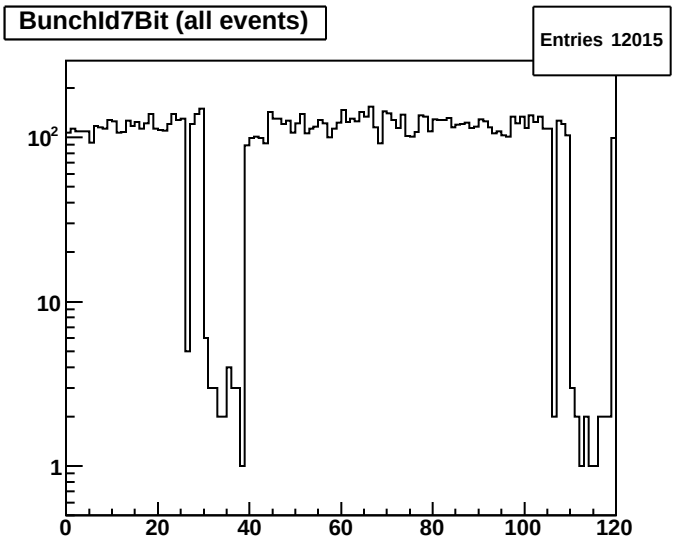

EMC L2 Input - HT23 bits
Entries 96120
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

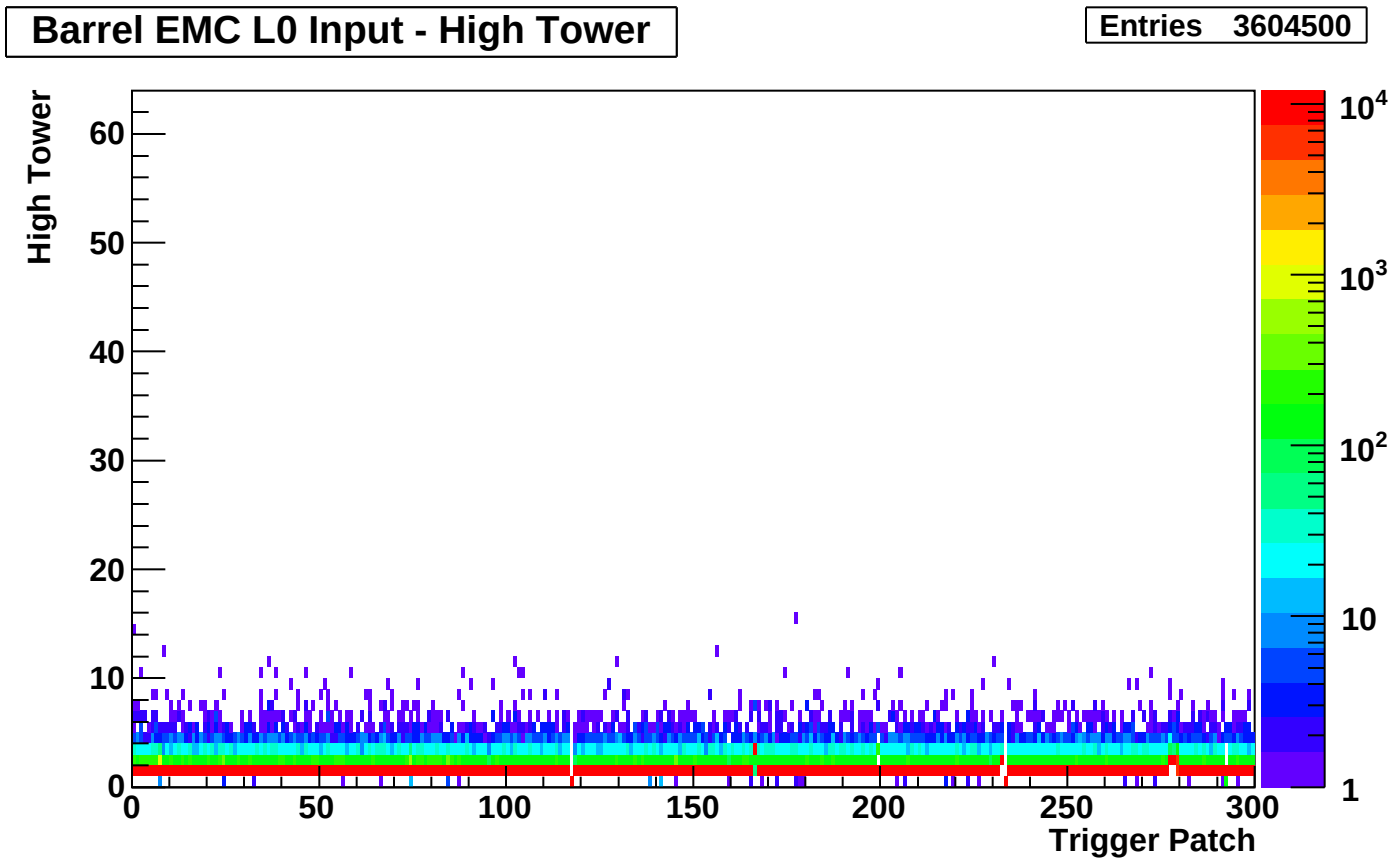

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

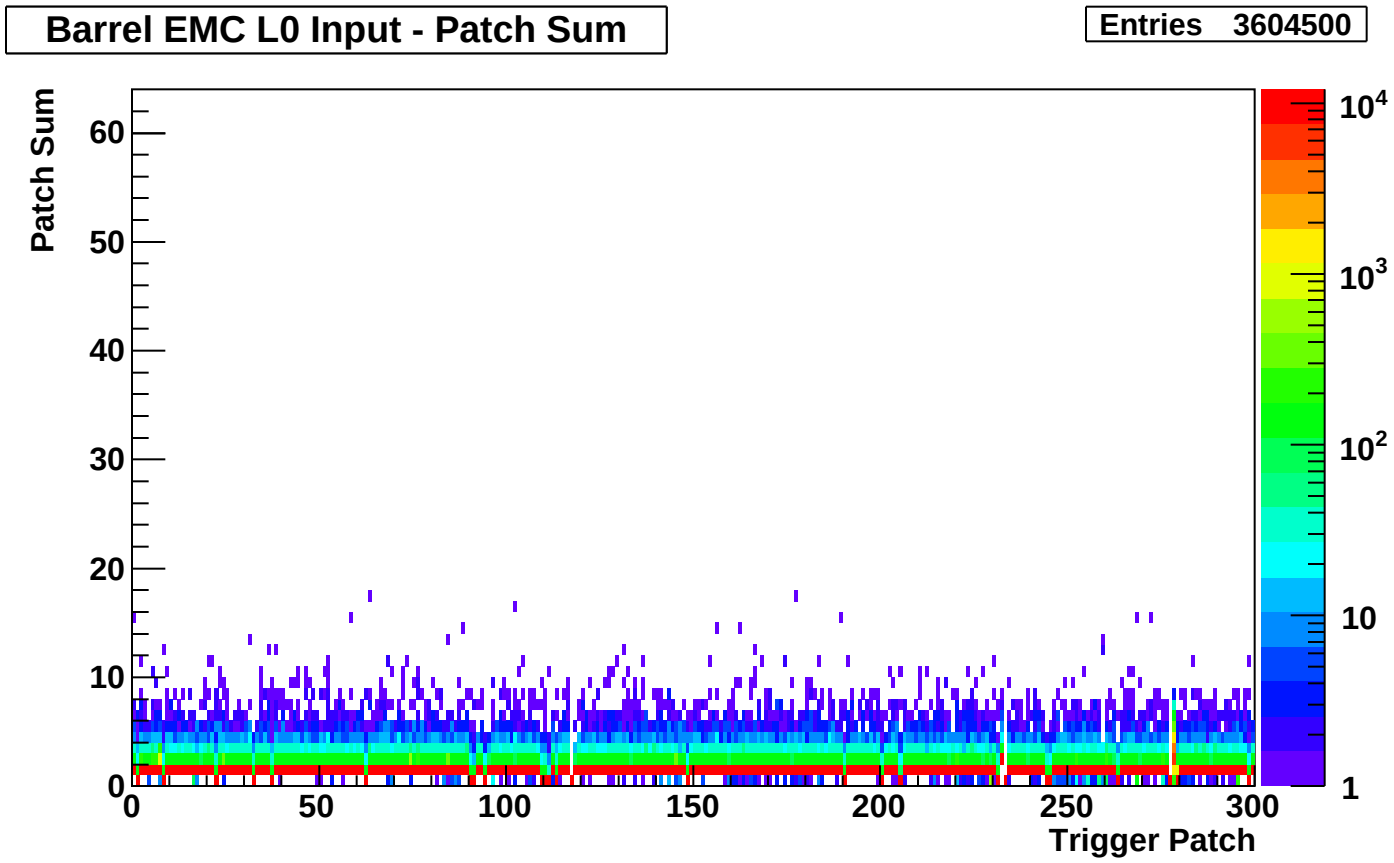

DAQ10k HT bits
Entries 4

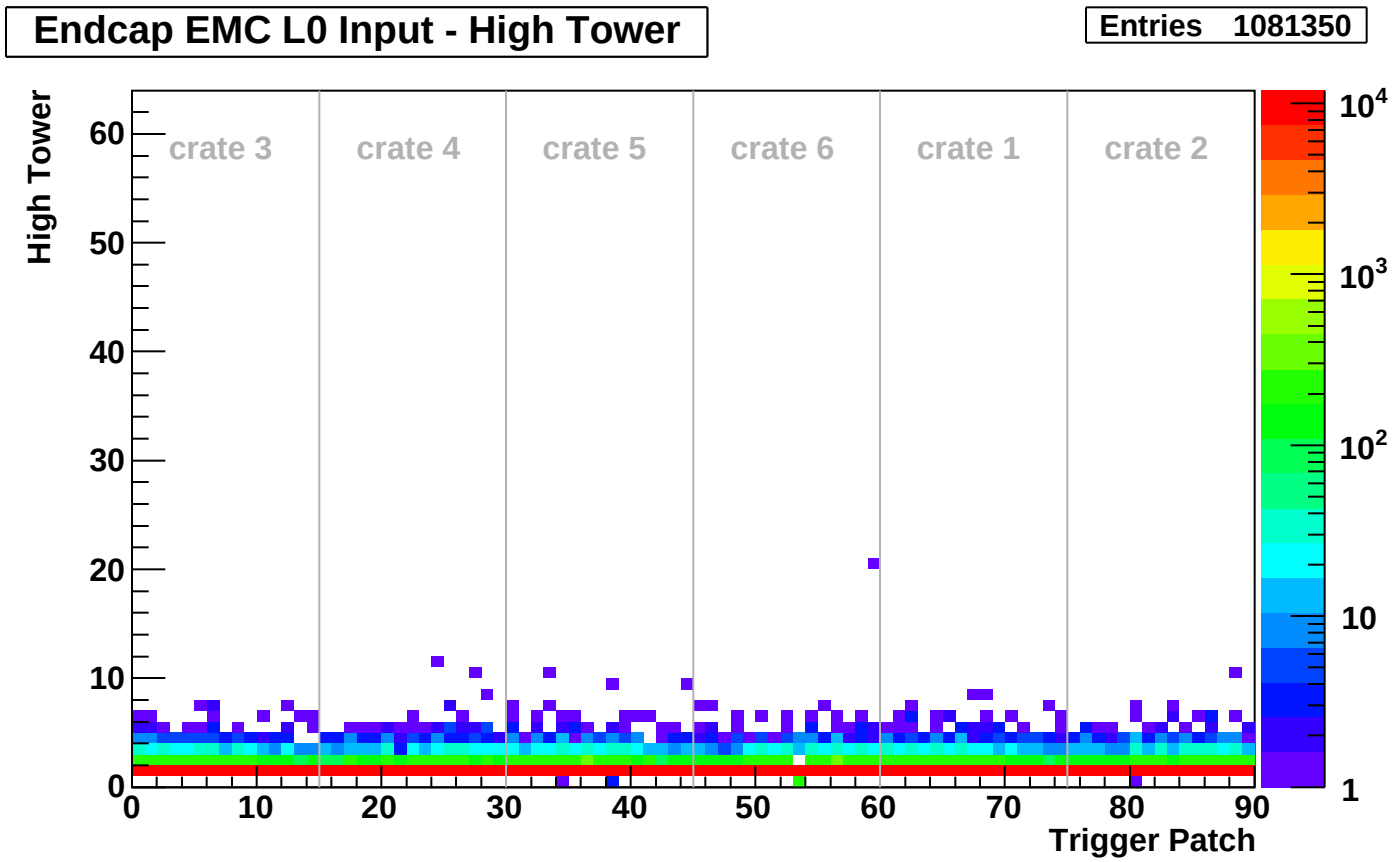

DAQ10k HT bits errors
Entries 0

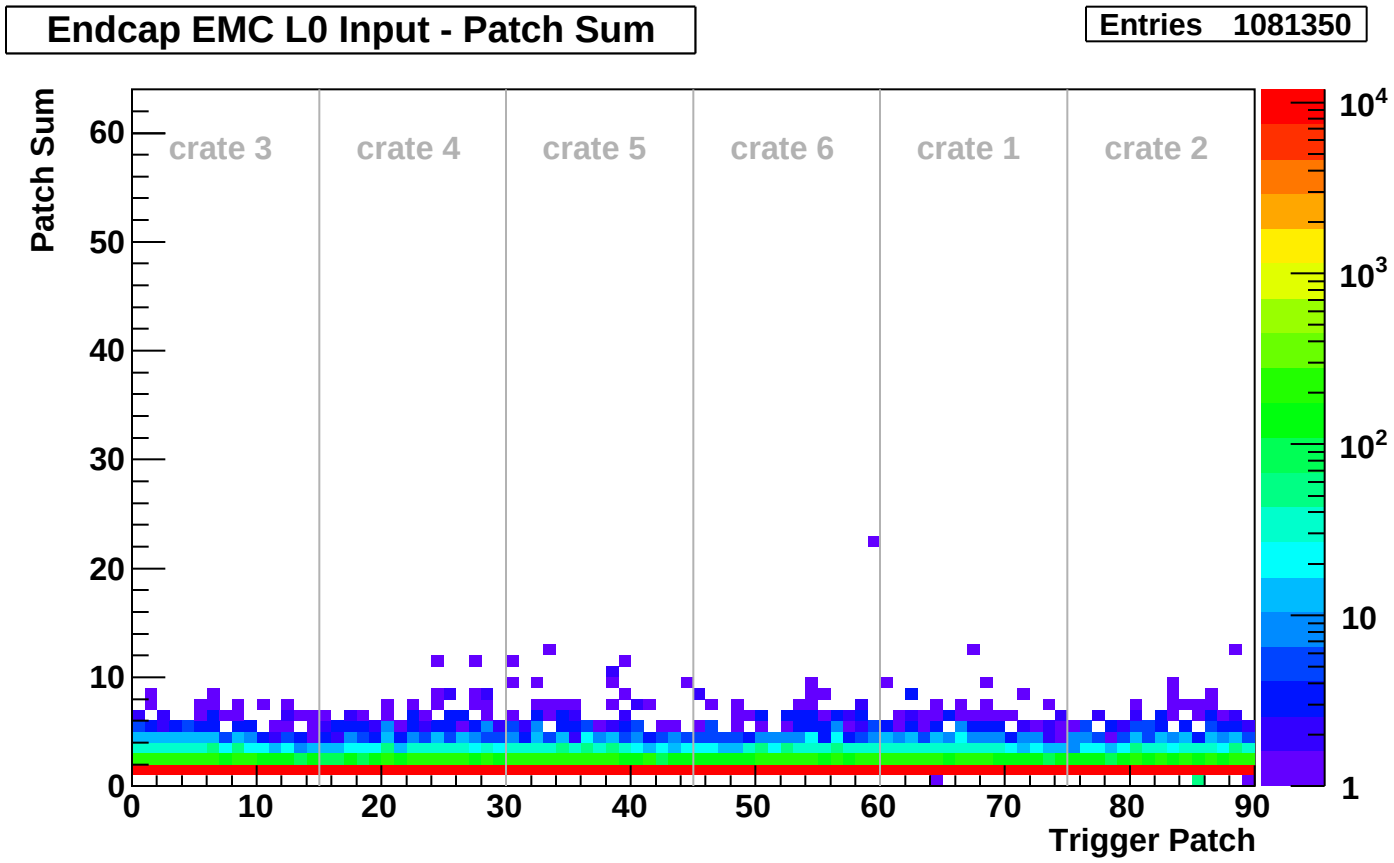

TPC Readout Mask (simulated)
Entries 7

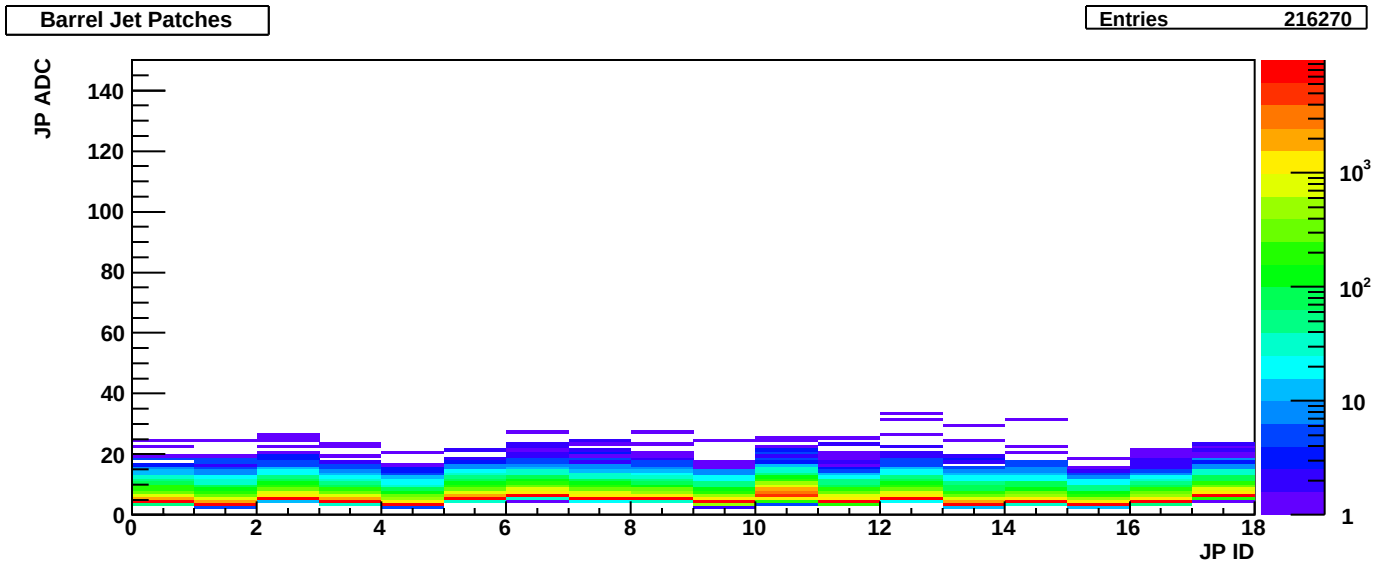

BunchId7Bit (all events)
Entries 12015

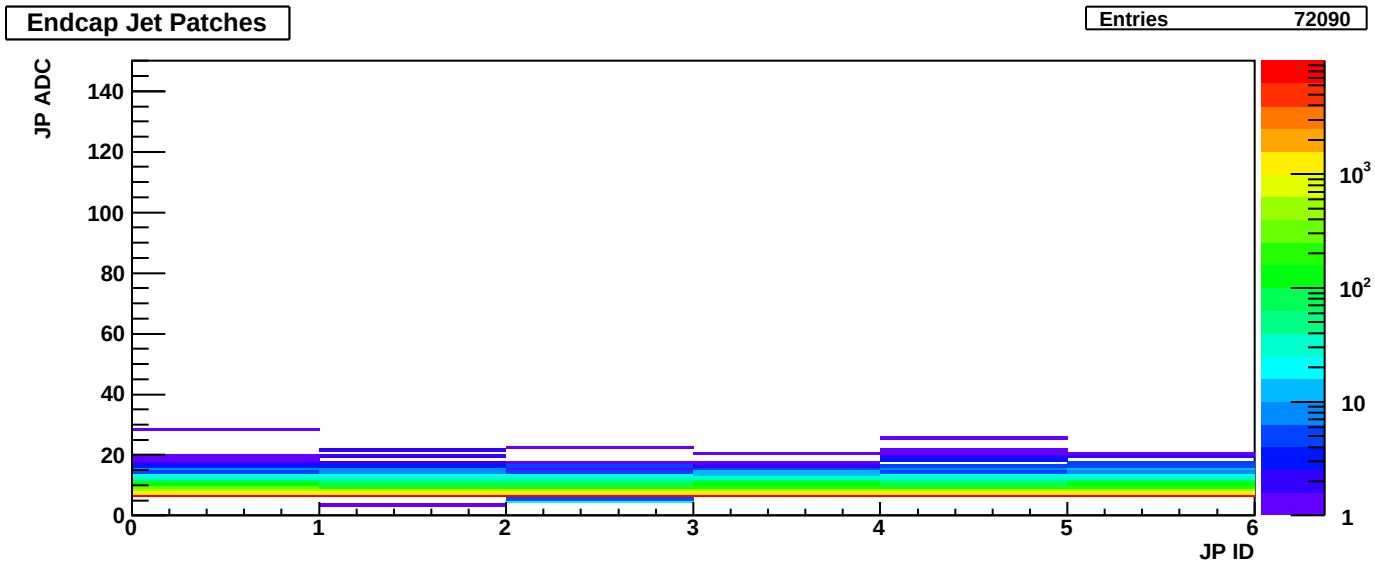

Barrel EMC L0 Input - High Tower
Entries 3604500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

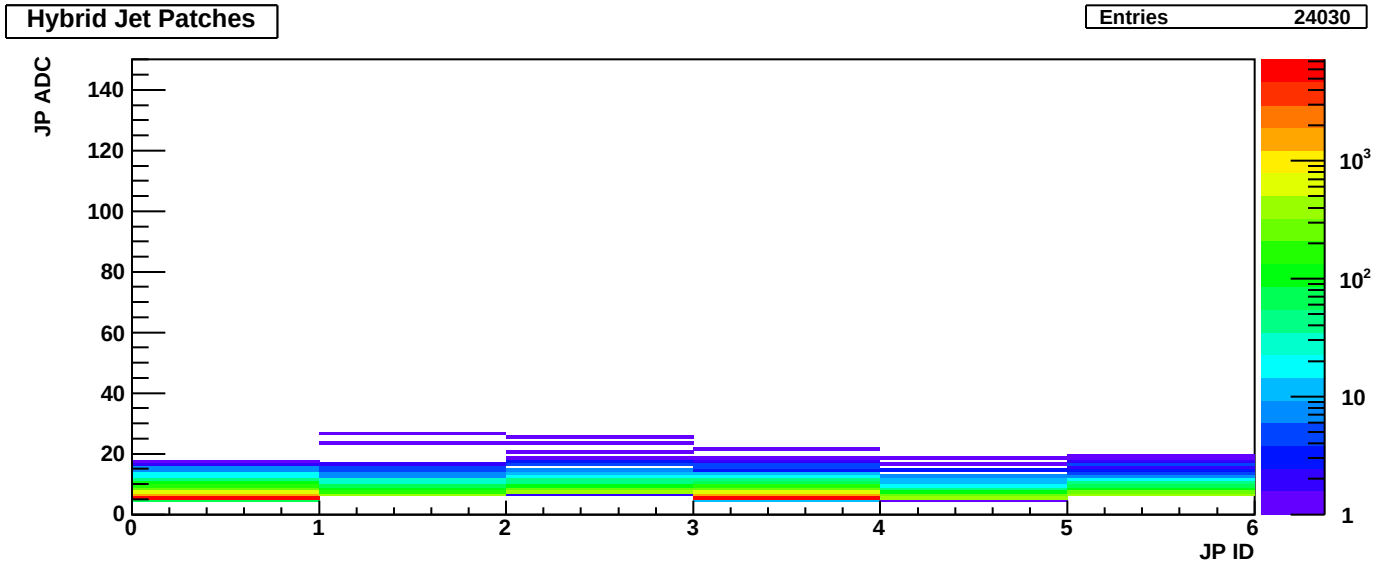

Barrel EMC L0 Input - Patch Sum
Entries 3604500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

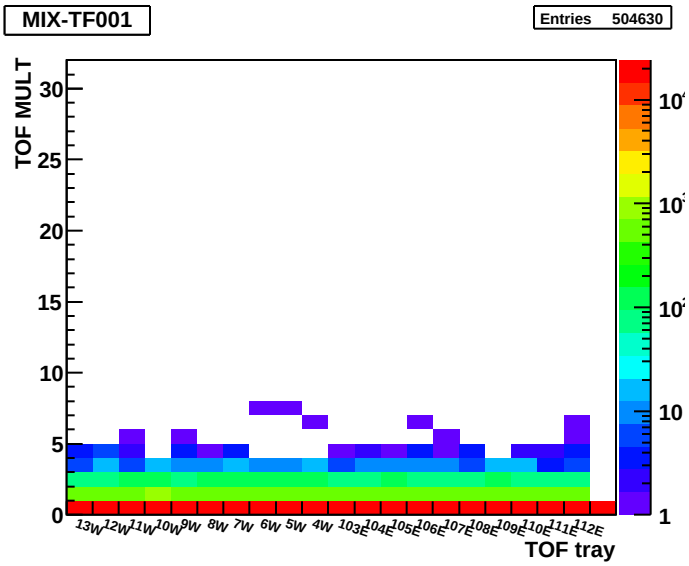

Endcap EMC L0 Input - High Tower
Entries 1081350
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

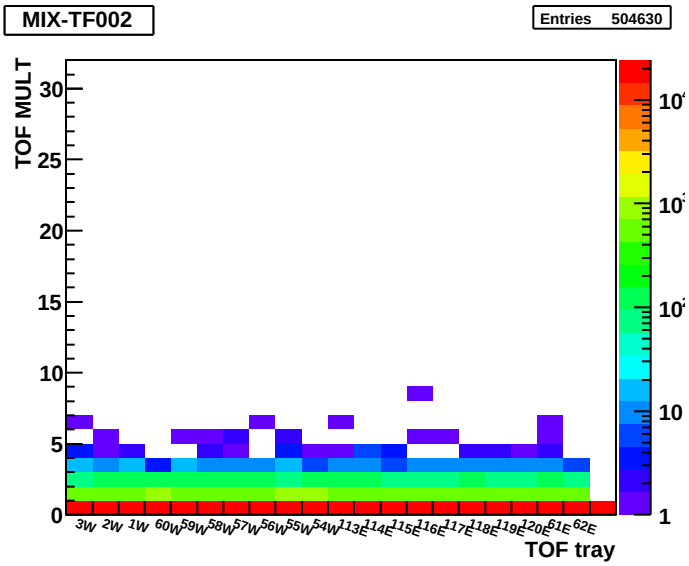

Endcap EMC L0 Input - Patch Sum
Entries 1081350
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

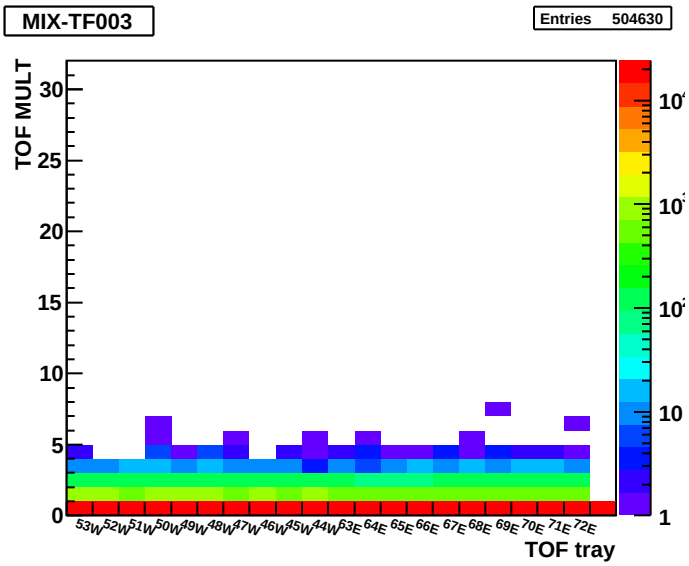

Barrel Jet Patches
Entries 216270
JP ADC
JP ID

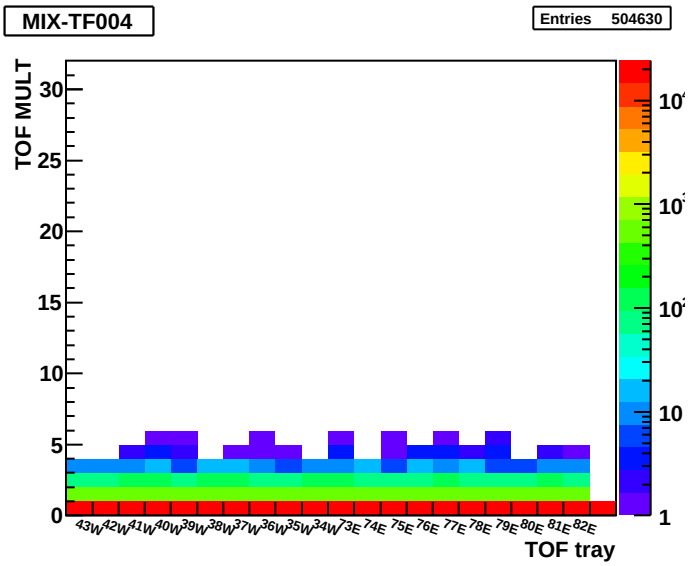

Endcap Jet Patches
Entries 72090
JP ADC
JP ID

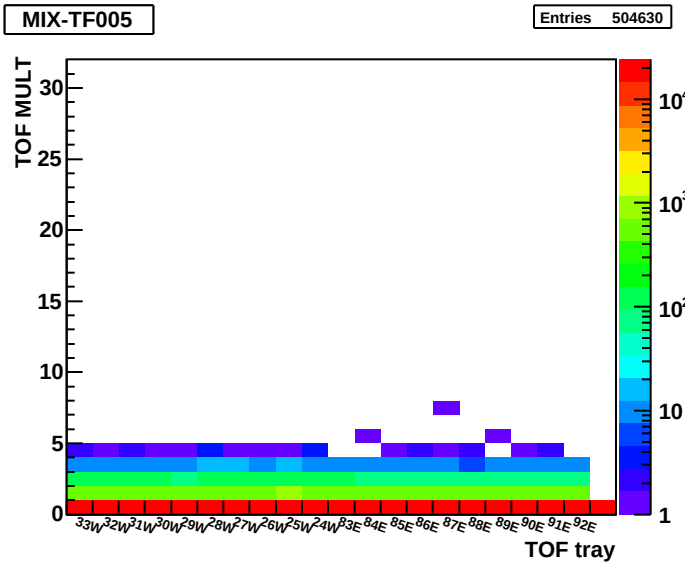

Hybrid Jet Patches
Entries 24030
JP ADC
JP ID

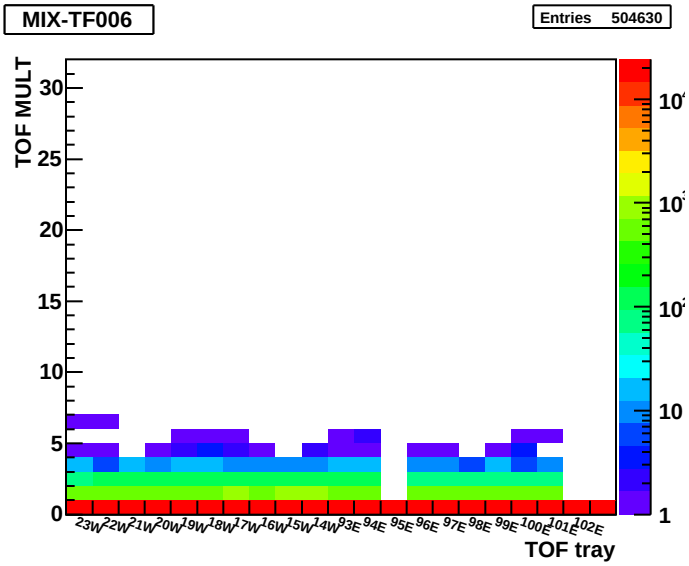

MIX-TF001
Entries 504630
TOF MULT
TOF tray

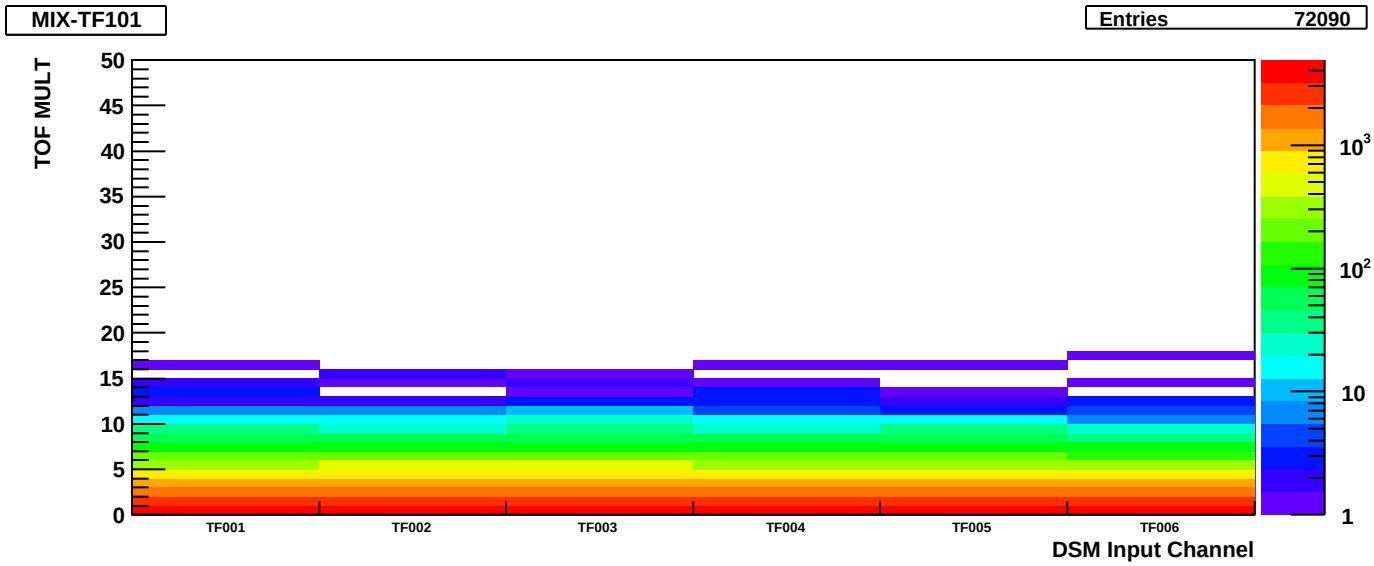

MIX-TF002
Entries 504630
TOF MULT
TOF tray

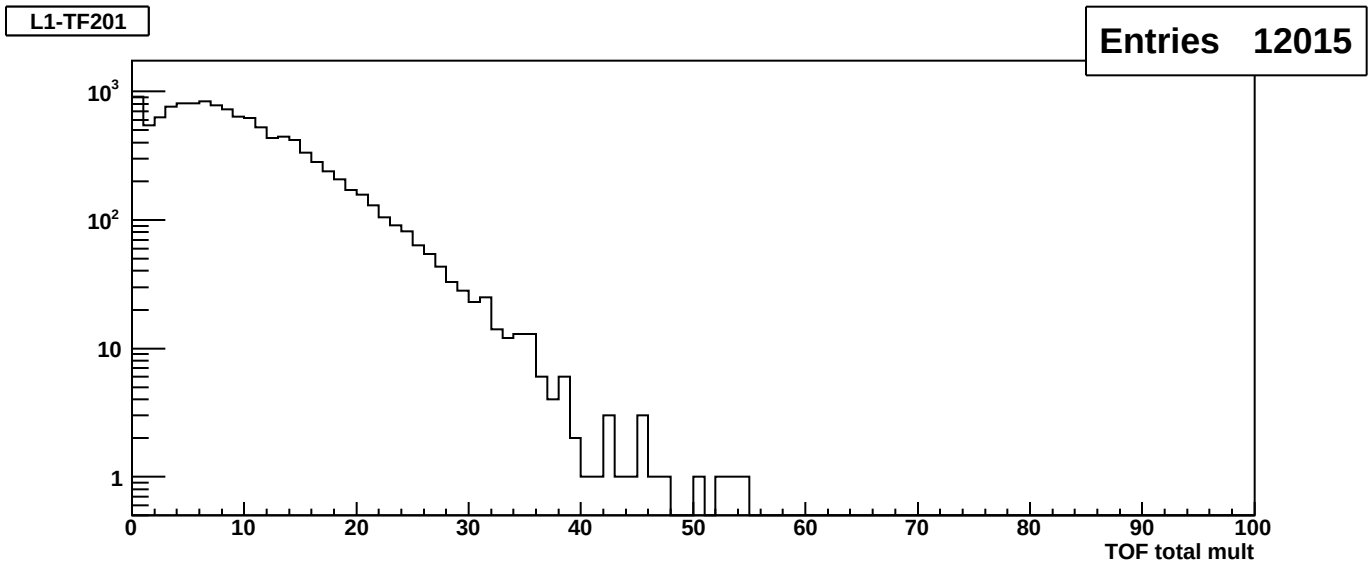

MIX-TF003
Entries 504630
TOF MULT
TOF tray

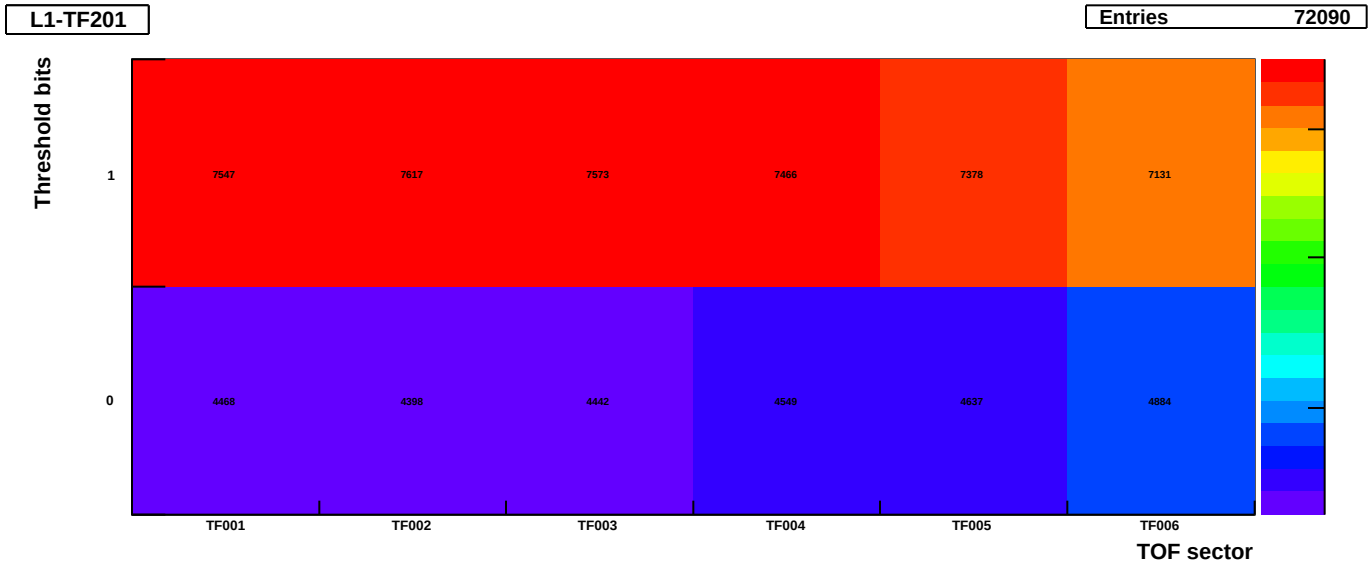

MIX-TF004
Entries 504630
TOF MULT
TOF tray

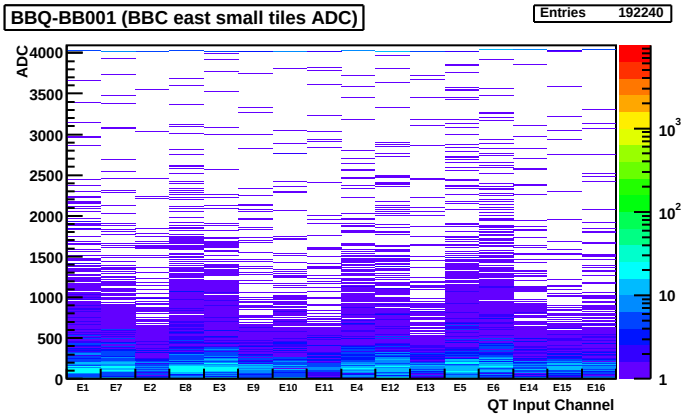

MIX-TF005
Entries 504630
TOF MULT
TOF tray

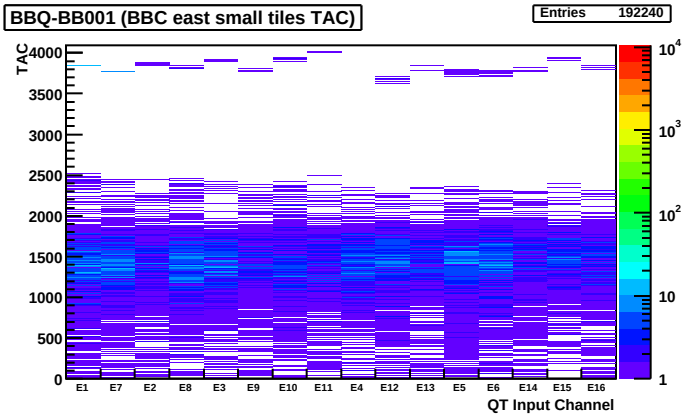

MIX-TF006
Entries 504630
TOF MULT
TOF tray

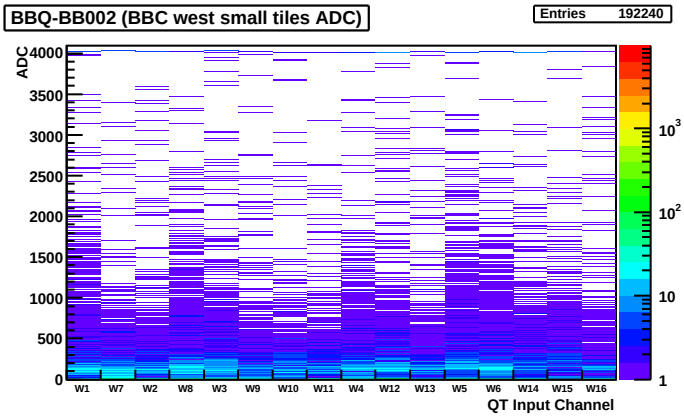

MIX-TF101
Entries 72090
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

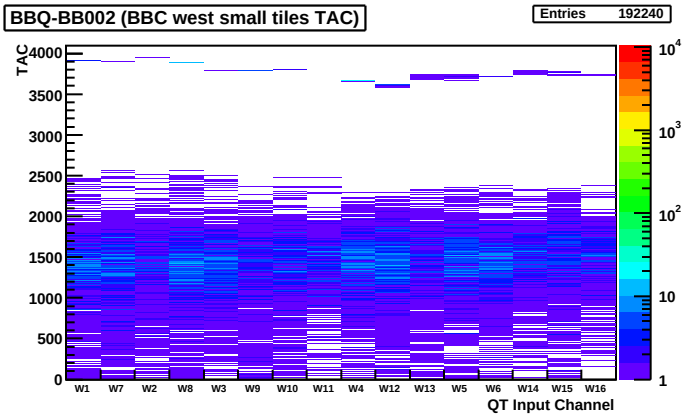

L1-TF201
Entries 12015
TOF total mult

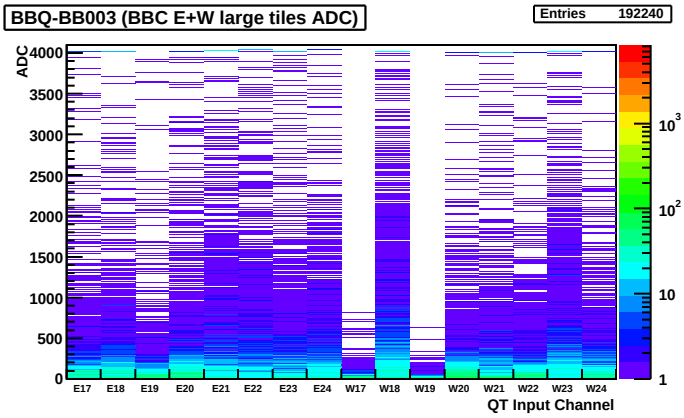

L1-TF201
Entries 72090
Threshold bits
7547
7617
7573
7466
7378
7131
4468
4398
4442
4549
4637
4884
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

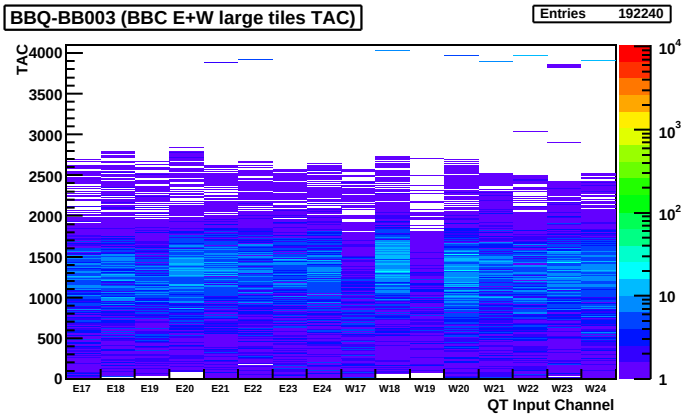

BBQ-BB001 (BBC east small tiles ADC)
Entries 192240
ADC
QT Input Channel

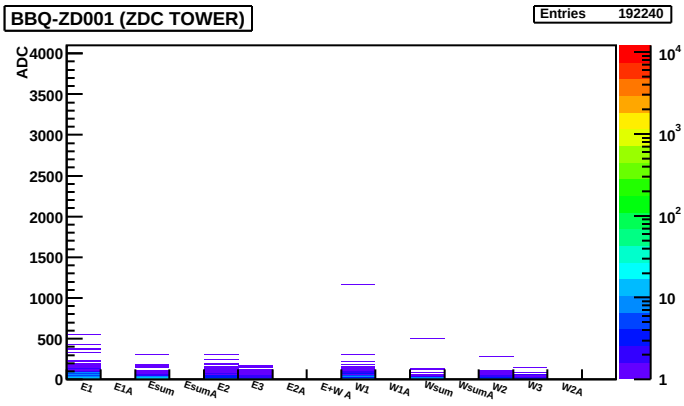

BBQ-BB001 (BBC east small tiles TAC)
Entries 192240
TAC
QT Input Channel

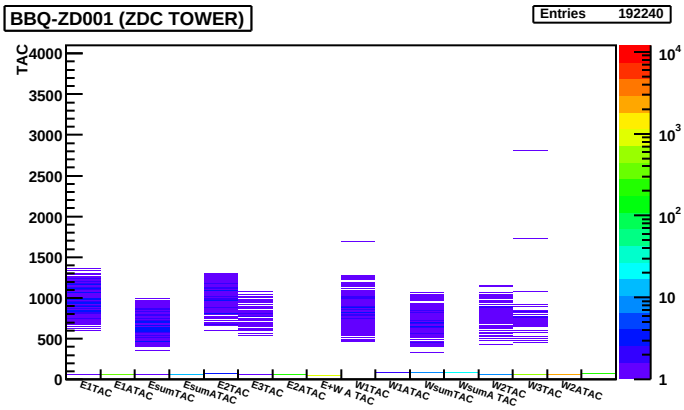

BBQ-BB002 (BBC west small tiles ADC)
Entries 192240
ADC
QT Input Channel

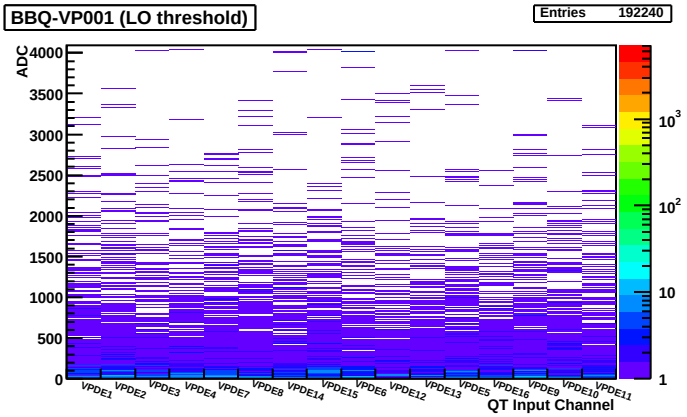

BBQ-BB002 (BBC west small tiles TAC)
Entries 192240
TAC
QT Input Channel

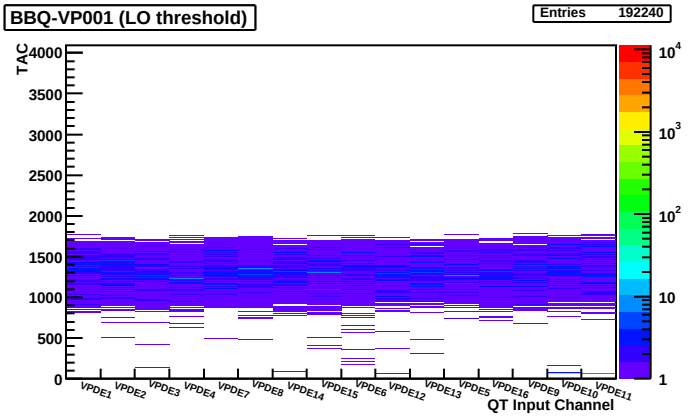

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 192240
ADC
QT Input Channel

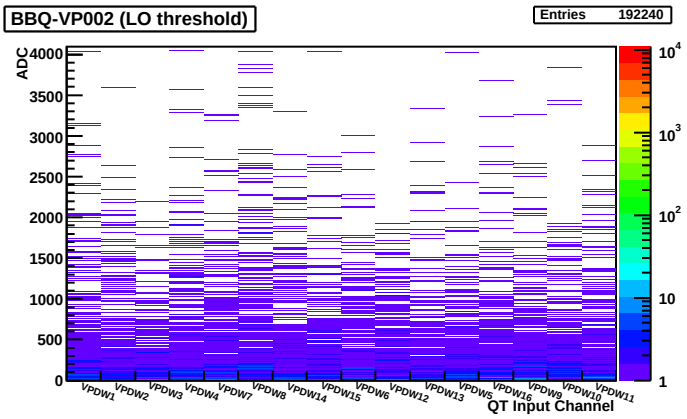

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 192240
TAC
QT Input Channel

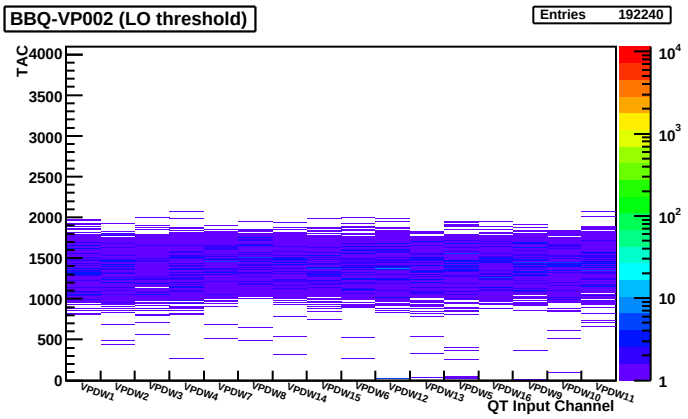

BBQ-ZD001 (ZDC TOWER)
Entries 192240
ADC

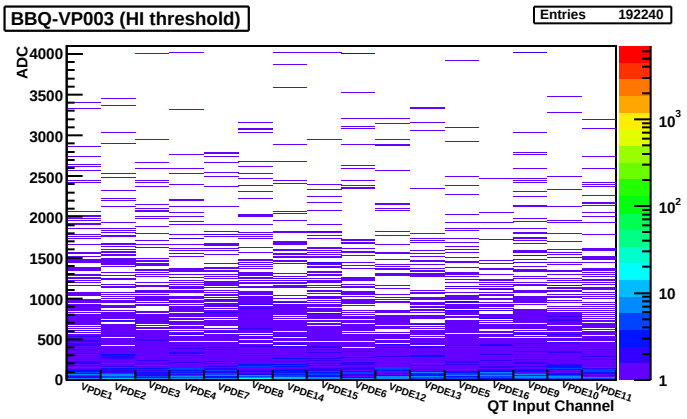

BBQ-ZD001 (ZDC TOWER)
Entries 192240
TAC

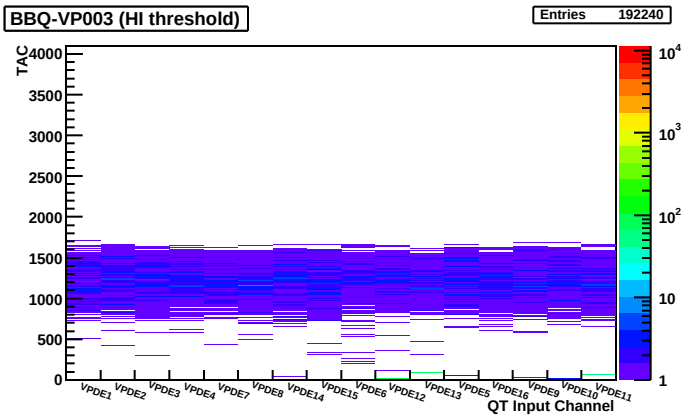

BBQ-VP001 (LO threshold)
Entries 192240
ADC
QT Input Channel

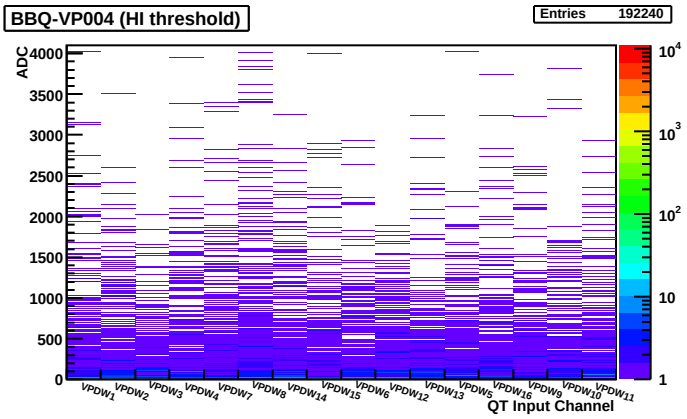

BBQ-VP001 (LO threshold)
Entries 192240
TAC
QT Input Channel

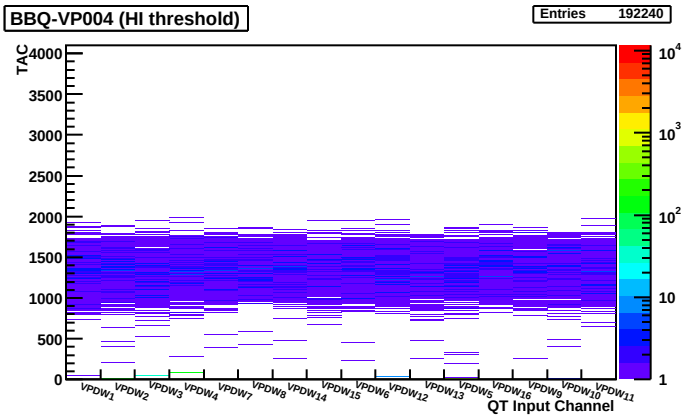

BBQ-VP002 (LO threshold)
Entries 192240
ADC
QT Input Channel

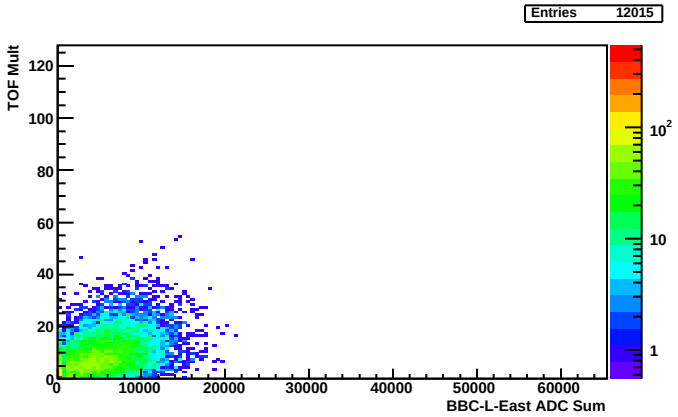

BBQ-VP002 (LO threshold)
Entries 192240
TAC
QT Input Channel

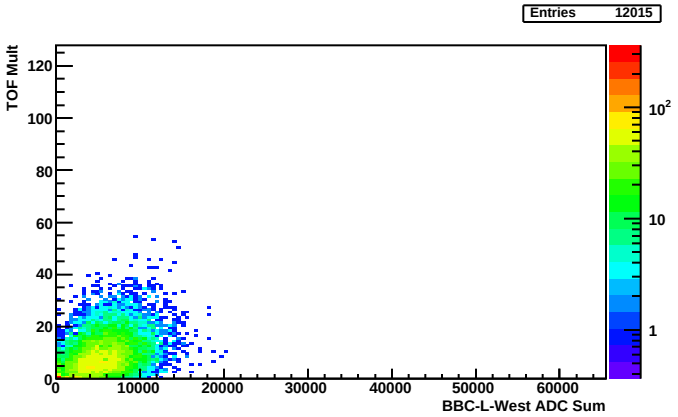

BBQ-VP003 (HI threshold)
Entries 192240
ADC
QT Input Channel

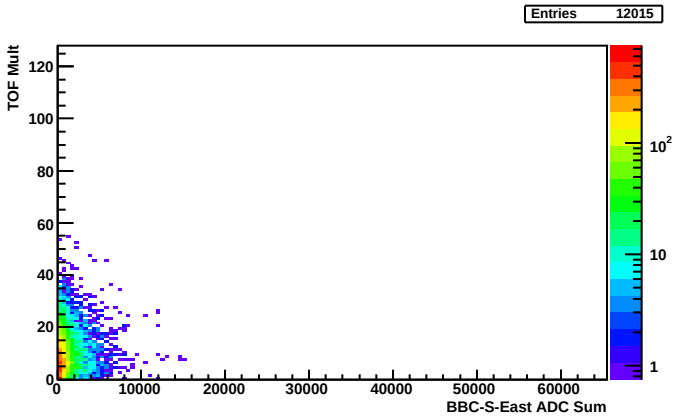

BBQ-VP003 (HI threshold)
Entries 192240
TAC
QT Input Channel

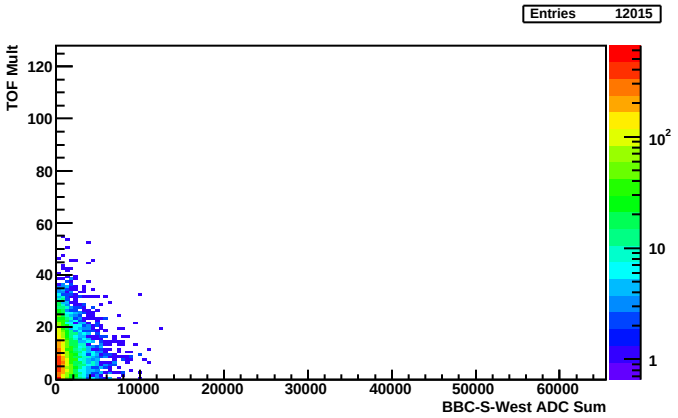

BBQ-VP004 (HI threshold)
Entries 192240
ADC
QT Input Channel

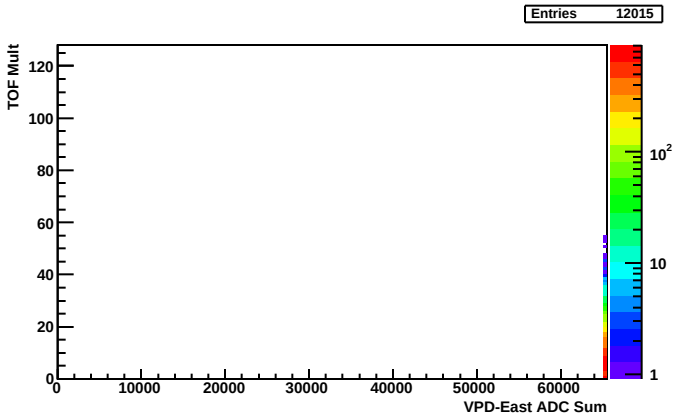

BBQ-VP004 (HI threshold)
Entries 192240
TAC
QT Input Channel

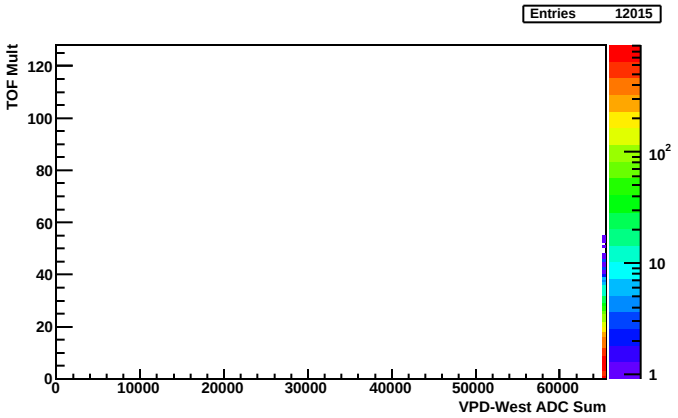

Entries 12015
TOF Mult
BBC-L-East ADC Sum

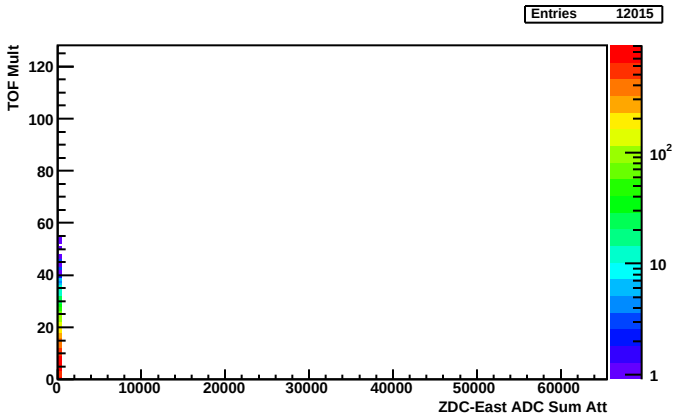

Entries 12015
TOF Mult
BBC-L-West ADC Sum

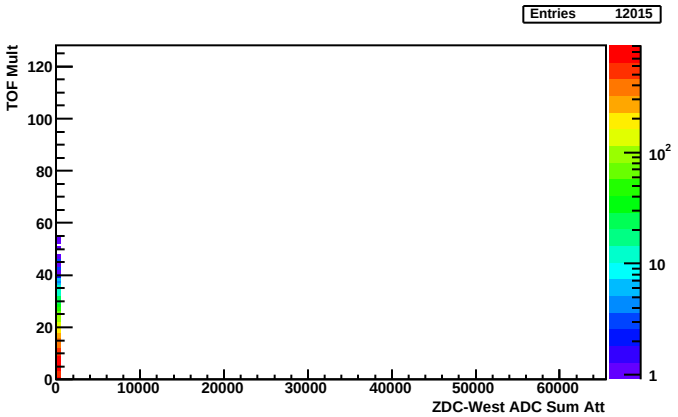

Entries 12015
TOF Mult
BBC-S-East ADC Sum

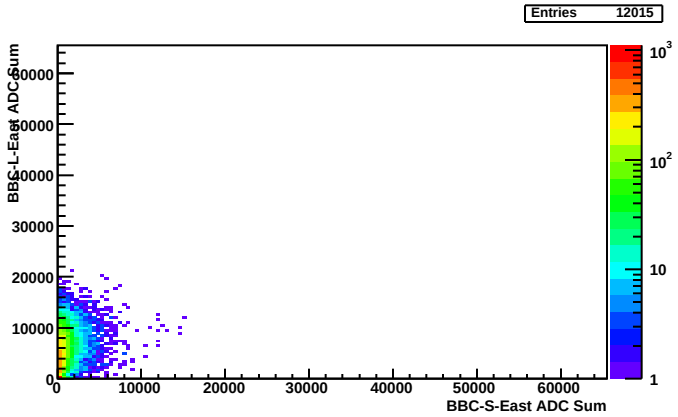

Entries 12015
TOF Mult
BBC-S-West ADC Sum

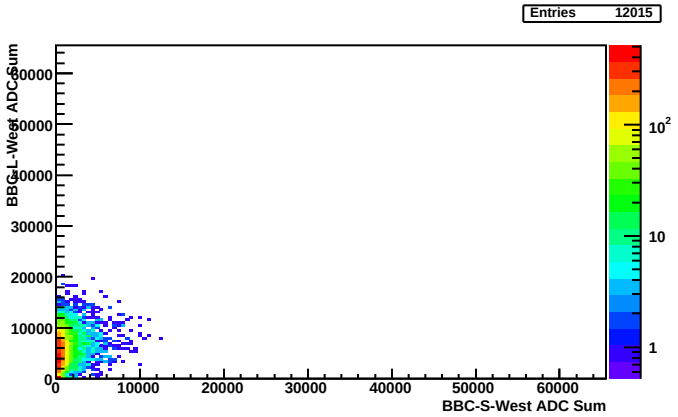

Entries 12015
TOF Mult
VPD-East ADC Sum

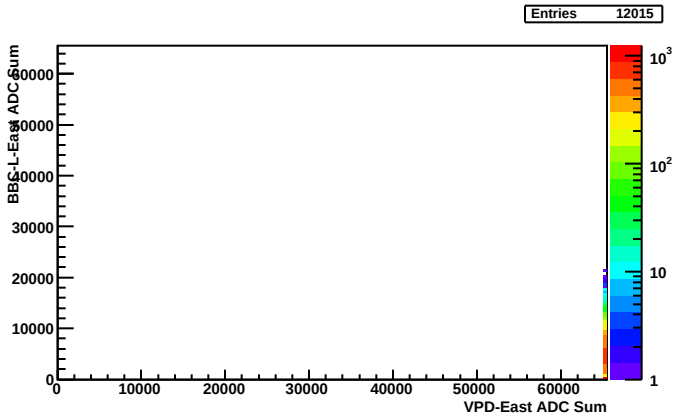

Entries 12015
TOF Mult
VPD-West ADC Sum

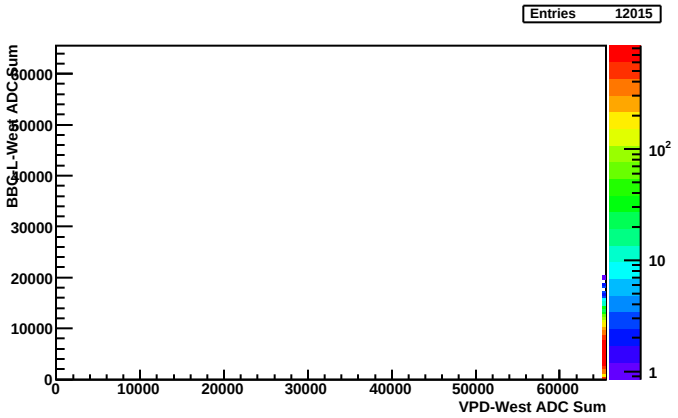

Entries 12015
TOF Mult
ZDC-East ADC Sum Att

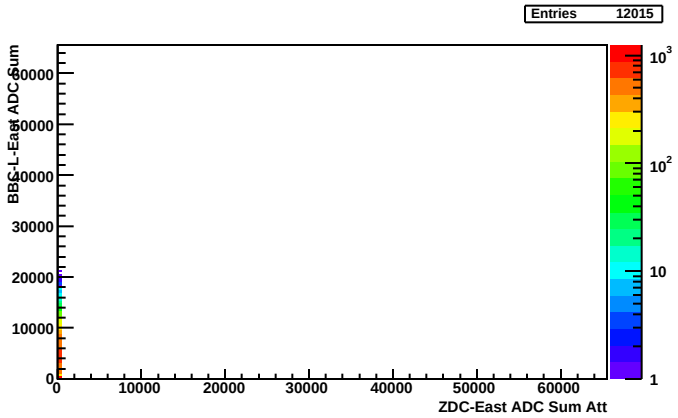

Entries 12015
TOF Mult
ZDC-West ADC Sum Att

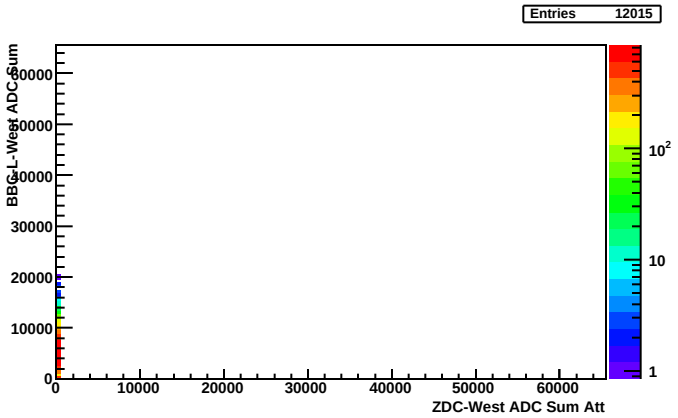

Entries 12015
BBC-L-East ADC Sum
BBC-S-East ADC Sum

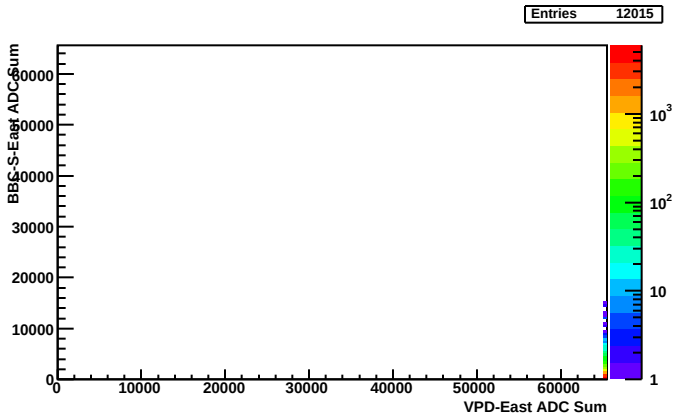

Entries 12015
BBC-L-West ADC Sum
BBC-S-West ADC Sum

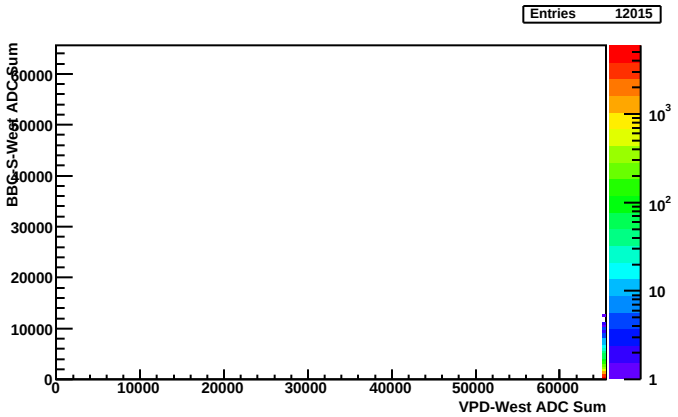

Entries 12015
BBC-L-East ADC Sum
VPD-East ADC Sum

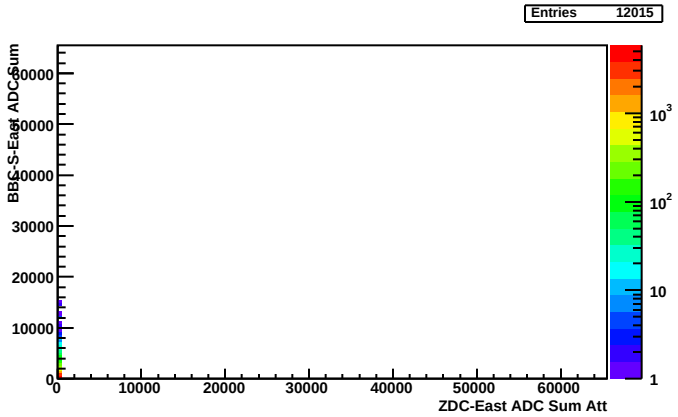

Entries 12015
BBC-L-West ADC Sum
VPD-West ADC Sum

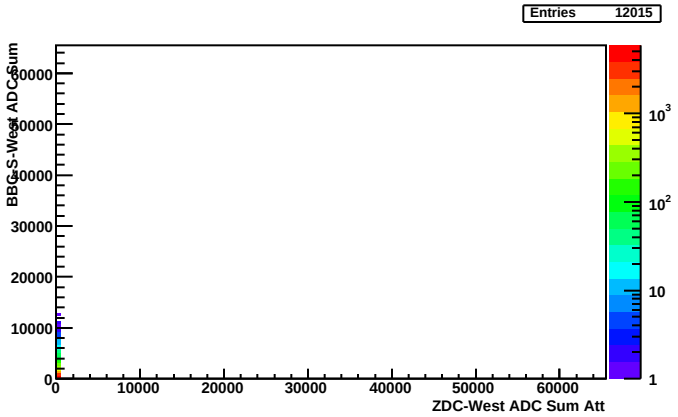

Entries 12015
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

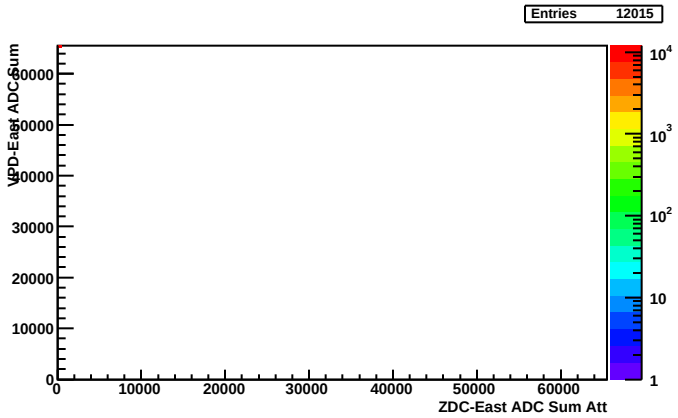

Entries 12015
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

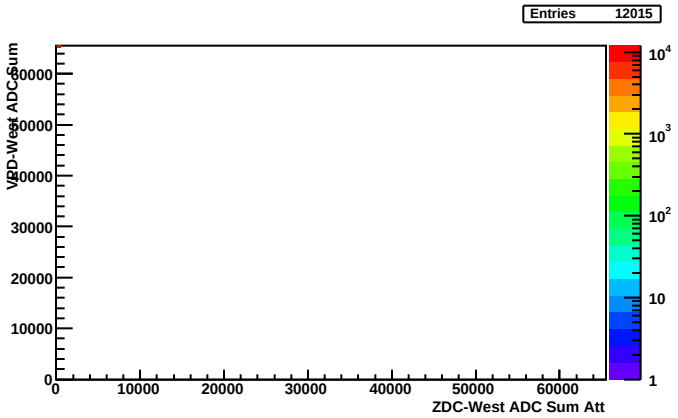

Entries 12015
BBC-S-East ADC Sum
VPD-East ADC Sum

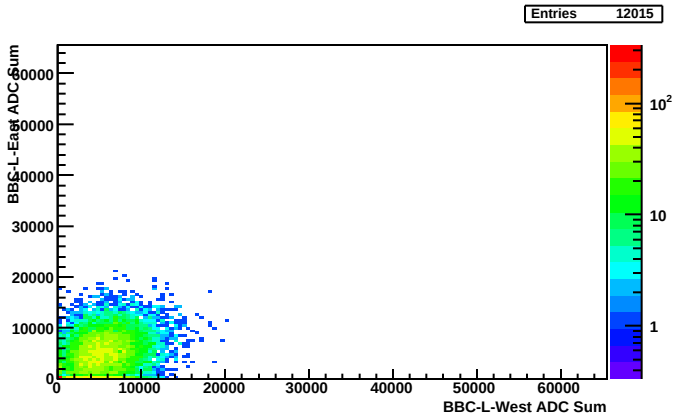

Entries 12015
BBC-S-West ADC Sum
VPD-West ADC Sum

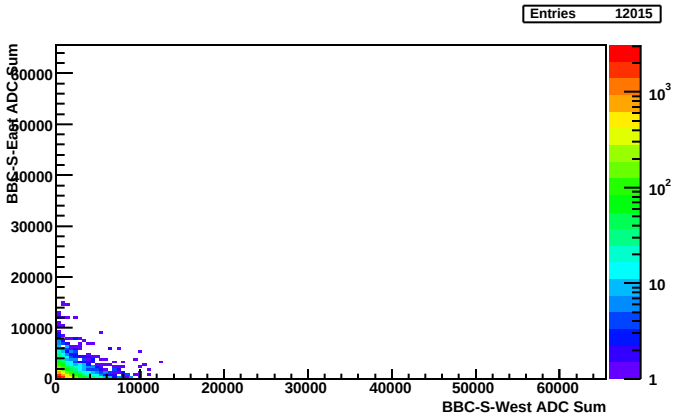

Entries 12015
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

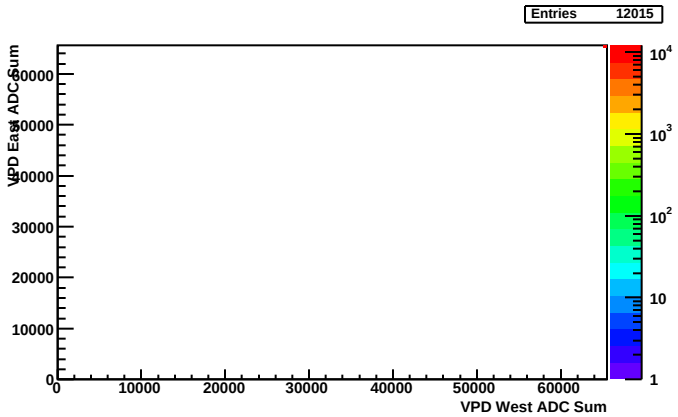

Entries 12015
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

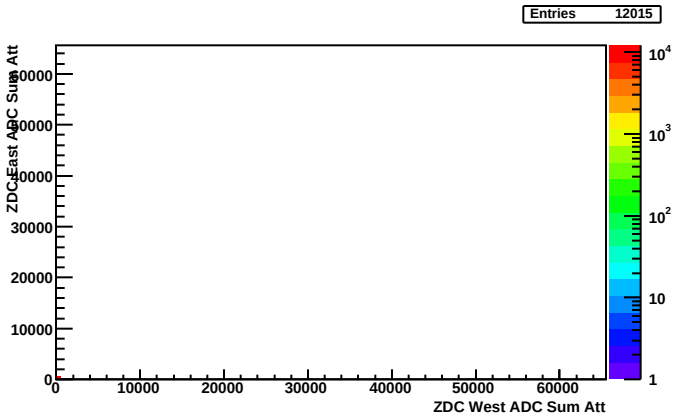

Entries 12015
VPD-East ADC Sum
ZDC-East ADC Sum Att

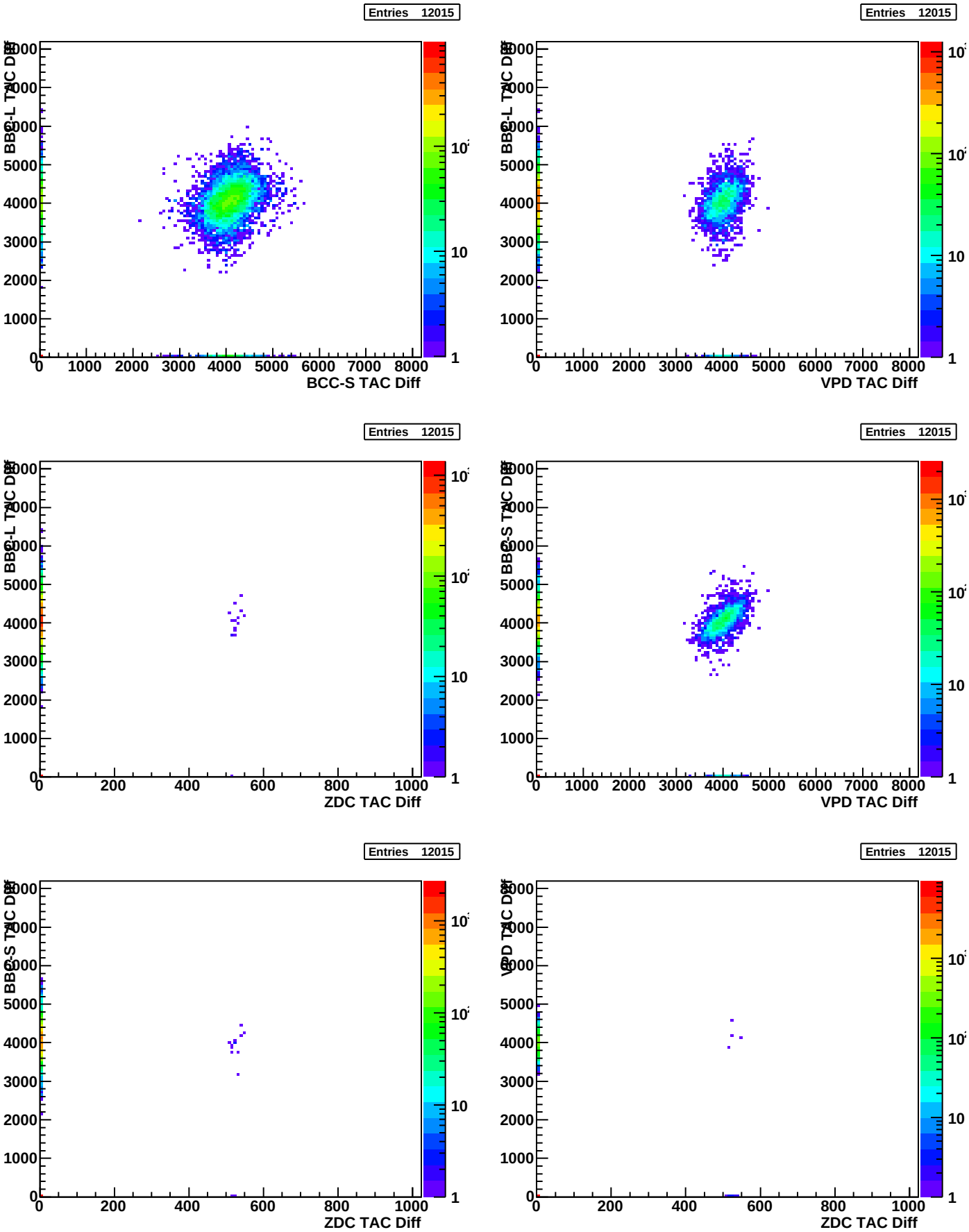

Entries 12015
VPD-West ADC Sum
ZDC-West ADC Sum Att

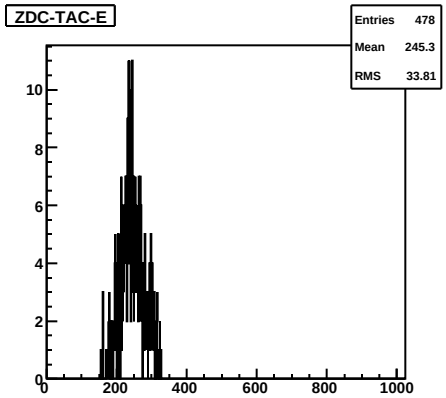

Entries 12015
BBC-L-East ADC Sum
BBC-L-West ADC Sum

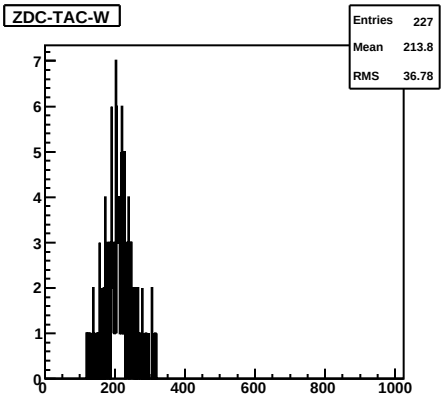

Entries 12015
BBC-S-East ADC Sum
BBC-S-West ADC Sum

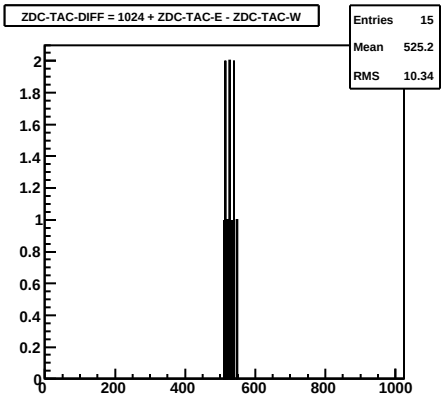

Entries 12015
VPD East ADC Sum
VPD West ADC Sum

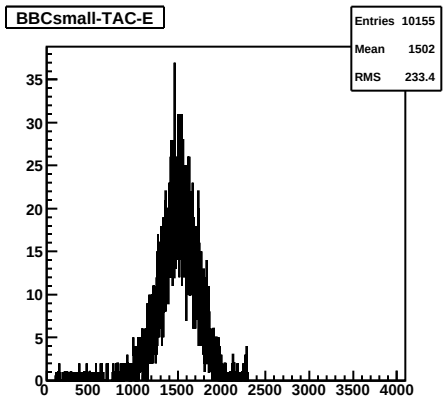

Entries 12015
ZDC East ADC Sum Att
ZDC West ADC Sum Att

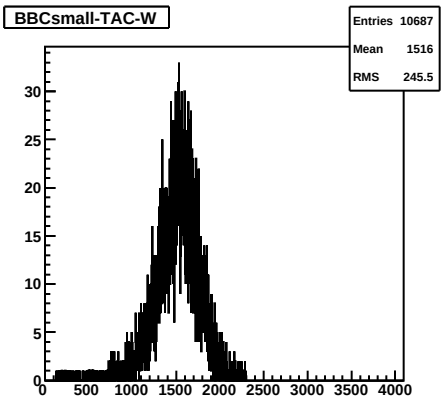

Entries 12015
BBC-L TAC Diff
BCC-S TAC Diff
Entries 12015
BBC-L TAC Diff
VPD TAC Diff
Entries 12015
BBC-L TAC Diff
ZDC TAC Diff
Entries 12015
BBC-S TAC Diff
VPD TAC Diff
Entries 12015
BBC-S TAC Diff
ZDC TAC Diff
Entries 12015
VPD TAC Diff
ZDC TAC Diff

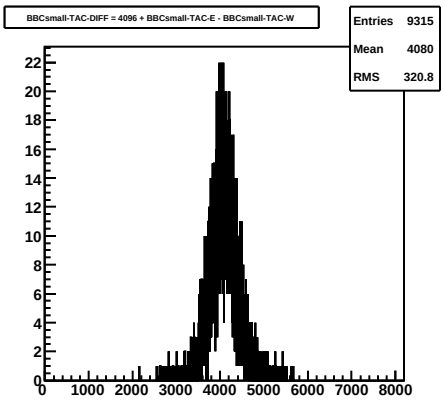

ZDC-TAC-E
Entries 478
Mean 245.3
RMS 33.81

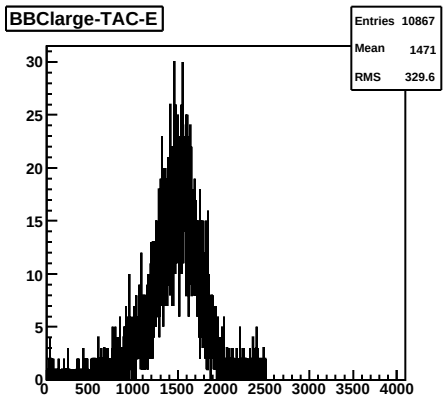

ZDC-TAC-W
Entries 227
Mean 213.8
RMS 36.78

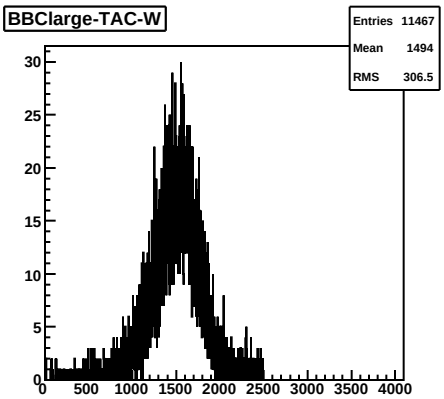

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 15
Mean 525.2
RMS 10.34

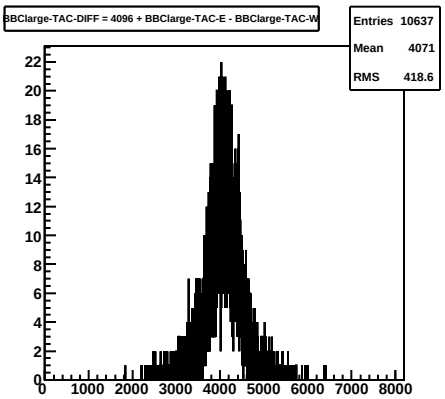

BBCsmall-TAC-E
Entries 10155
Mean 1502
RMS 233.4

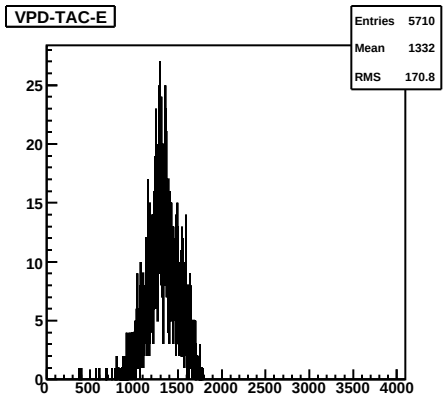

BBCsmall-TAC-W
Entries 10687
Mean 1516
RMS 245.5

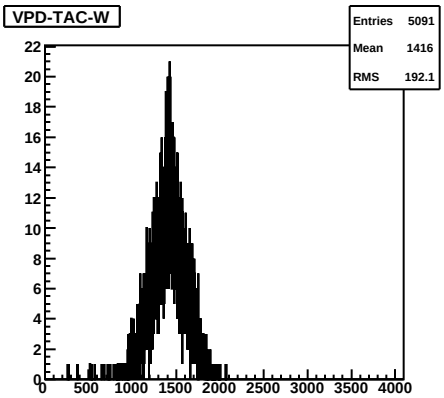

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 9315
Mean 4080
RMS 320.8

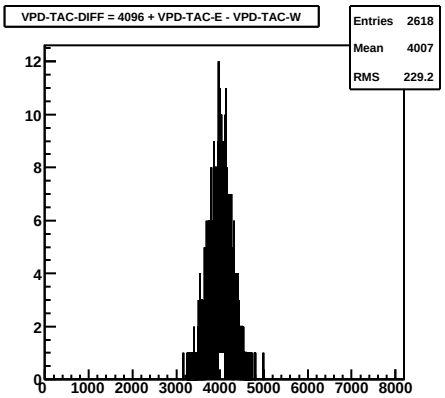

BBClarge-TAC-E
Entries 10867
Mean 1471
RMS 329.6

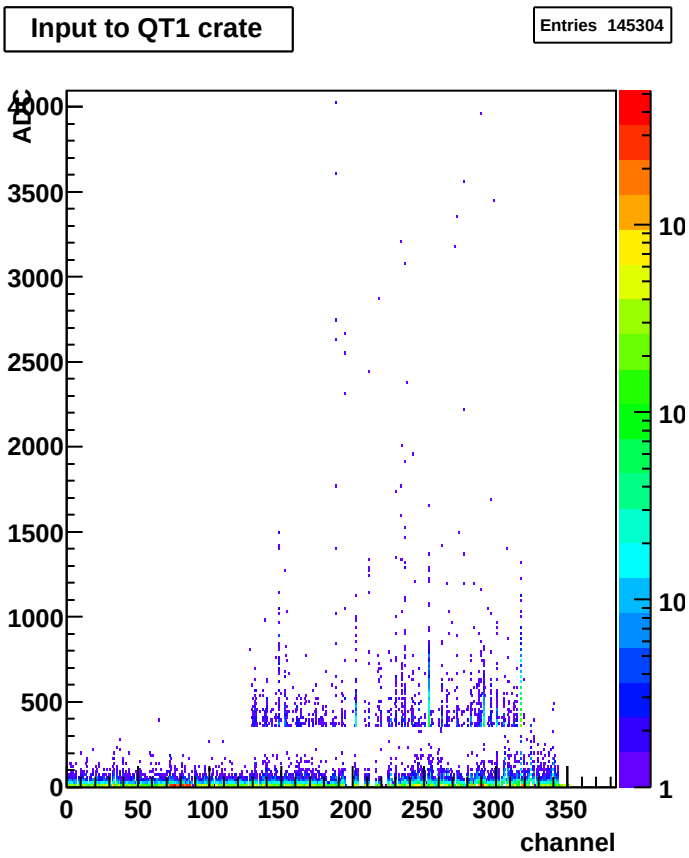

BBClarge-TAC-W
Entries 11467
Mean 1494
RMS 306.5

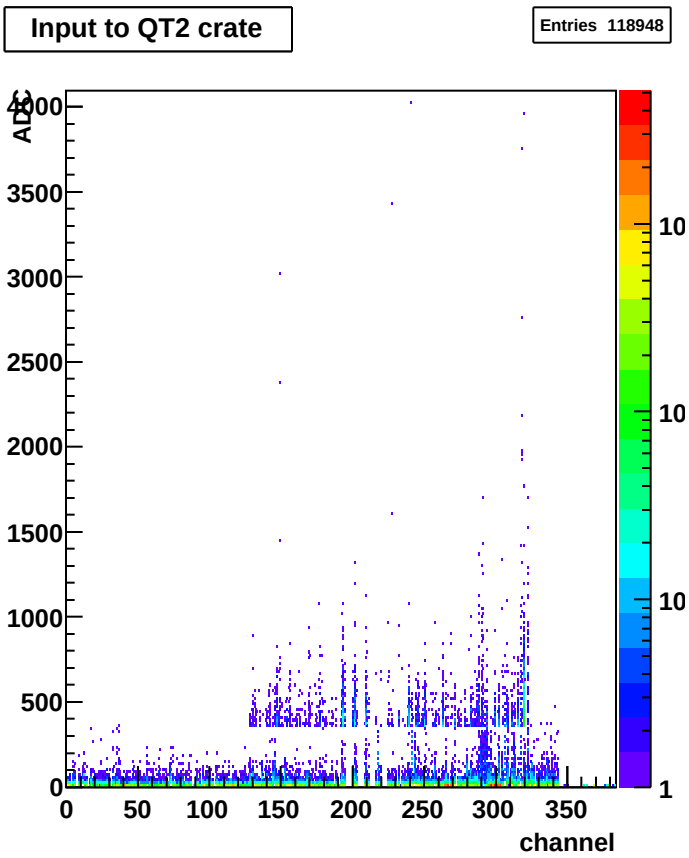

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 10637
Mean 4071
RMS 418.6

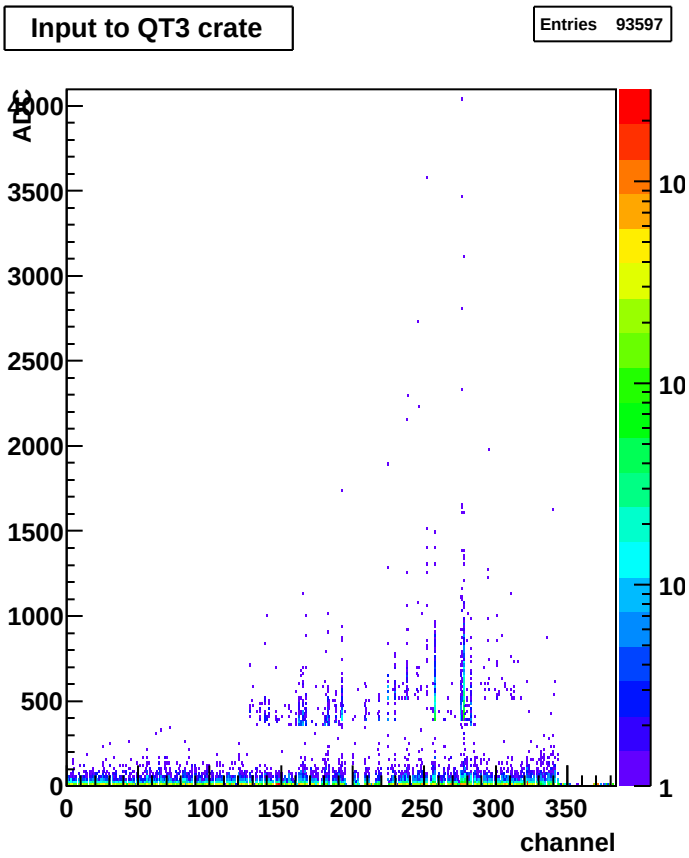

VPD-TAC-E
Entries 5710
Mean 1332
RMS 170.8

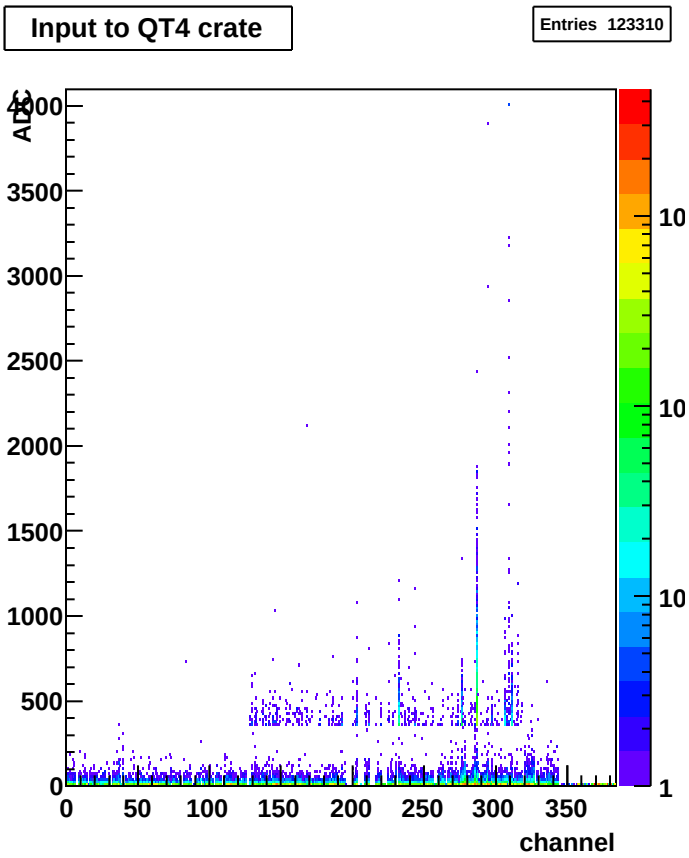

VPD-TAC-W
Entries 5091
Mean 1416
RMS 192.1

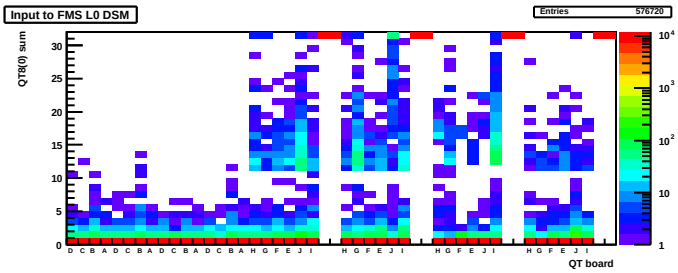

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 2618
Mean 4007
RMS 229.2

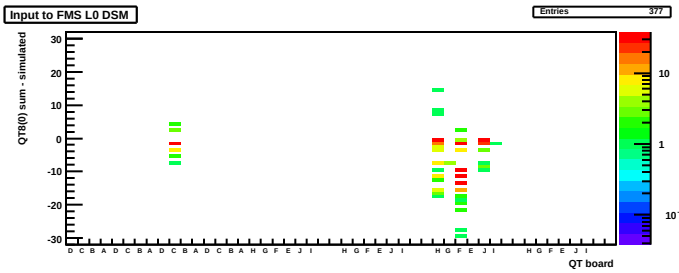

Input to QT1 crate
Entries 145304
ADC
channel

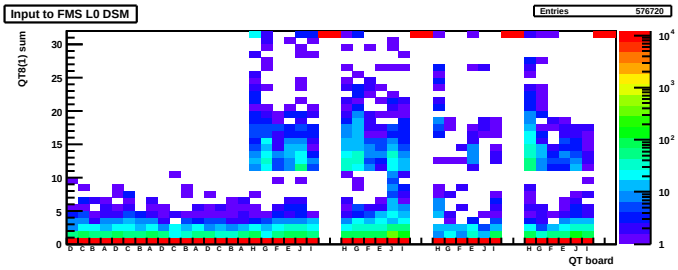

Input to QT2 crate
Entries 118948
ADC
channel

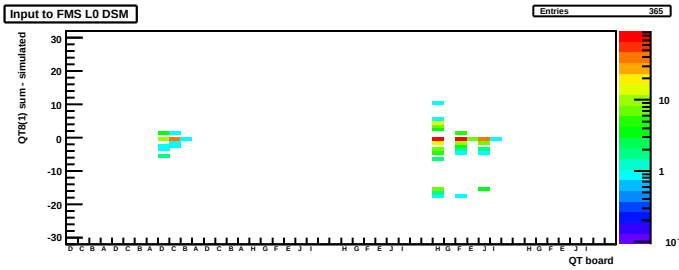

Input to QT3 crate
Entries 93597
ADC
channel

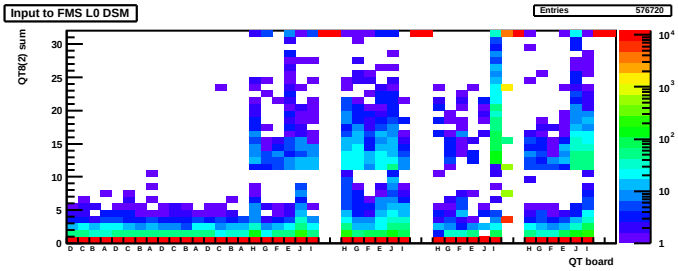

Input to QT4 crate
Entries 123310
ADC
channel

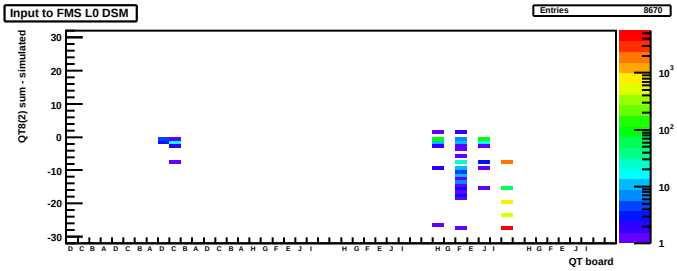

Input to FMS L0 DSM
Entries 576720
QT8(0) sum
QT board

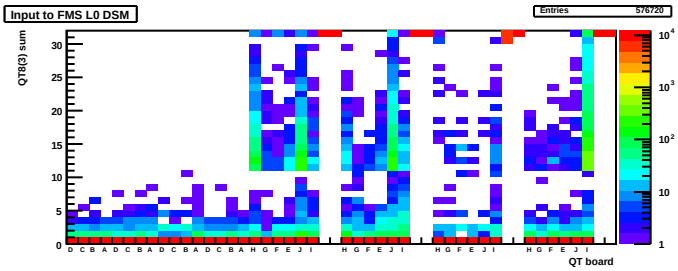

Input to FMS L0 DSM
Entries 377
QT8(0) sum - simulated
QT board

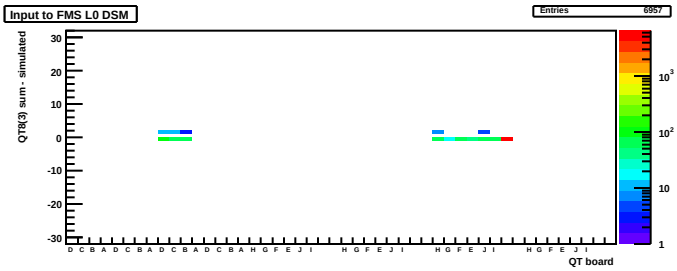

Input to FMS L0 DSM
Entries 576720
QT8(1) sum
QT board

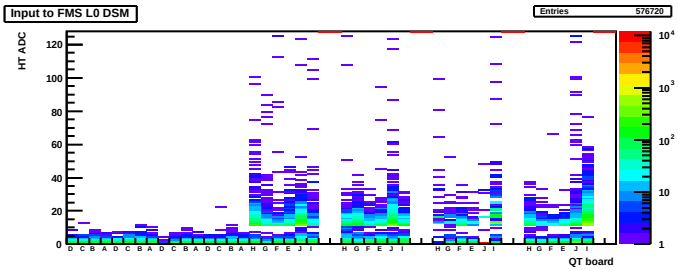

Input to FMS L0 DSM
Entries 365
QT8(1) sum - simulated
QT board

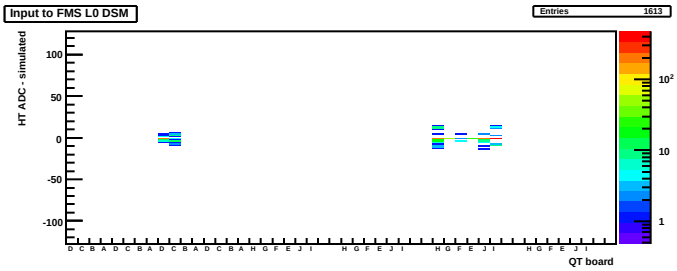

Input to FMS L0 DSM
Entries 576720
QT8(2) sum
QT board

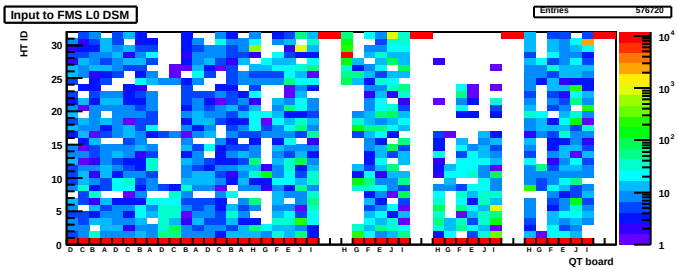

Input to FMS L0 DSM
Entries 8670
QT8(2) sum - simulated
QT board

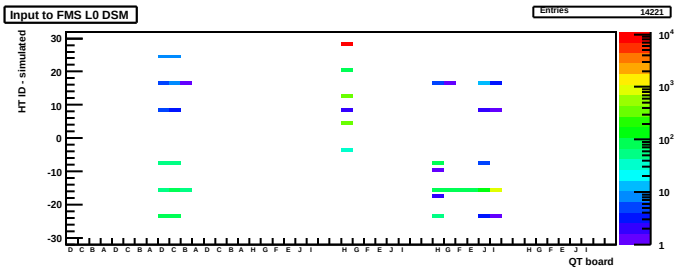

Input to FMS L0 DSM
Entries 576720
QT8(3) sum
QT board

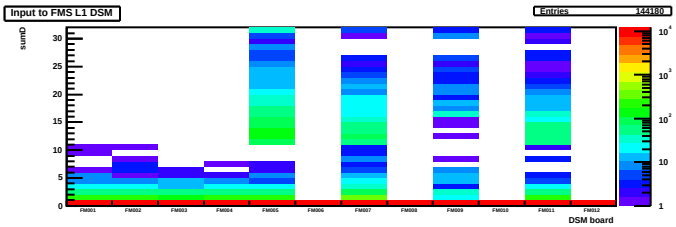

Input to FMS L0 DSM
Entries 6957
QT8(3) sum - simulated
QT board

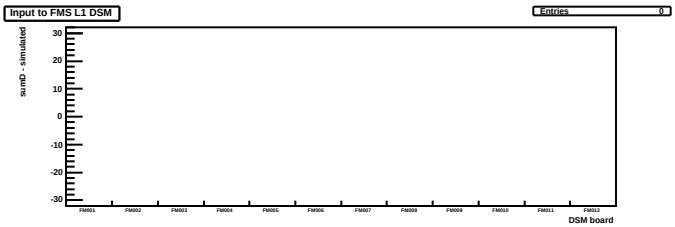

Input to FMS L0 DSM
Entries 576720
HT ADC
QT board

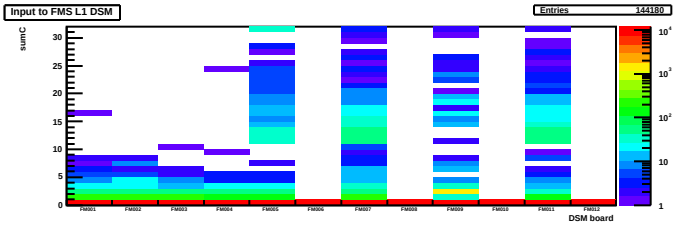

Input to FMS L0 DSM
Entries 1613
HT ADC - simulated
QT board

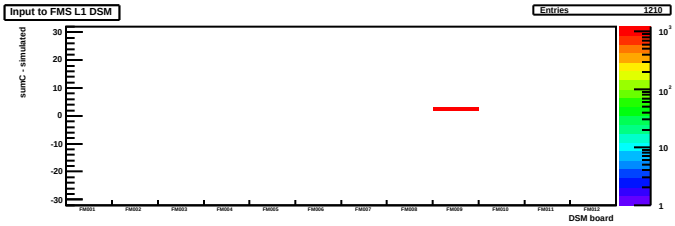

Input to FMS L0 DSM
Entries 576720
HT ID
QT board

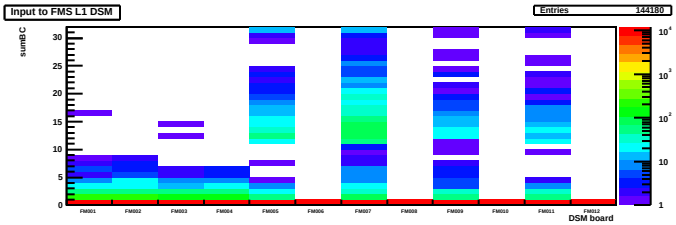

Input to FMS L0 DSM
Entries 14221
HT ID - simulated
QT board

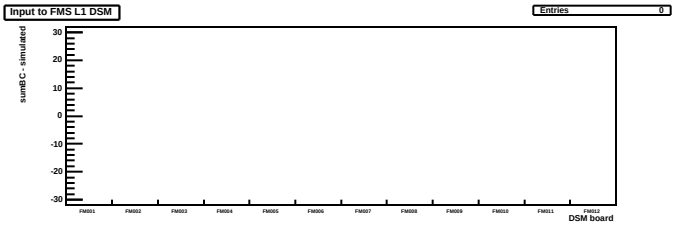

Input to FMS L1 DSM
Entries 144180
sumD
DSM board

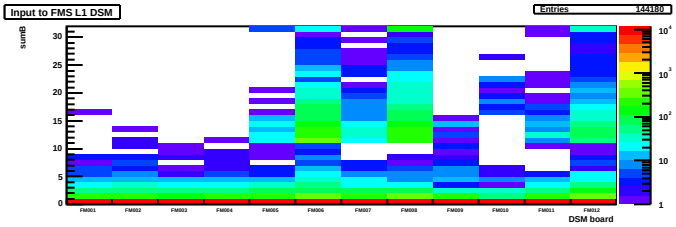

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

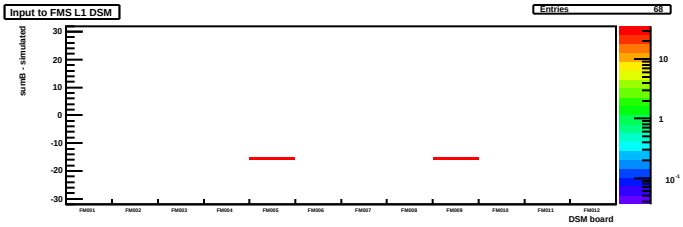

Input to FMS L1 DSM
Entries 144180
sumC
DSM board

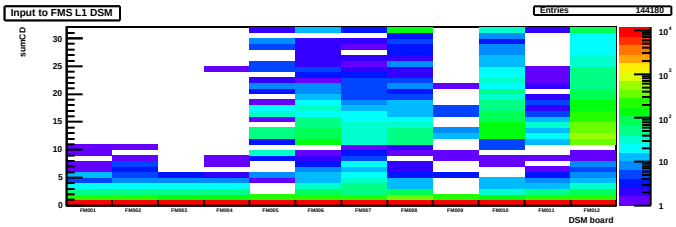

Input to FMS L1 DSM
Entries 1210
sumC - simulated
DSM board

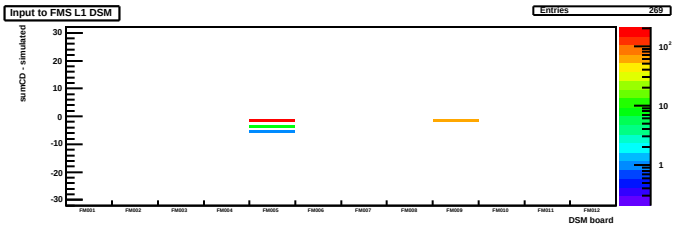

Input to FMS L1 DSM
Entries 144180
sumBC
DSM board

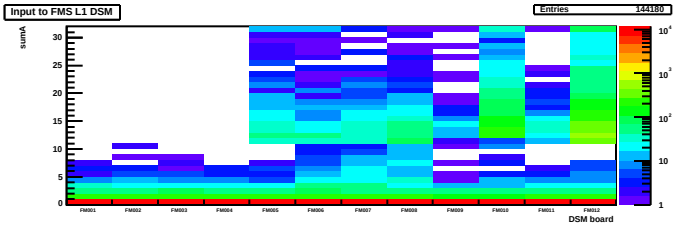

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

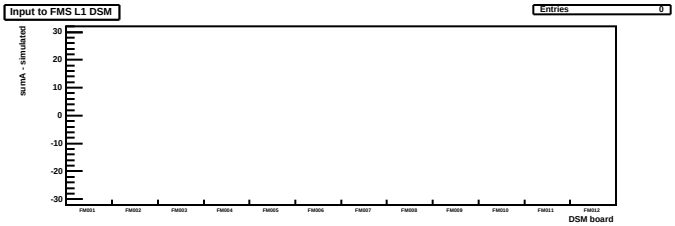

Input to FMS L1 DSM
Entries 144180
sumB
DSM board

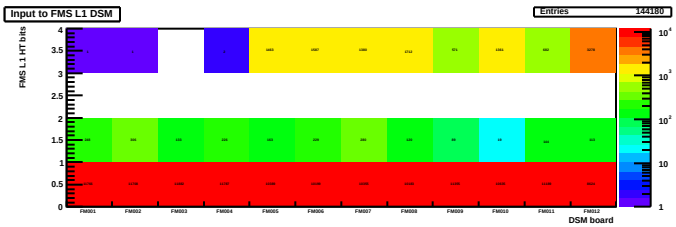

Input to FMS L1 DSM
Entries 68
sumB - simulated
DSM board

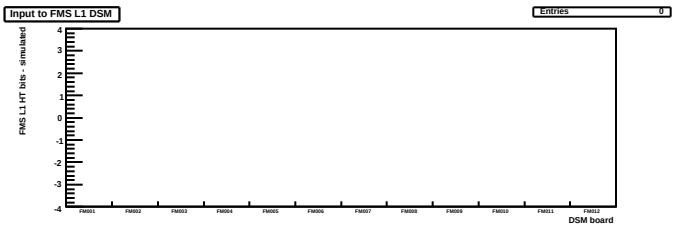

Input to FMS L1 DSM
Entries 144180
sumCD
DSM board

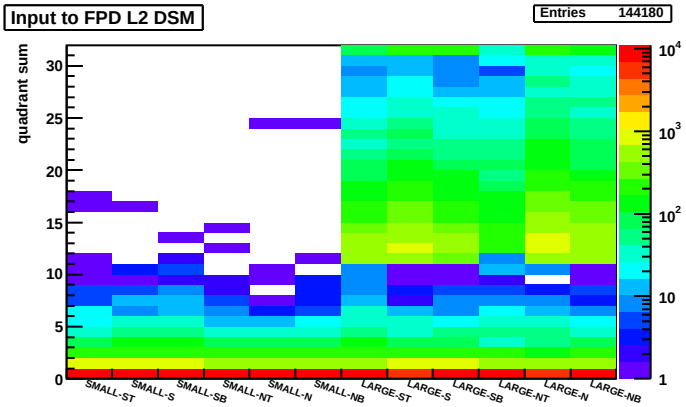

Input to FMS L1 DSM
Entries 269
sumCD - simulated
DSM board

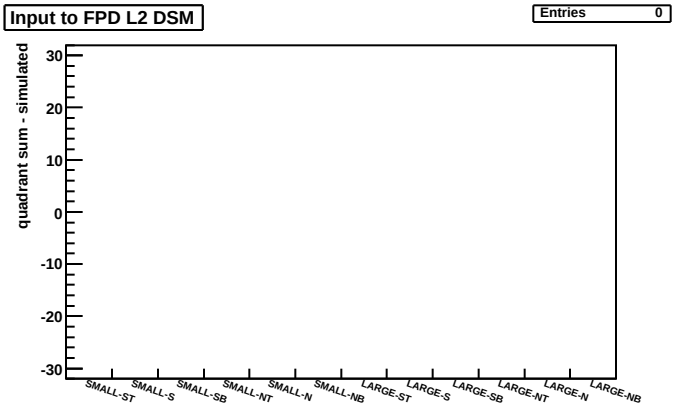

Input to FMS L1 DSM
Entries 144180
sumA
DSM board

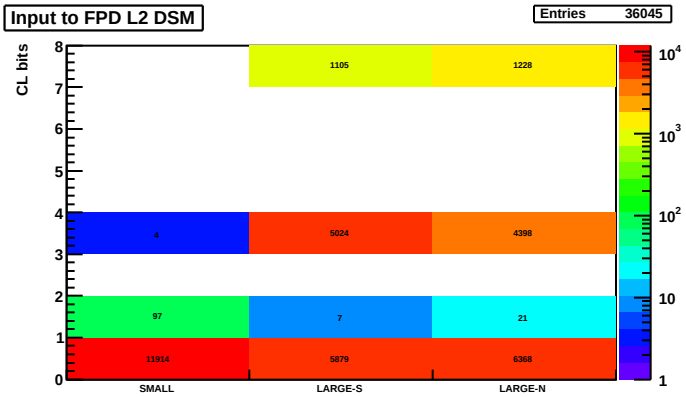

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

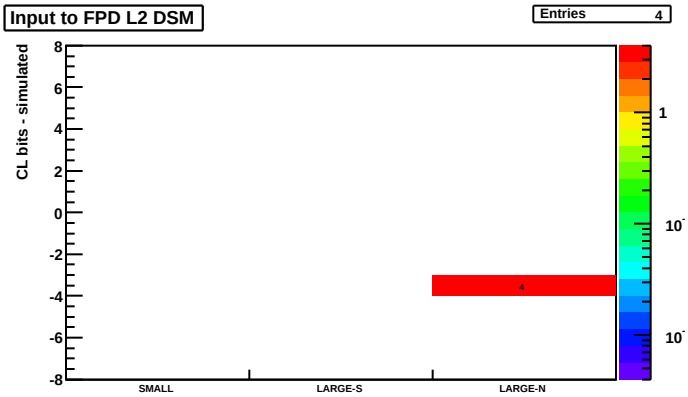

Input to FMS L1 DSM
Entries 144180
FMS L1 HT bits
DSM board

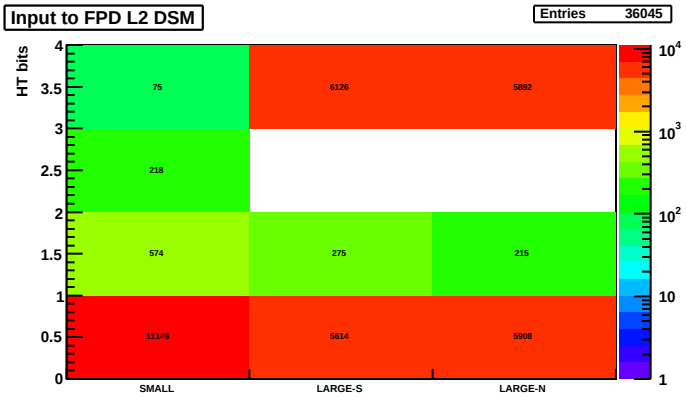

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

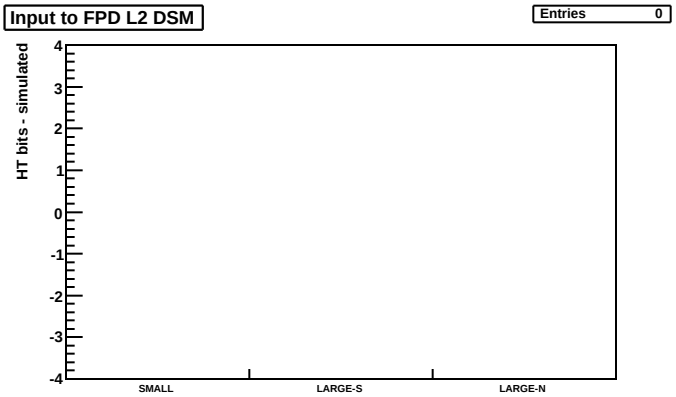

Input to FPD L2 DSM
Entries 144180
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

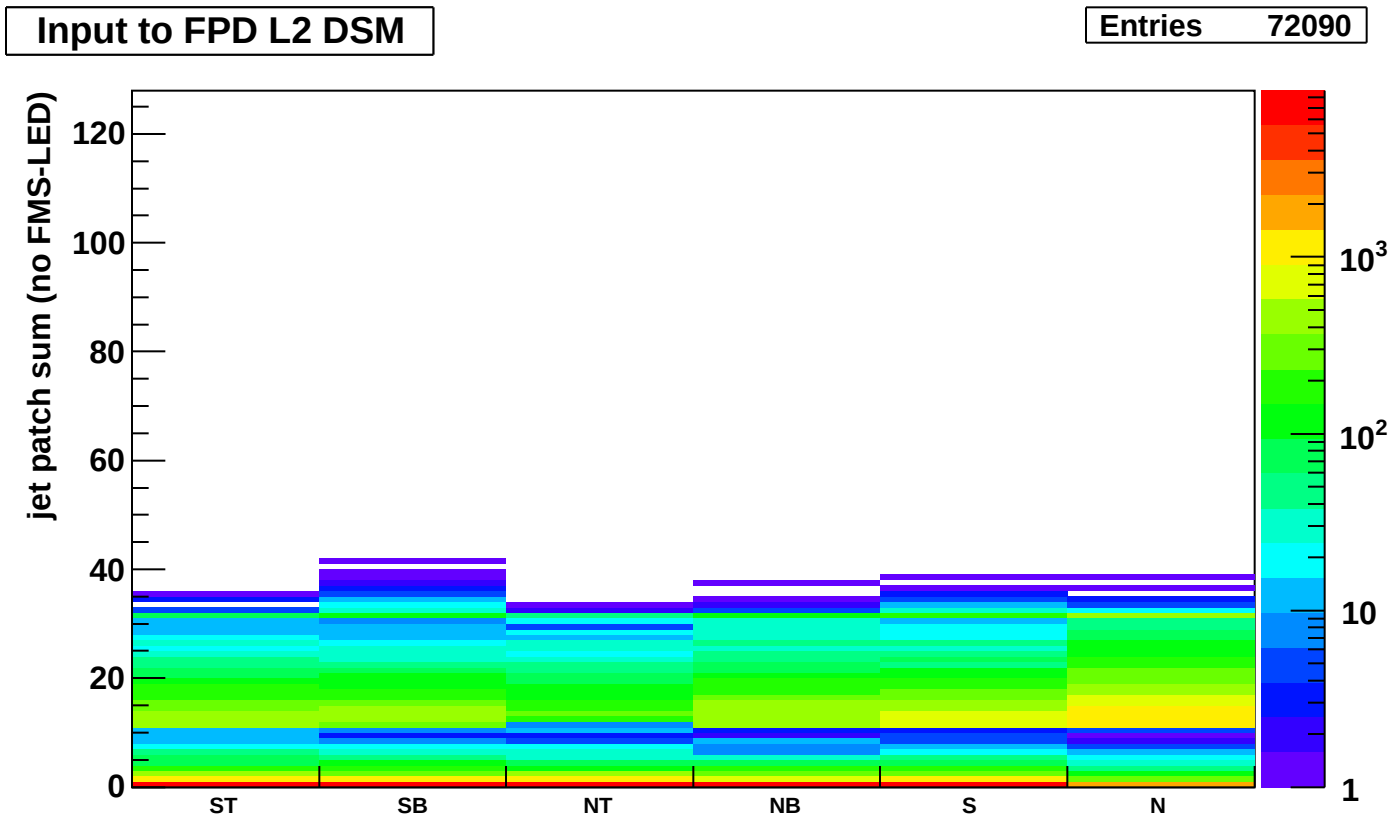

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

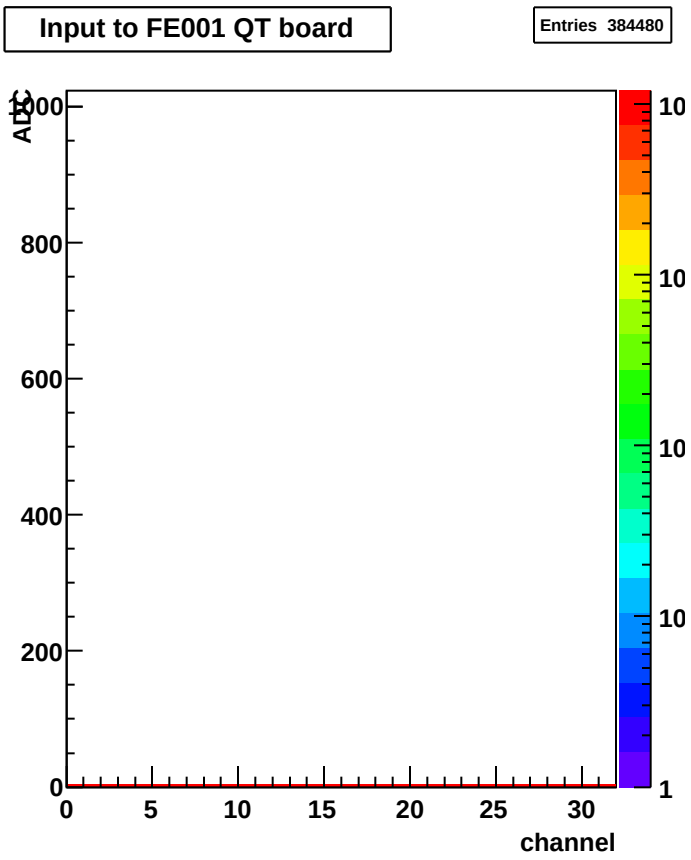

Input to FPD L2 DSM
Entries 36045
CL bits
1105
1228
4
5024
4398
97
7
21
11914
5879
6368
SMALL
LARGE-S
LARGE-N

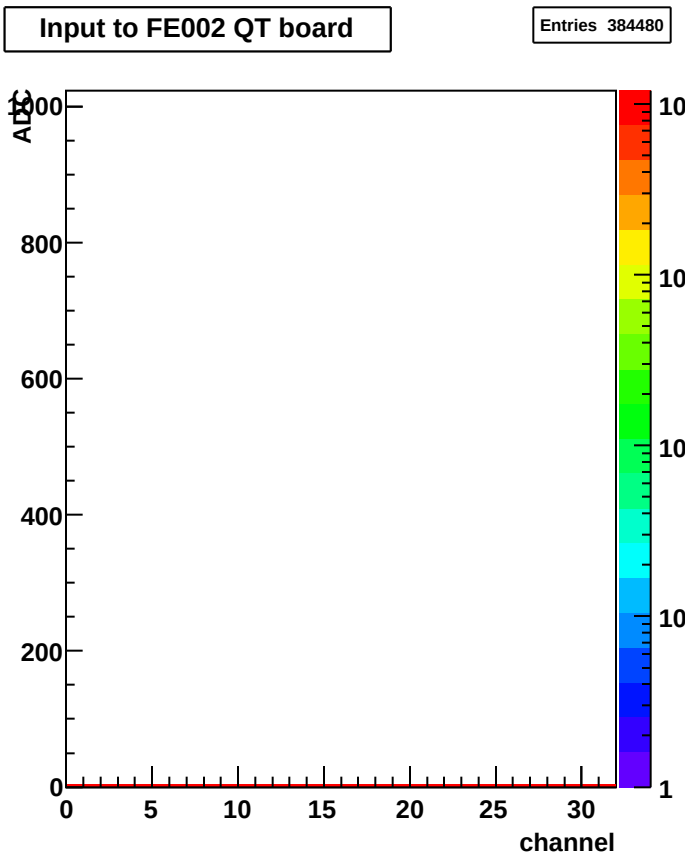

Input to FPD L2 DSM
Entries 4
CL bits - simulated
4
SMALL
LARGE-S
LARGE-N

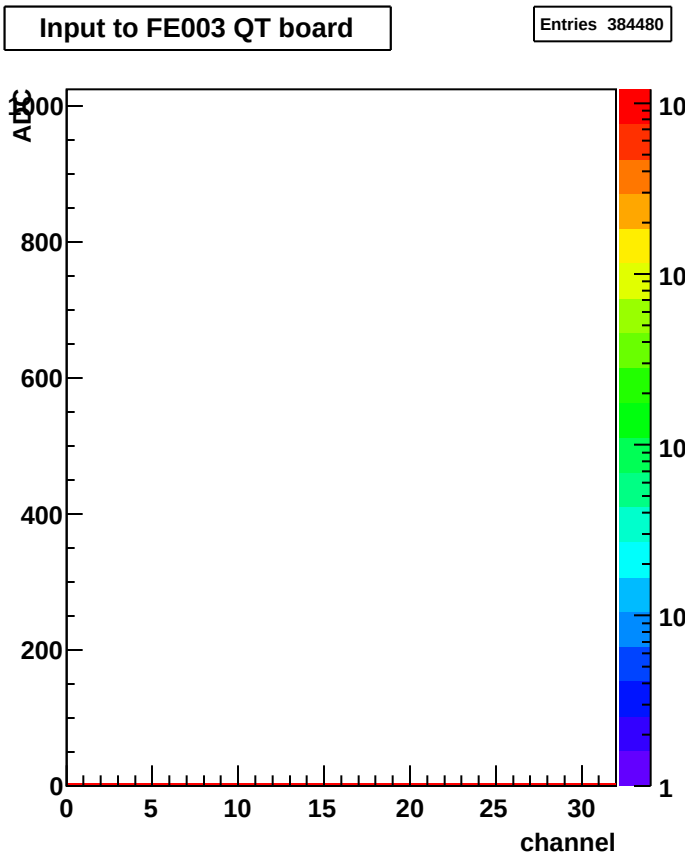

Input to FPD L2 DSM
Entries 36045
HT bits
75
6126
5892
218
574
275
215
11148
5614
5908
SMALL
LARGE-S
LARGE-N

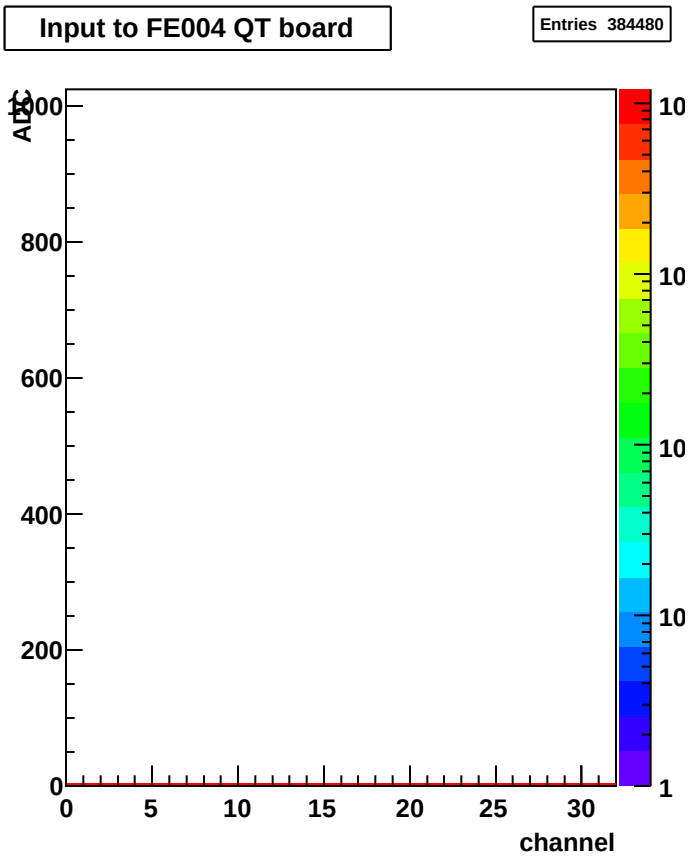

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

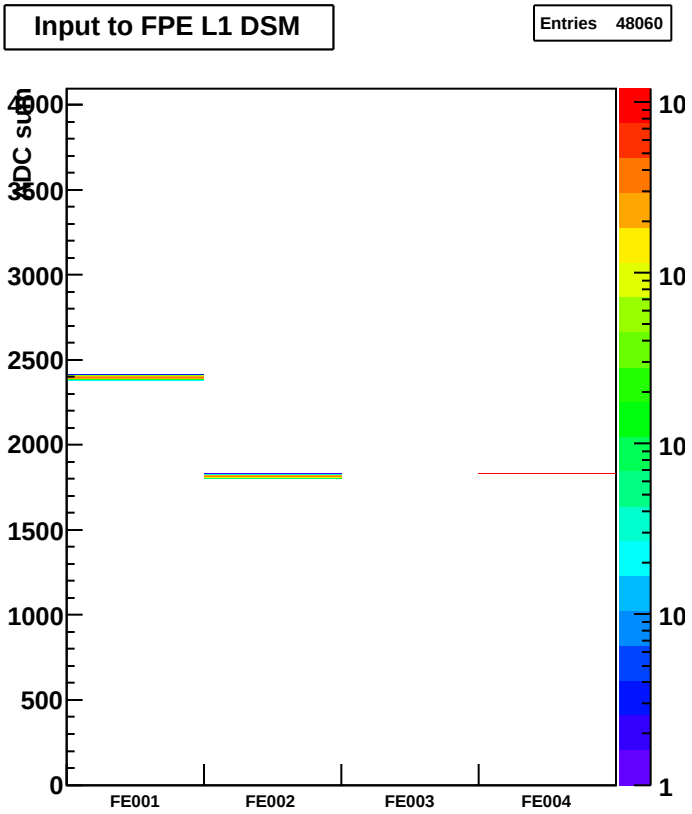

Input to FPD L2 DSM
Entries 72090
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3

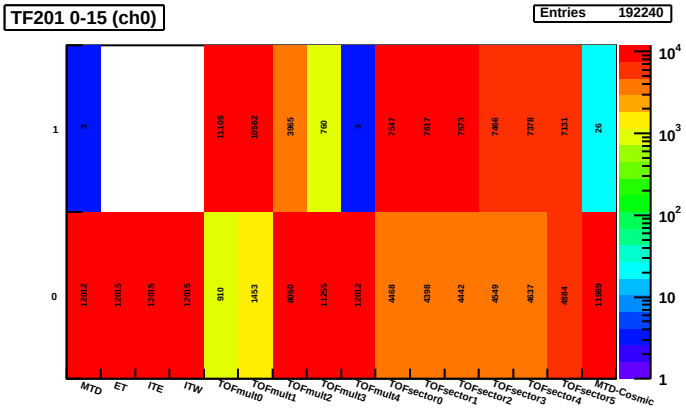

Input to FE001 QT board
Entries 384480
ADC
channel

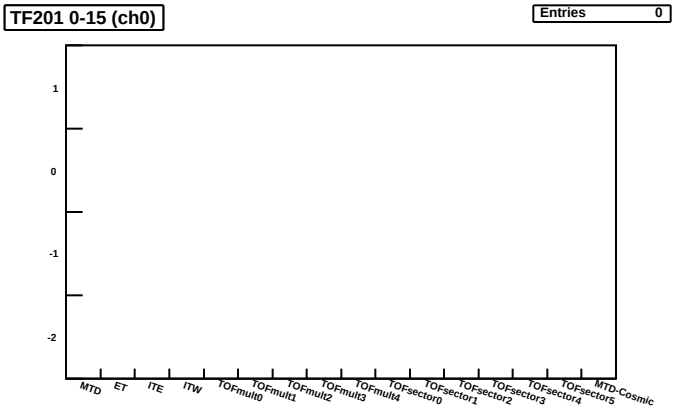

Input to FE002 QT board
Entries 384480
ADC
channel

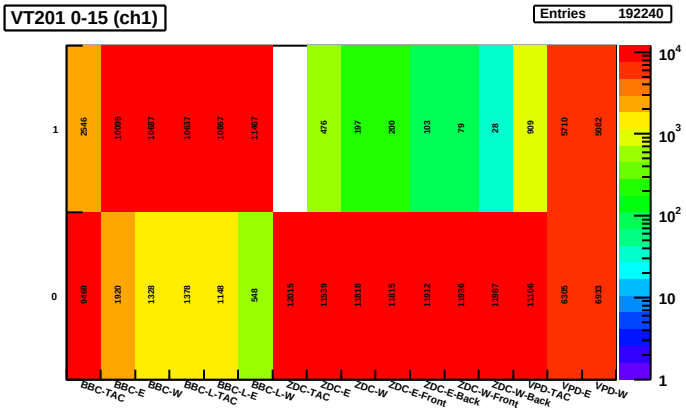

Input to FE003 QT board
Entries 384480
ADC
channel

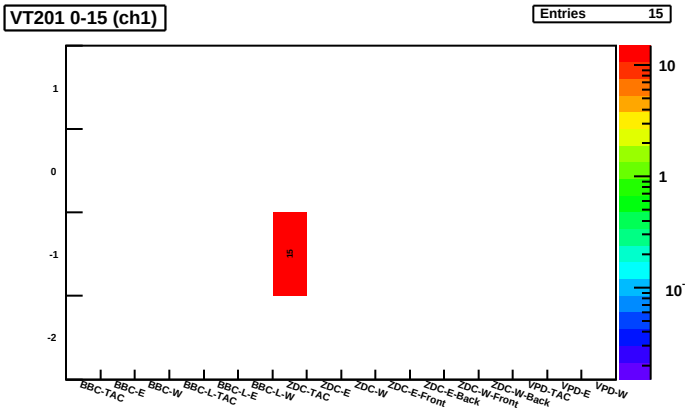

Input to FE004 QT board
Entries 384480
ADC
channel

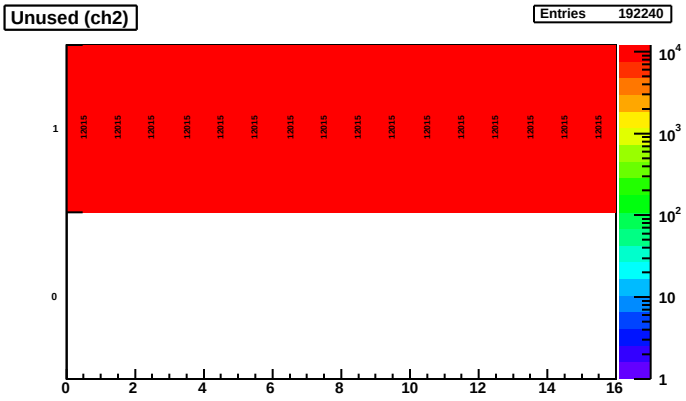

Input to FPE L1 DSM
Entries 48060
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

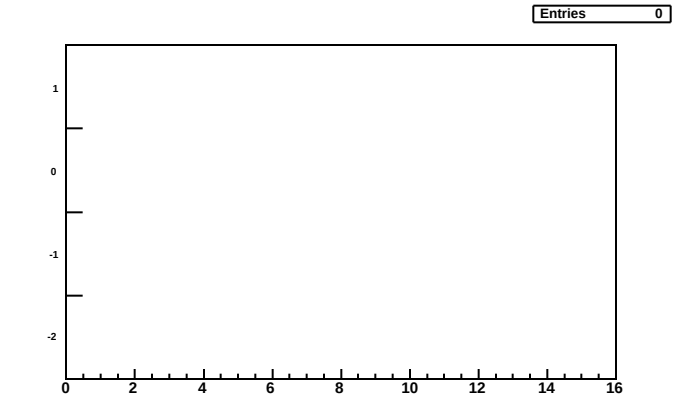

TF201 0-15 (ch0)
Entries 192240
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

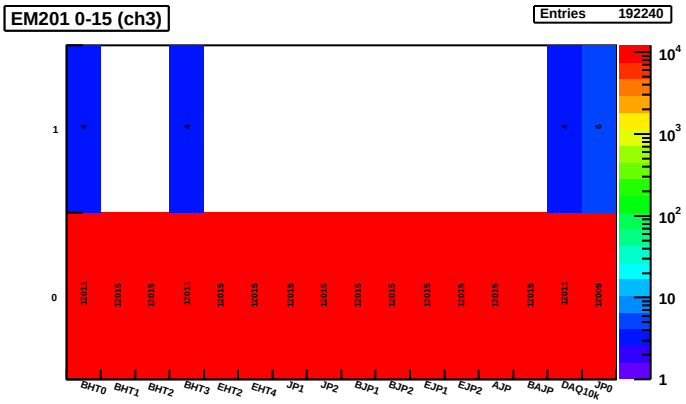

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

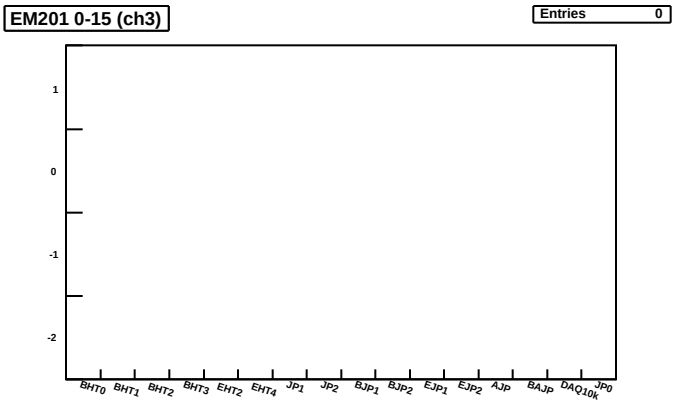

VT201 0-15 (ch1)
Entries 192240
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 15
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

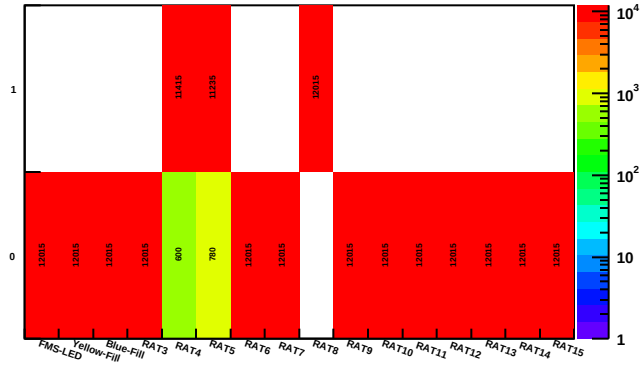

Unused (ch2)
Entries 192240

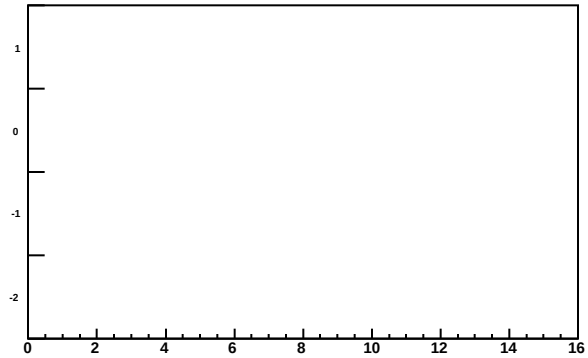

Entries 0


EM201 0-15 (ch3)
Entries 192240
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

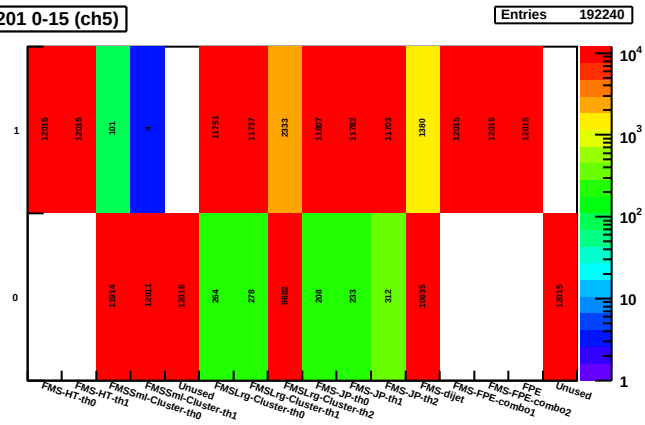

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

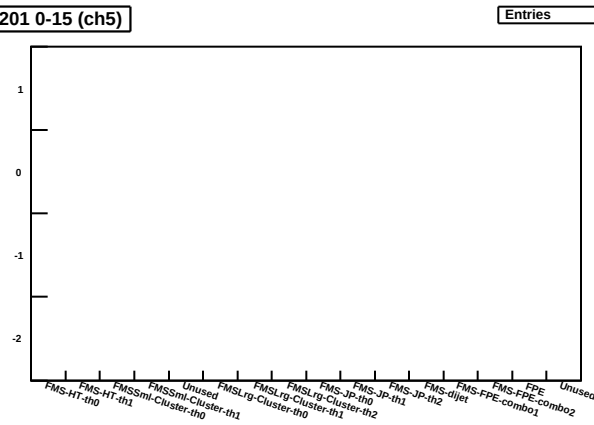




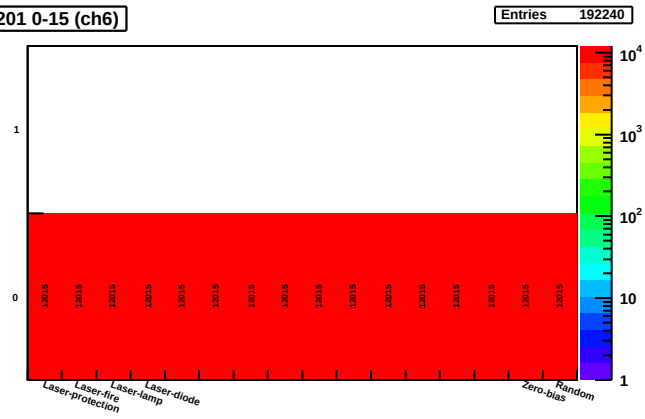
FP201 0-15 (ch5)

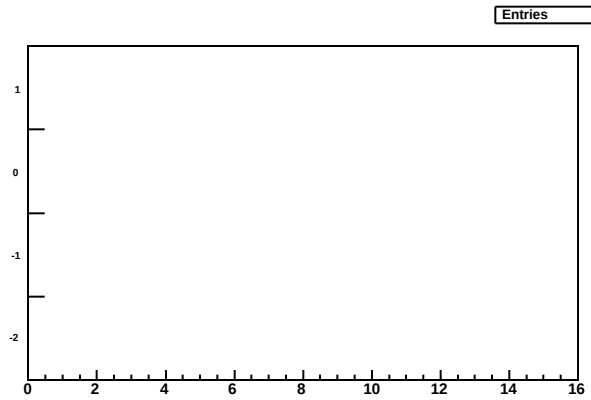
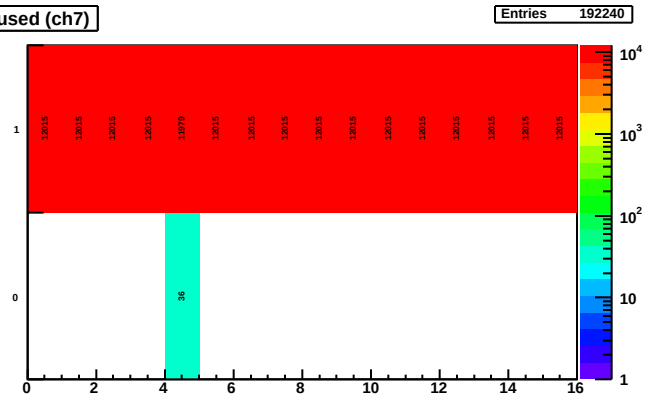
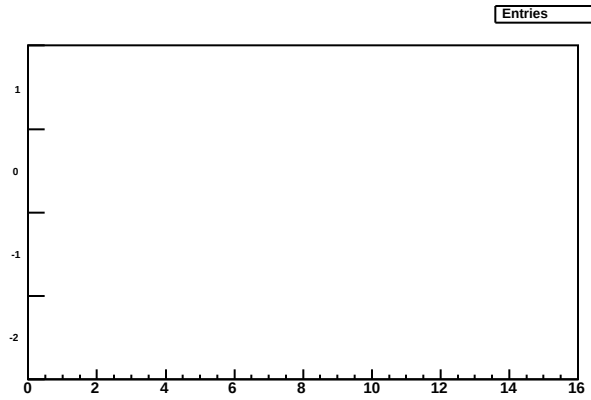


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)