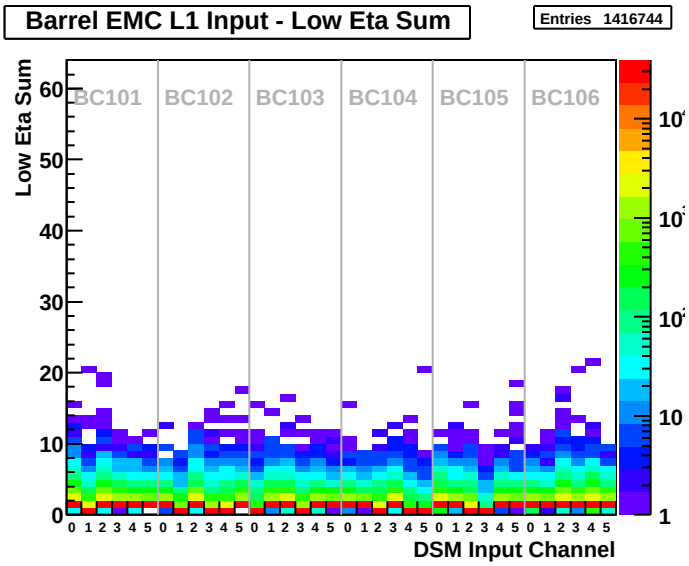
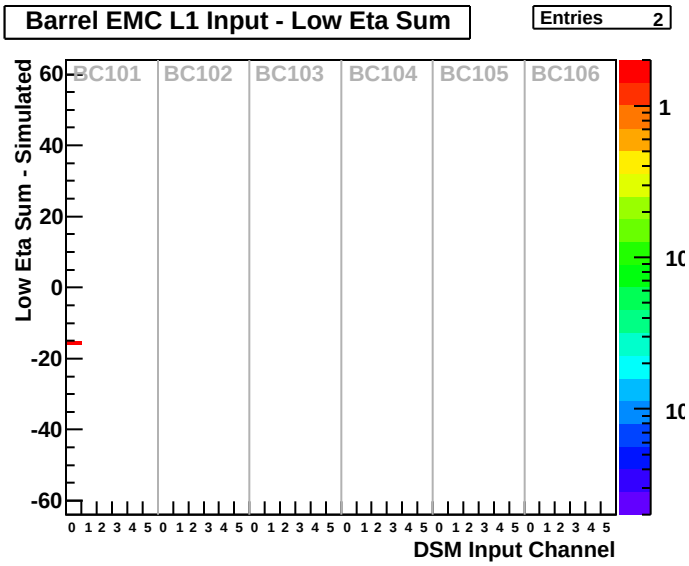
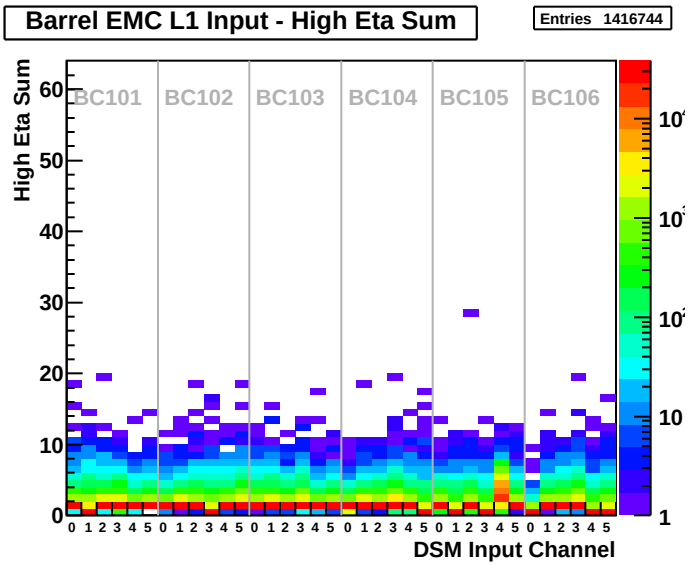
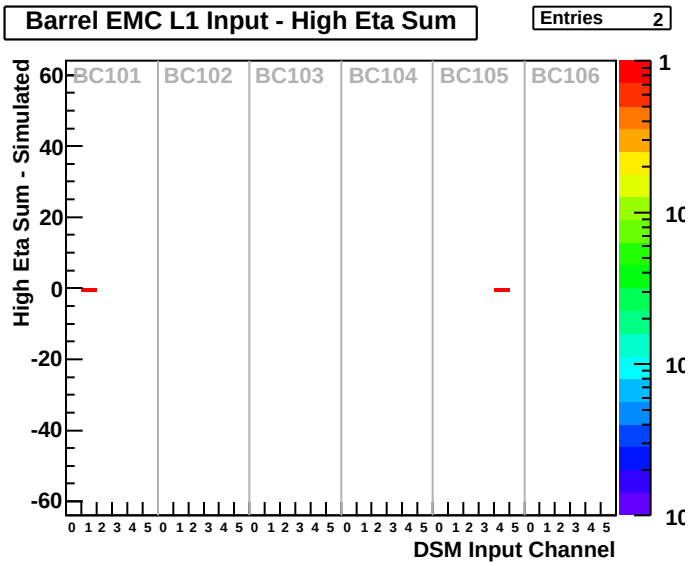
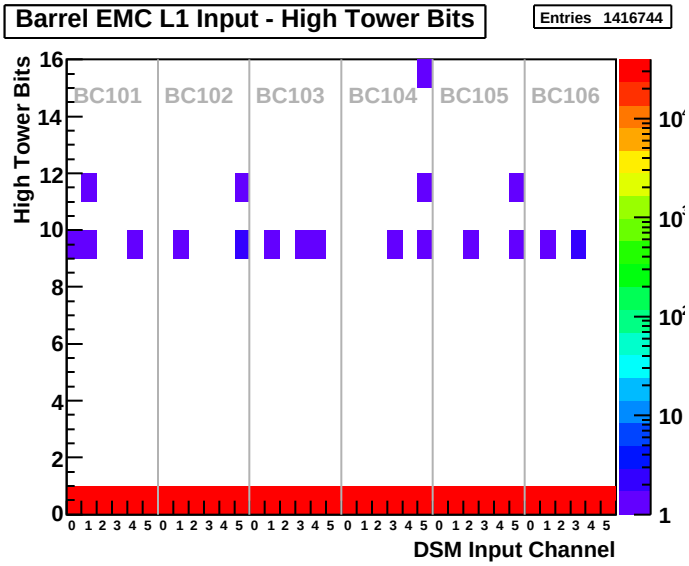
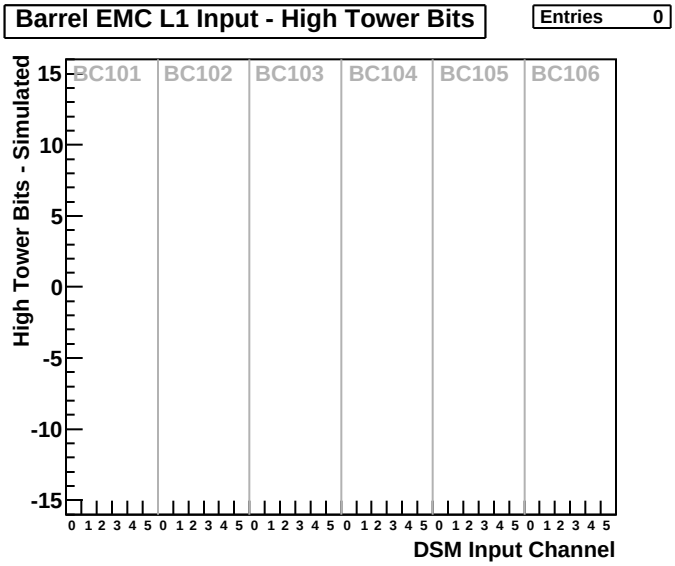

Barrel EMC L1 Input - Low Eta Sum
Entries 1416744
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

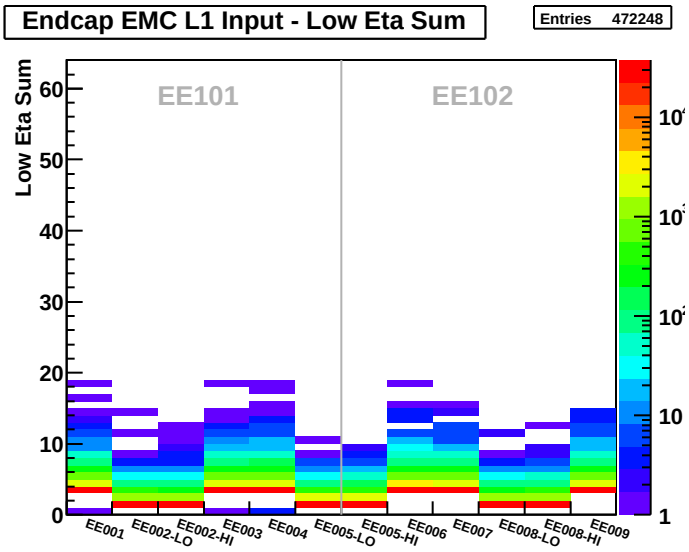

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

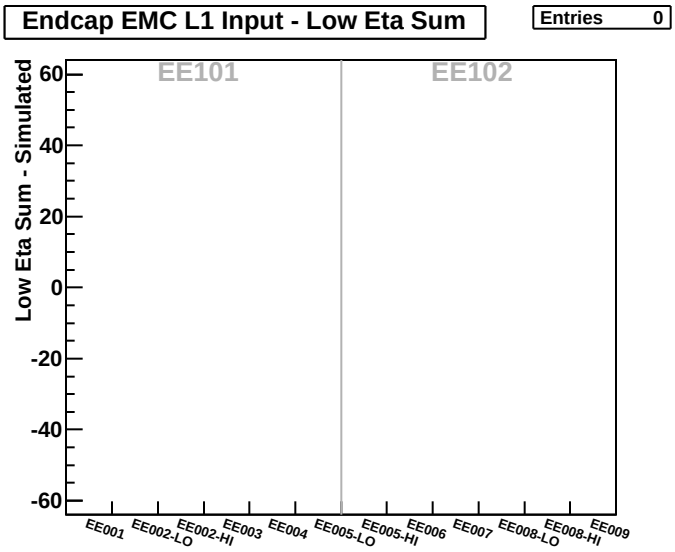

Barrel EMC L1 Input - High Eta Sum
Entries 1416744
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

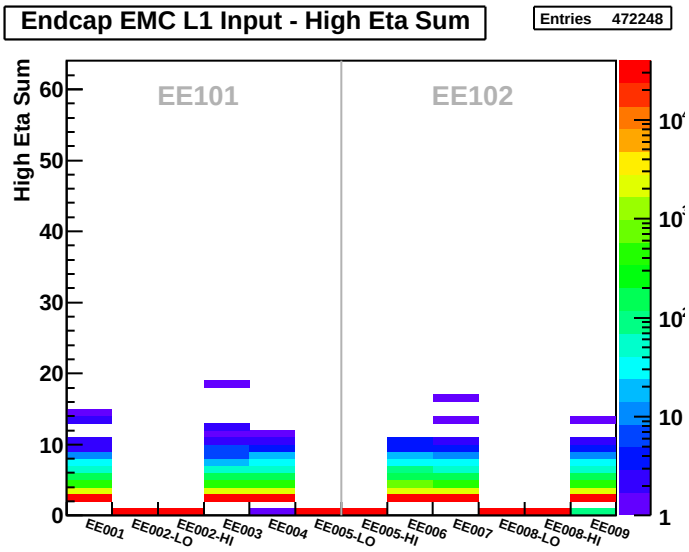

Barrel EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

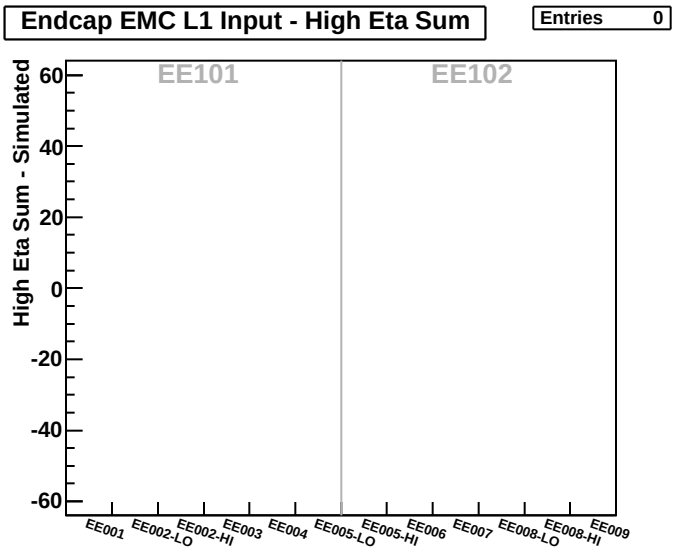

Barrel EMC L1 Input - High Tower Bits
Entries 1416744
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

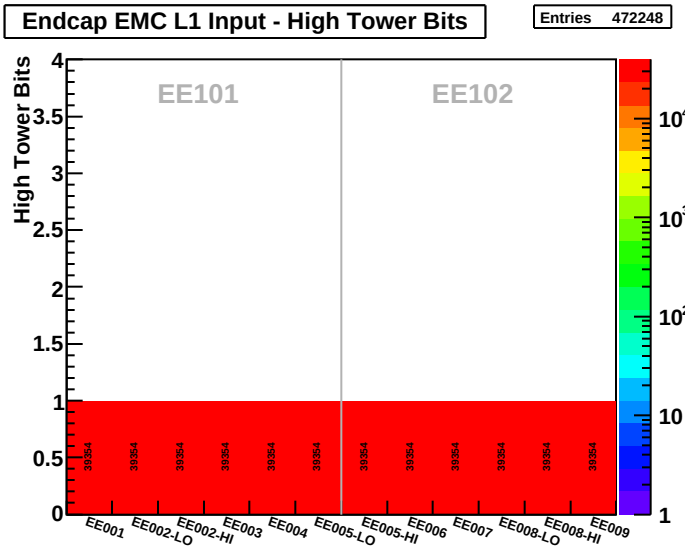

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

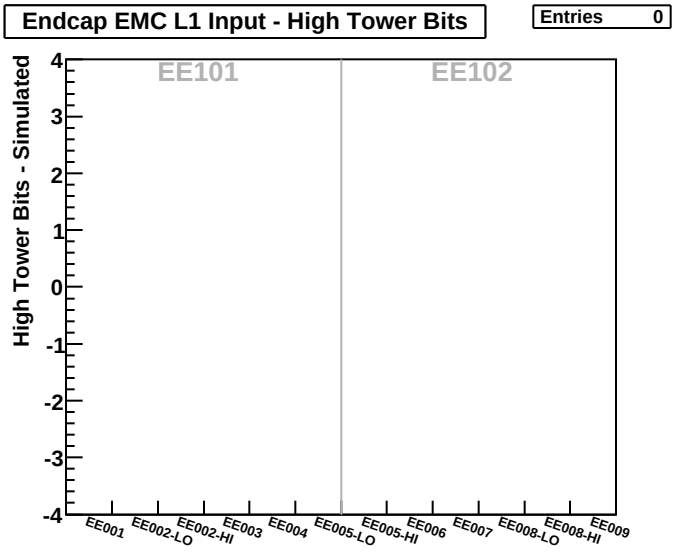

Endcap EMC L1 Input - Low Eta Sum
Entries 472248
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

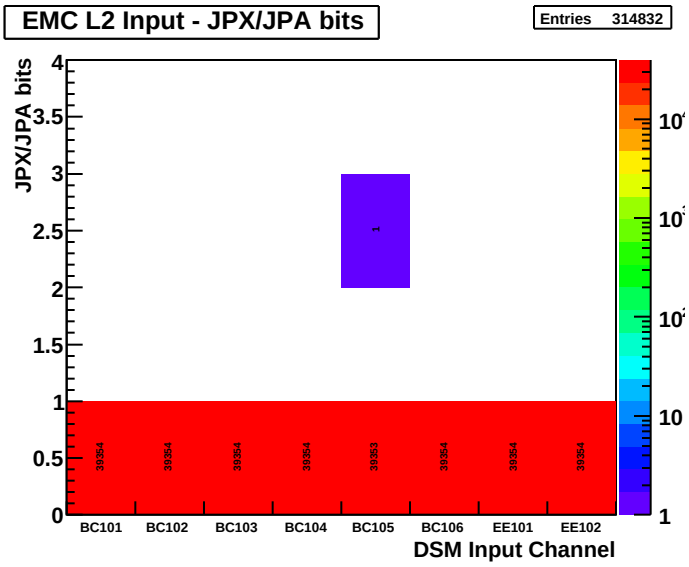

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

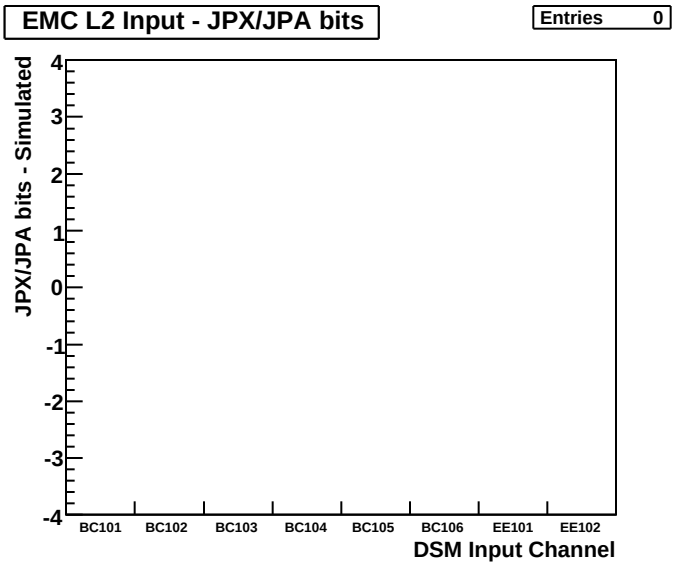

Endcap EMC L1 Input - High Eta Sum
Entries 472248
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

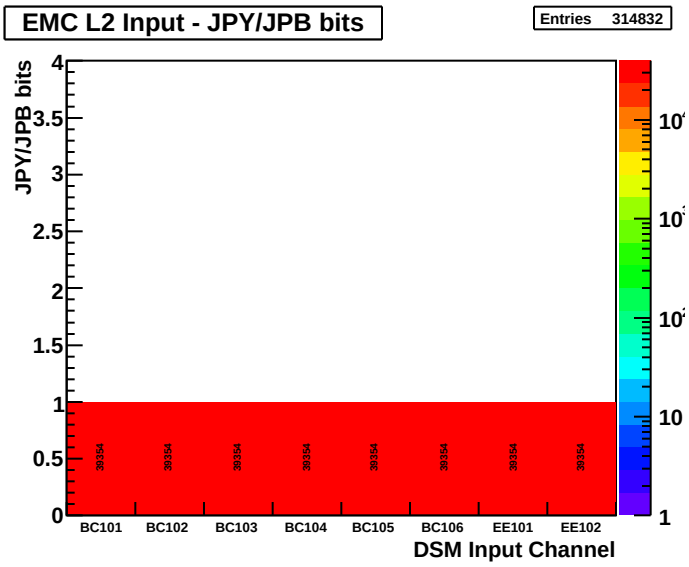

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

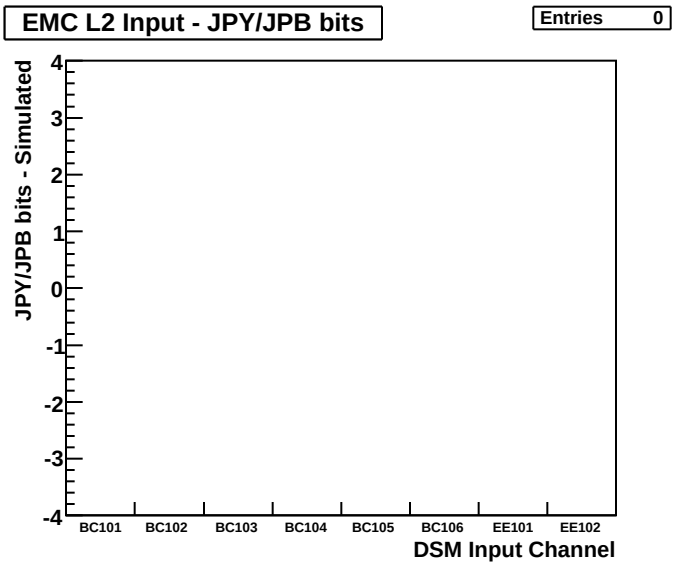

Endcap EMC L1 Input - High Tower Bits
Entries 472248
High Tower Bits
EE101
EE102
39354
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

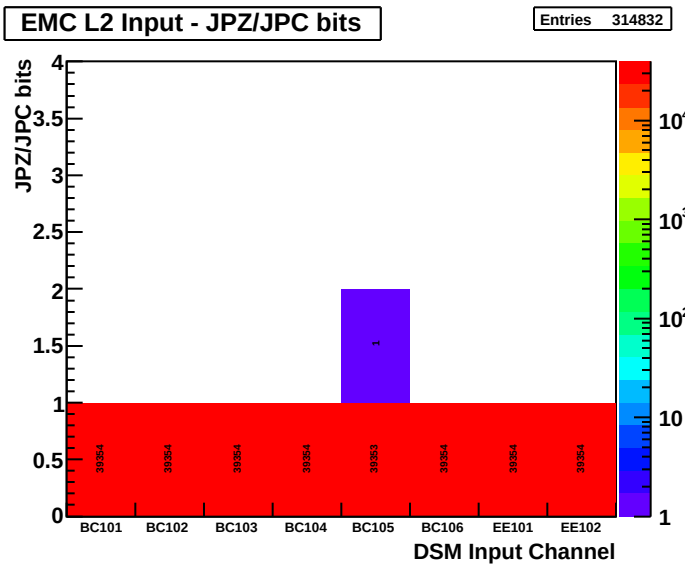

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

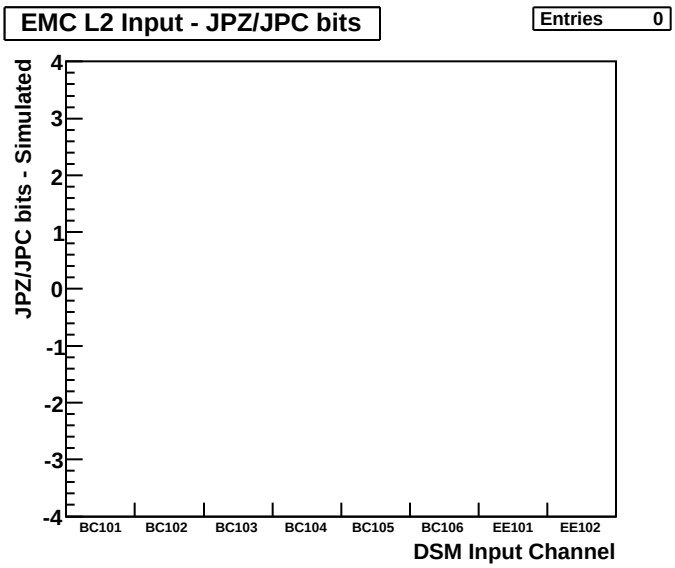

EMC L2 Input - JPX/JPA bits
Entries 314832
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

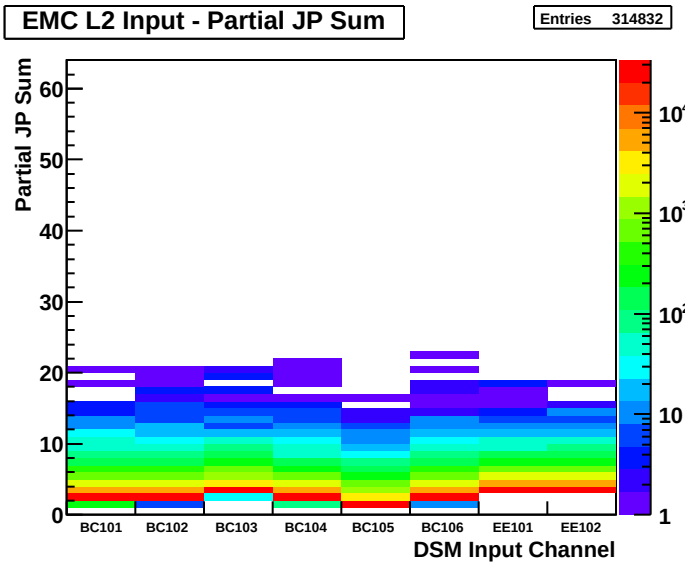

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

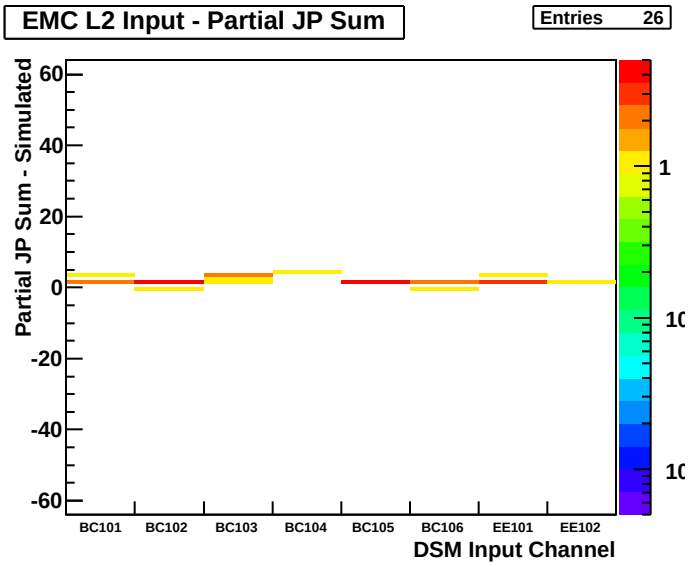

EMC L2 Input - JPY/JPB bits
Entries 314832
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

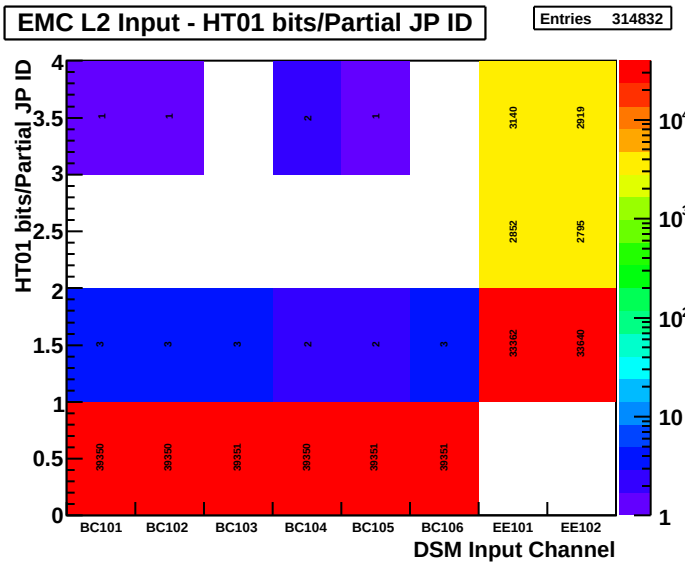

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

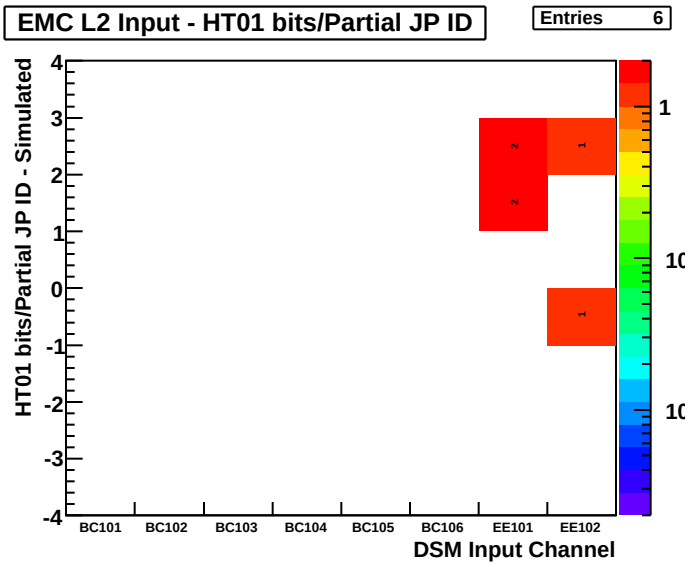

EMC L2 Input - JPZ/JPC bits
Entries 314832
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

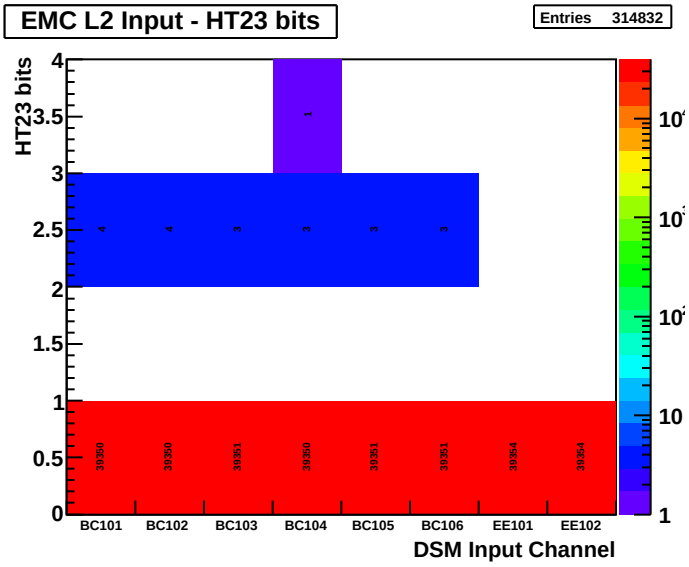

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

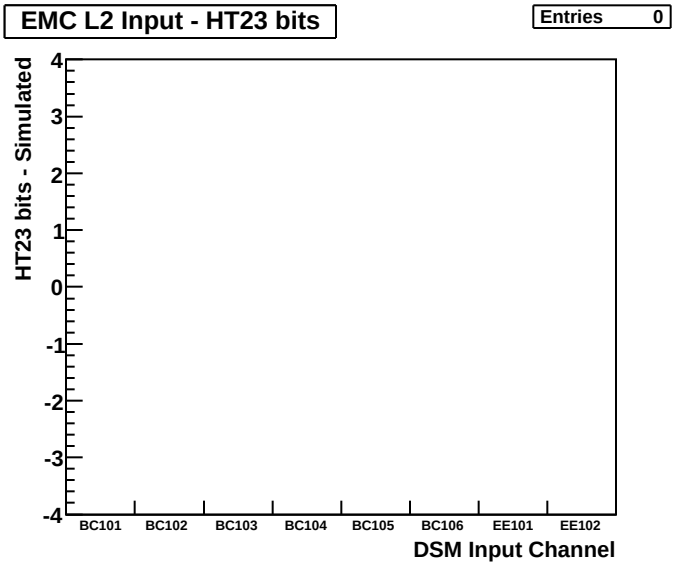

EMC L2 Input - Partial JP Sum
Entries 314832
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

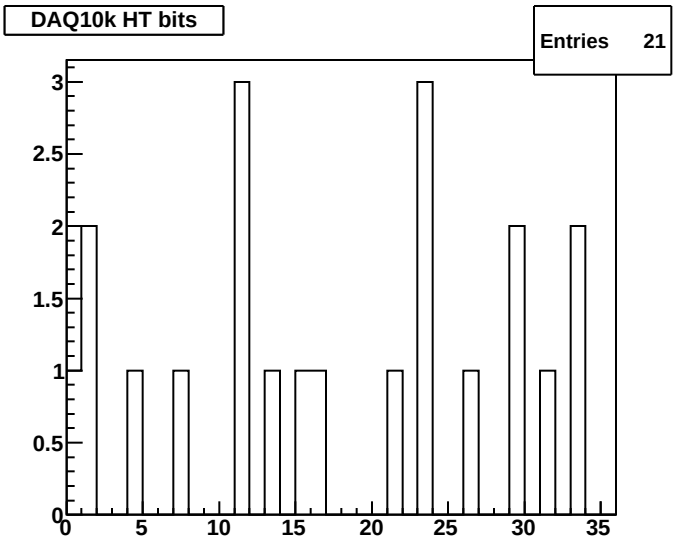

EMC L2 Input - Partial JP Sum
Entries 26
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

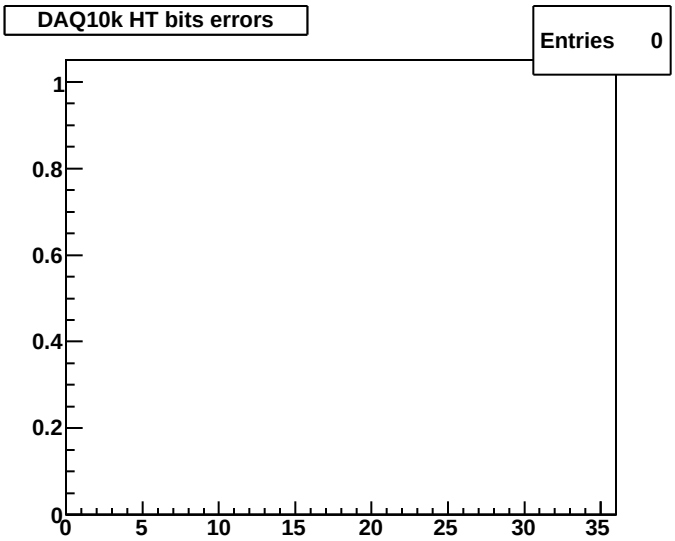

EMC L2 Input - HT01 bits/Partial JP ID
Entries 314832
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

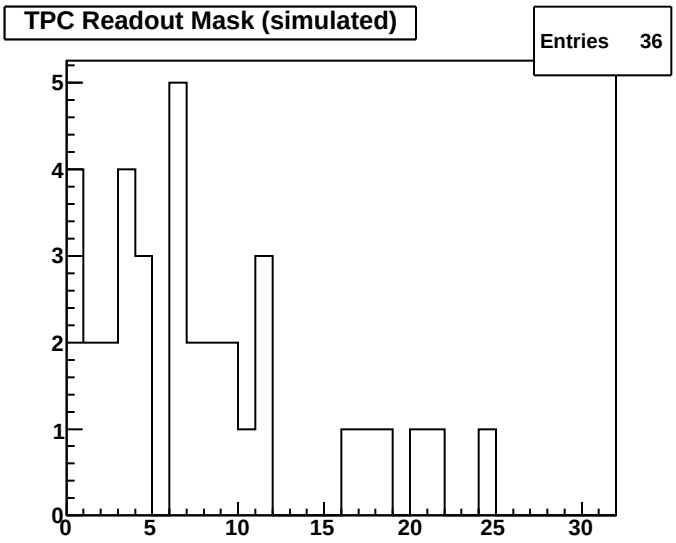

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

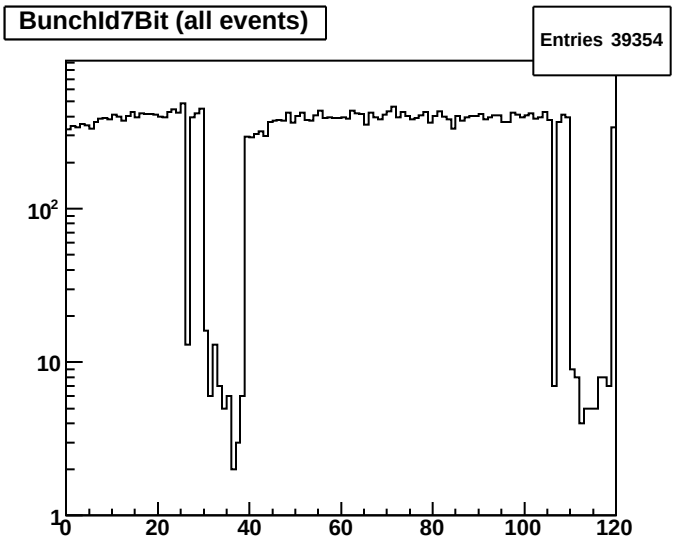

EMC L2 Input - HT23 bits
Entries 314832
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

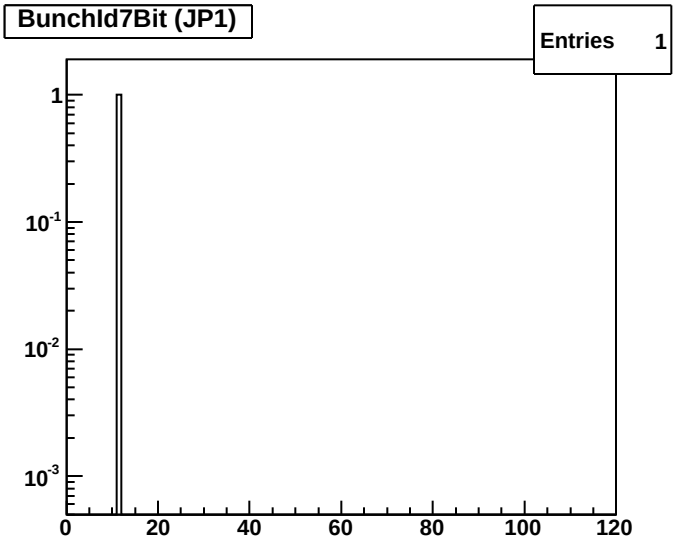

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

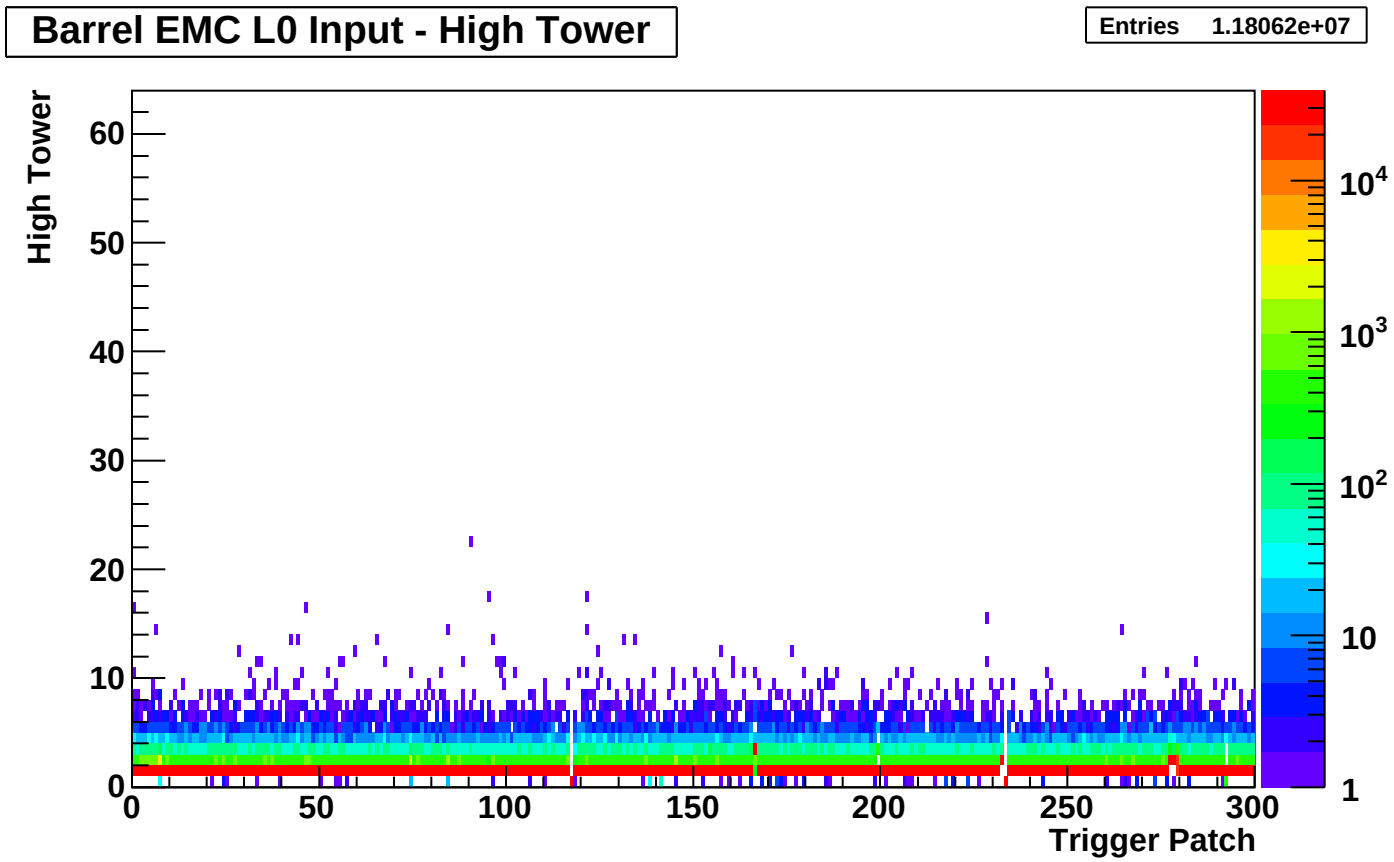

DAQ10k HT bits
Entries 21

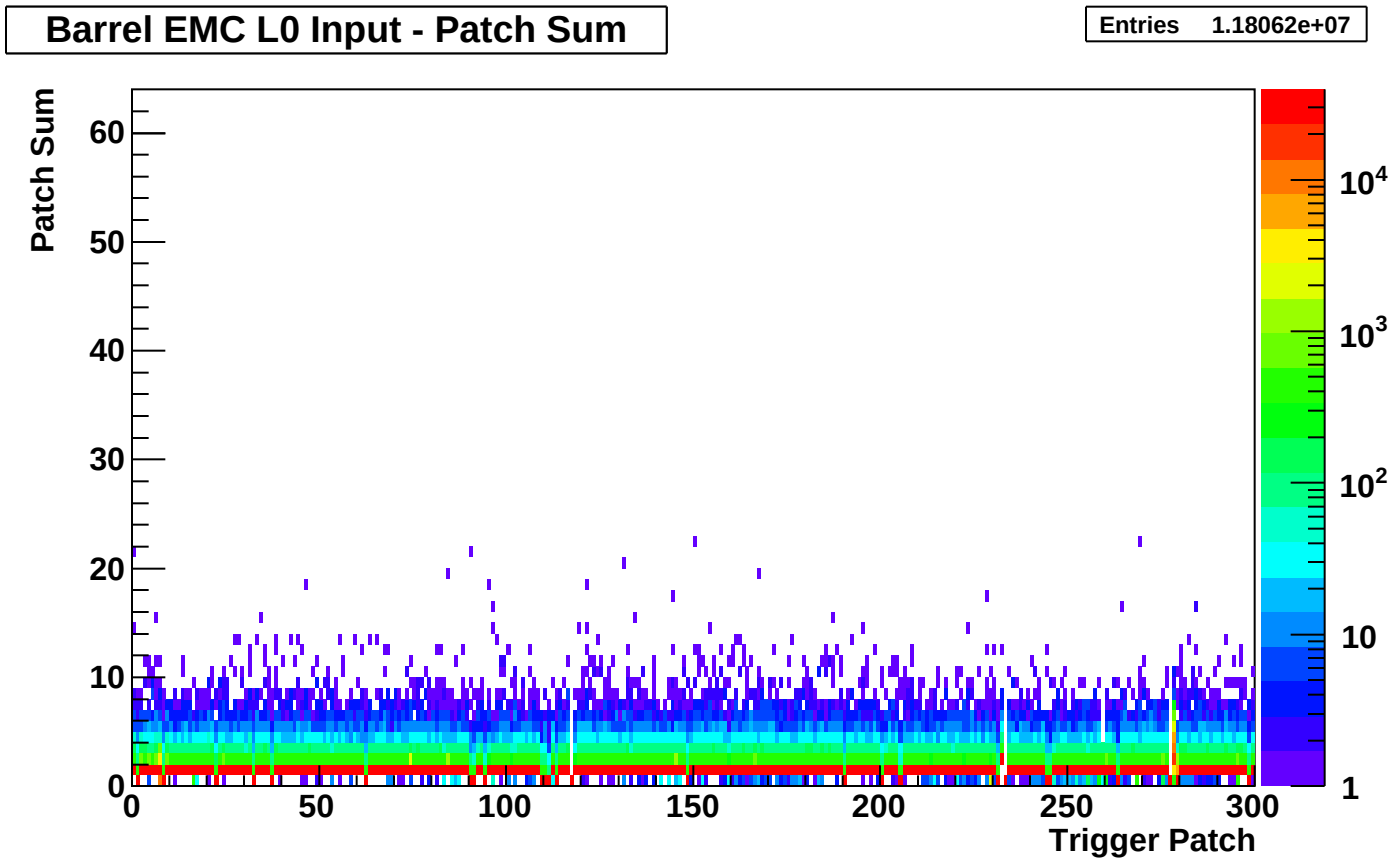

DAQ10k HT bits errors
Entries 0

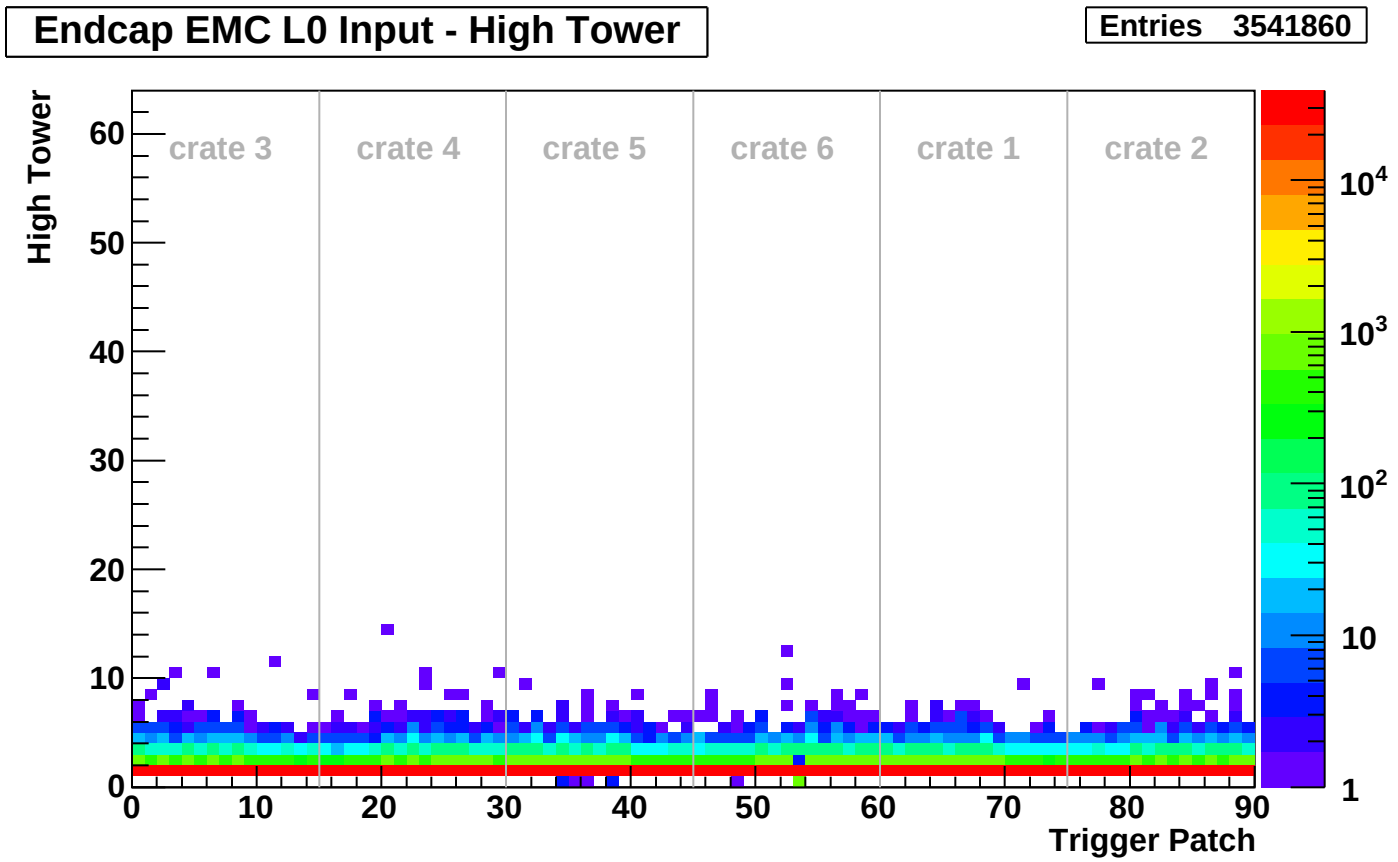

TPC Readout Mask (simulated)
Entries 36

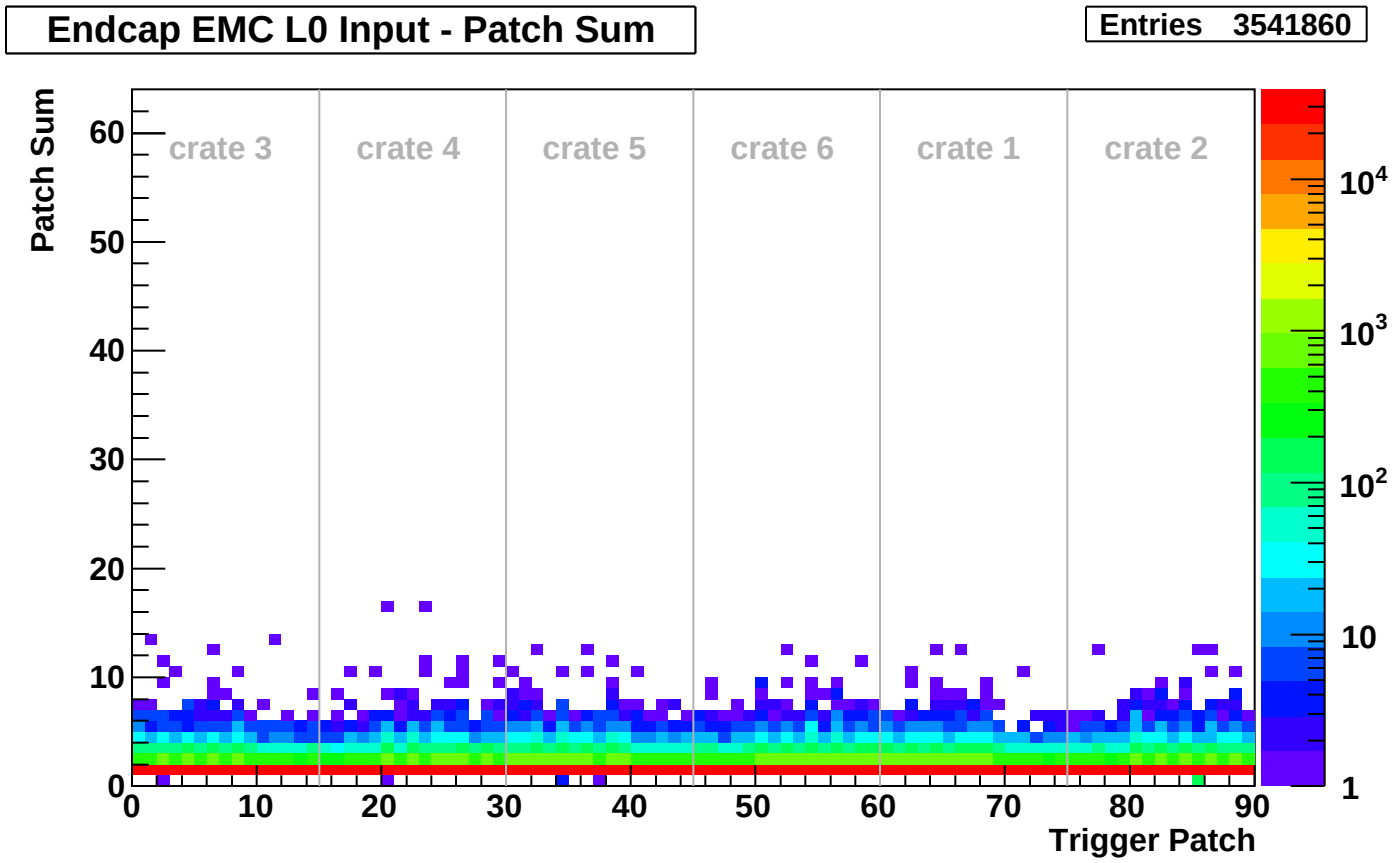

BunchId7Bit (all events)
Entries 39354

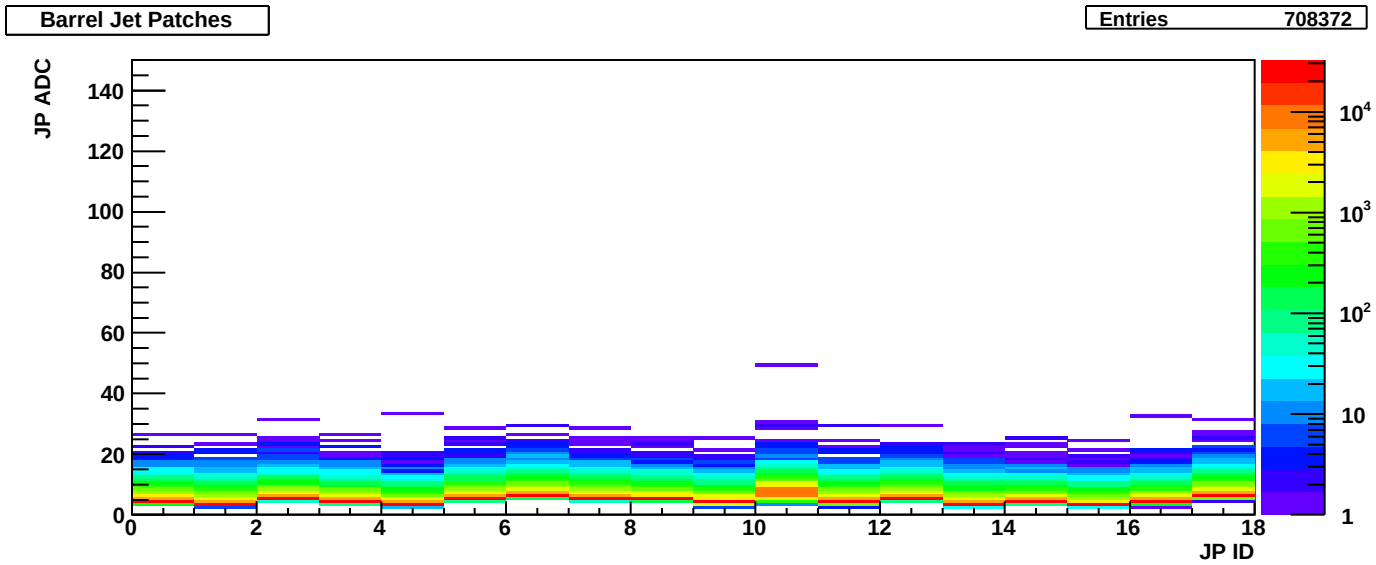

BunchId7Bit (JP1)
Entries 1

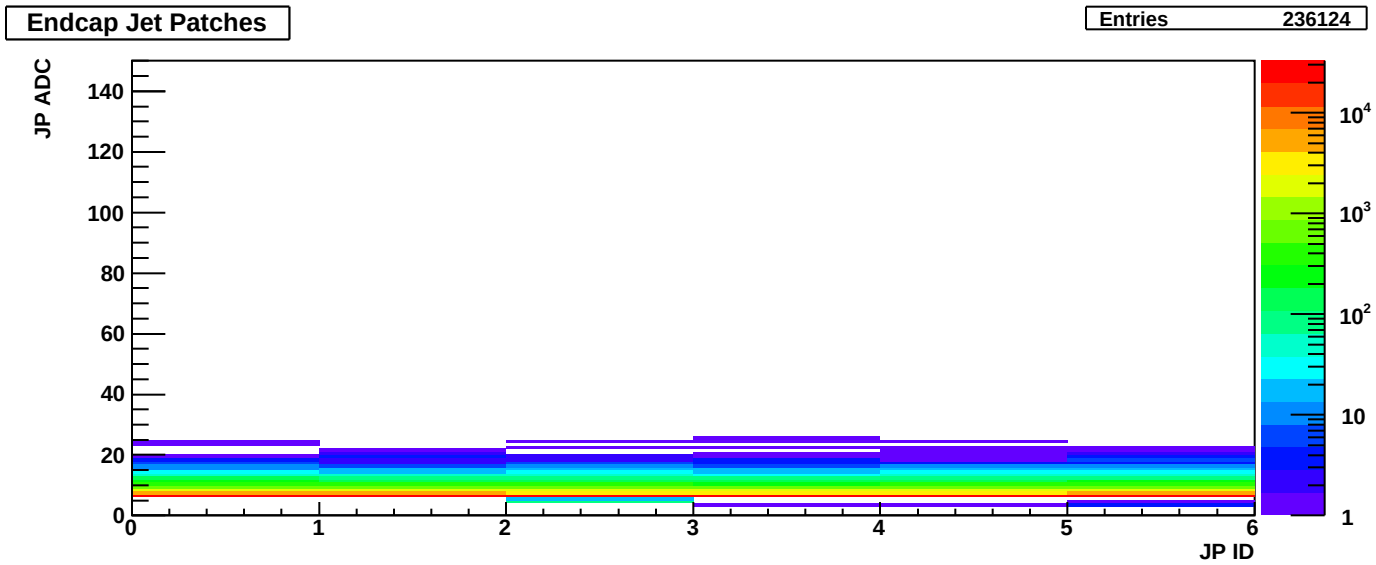

Barrel EMC L0 Input - High Tower
Entries 1.18062e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

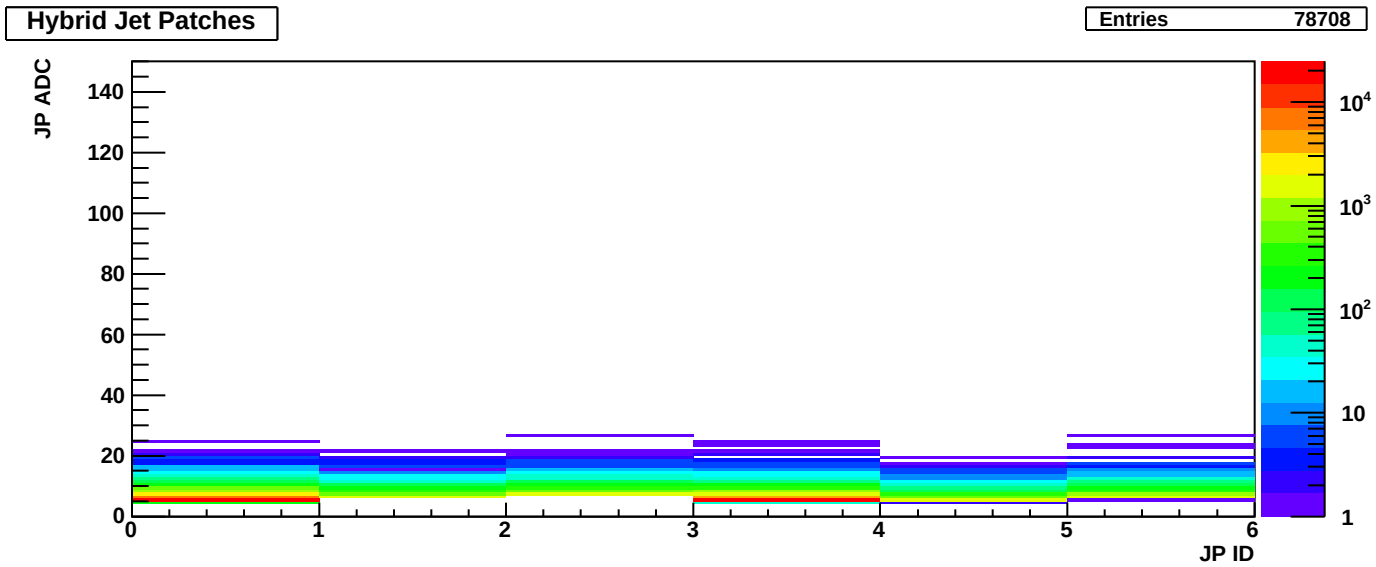

Barrel EMC L0 Input - Patch Sum
Entries 1.18062e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

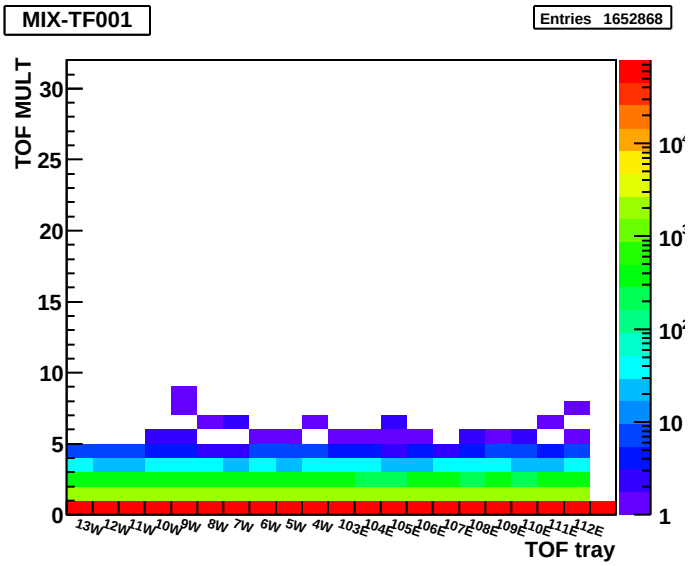

Endcap EMC L0 Input - High Tower
Entries 3541860
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

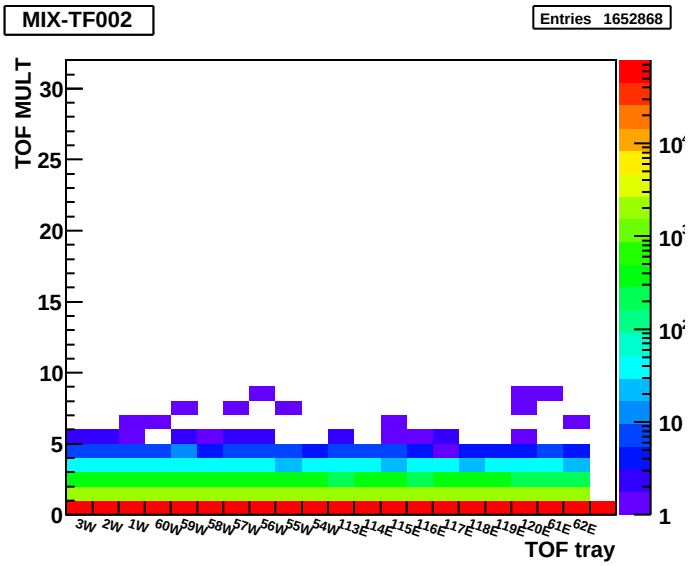

Endcap EMC L0 Input - Patch Sum
Entries 3541860
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

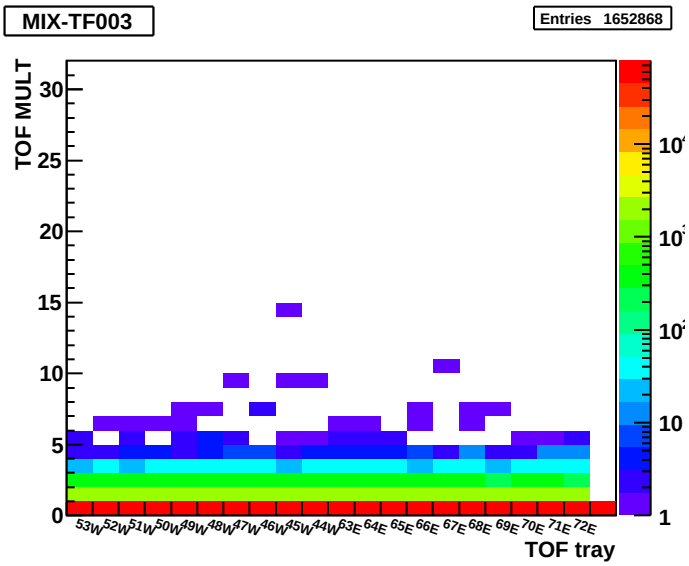

Barrel Jet Patches
Entries 708372
JP ADC
JP ID

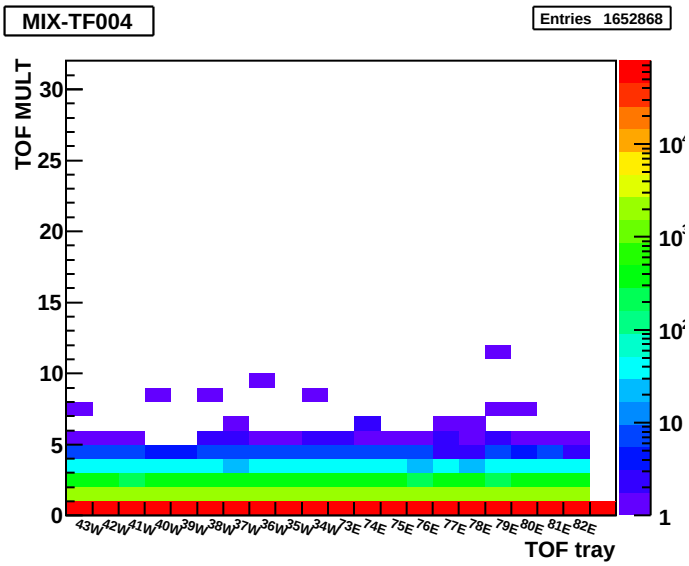

Endcap Jet Patches
Entries 236124
JP ADC
JP ID

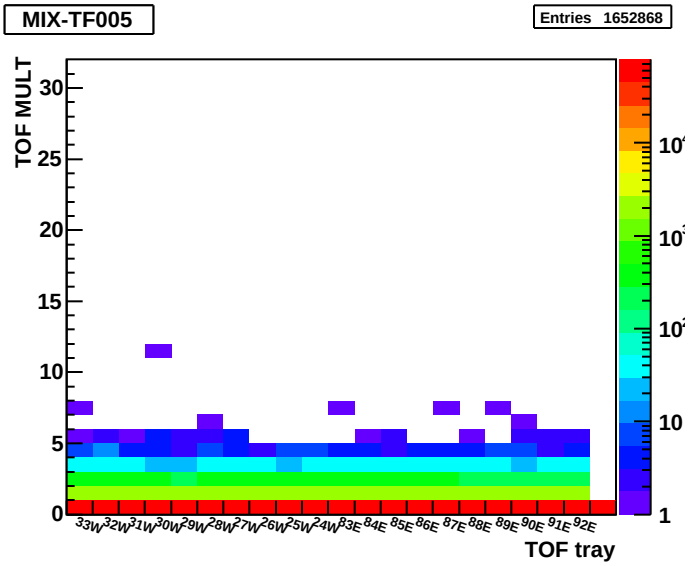

Hybrid Jet Patches
Entries 78708
JP ADC
JP ID

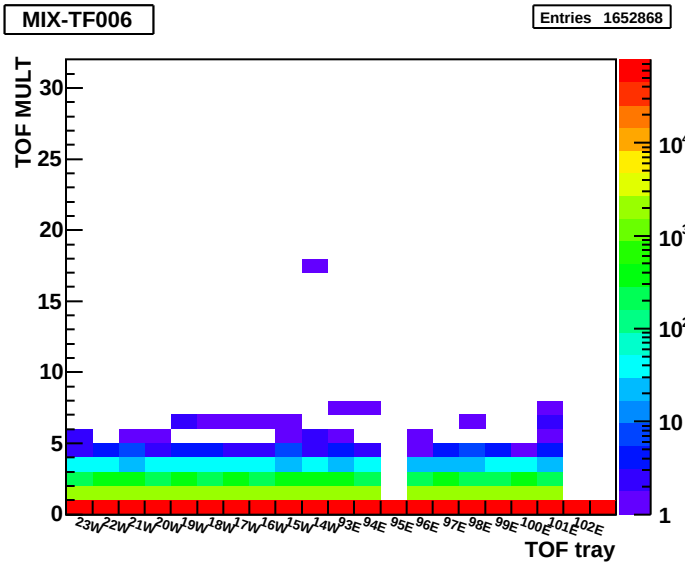

MIX-TF001
Entries 1652868
TOF MULT
TOF tray

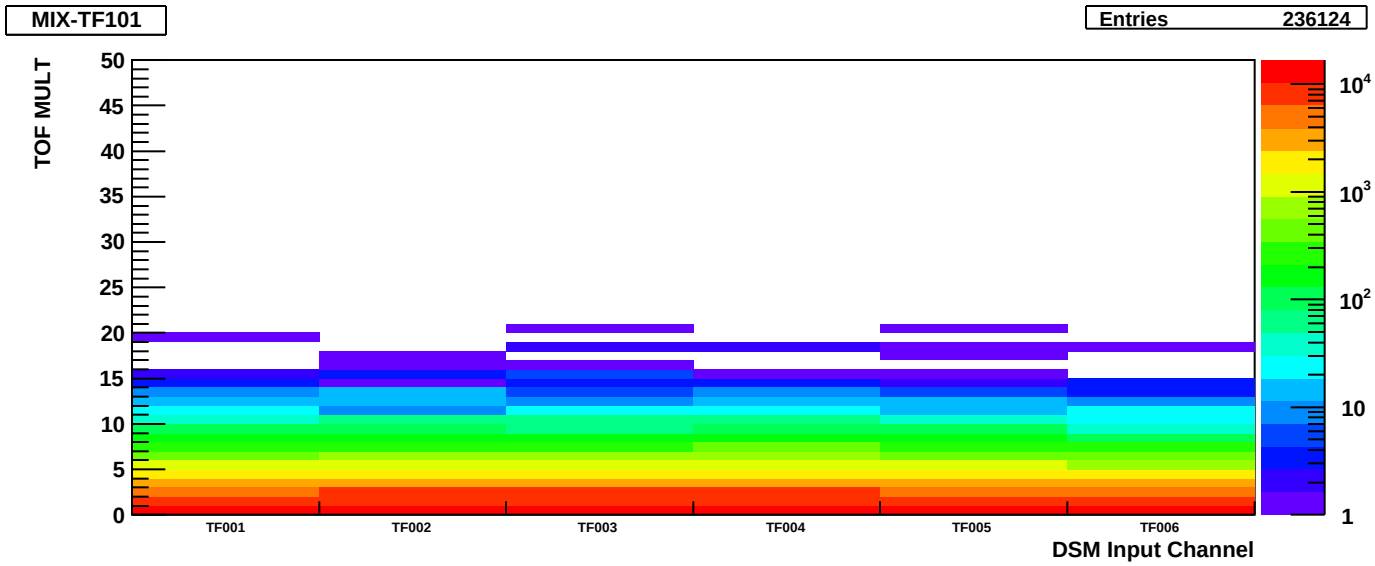

MIX-TF002
Entries 1652868
TOF MULT
TOF tray

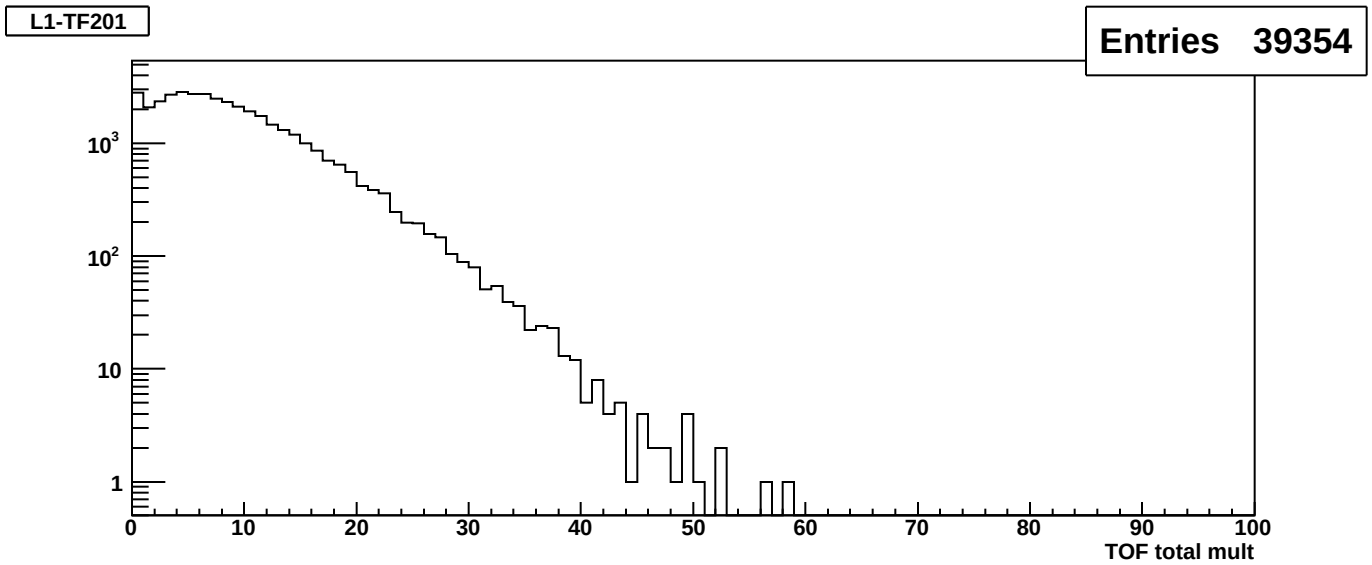

MIX-TF003
Entries 1652868
TOF MULT
TOF tray

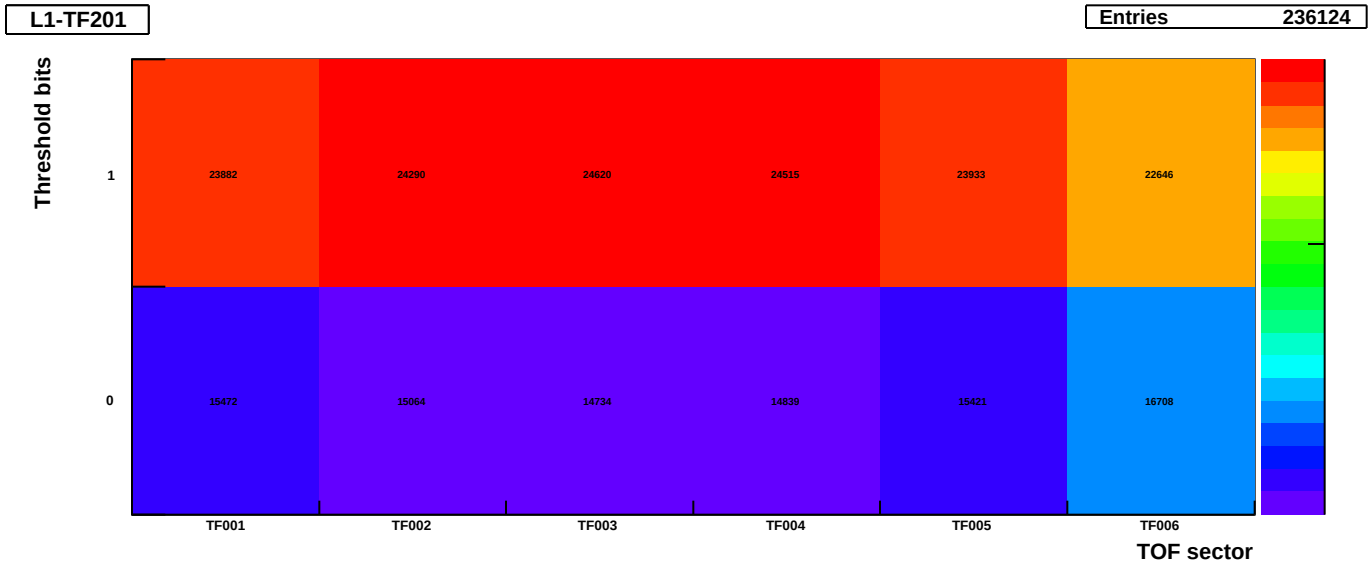

MIX-TF004
Entries 1652868
TOF MULT
TOF tray

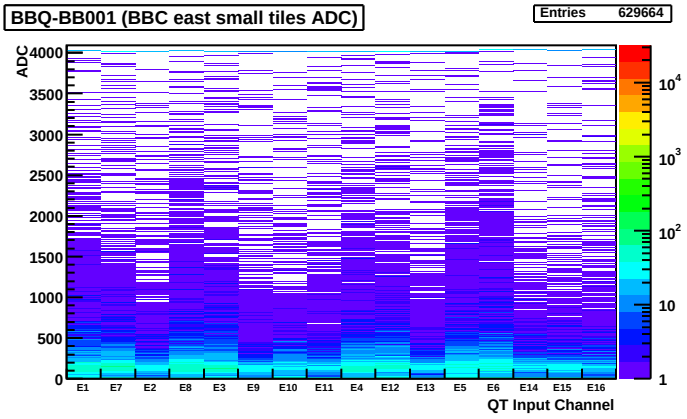

MIX-TF005
Entries 1652868
TOF MULT
TOF tray

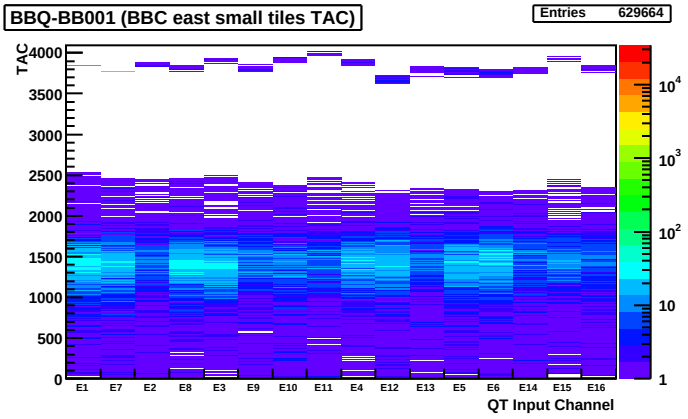

MIX-TF006
Entries 1652868
TOF MULT
TOF tray

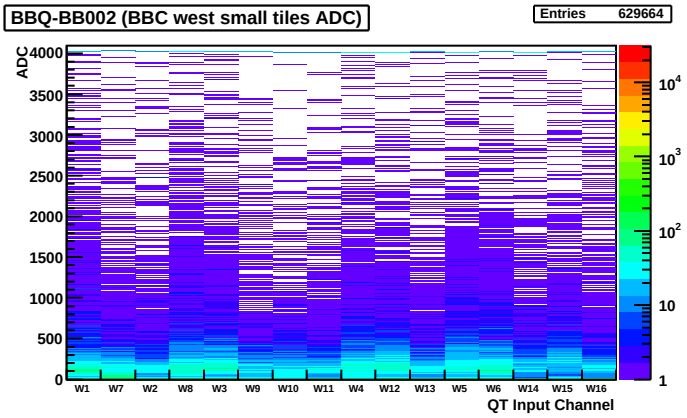

MIX-TF101
Entries 236124
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

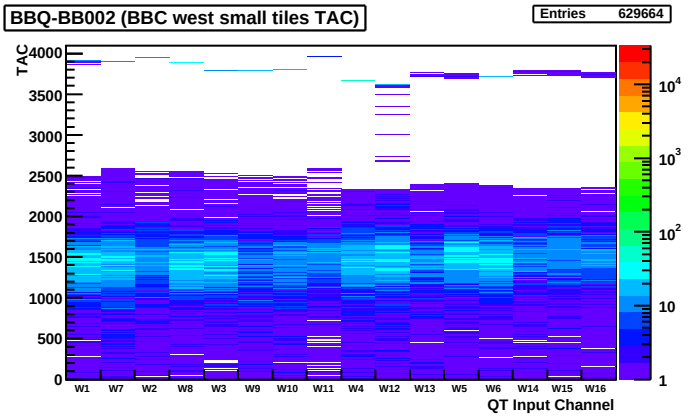

L1-TF201
Entries 39354
TOF total mult

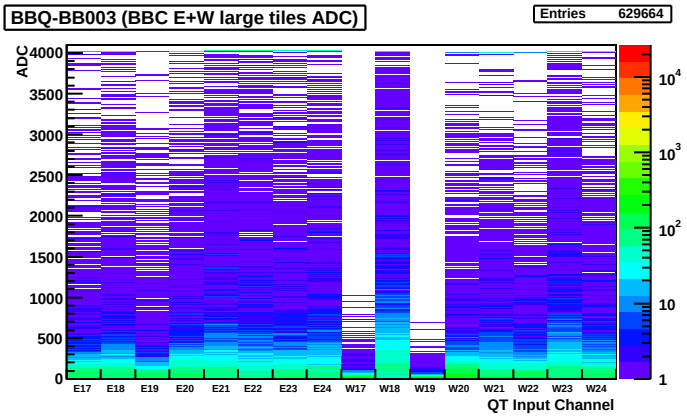

L1-TF201
Entries 236124
Threshold bits
23882
24290
24620
24515
23933
22646
15472
15064
14734
14839
15421
16708
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

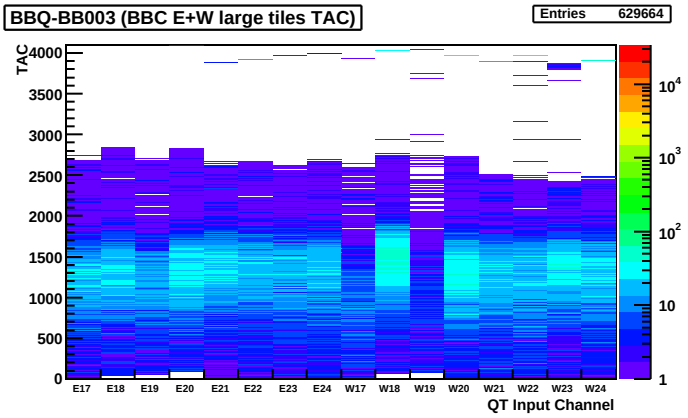

BBQ-BB001 (BBC east small tiles ADC)
Entries 629664
ADC
QT Input Channel

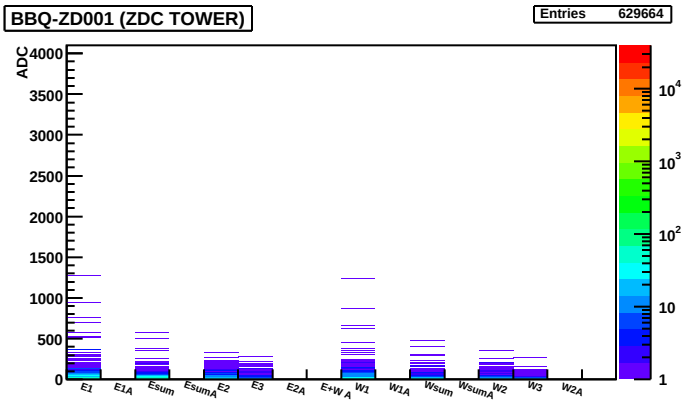

BBQ-BB001 (BBC east small tiles TAC)
Entries 629664
TAC
QT Input Channel

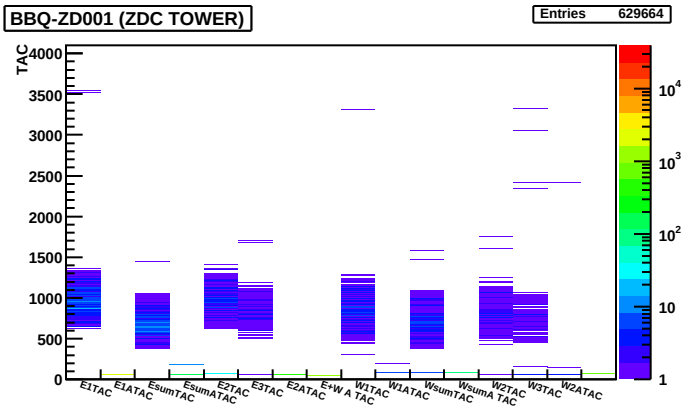

BBQ-BB002 (BBC west small tiles ADC)
Entries 629664
ADC
QT Input Channel

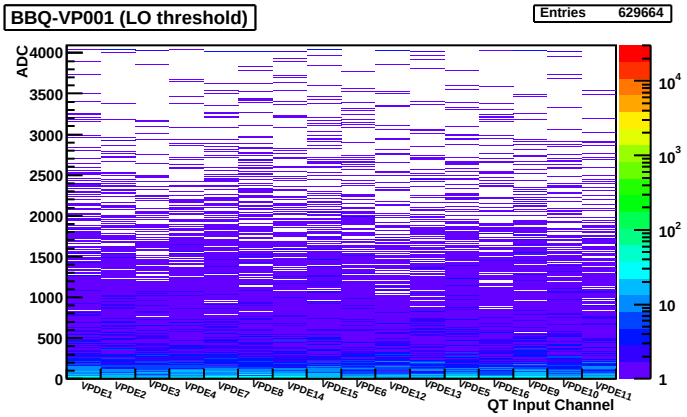

BBQ-BB002 (BBC west small tiles TAC)
Entries 629664
TAC
QT Input Channel

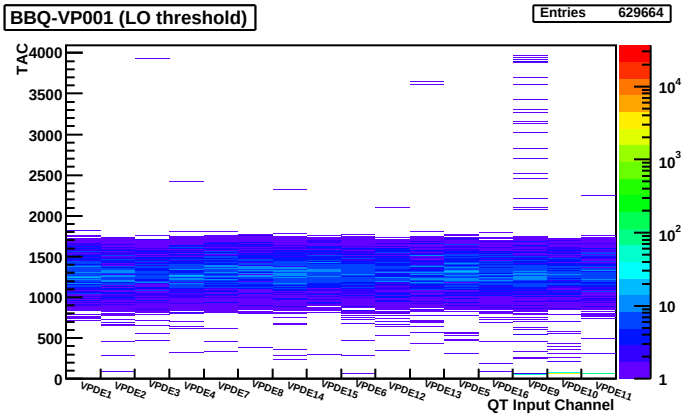

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 629664
ADC
QT Input Channel

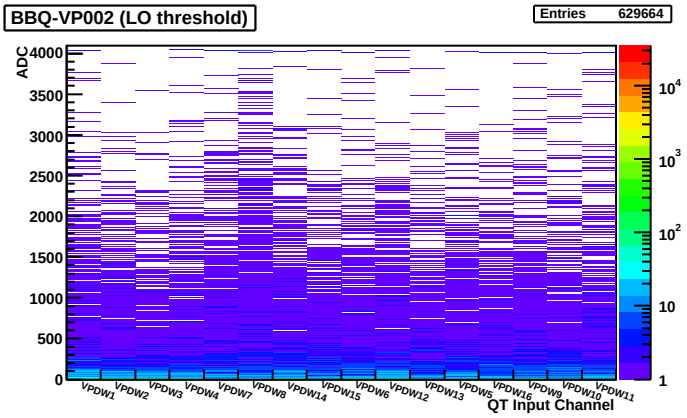

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 629664
TAC
QT Input Channel

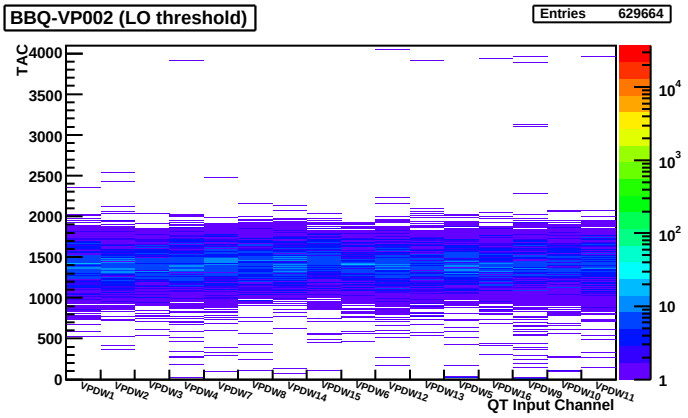

BBQ-ZD001 (ZDC TOWER)
Entries 629664
ADC

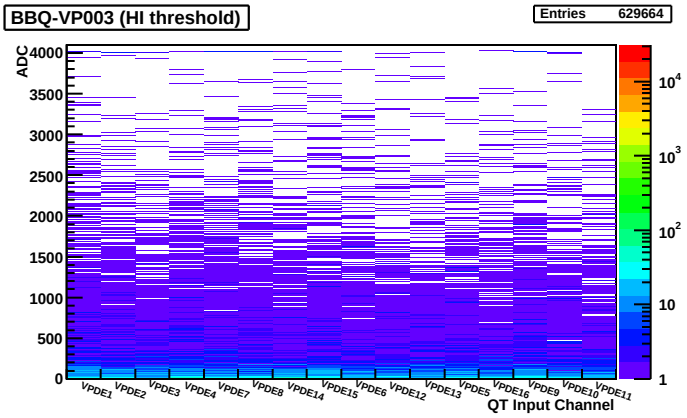

BBQ-ZD001 (ZDC TOWER)
Entries 629664
TAC

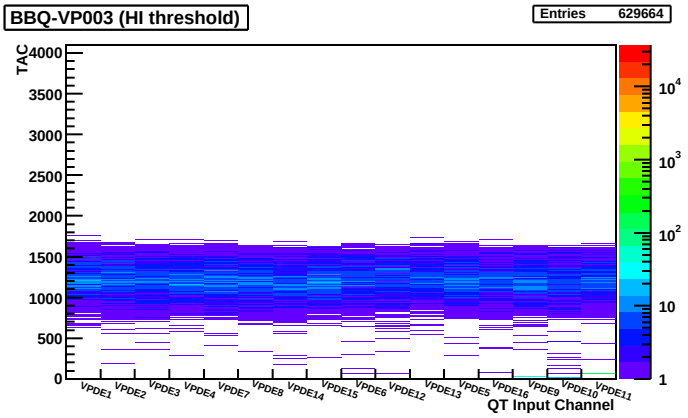

BBQ-VP001 (LO threshold)
Entries 629664
ADC
QT Input Channel

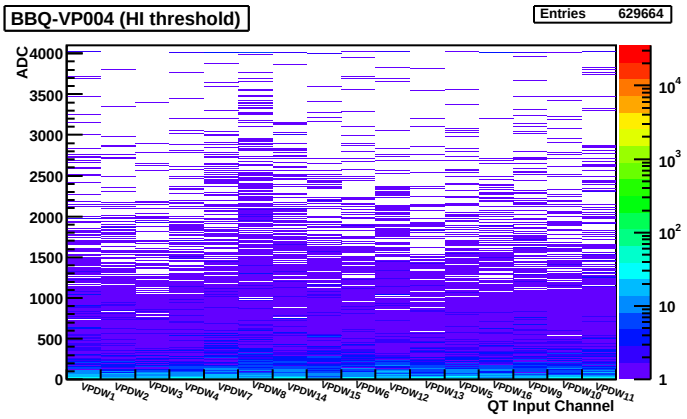

BBQ-VP001 (LO threshold)
Entries 629664
TAC
QT Input Channel

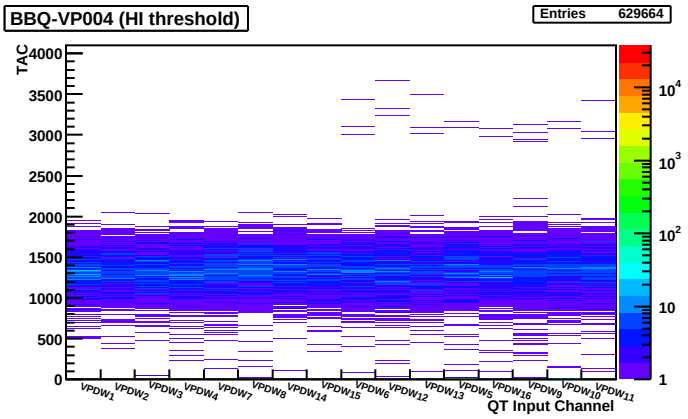

BBQ-VP002 (LO threshold)
Entries 629664
ADC
QT Input Channel

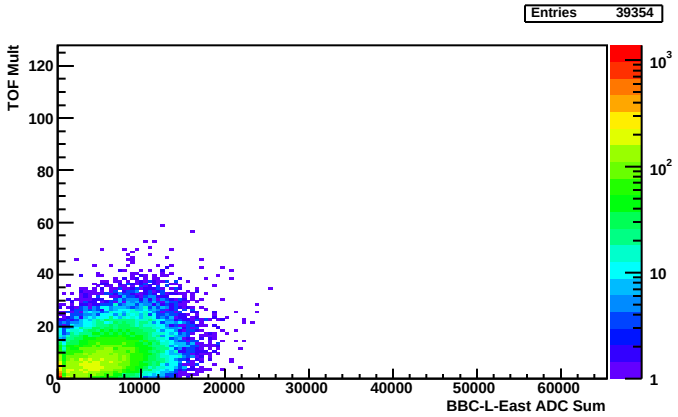

BBQ-VP002 (LO threshold)
Entries 629664
TAC
QT Input Channel

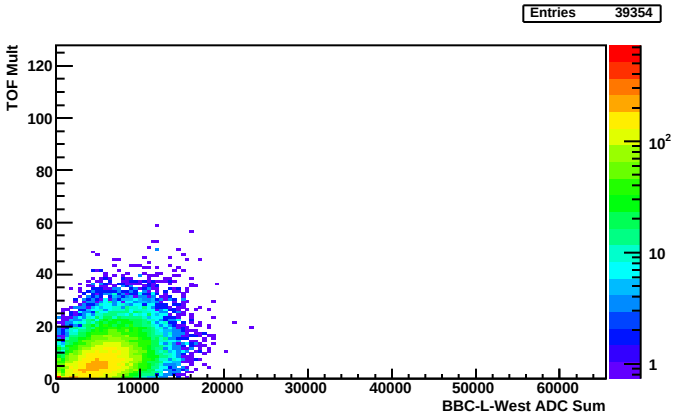

BBQ-VP003 (HI threshold)
Entries 629664
ADC
QT Input Channel

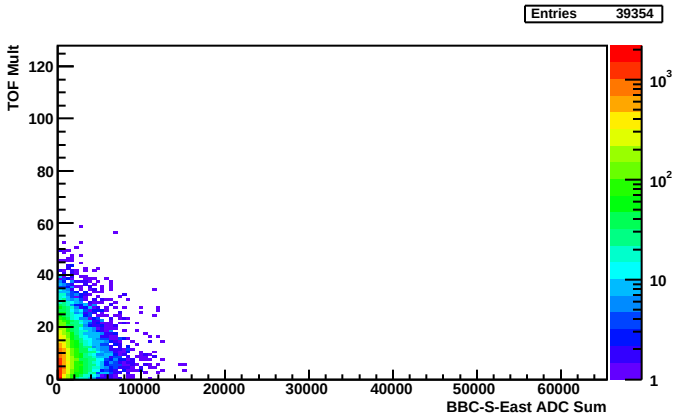

BBQ-VP003 (HI threshold)
Entries 629664
TAC
QT Input Channel

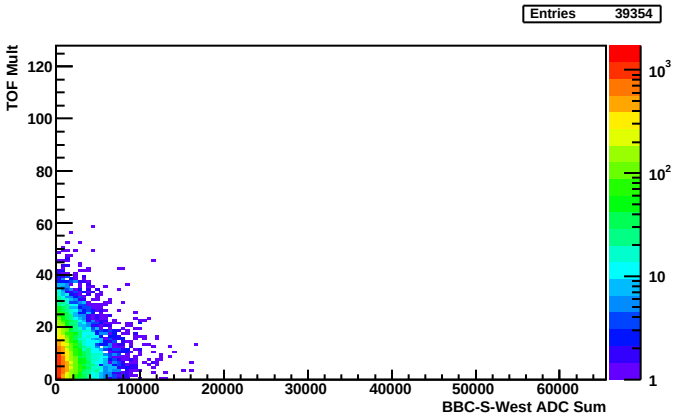

BBQ-VP004 (HI threshold)
Entries 629664
ADC
QT Input Channel

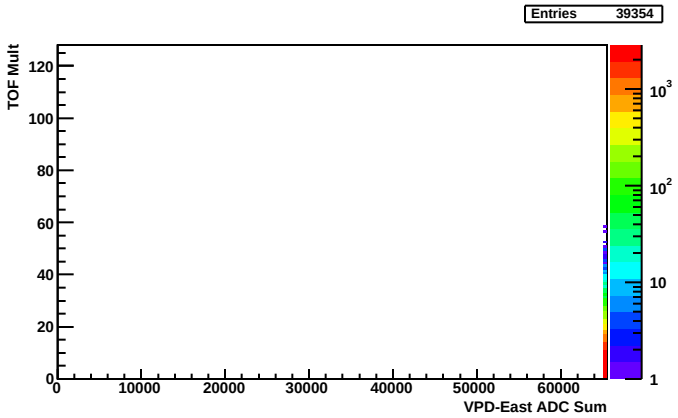

BBQ-VP004 (HI threshold)
Entries 629664
TAC
QT Input Channel

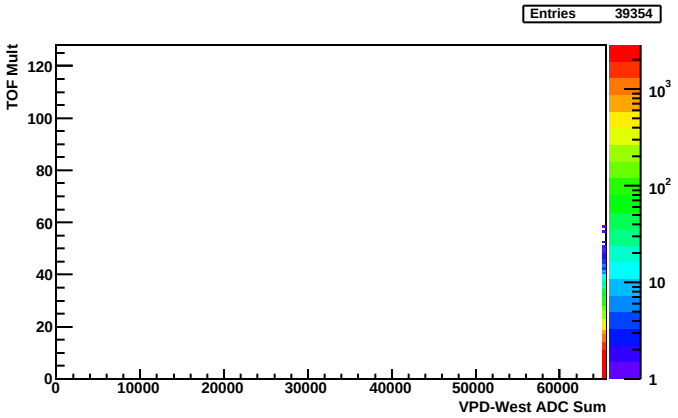

Entries 39354
TOF Mult
BBC-L-East ADC Sum

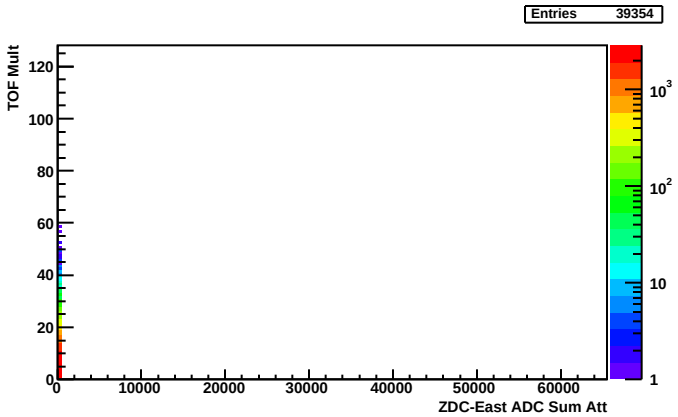

Entries 39354
TOF Mult
BBC-L-West ADC Sum

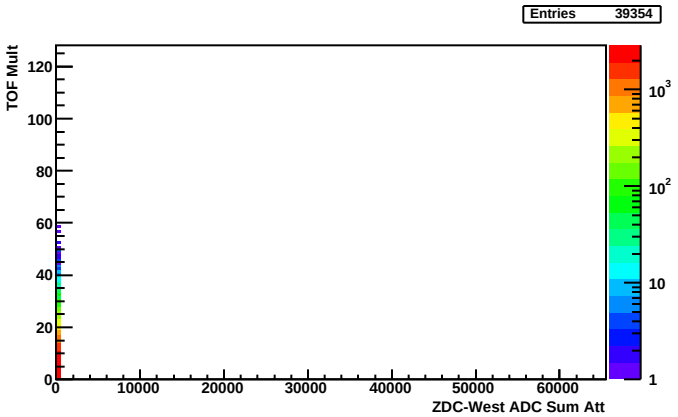

Entries 39354
TOF Mult
BBC-S-East ADC Sum

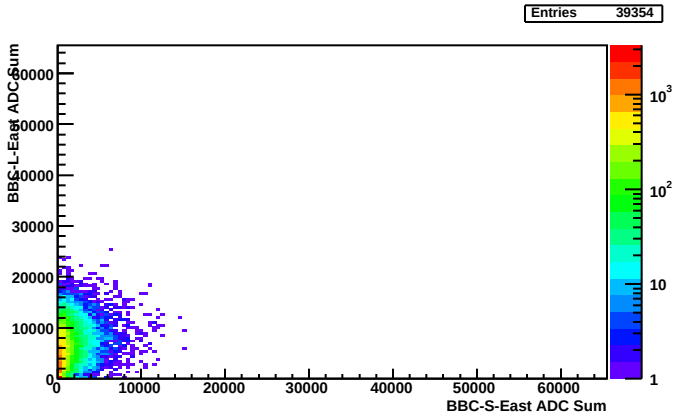

Entries 39354
TOF Mult
BBC-S-West ADC Sum

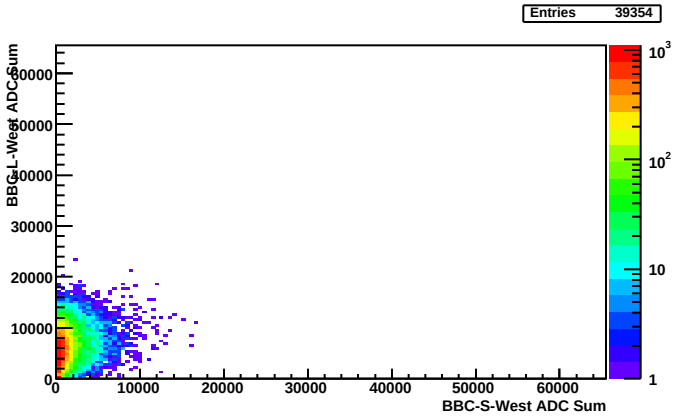

Entries 39354
TOF Mult
VPD-East ADC Sum

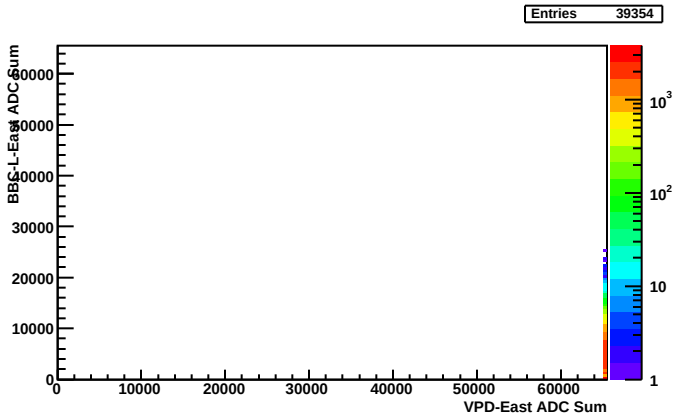

Entries 39354
TOF Mult
VPD-West ADC Sum

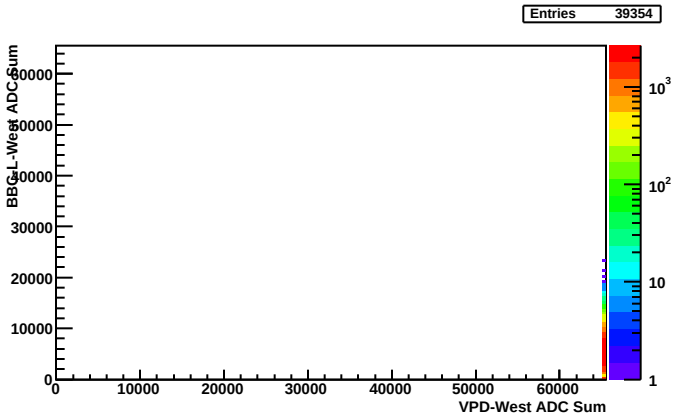

Entries 39354
TOF Mult
ZDC-East ADC Sum Att

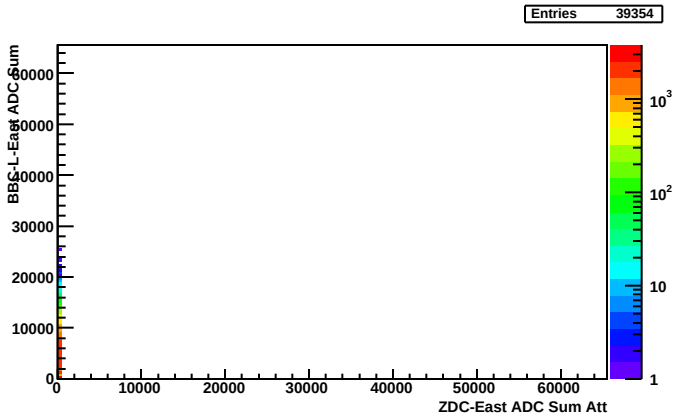

Entries 39354
TOF Mult
ZDC-West ADC Sum Att

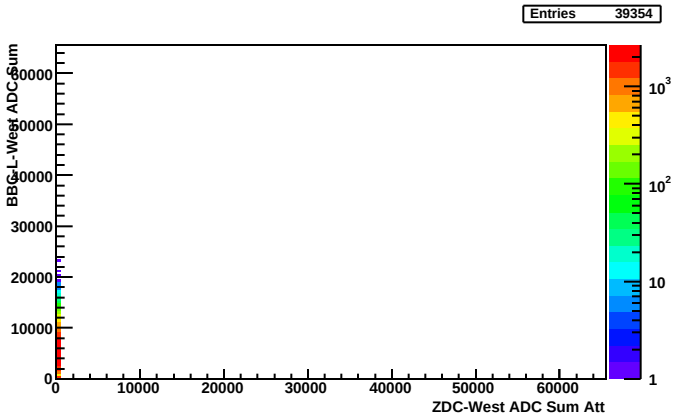

Entries 39354
BBC-L-East ADC Sum
BBC-S-East ADC Sum

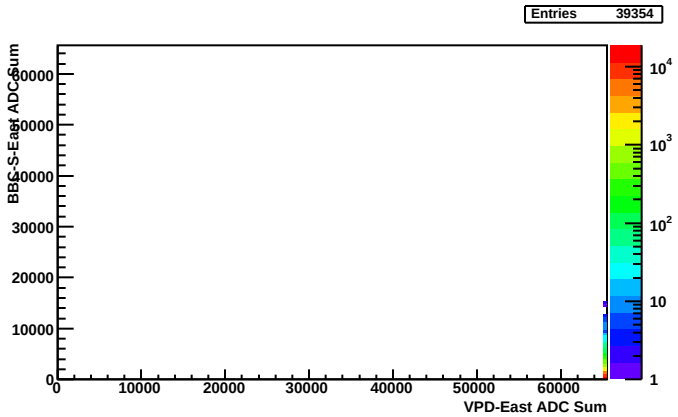

Entries 39354
BBC-L-West ADC Sum
BBC-S-West ADC Sum

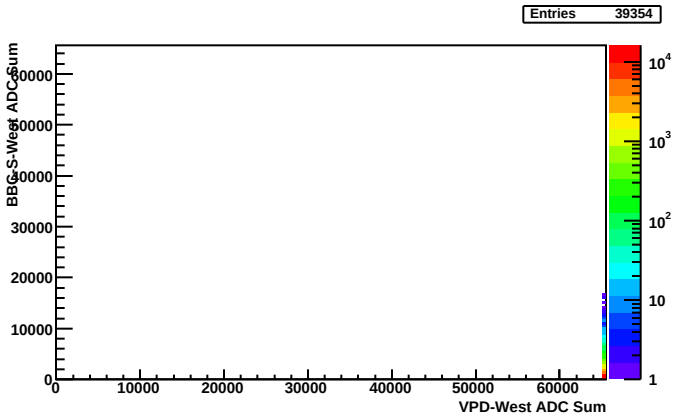

Entries 39354
BBC-L-East ADC Sum
VPD-East ADC Sum

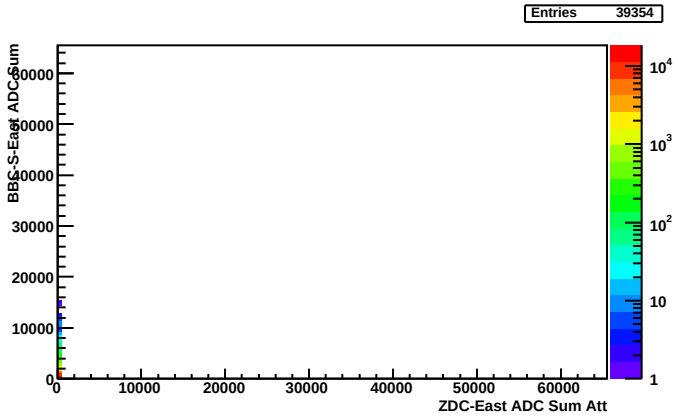

Entries 39354
BBC-L-West ADC Sum
VPD-West ADC Sum

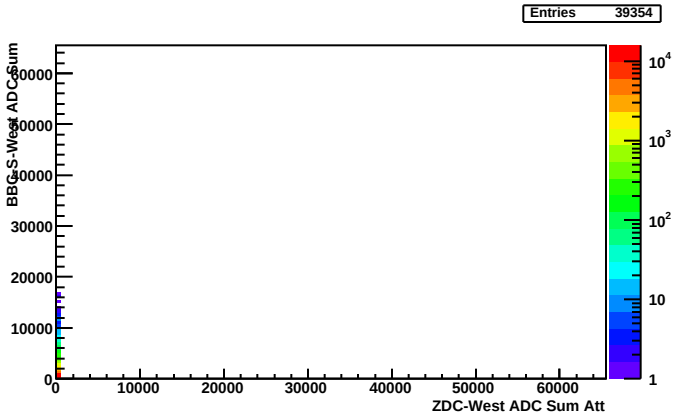

Entries 39354
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

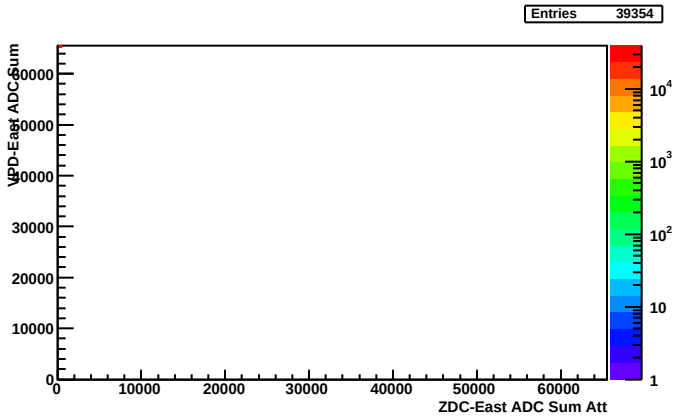

Entries 39354
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

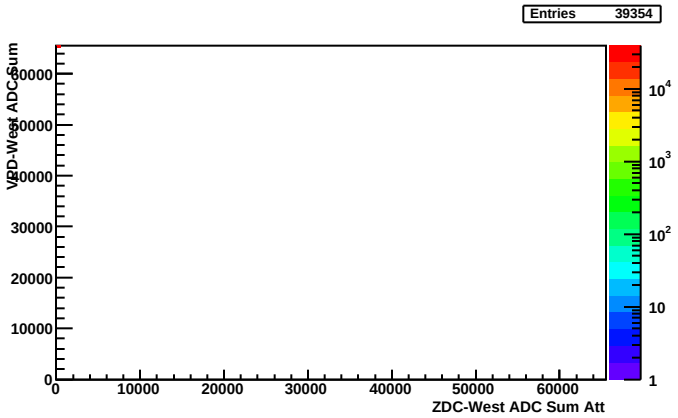

Entries 39354
BBC-S-East ADC Sum
VPD-East ADC Sum

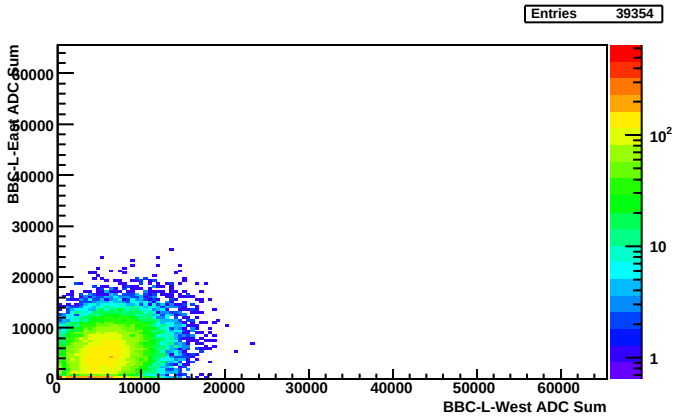

Entries 39354
BBC-S-West ADC Sum
VPD-West ADC Sum

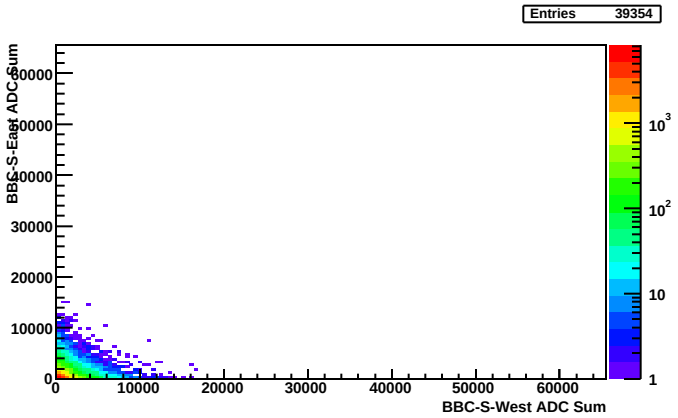

Entries 39354
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

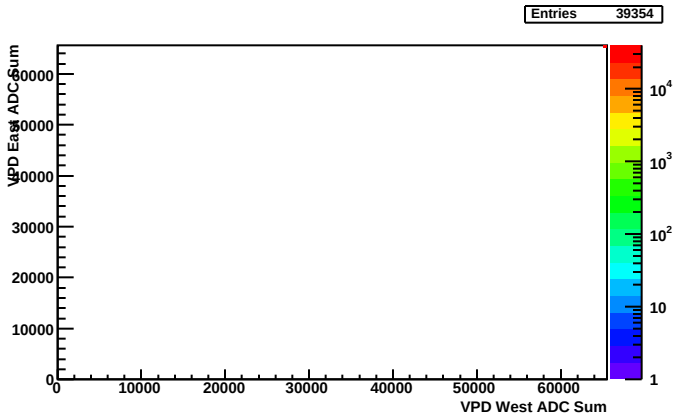

Entries 39354
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

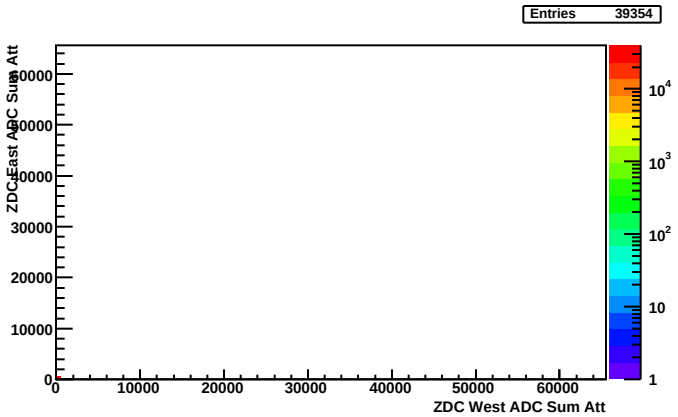

Entries 39354
VPD-East ADC Sum
ZDC-East ADC Sum Att

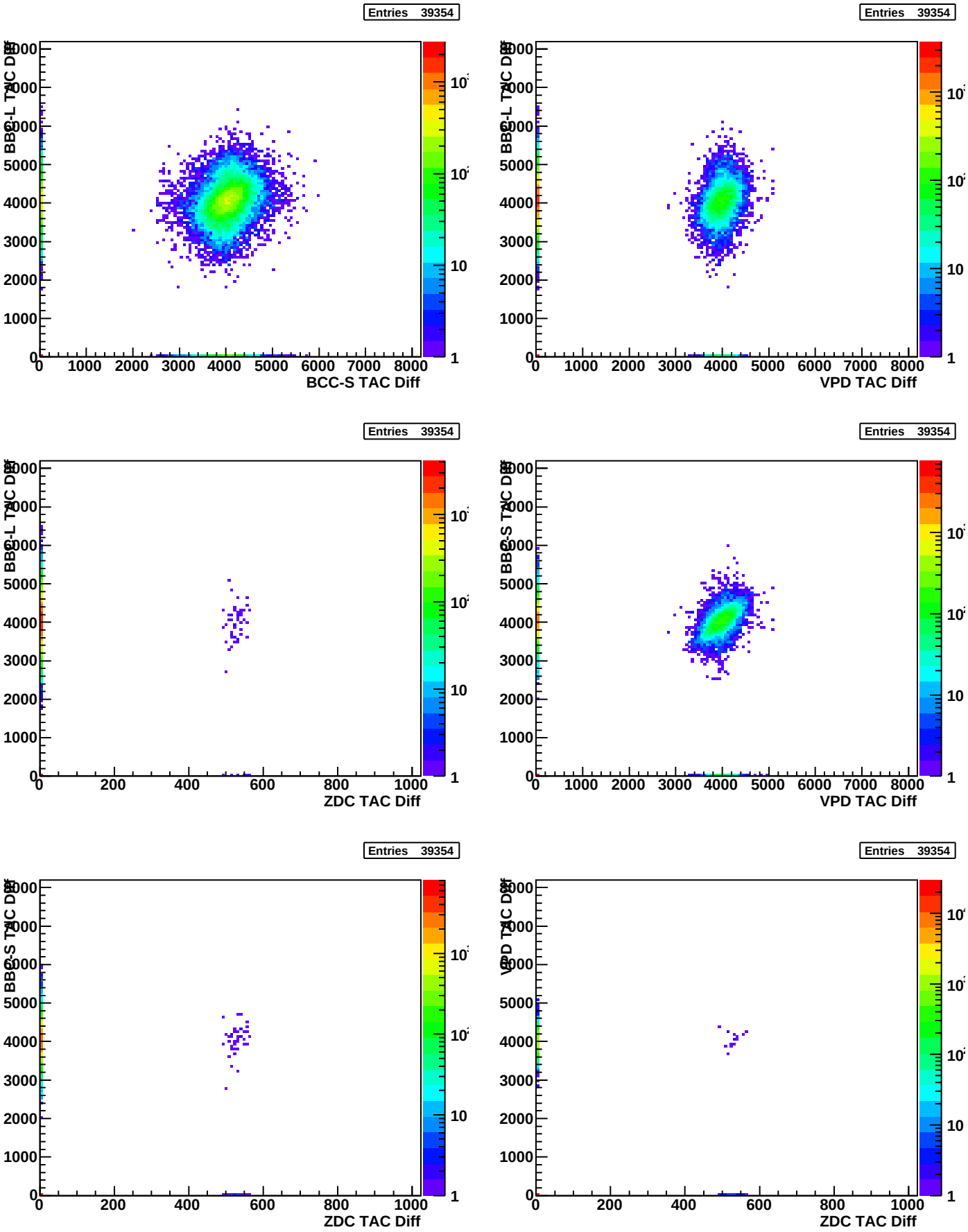

Entries 39354
VPD-West ADC Sum
ZDC-West ADC Sum Att

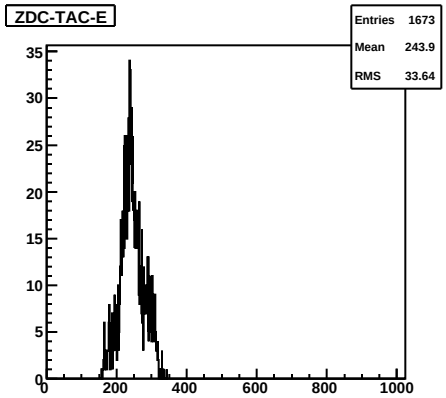

Entries 39354
BBC-L-East ADC Sum
BBC-L-West ADC Sum

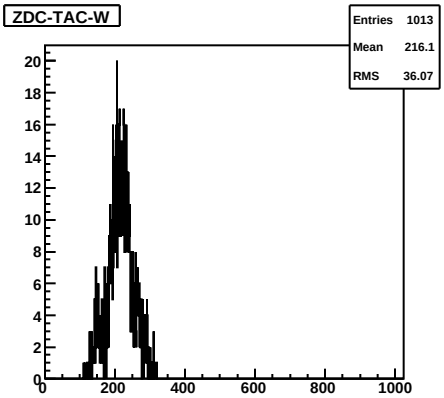

Entries 39354
BBC-S-East ADC Sum
BBC-S-West ADC Sum

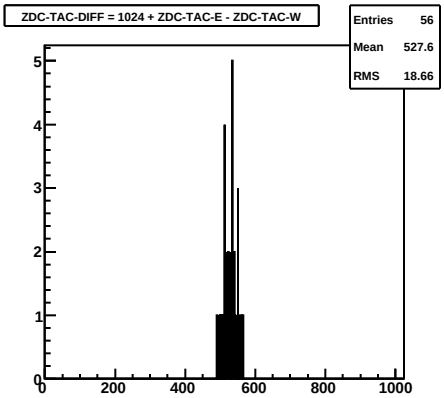

Entries 39354
VPD East ADC Sum
VPD West ADC Sum

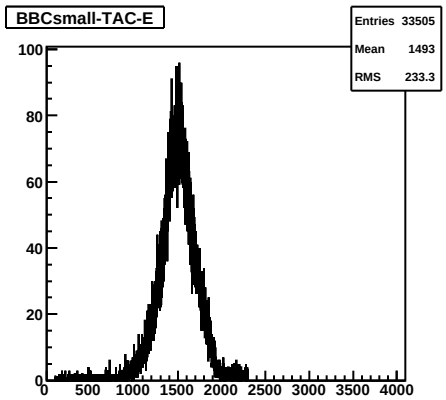

Entries 39354
ZDC East ADC Sum Att
ZDC West ADC Sum Att

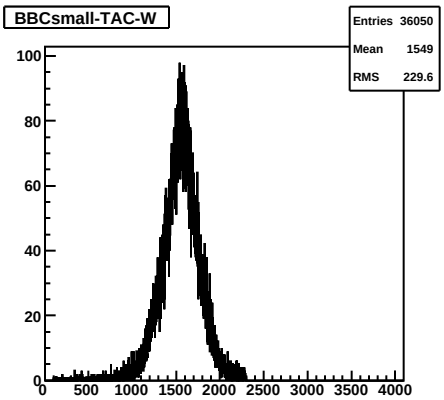

Entries 39354
BBC-L TAC Diff
BCC-S TAC Diff
Entries 39354
BBC-L TAC Diff
VPD TAC Diff
Entries 39354
BBC-L TAC Diff
ZDC TAC Diff
Entries 39354
BBC-S TAC Diff
VPD TAC Diff
Entries 39354
BBC-S TAC Diff
ZDC TAC Diff
Entries 39354
VPD TAC Diff
ZDC TAC Diff

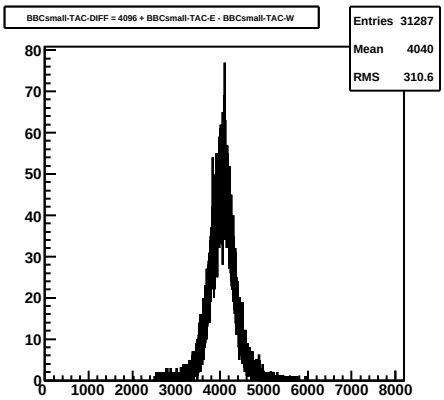

ZDC-TAC-E
Entries 1673
Mean 243.9
RMS 33.64

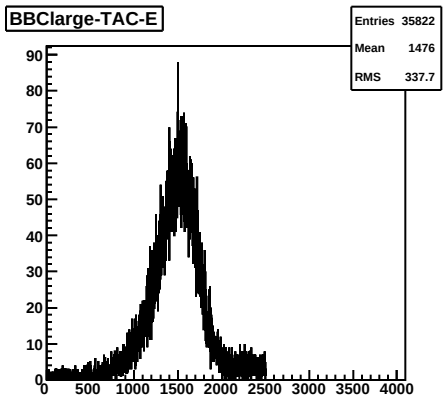

ZDC-TAC-W
Entries 1013
Mean 216.1
RMS 36.07

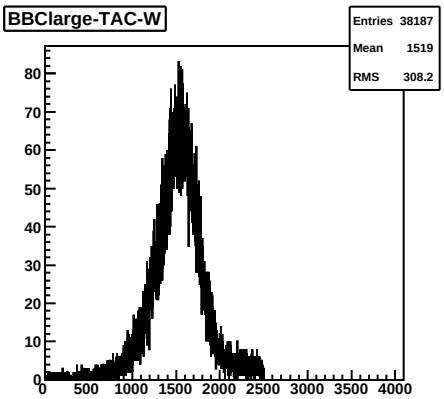

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 56
Mean 527.6
RMS 18.66

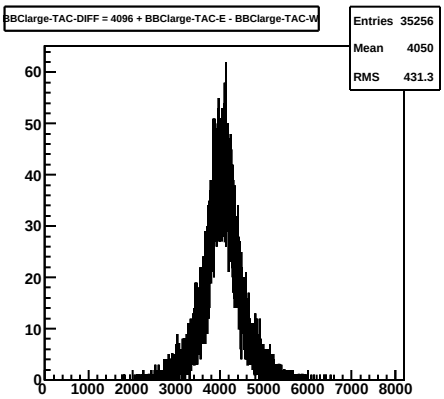

BBCsmall-TAC-E
Entries 33505
Mean 1493
RMS 233.3

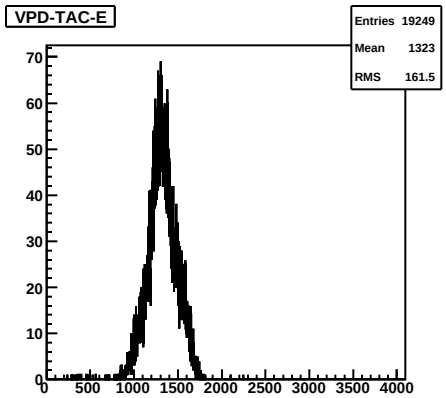

BBCsmall-TAC-W
Entries 36050
Mean 1549
RMS 229.6

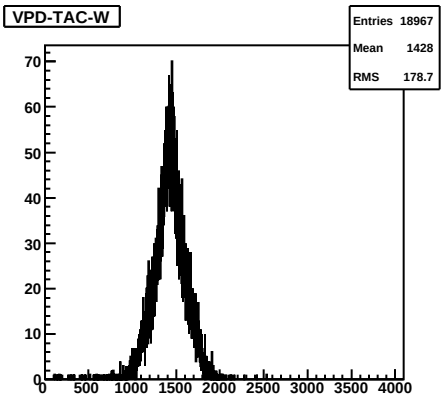

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 31287
Mean 4040
RMS 310.6

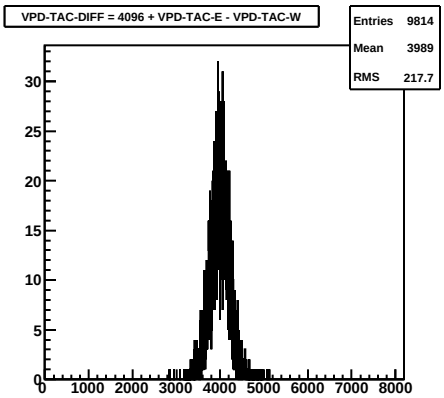

BBClarge-TAC-E
Entries 35822
Mean 1476
RMS 337.7

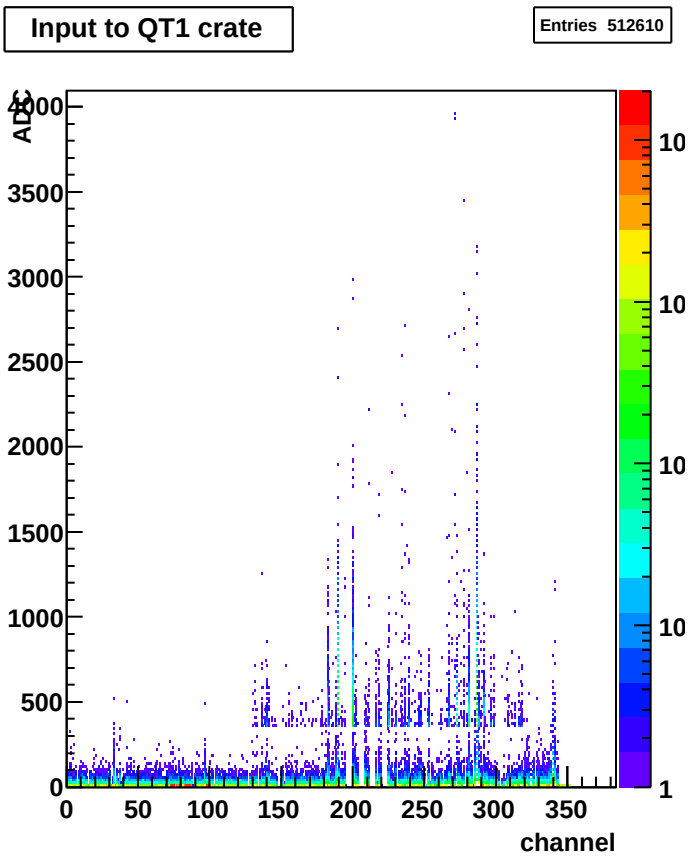

BBClarge-TAC-W
Entries 38187
Mean 1519
RMS 308.2

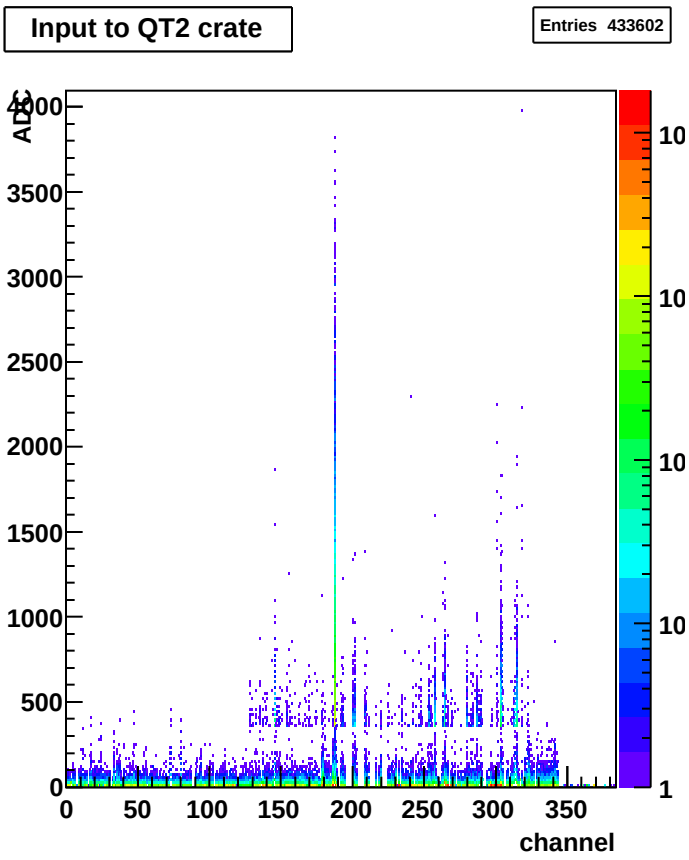

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 35256
Mean 4050
RMS 431.3

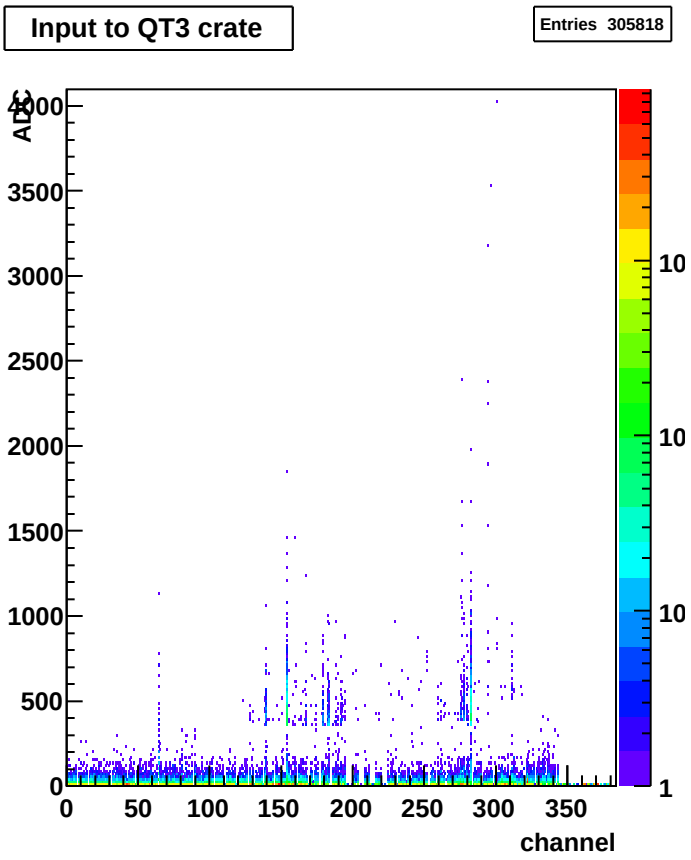

VPD-TAC-E
Entries 19249
Mean 1323
RMS 161.5

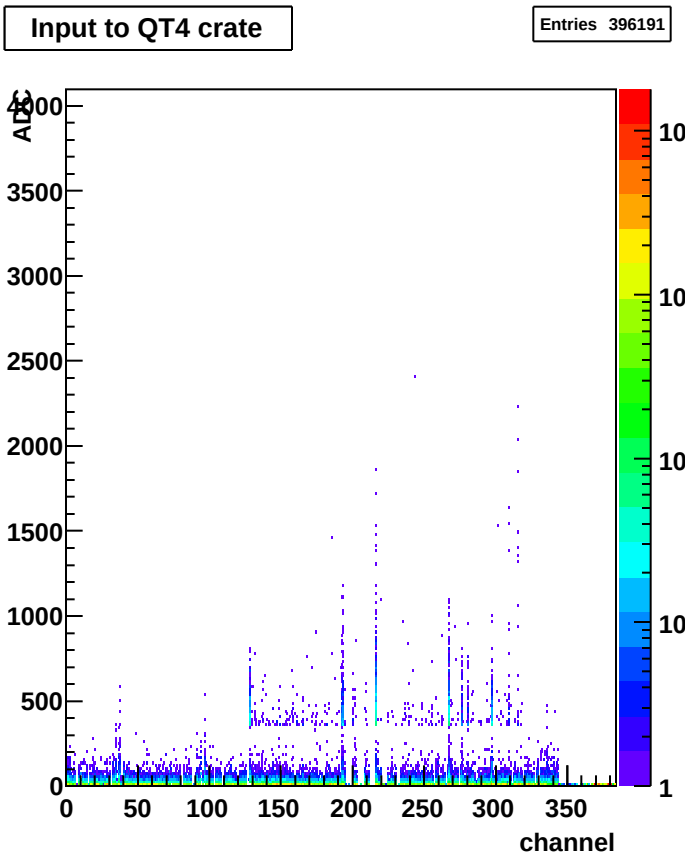

VPD-TAC-W
Entries 18967
Mean 1428
RMS 178.7

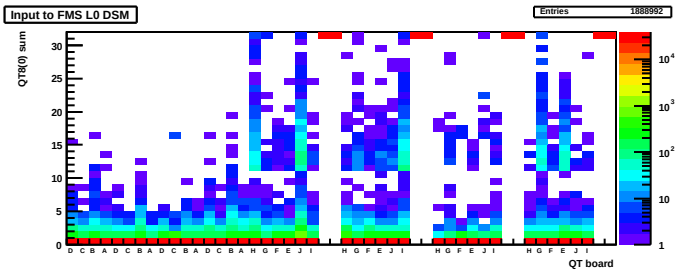

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 9814
Mean 3989
RMS 217.7

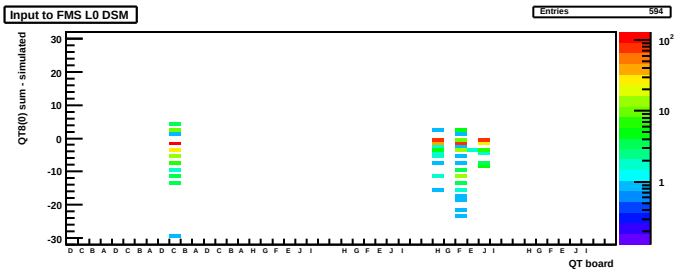

Input to QT1 crate
Entries 512610
ADC
channel

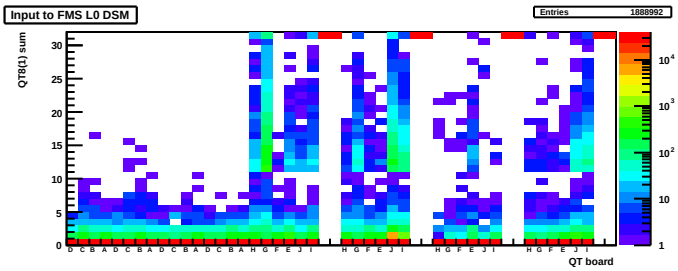

Input to QT2 crate
Entries 433602
ADC
channel

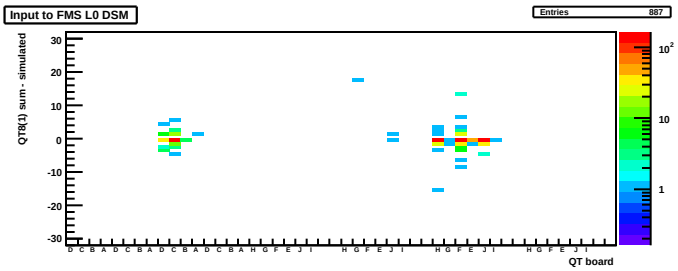

Input to QT3 crate
Entries 305818
ADC
channel

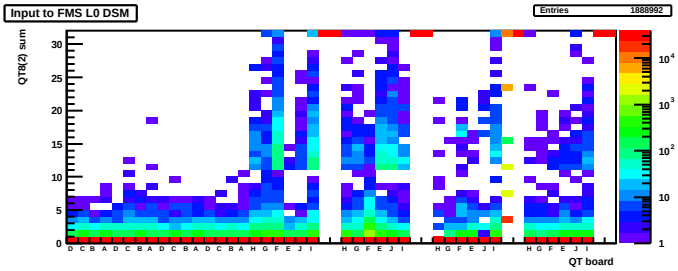

Input to QT4 crate
Entries 396191
ADC
channel

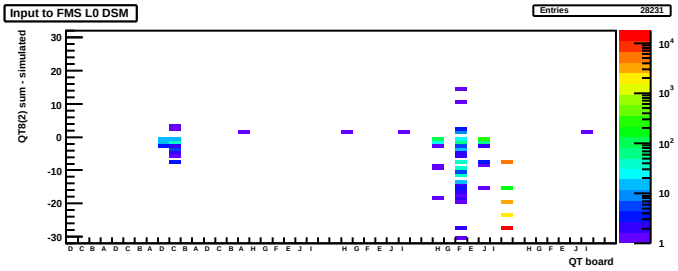

Input to FMS L0 DSM
Entries 1888992
QT8(0) sum
QT board

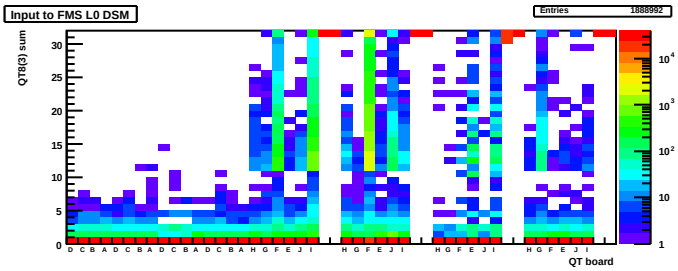

Input to FMS L0 DSM
Entries 594
QT8(0) sum - simulated
QT board

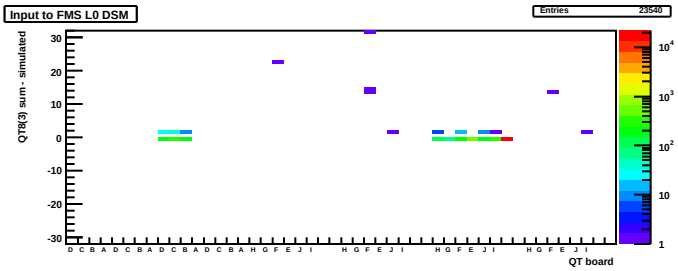

Input to FMS L0 DSM
Entries 1888992
QT8(1) sum
QT board

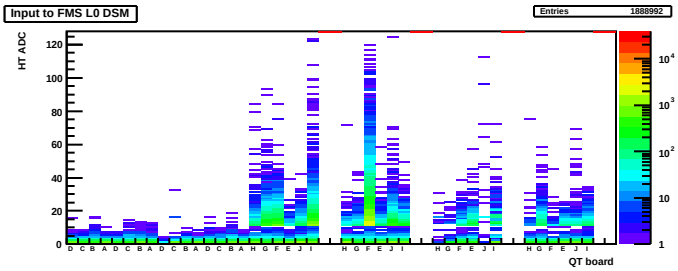

Input to FMS L0 DSM
Entries 887
QT8(1) sum - simulated
QT board

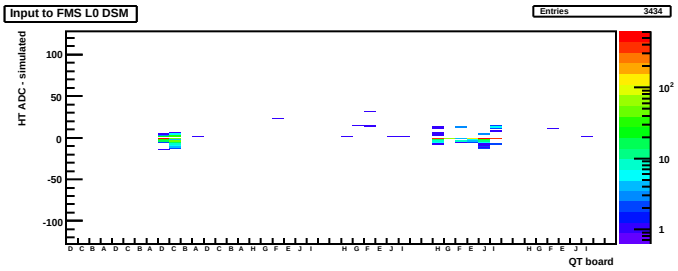

Input to FMS L0 DSM
Entries 1888992
QT8(2) sum
QT board

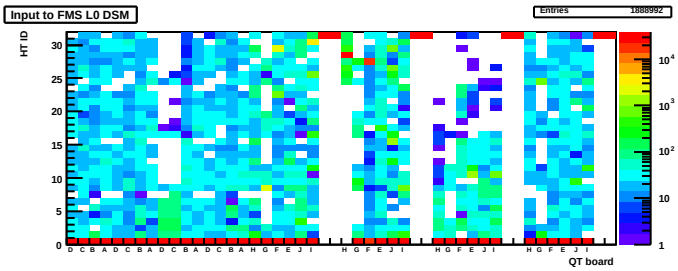

Input to FMS L0 DSM
Entries 28231
QT8(2) sum - simulated
QT board

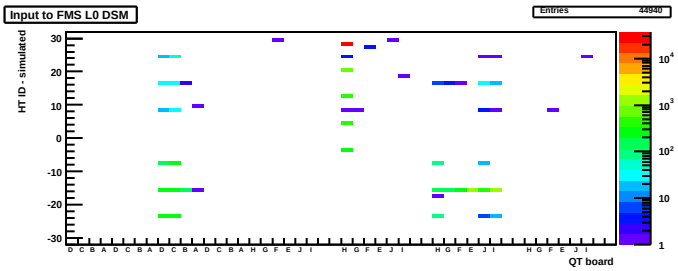

Input to FMS L0 DSM
Entries 1888992
QT8(3) sum
QT board

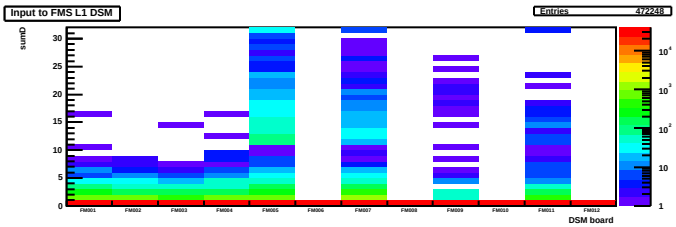

Input to FMS L0 DSM
Entries 23540
QT8(3) sum - simulated
QT board

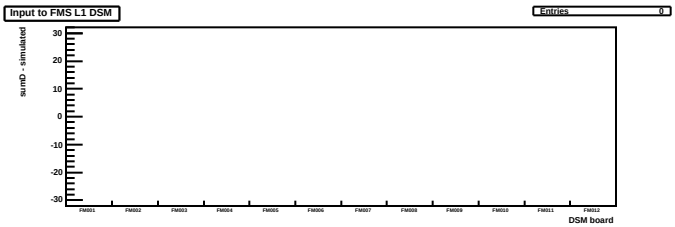

Input to FMS L0 DSM
Entries 1888992
HT ADC
QT board

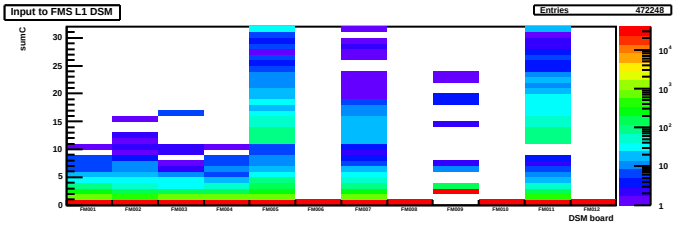

Input to FMS L0 DSM
Entries 3434
HT ADC - simulated
QT board

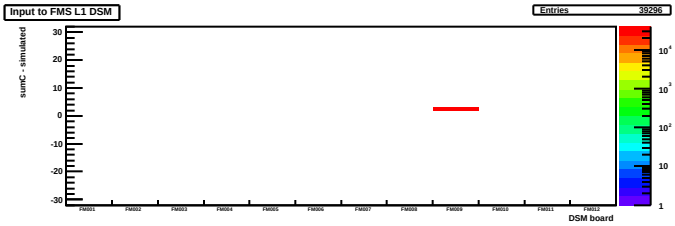

Input to FMS L0 DSM
Entries 1888992
HT ID
QT board

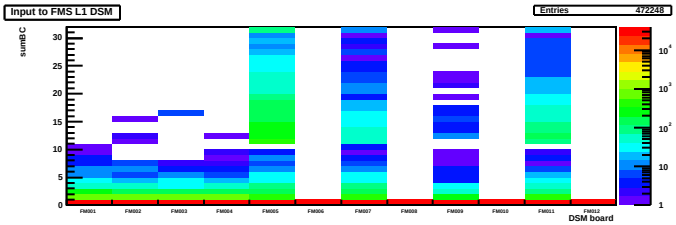

Input to FMS L0 DSM
Entries 44940
HT ID - simulated
QT board

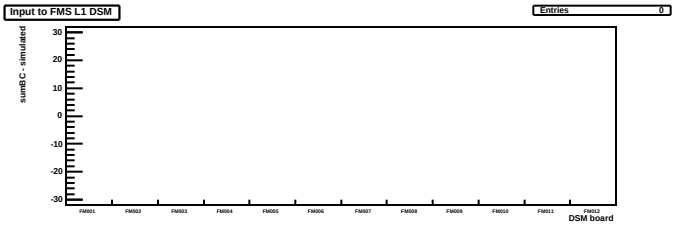

Input to FMS L1 DSM
Entries 472248
sumD
DSM board

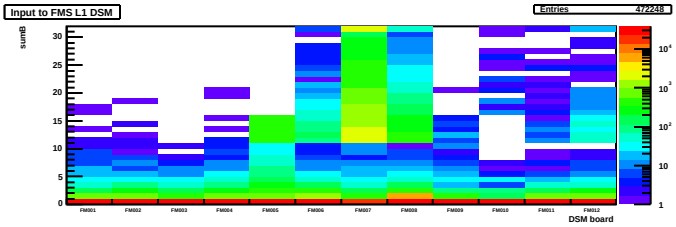

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

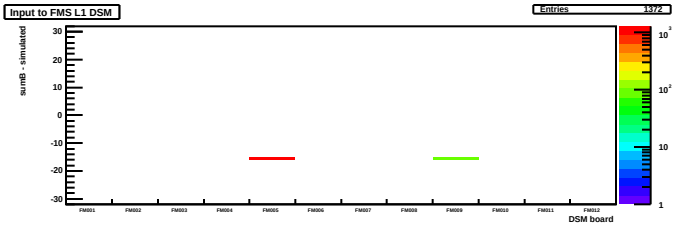

Input to FMS L1 DSM
Entries 472248
sumC
DSM board

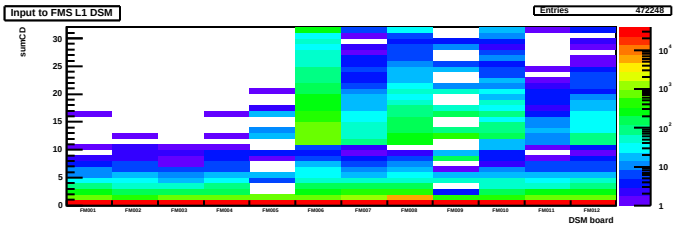

Input to FMS L1 DSM
Entries 39296
sumC - simulated
DSM board

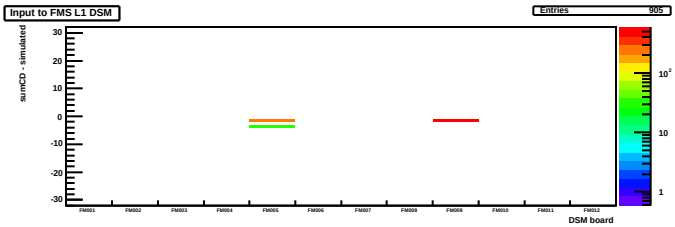

Input to FMS L1 DSM
Entries 472248
sumBC
DSM board

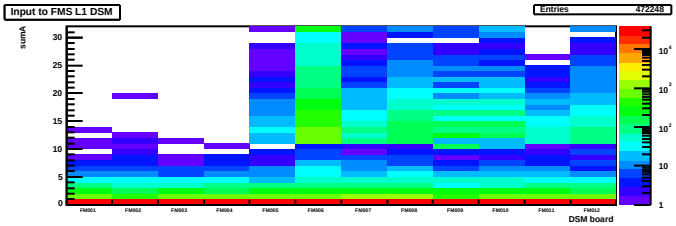

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

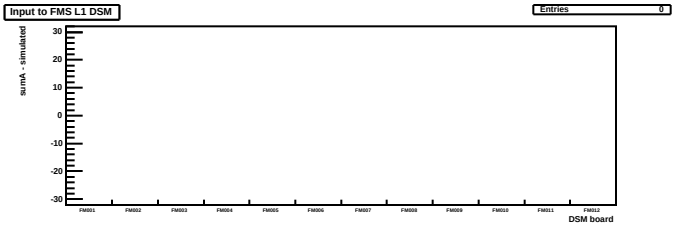

Input to FMS L1 DSM
Entries 472248
sumB
DSM board

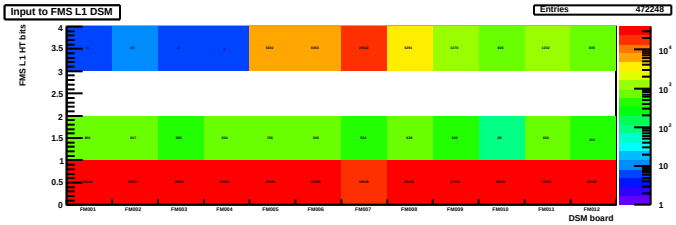

Input to FMS L1 DSM
Entries 1372
sumB - simulated
DSM board

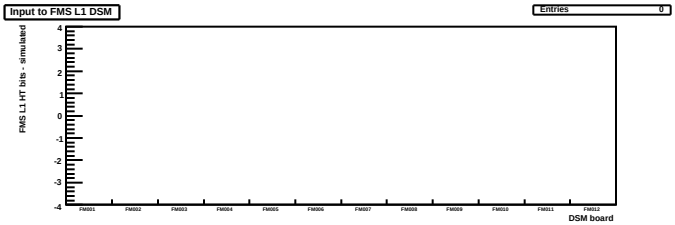

Input to FMS L1 DSM
Entries 472248
sumCD
DSM board

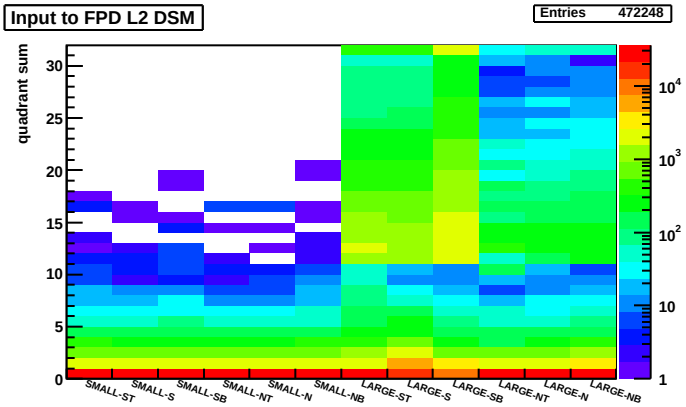

Input to FMS L1 DSM
Entries 905
sumCD - simulated
DSM board

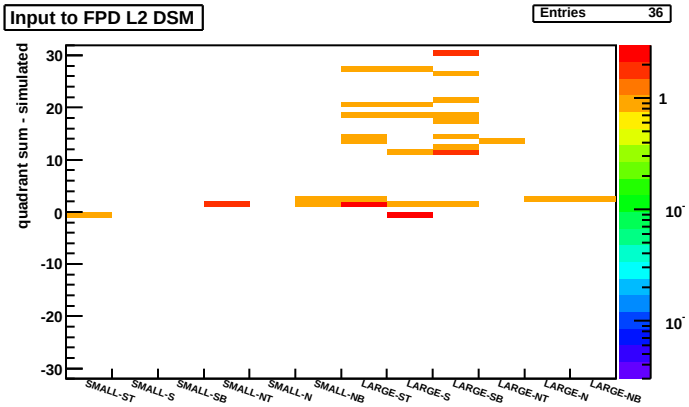

Input to FMS L1 DSM
Entries 472248
sumA
DSM board

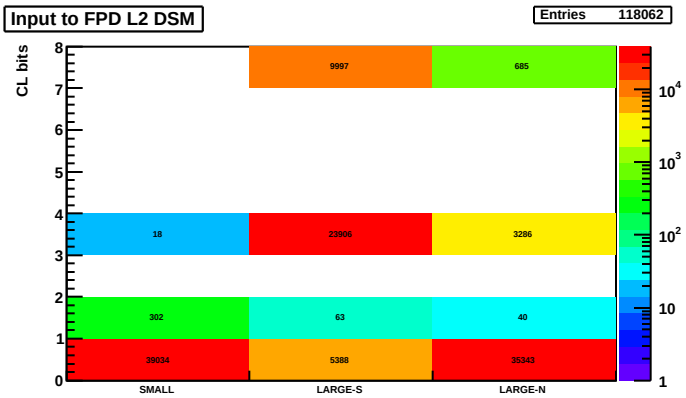

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

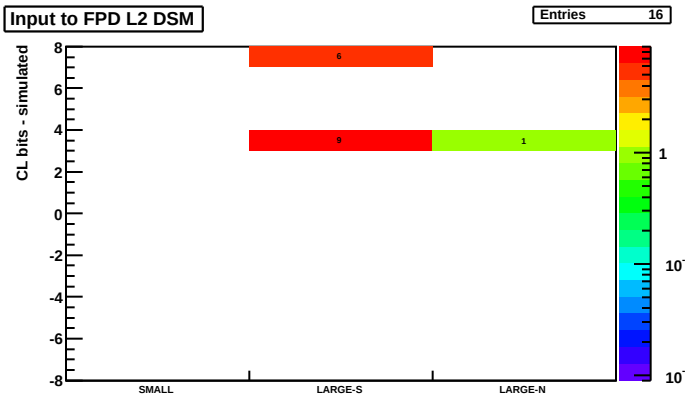

Input to FMS L1 DSM
Entries 472248
FMS L1 HT bits
DSM board

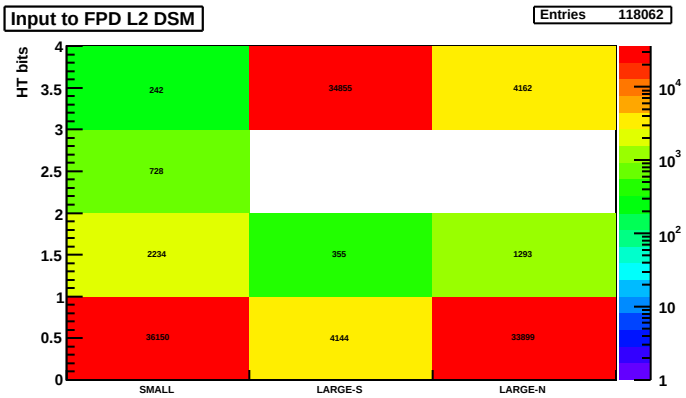

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

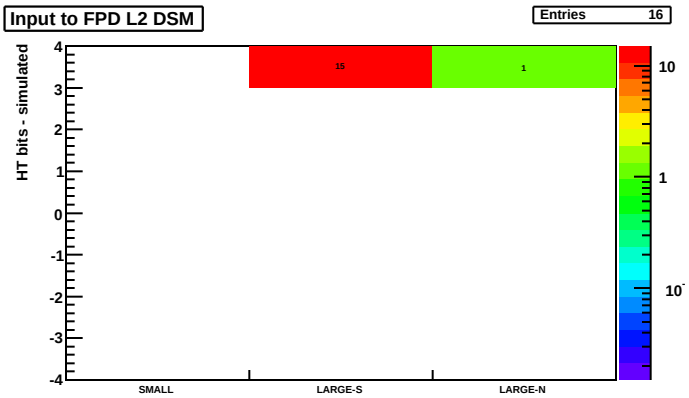

Input to FPD L2 DSM
Entries 472248
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

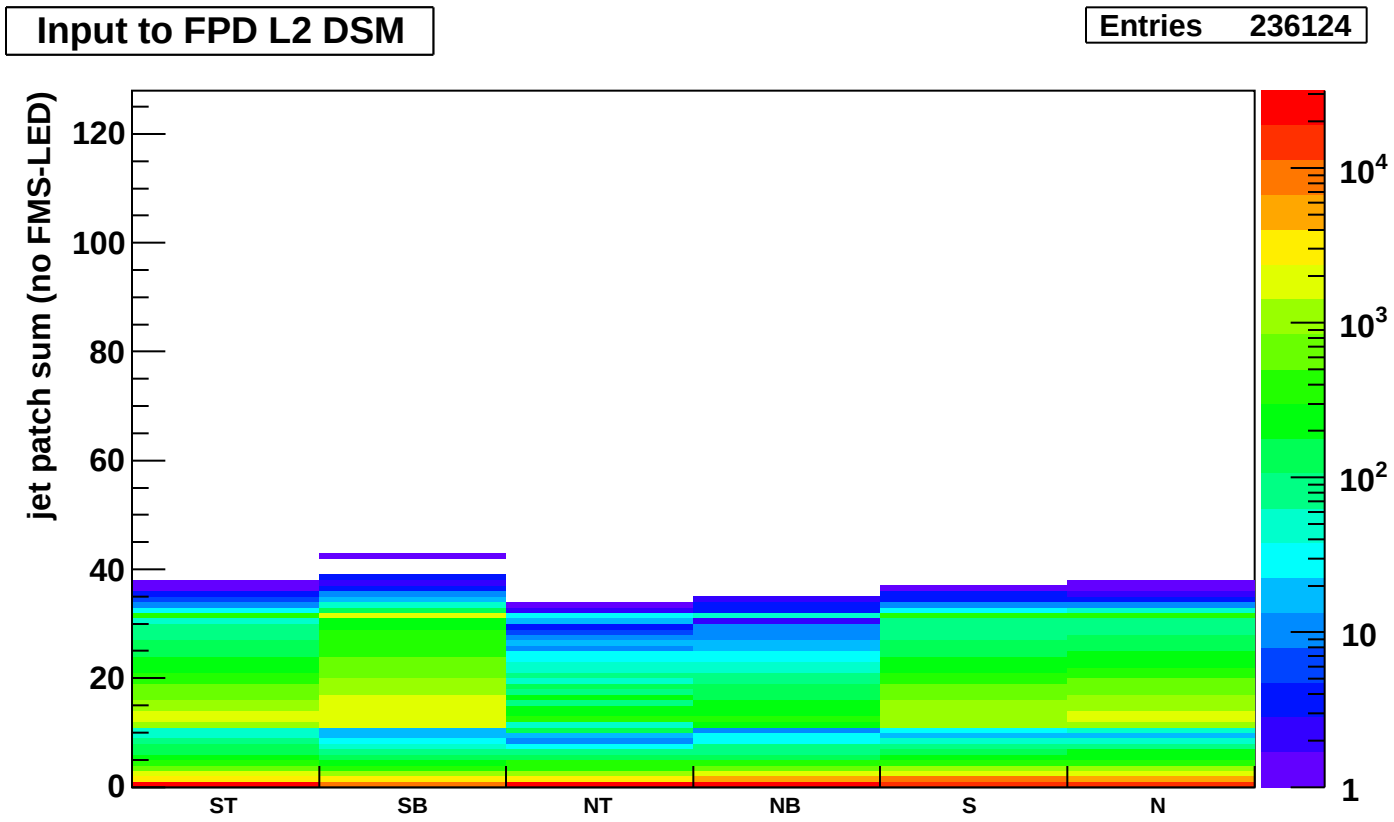

Input to FPD L2 DSM
Entries 36
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

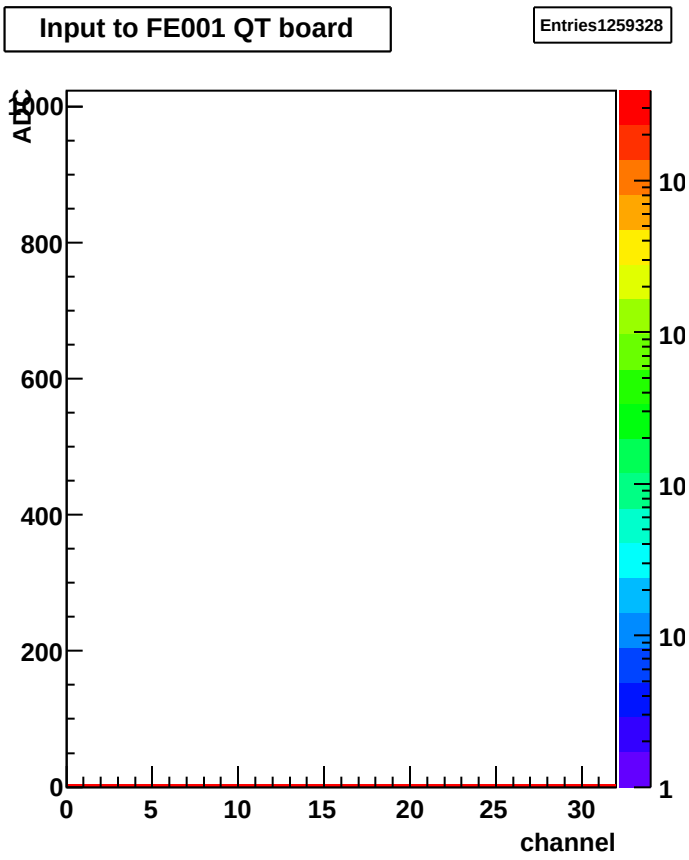

Input to FPD L2 DSM
Entries 118062
CL bits
9997
685
18
23906
3286
302
63
40
39034
5388
35343
SMALL
LARGE-S
LARGE-N

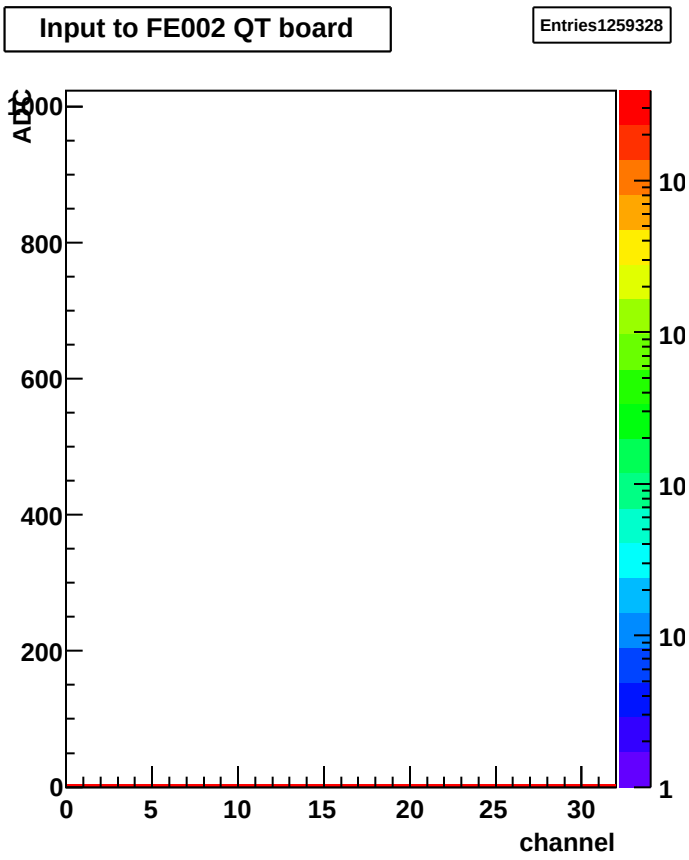

Input to FPD L2 DSM
Entries 16
CL bits - simulated
6
9
1
SMALL
LARGE-S
LARGE-N

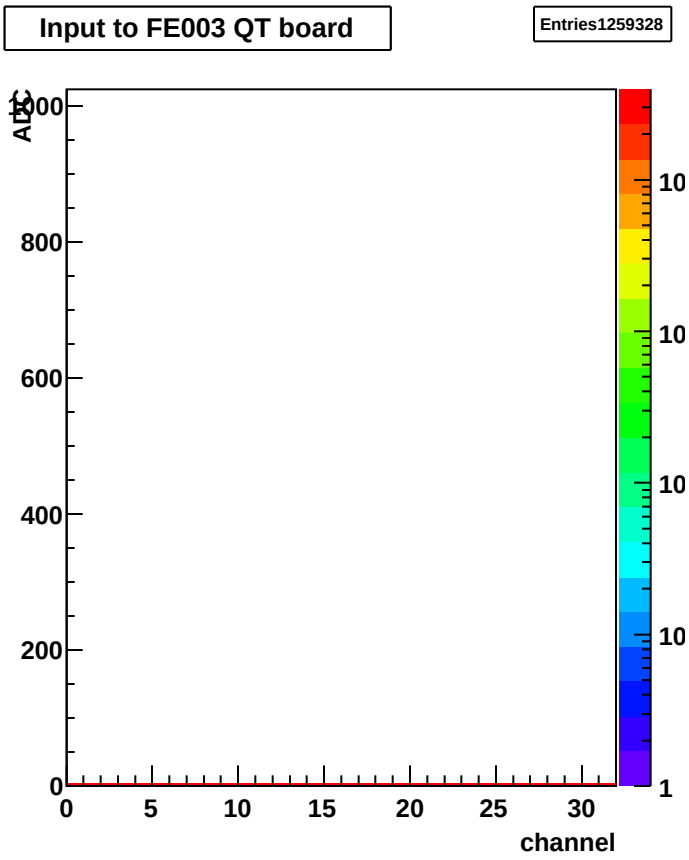

Input to FPD L2 DSM
Entries 118062
HT bits
242
34855
4162
728
2234
355
1293
36150
4144
33899
SMALL
LARGE-S
LARGE-N

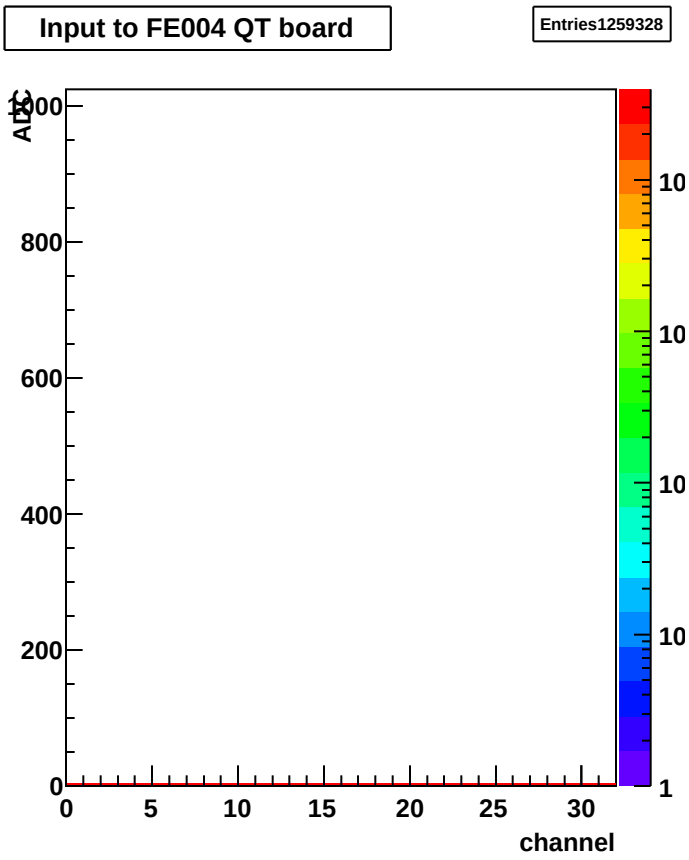

Input to FPD L2 DSM
Entries 16
HT bits - simulated
15
1
SMALL
LARGE-S
LARGE-N

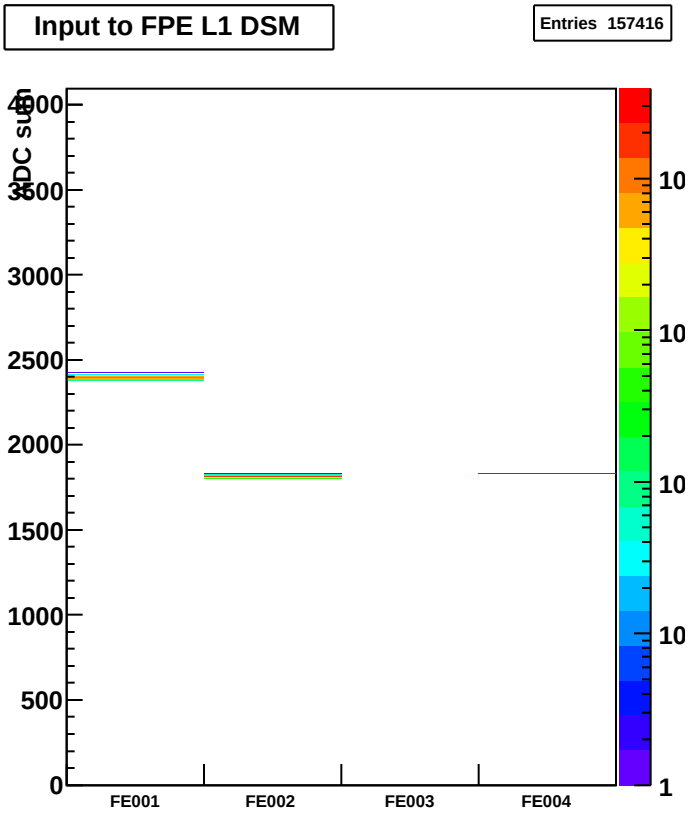

Input to FPD L2 DSM
Entries 236124
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

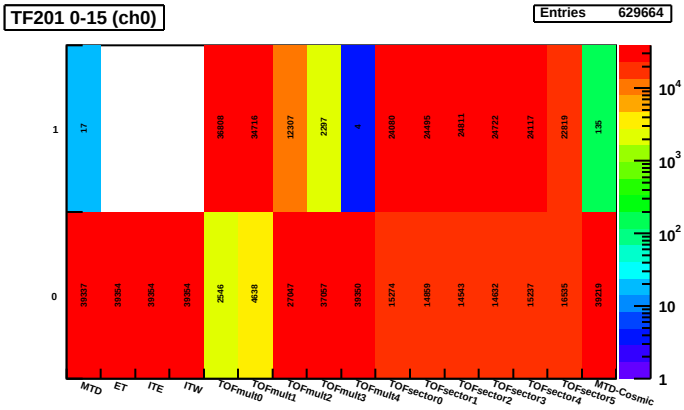

Input to FE001 QT board
Entries1259328
ADC
channel

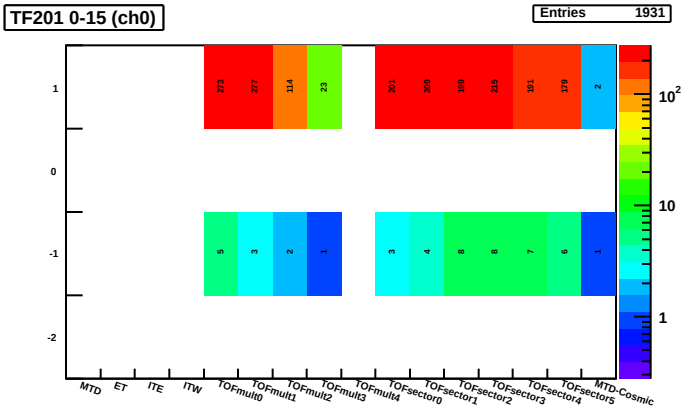

Input to FE002 QT board
Entries1259328
ADC
channel

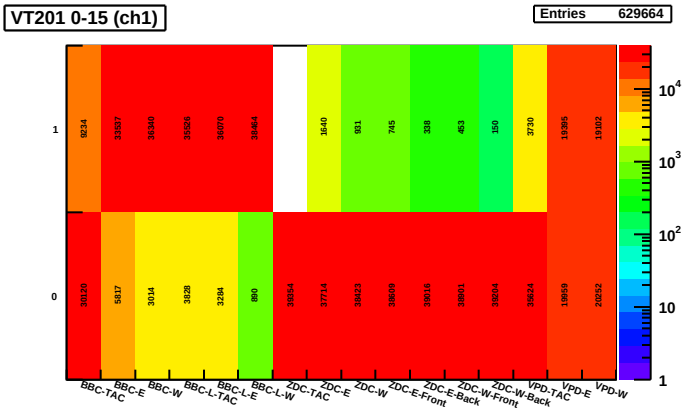

Input to FE003 QT board
Entries1259328
ADC
channel

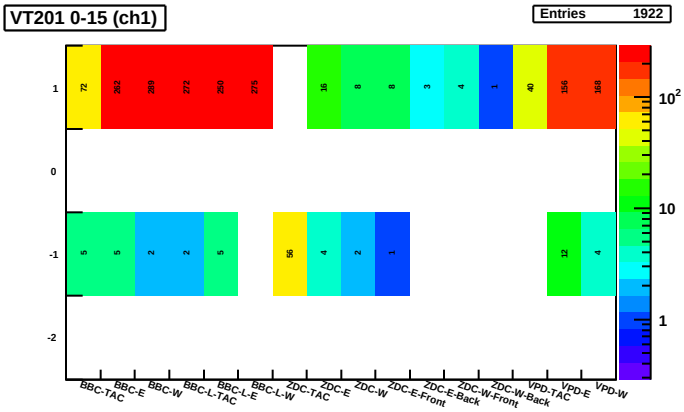

Input to FE004 QT board
Entries1259328
ADC
channel

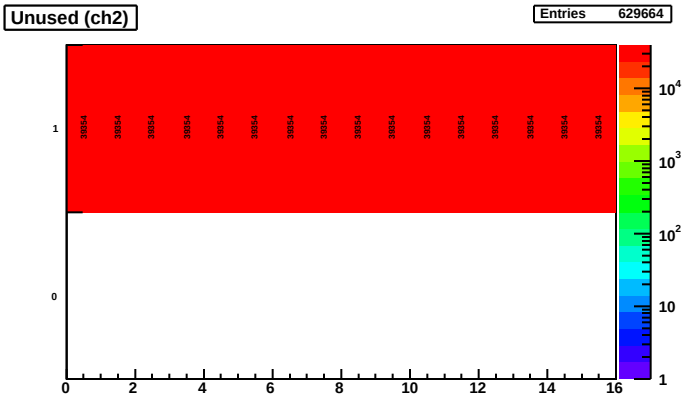

Input to FPE L1 DSM
Entries 157416
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

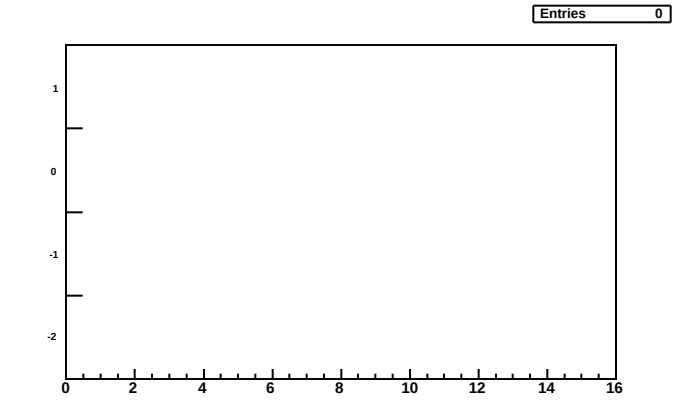

TF201 0-15 (ch0)
Entries 629664
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

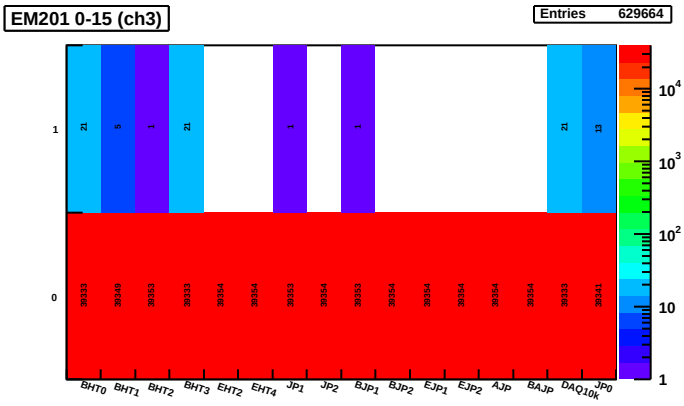

TF201 0-15 (ch0)
Entries 1931
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

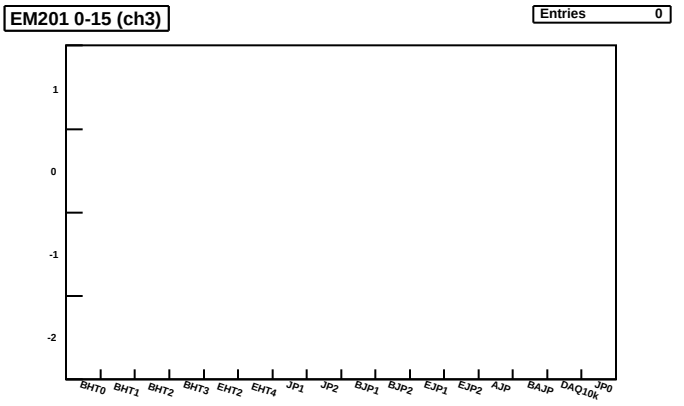

VT201 0-15 (ch1)
Entries 629664
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1922
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 629664


Entries 0


EM201 0-15 (ch3)
Entries 629664
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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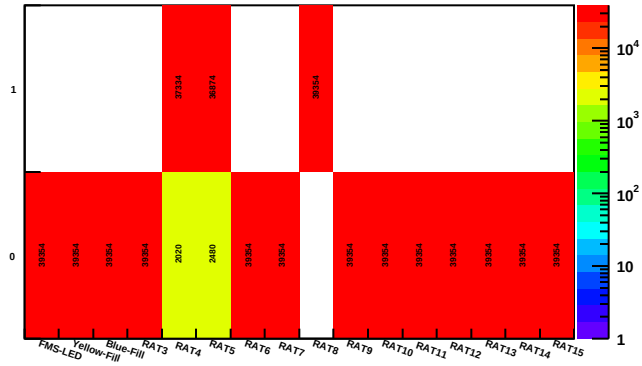

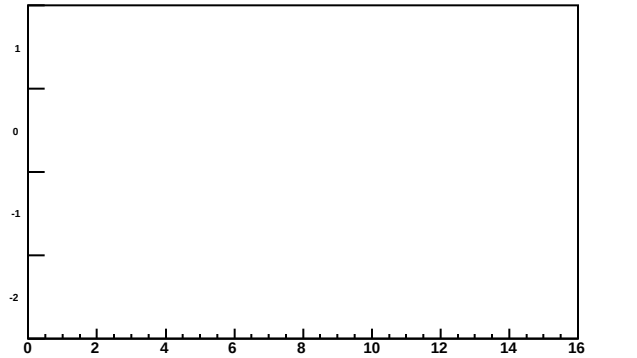

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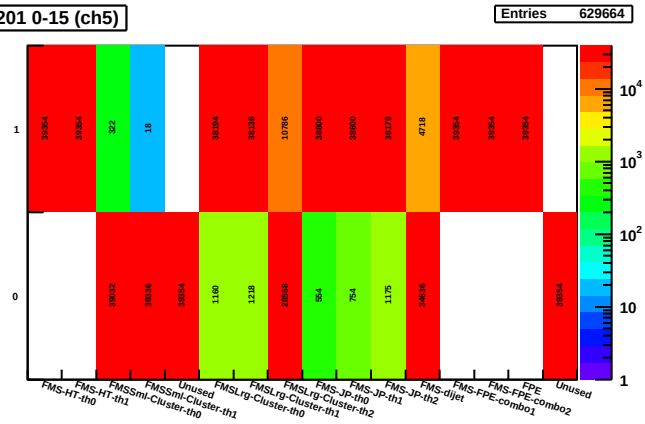

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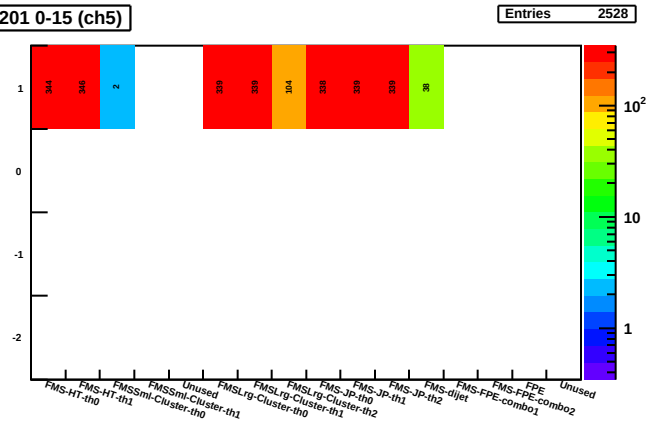

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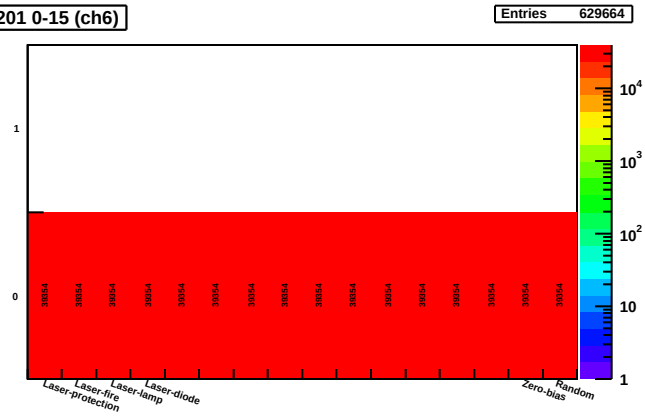

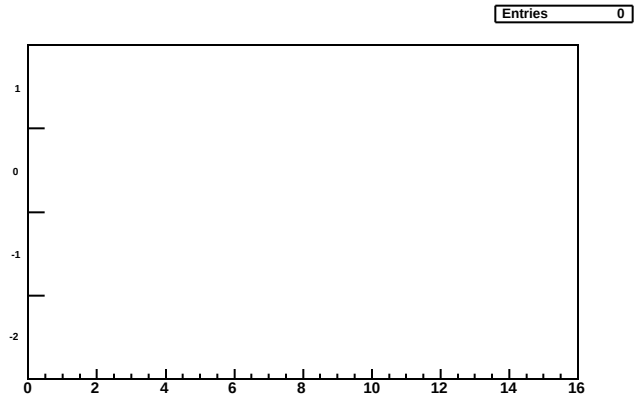
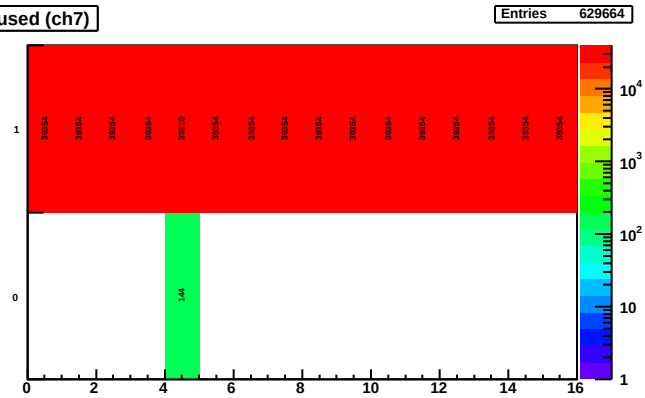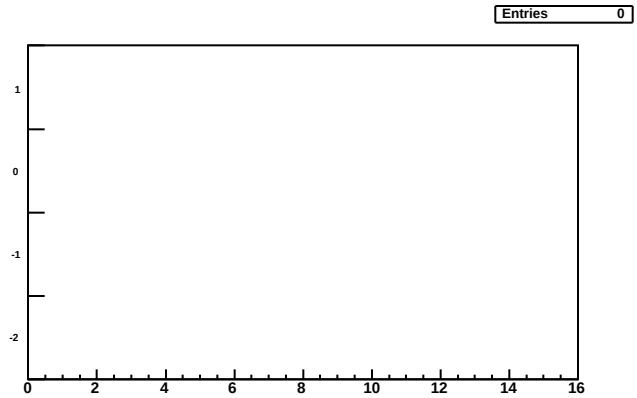
Unused (ch7)