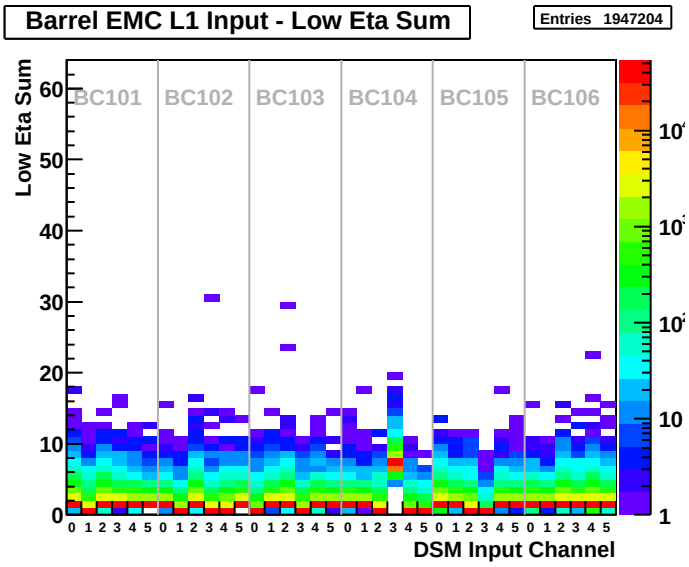
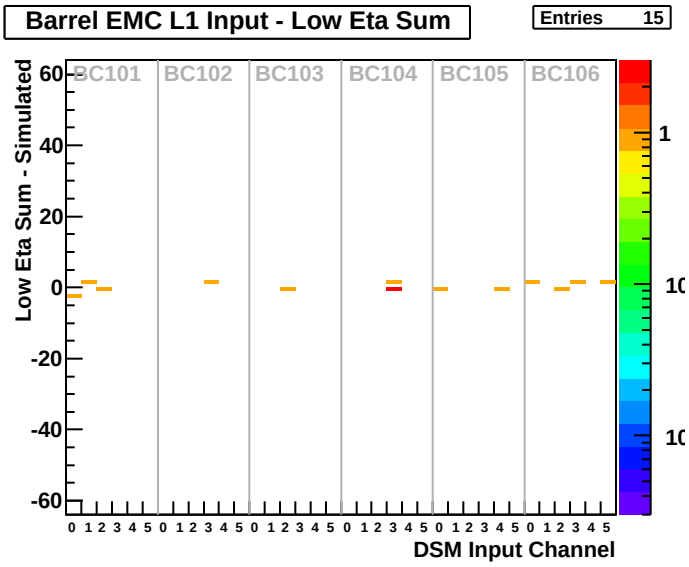
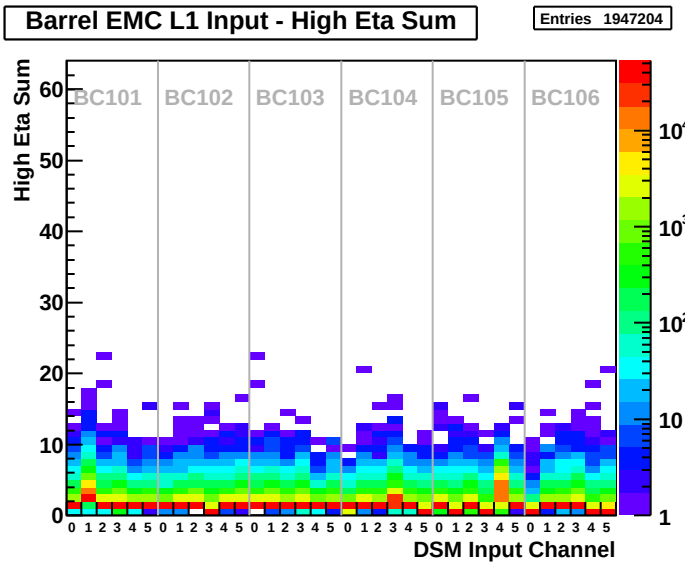
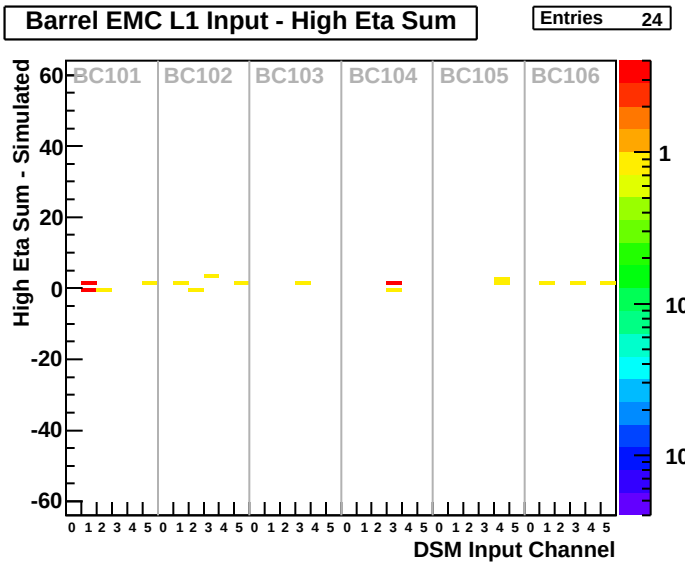
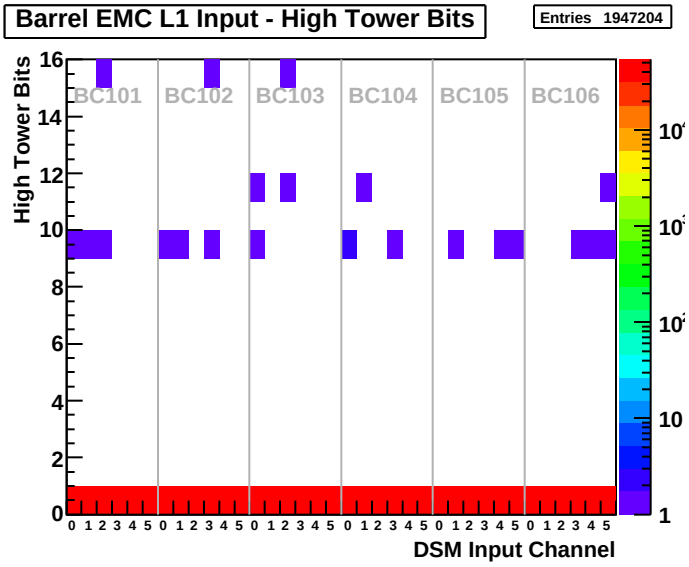
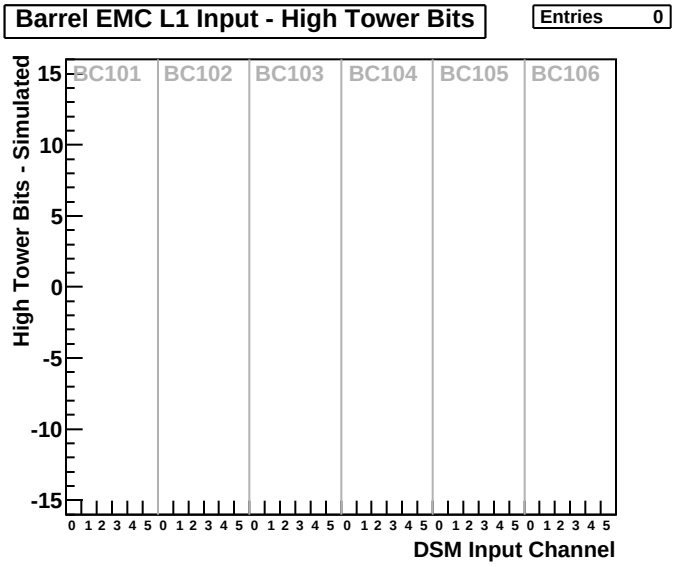

Barrel EMC L1 Input - Low Eta Sum
Entries 1947204
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

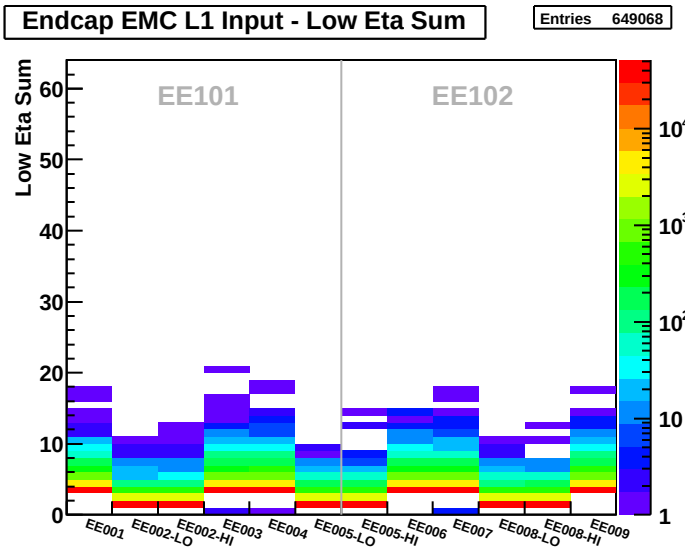

Barrel EMC L1 Input - Low Eta Sum
Entries 15
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

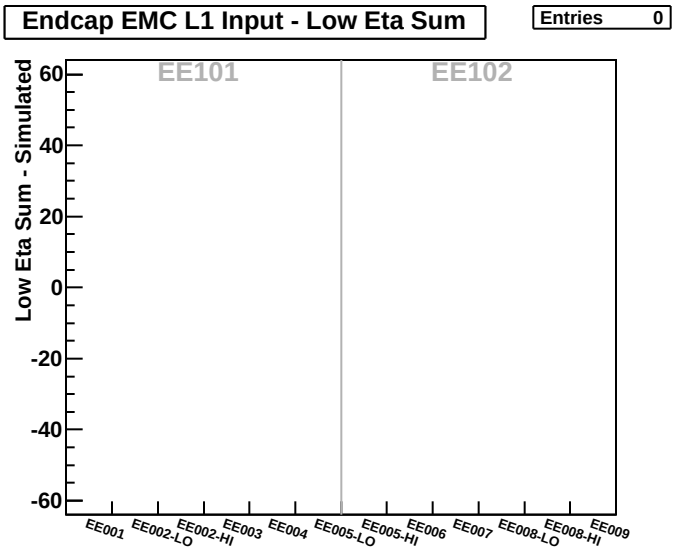

Barrel EMC L1 Input - High Eta Sum
Entries 1947204
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

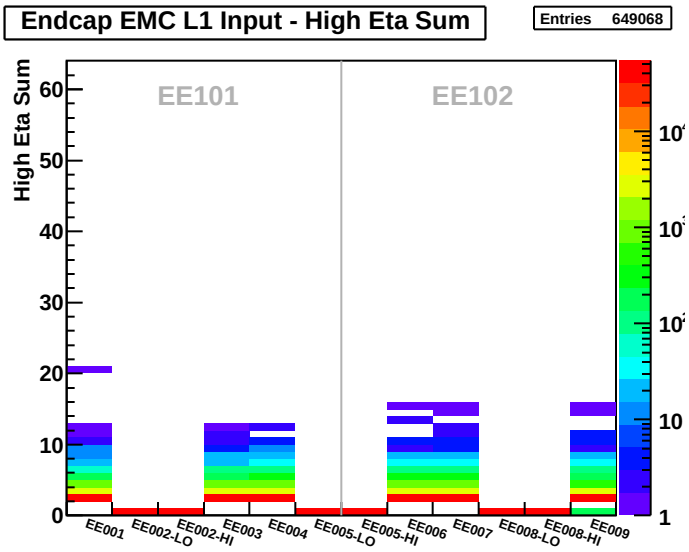

Barrel EMC L1 Input - High Eta Sum
Entries 24
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

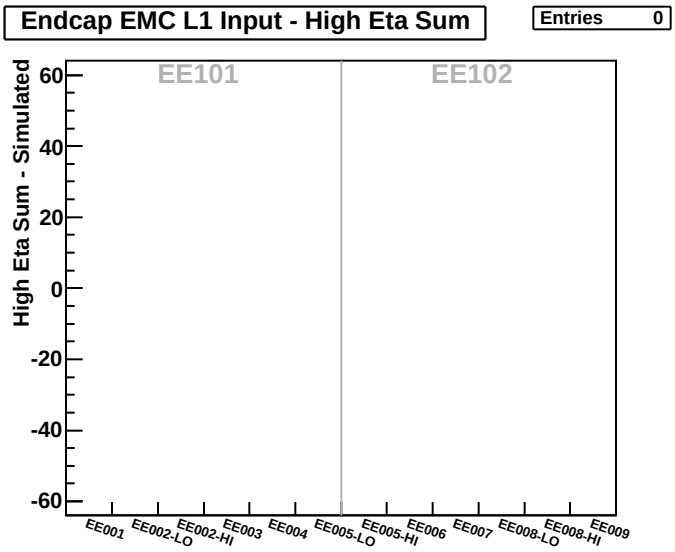

Barrel EMC L1 Input - High Tower Bits
Entries 1947204
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

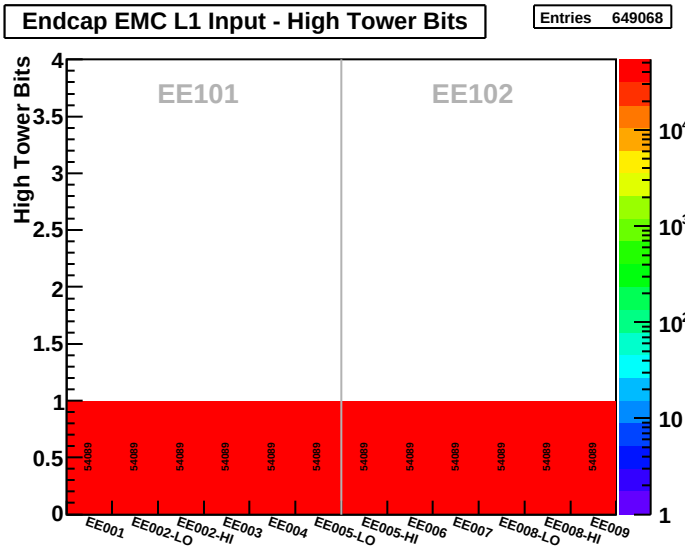

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

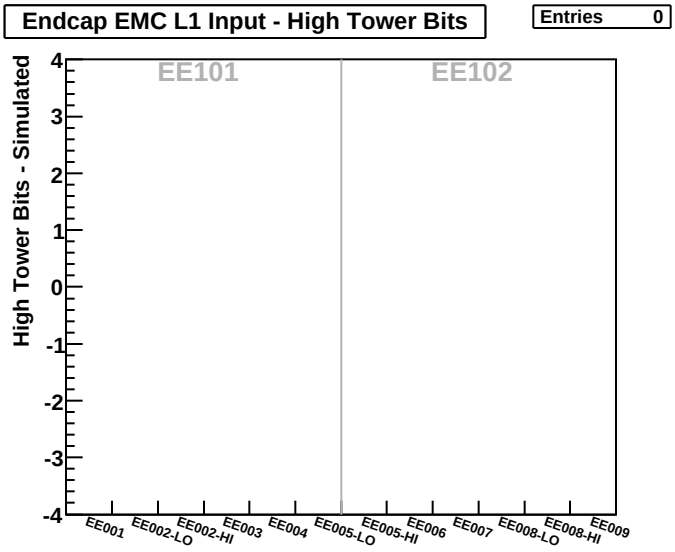

Endcap EMC L1 Input - Low Eta Sum
Entries 649068
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

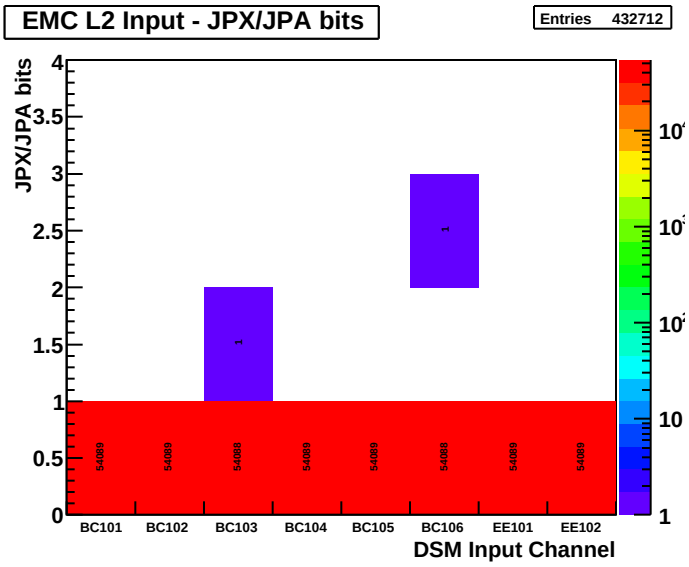

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

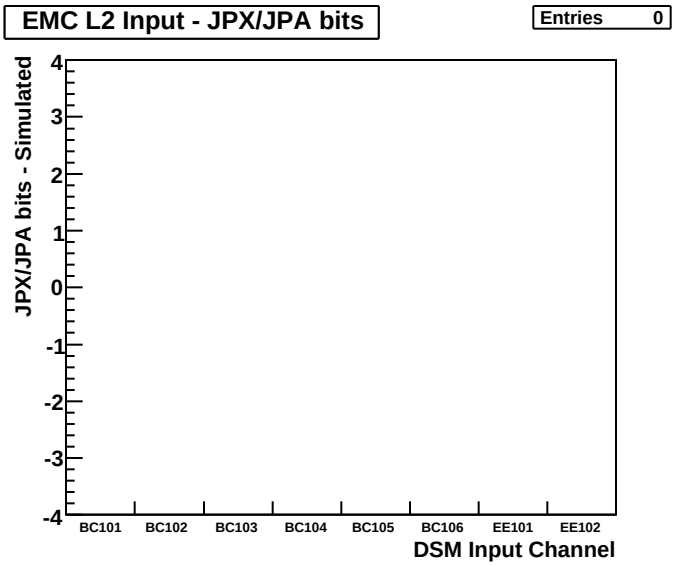

Endcap EMC L1 Input - High Eta Sum
Entries 649068
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

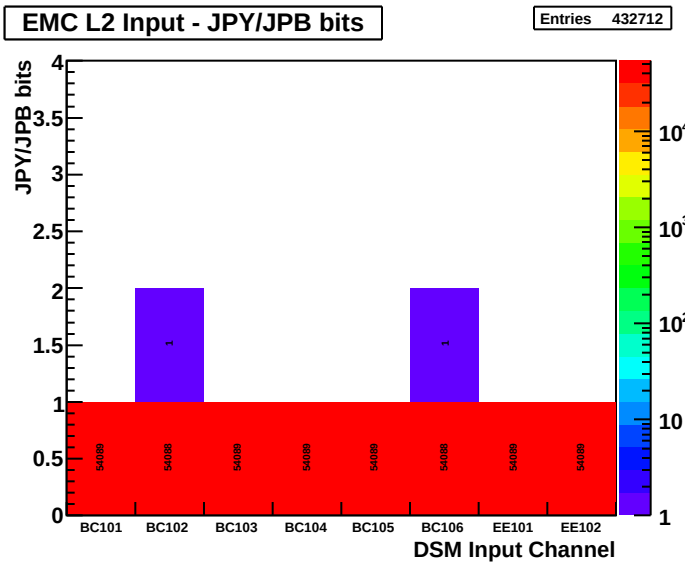

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

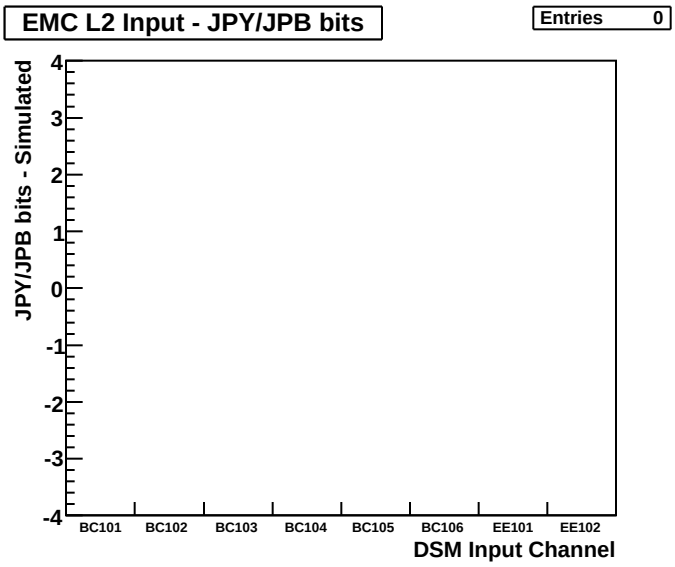

Endcap EMC L1 Input - High Tower Bits
Entries 649068
High Tower Bits
EE101
EE102
54089
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

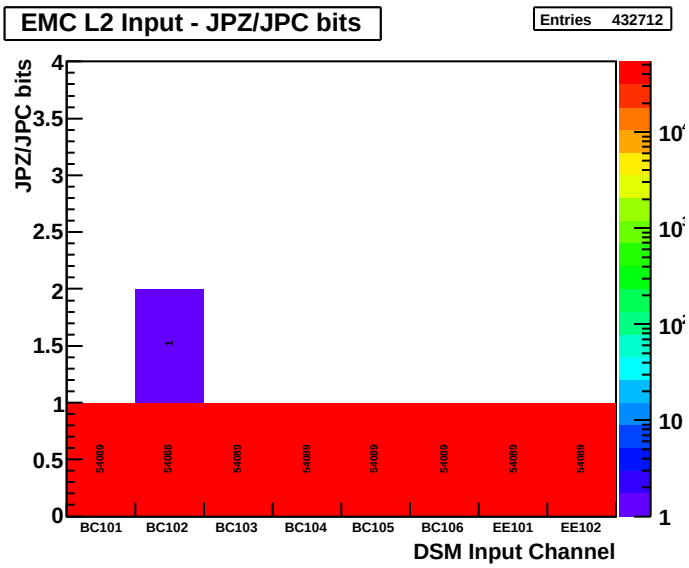

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

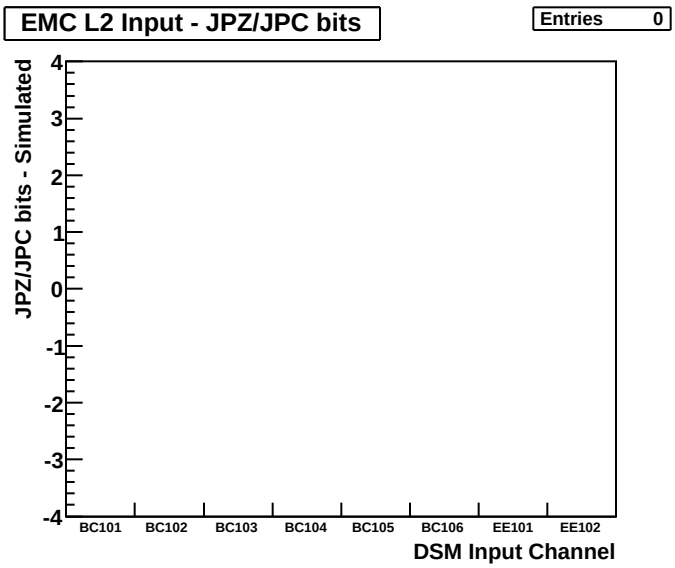

EMC L2 Input - JPX/JPA bits
Entries 432712
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

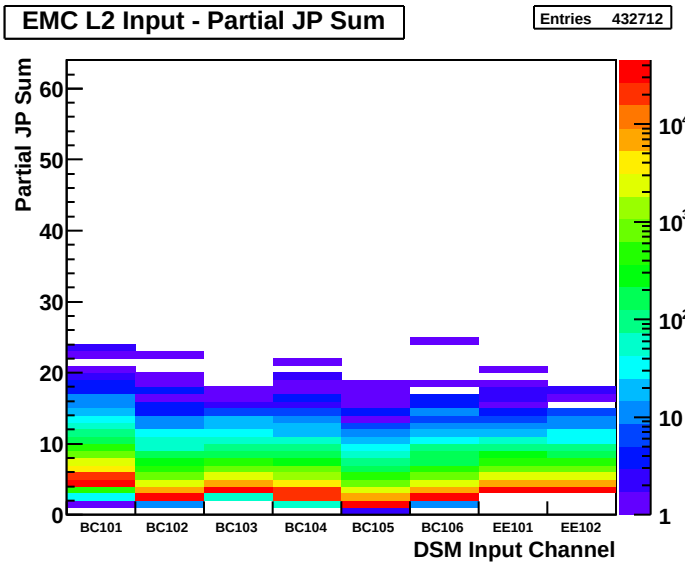

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

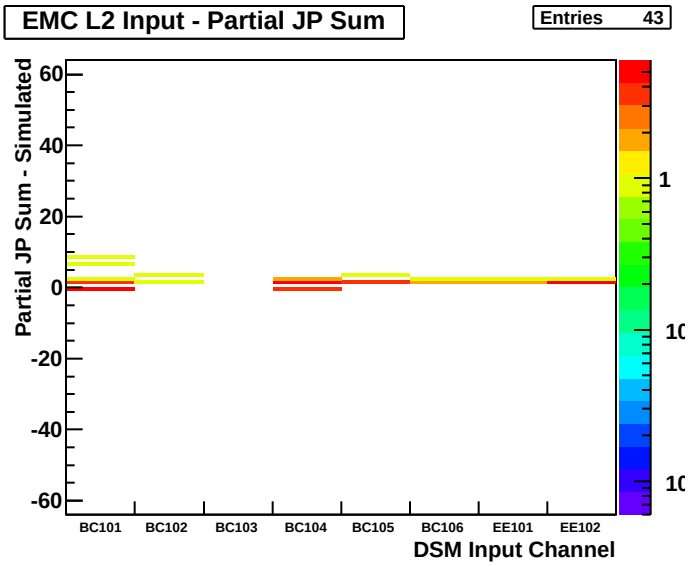

EMC L2 Input - JPY/JPB bits
Entries 432712
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

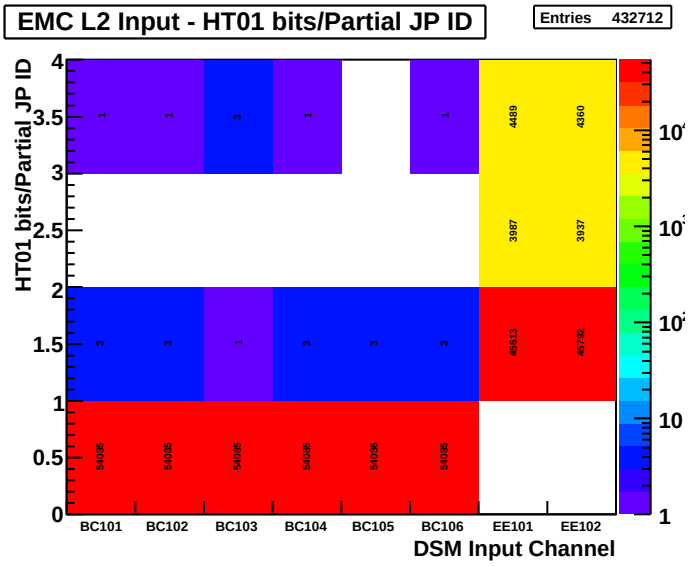

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

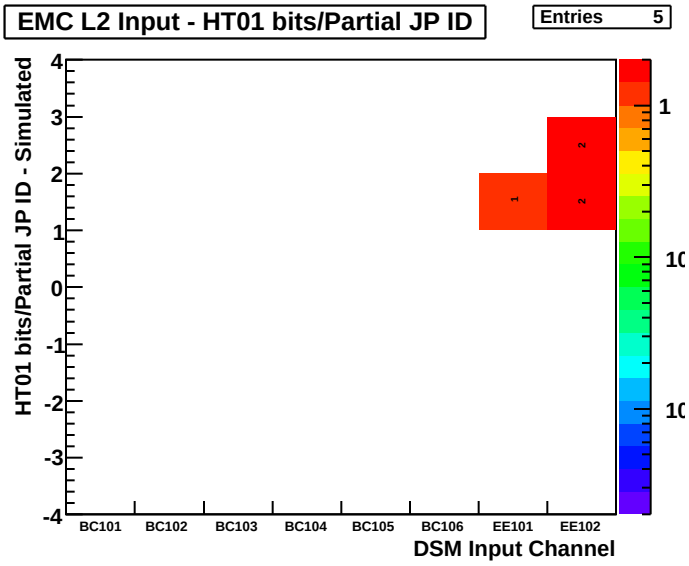

EMC L2 Input - JPZ/JPC bits
Entries 432712
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

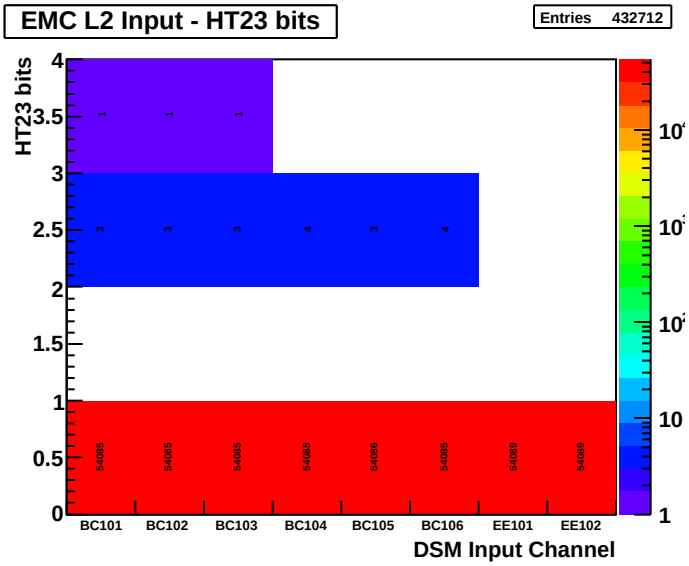

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

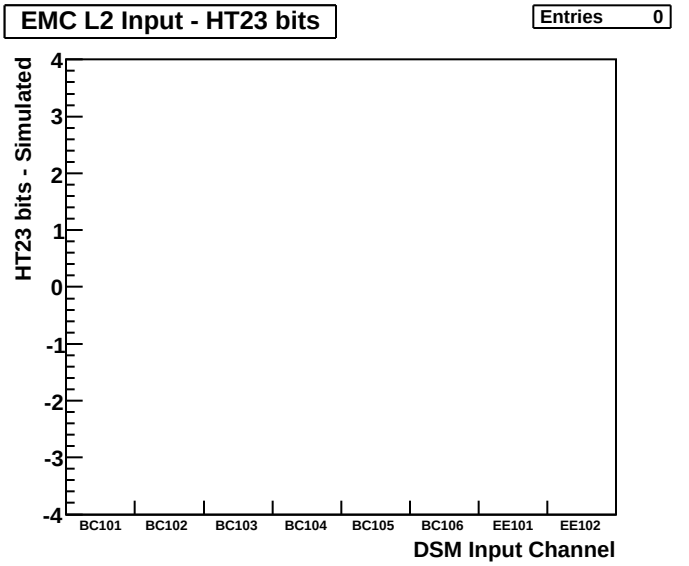

EMC L2 Input - Partial JP Sum
Entries 432712
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

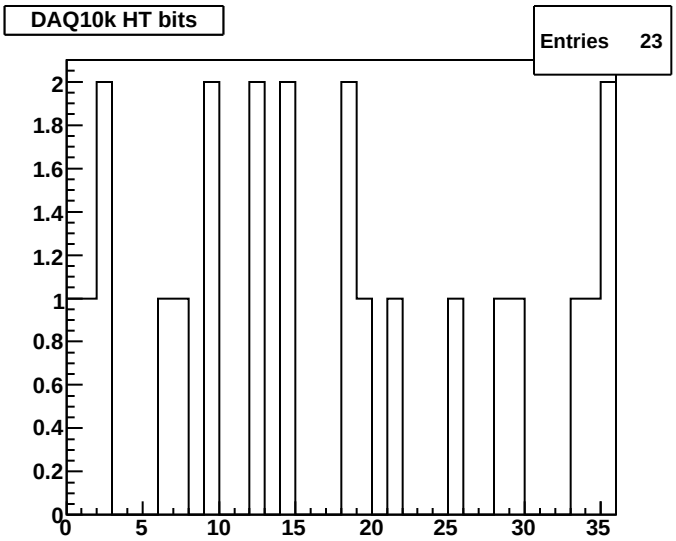

EMC L2 Input - Partial JP Sum
Entries 43
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

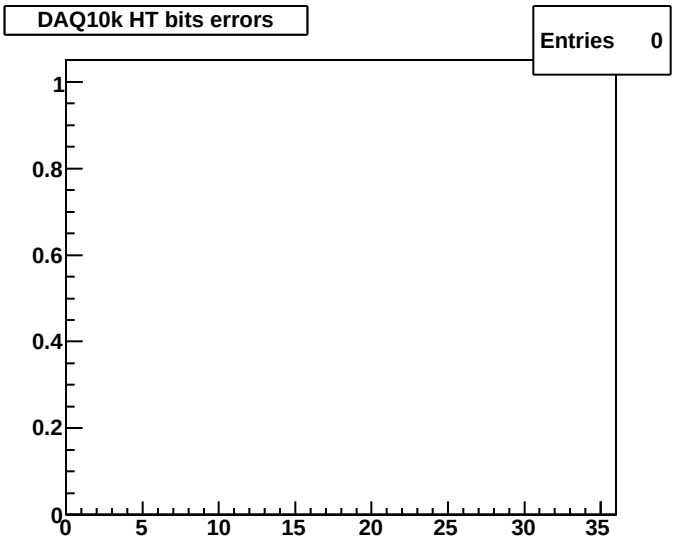

EMC L2 Input - HT01 bits/Partial JP ID
Entries 432712
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

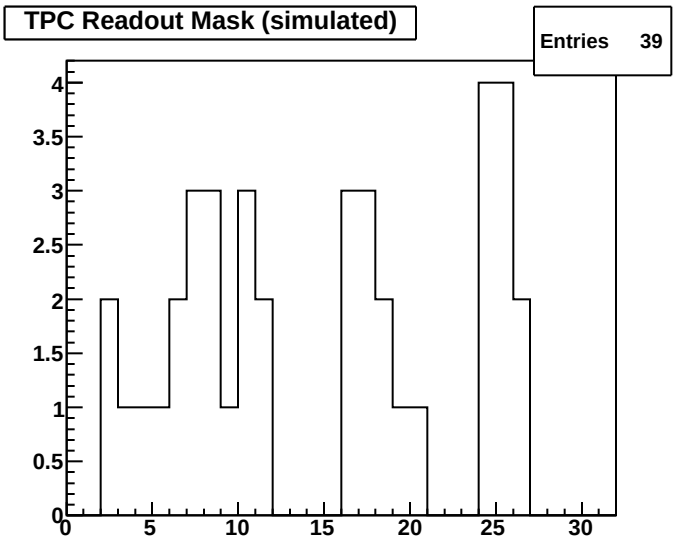

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

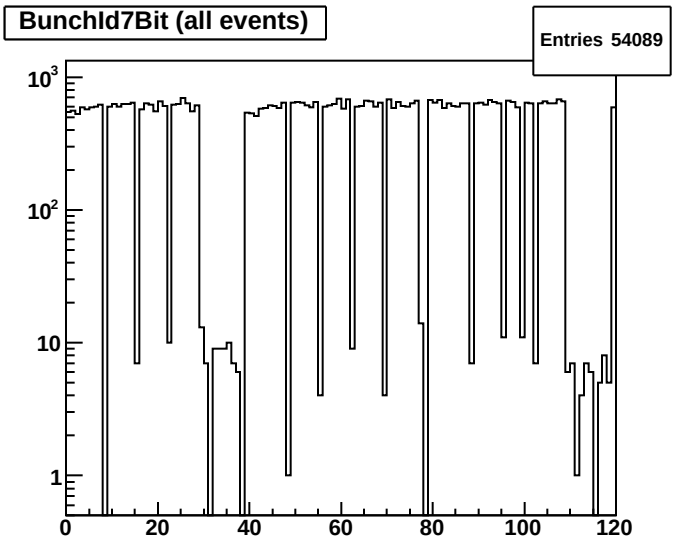

EMC L2 Input - HT23 bits
Entries 432712
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

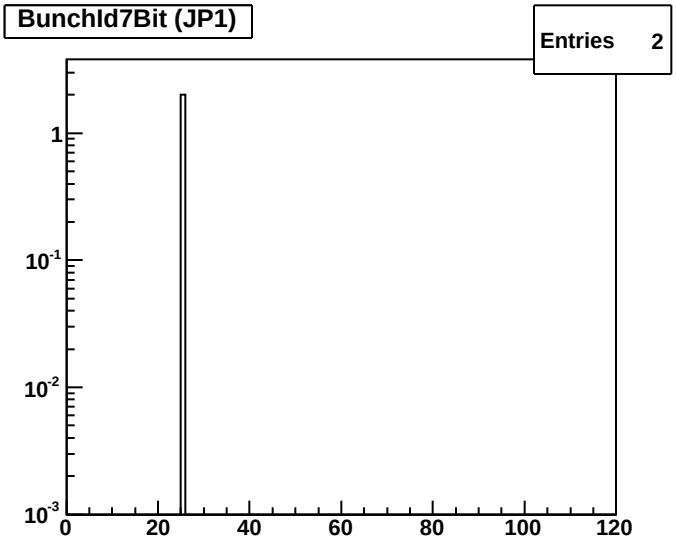

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

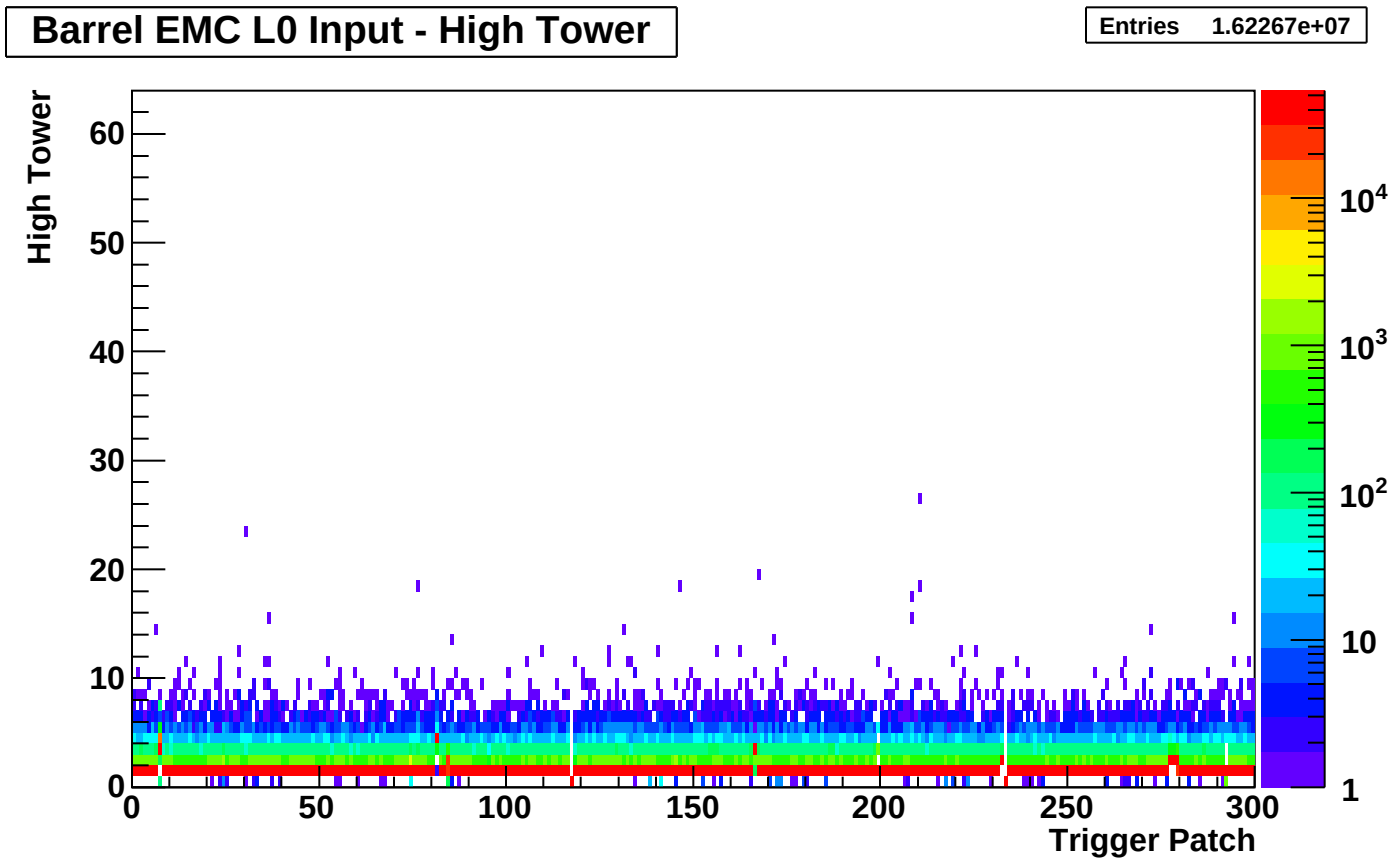

DAQ10k HT bits
Entries 23

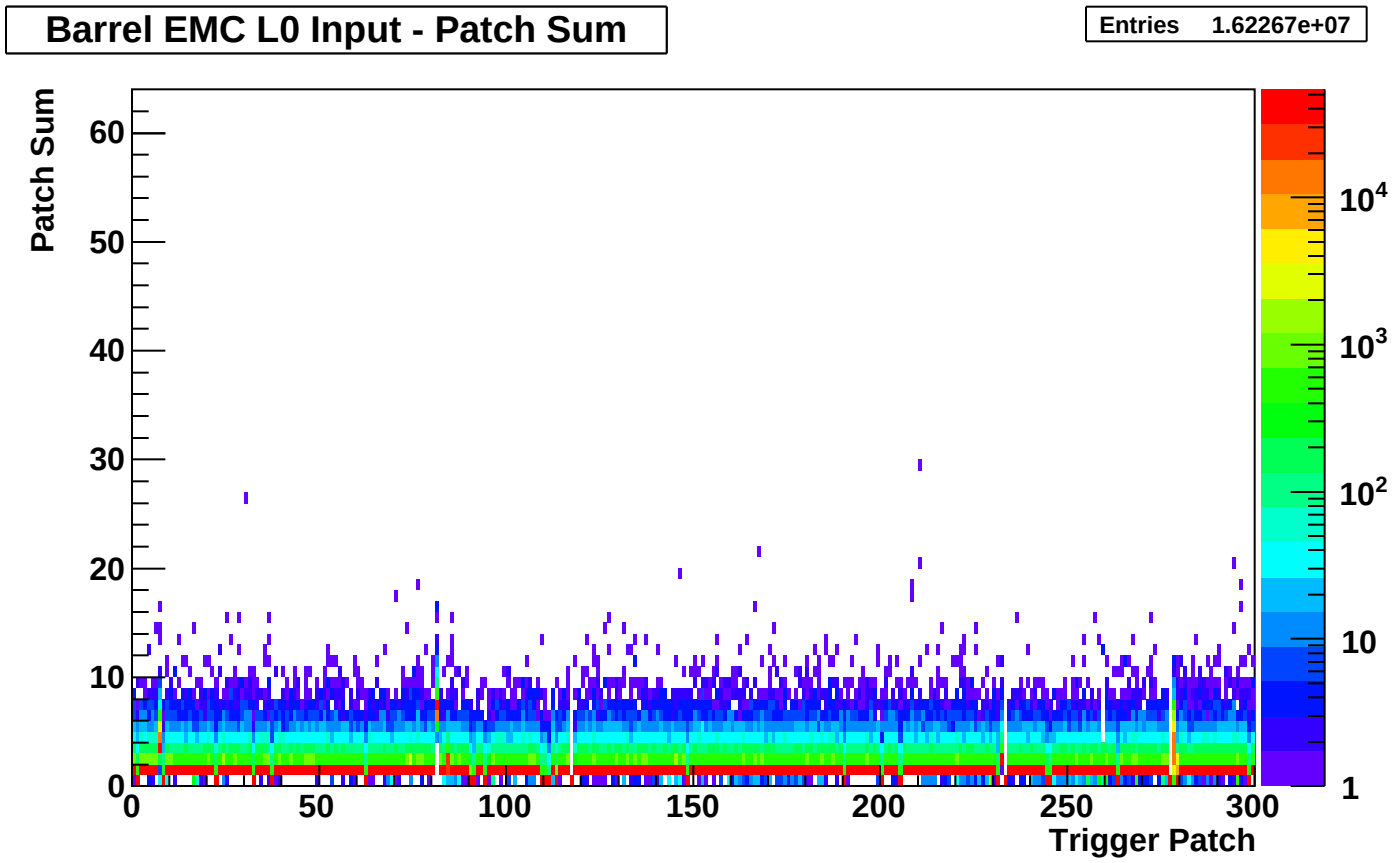

DAQ10k HT bits errors
Entries 0

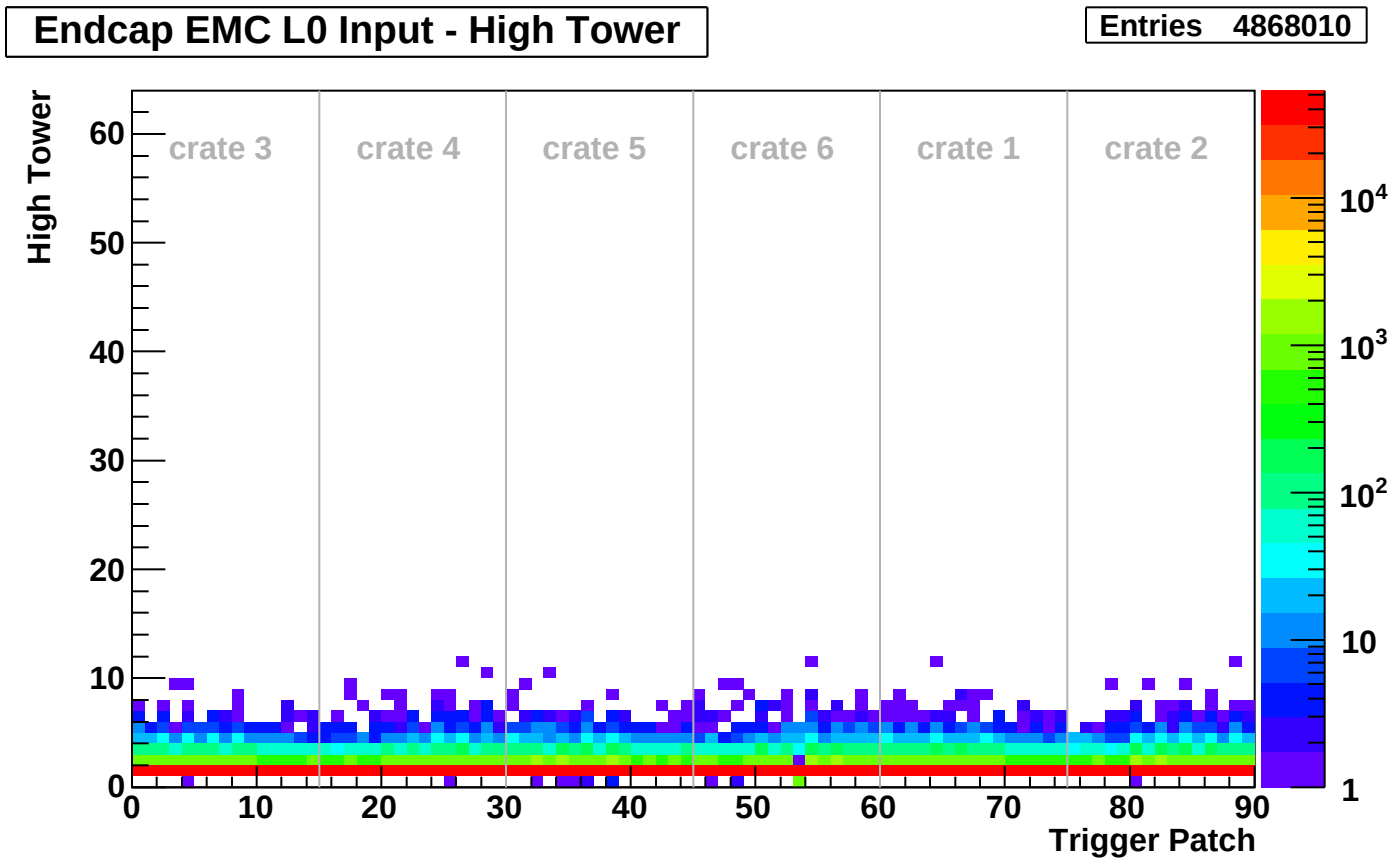

TPC Readout Mask (simulated)
Entries 39

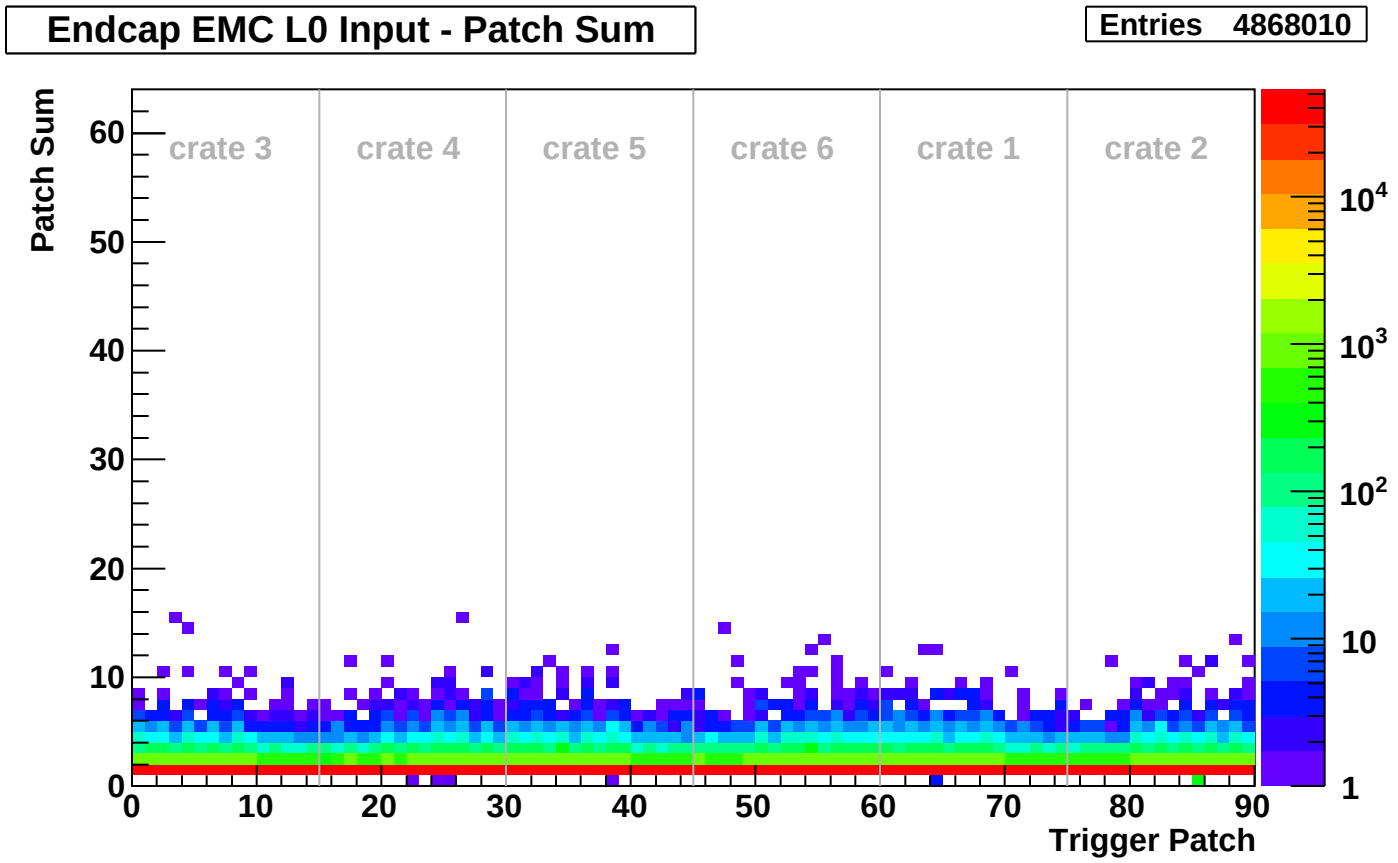

BunchId7Bit (all events)
Entries 54089

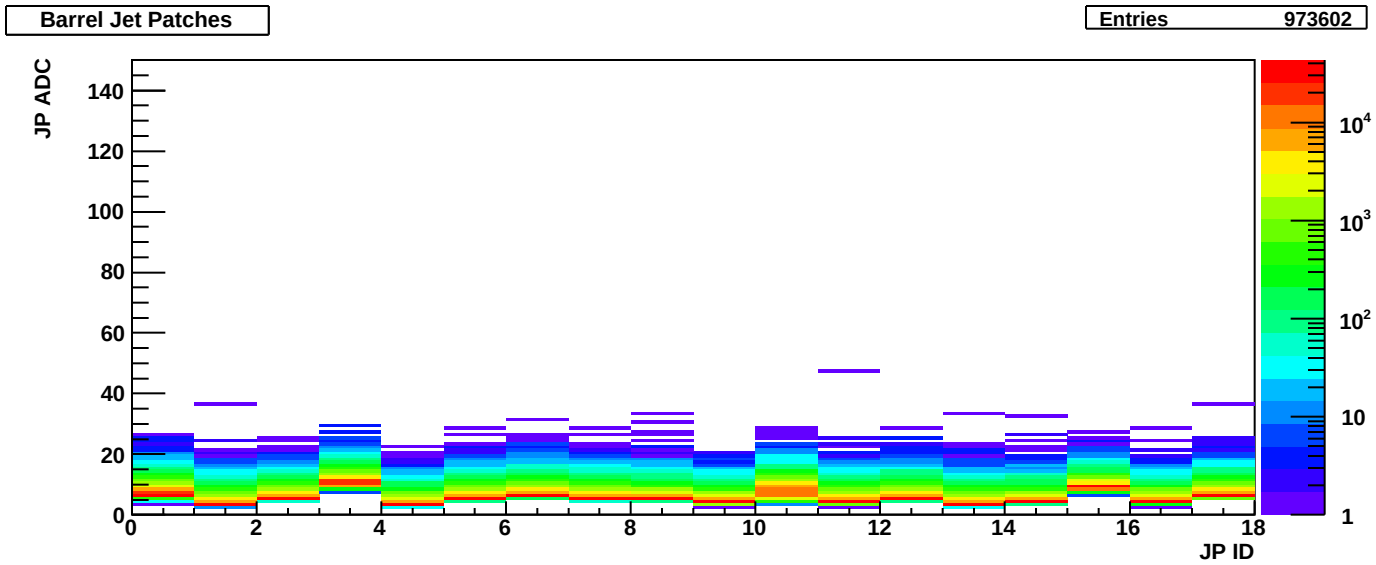

BunchId7Bit (JP1)
Entries 2

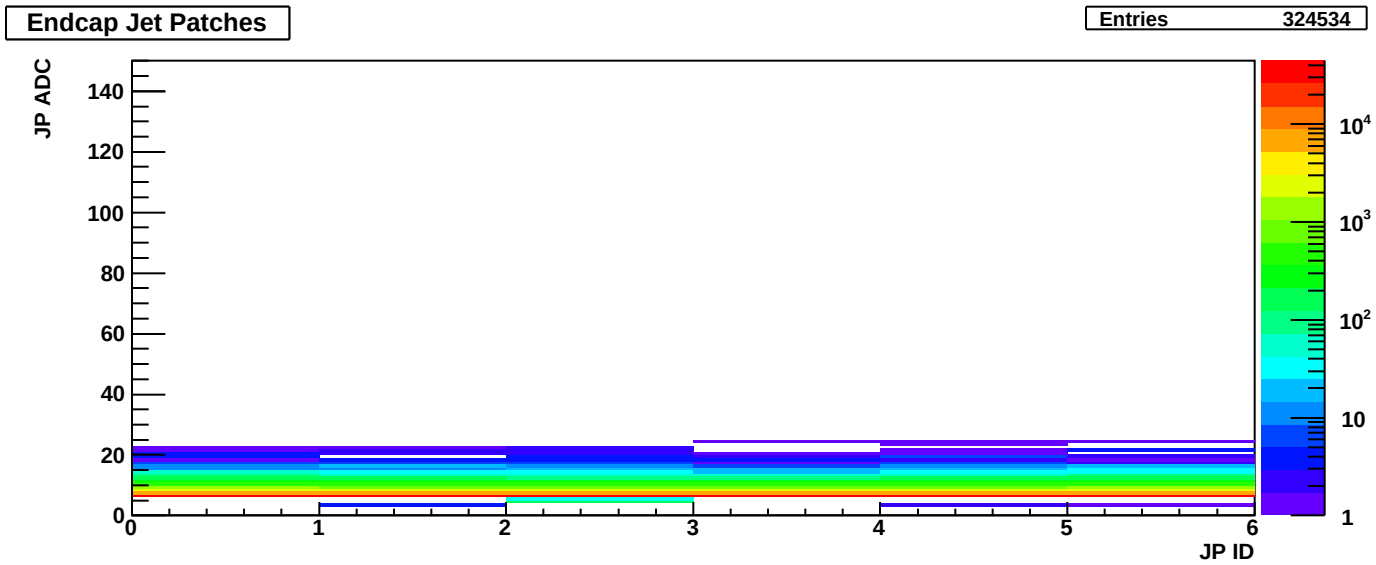

Barrel EMC L0 Input - High Tower
Entries 1.62267e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

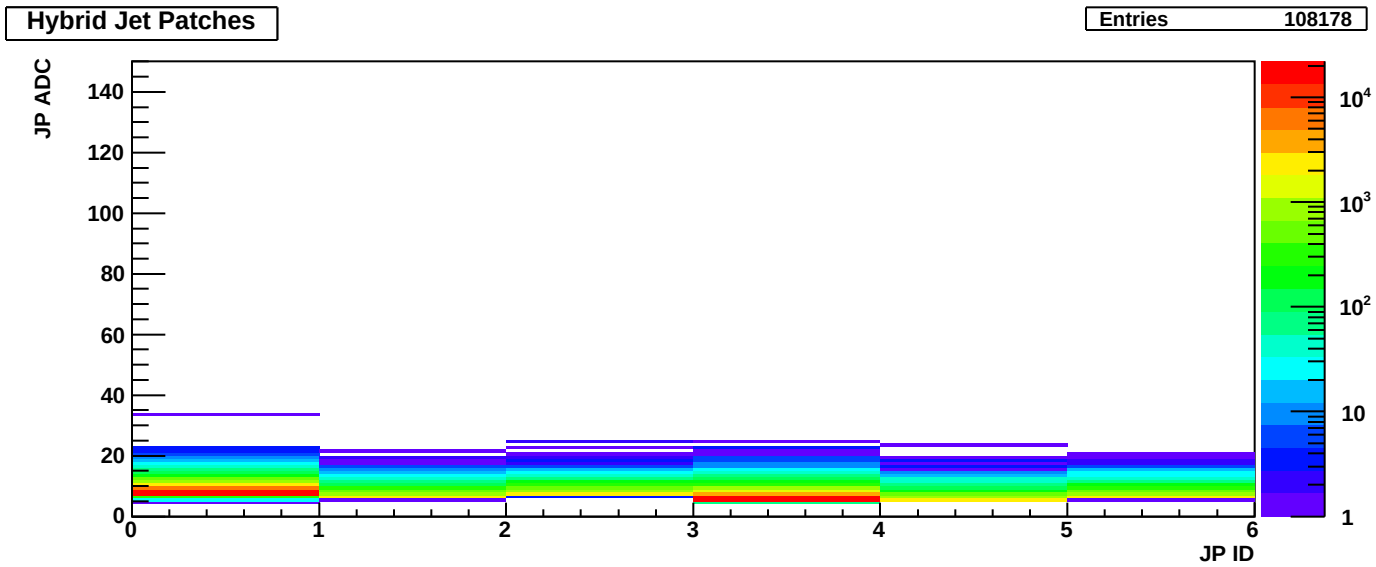

Barrel EMC L0 Input - Patch Sum
Entries 1.62267e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

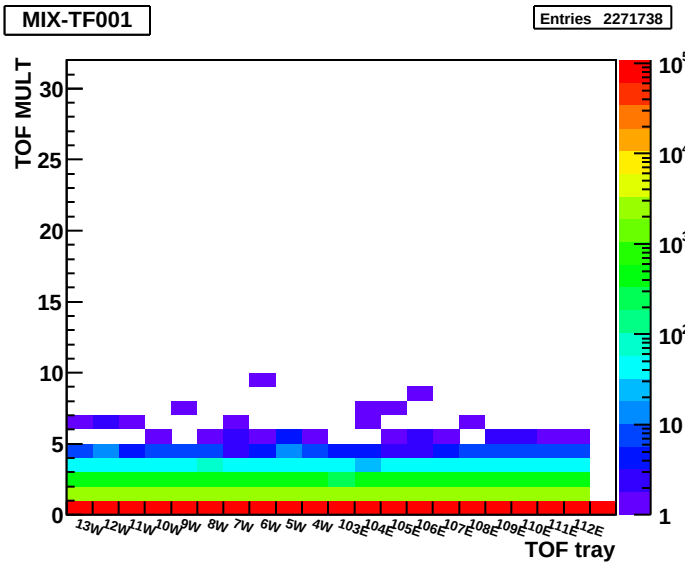

Endcap EMC L0 Input - High Tower
Entries 4868010
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

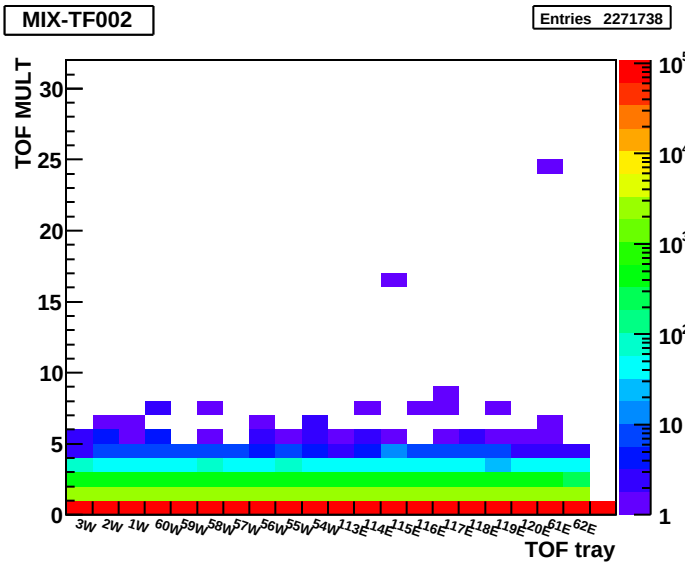

Endcap EMC L0 Input - Patch Sum
Entries 4868010
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

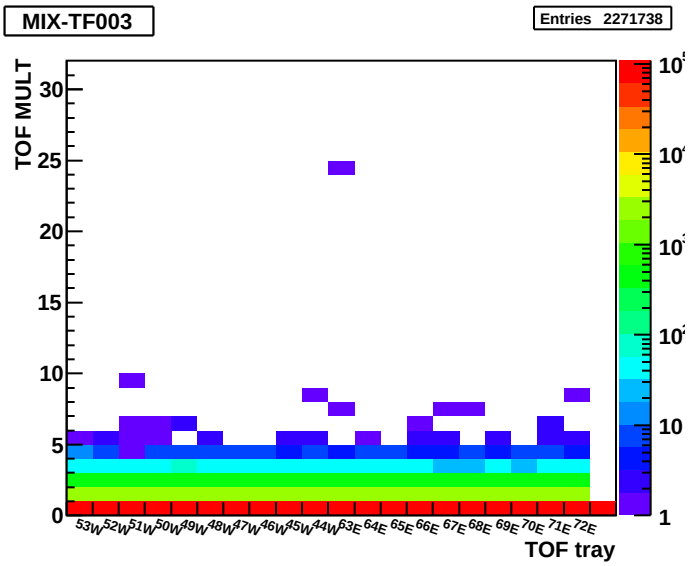

Barrel Jet Patches
Entries 973602
JP ADC
JP ID

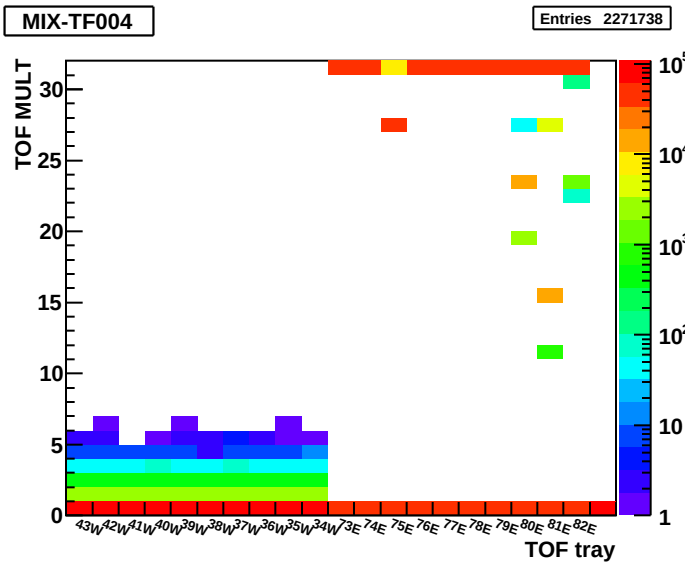

Endcap Jet Patches
Entries 324534
JP ADC
JP ID

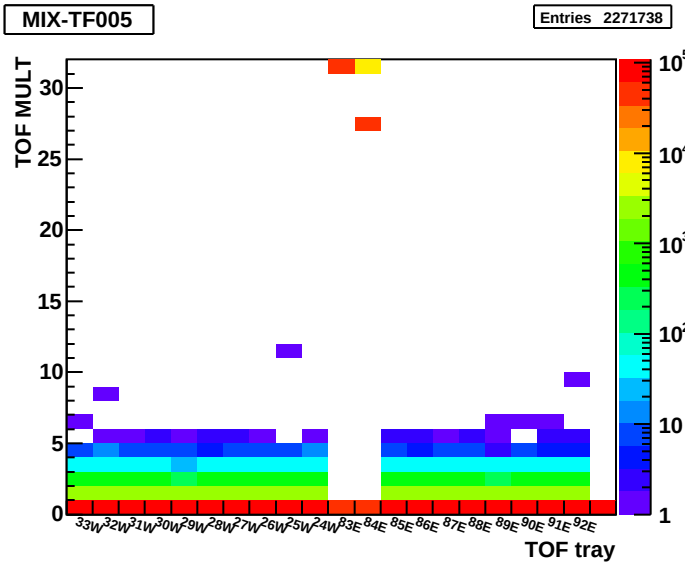

Hybrid Jet Patches
Entries 108178
JP ADC
JP ID

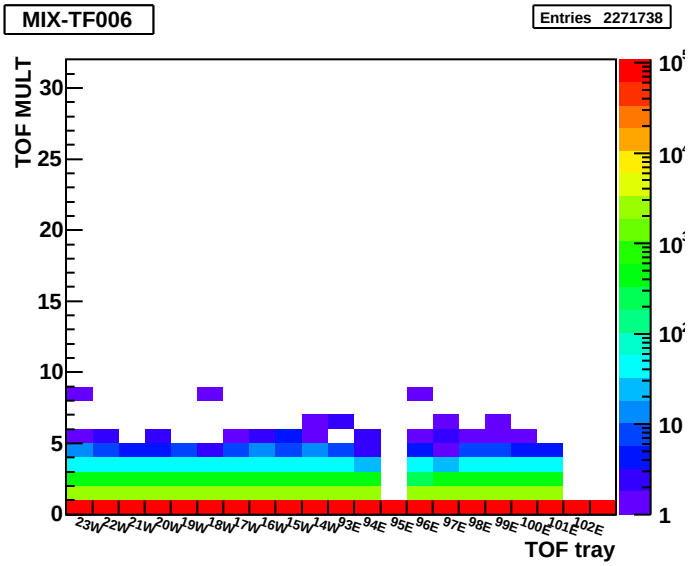

MIX-TF001
Entries 2271738
TOF MULT
TOF tray

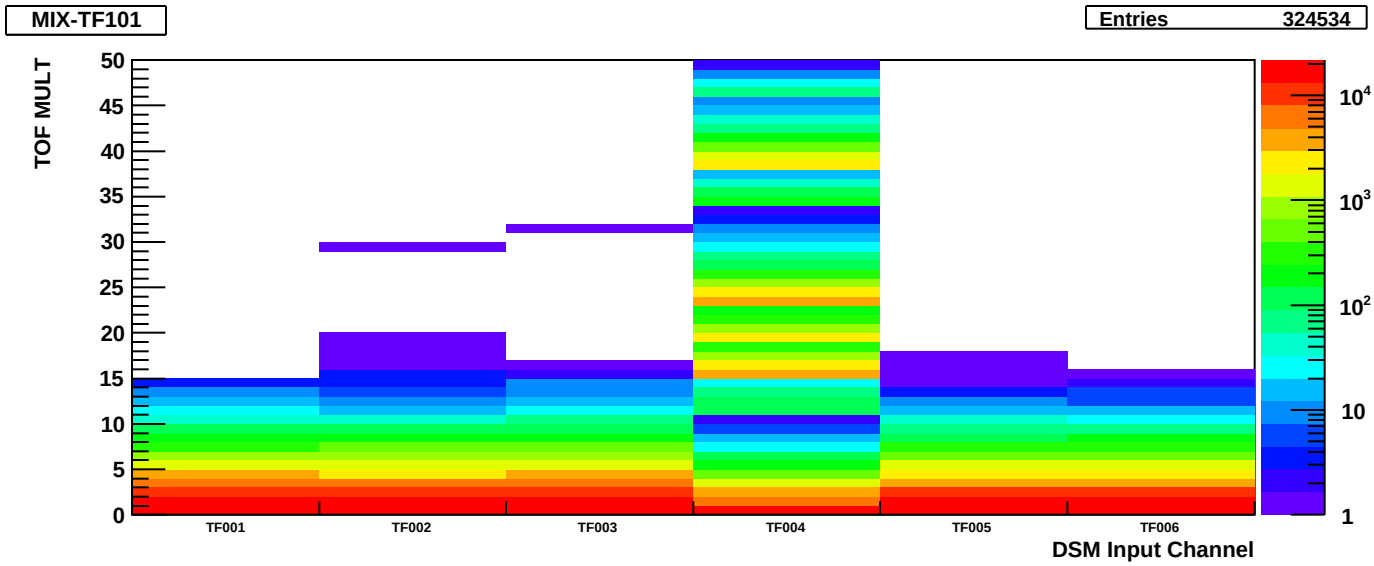

MIX-TF002
Entries 2271738
TOF MULT
TOF tray

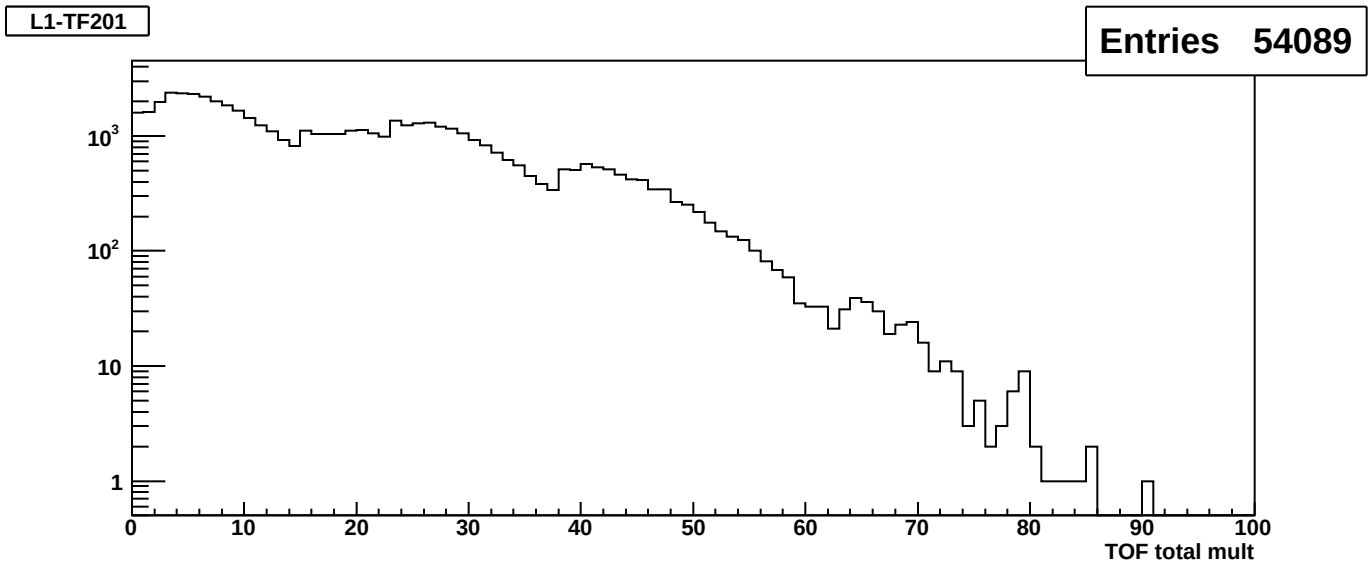

MIX-TF003
Entries 2271738
TOF MULT
TOF tray

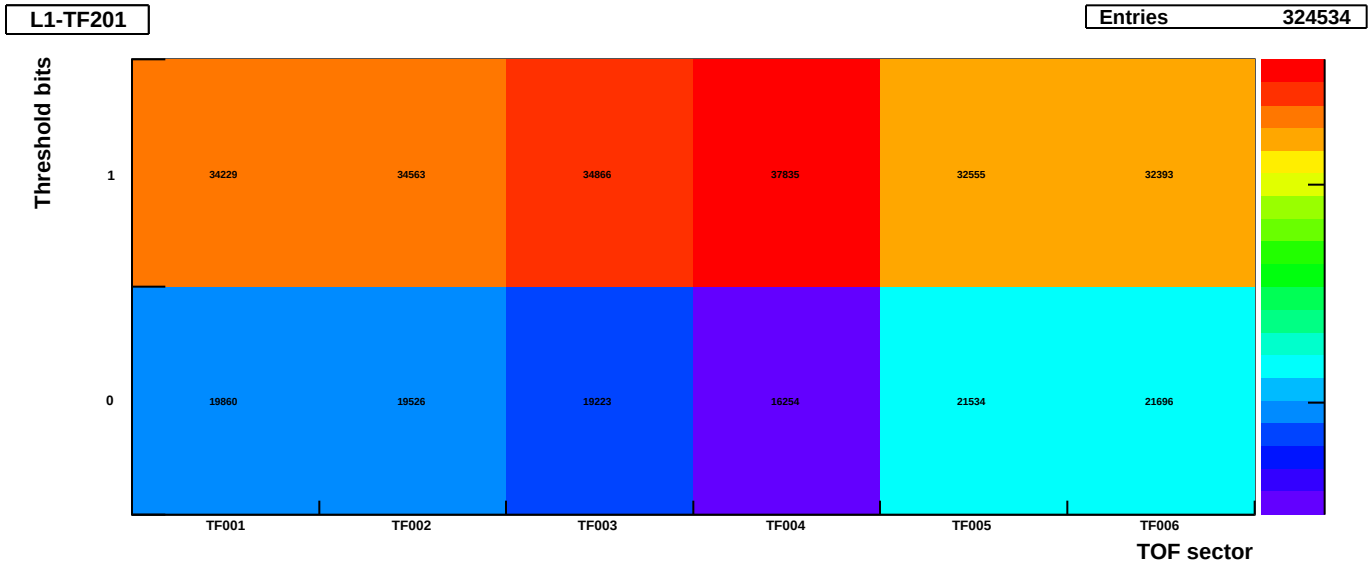

MIX-TF004
Entries 2271738
TOF MULT
TOF tray

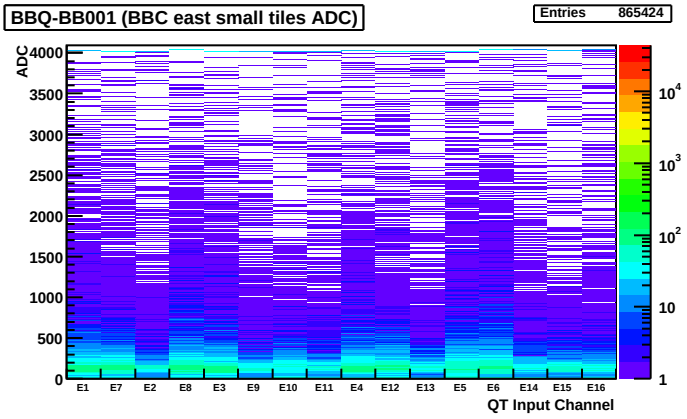

MIX-TF005
Entries 2271738
TOF MULT
TOF tray

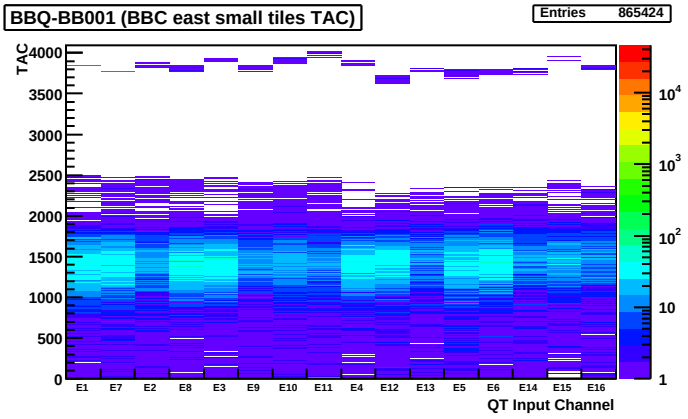

MIX-TF006
Entries 2271738
TOF MULT
TOF tray

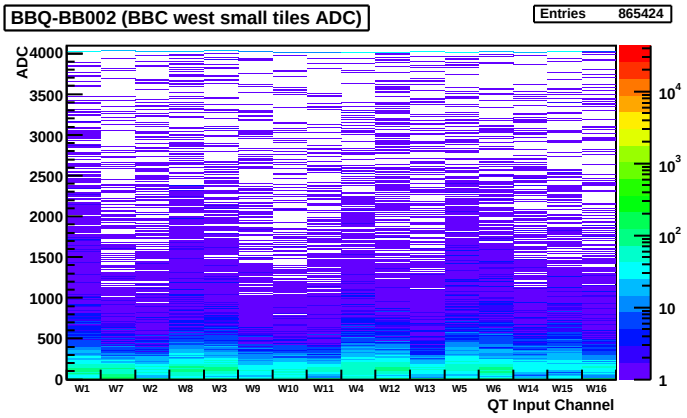

MIX-TF101
Entries 324534
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

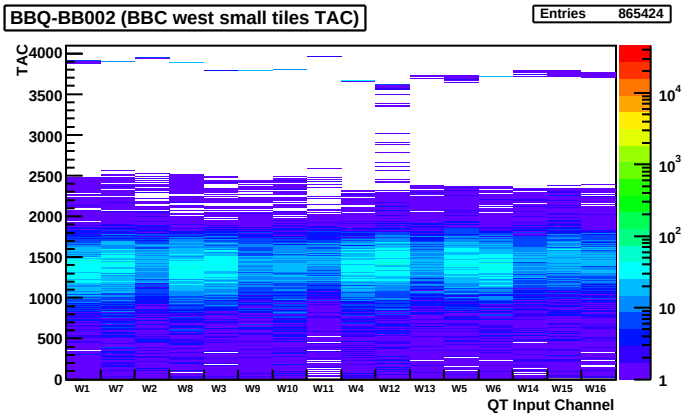

L1-TF201
Entries 54089
TOF total mult

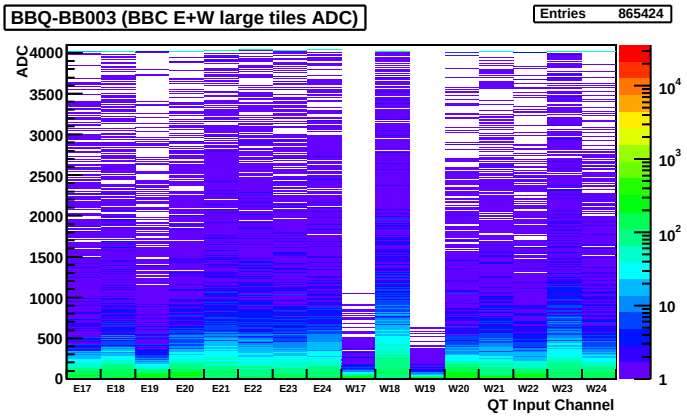

L1-TF201
Entries 324534
Threshold bits
34229
34563
34866
37835
32555
32393
19860
19526
19223
16254
21534
21696
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

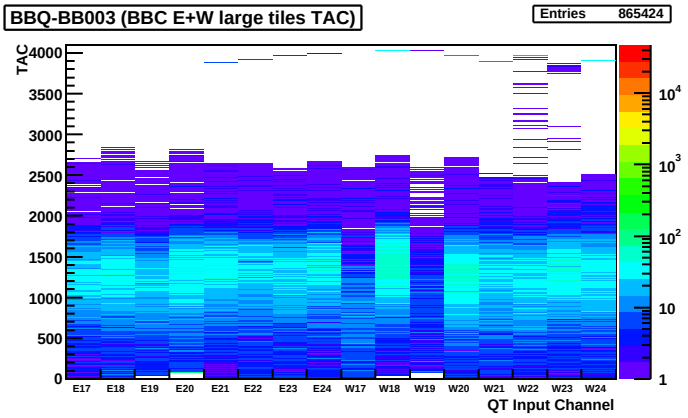

BBQ-BB001 (BBC east small tiles ADC)
Entries 865424
ADC
QT Input Channel

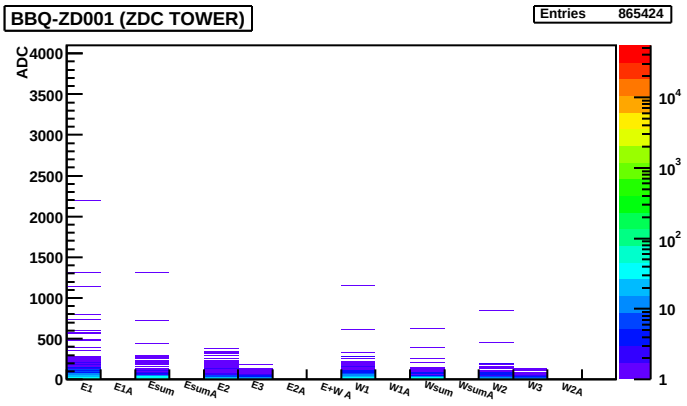

BBQ-BB001 (BBC east small tiles TAC)
Entries 865424
TAC
QT Input Channel

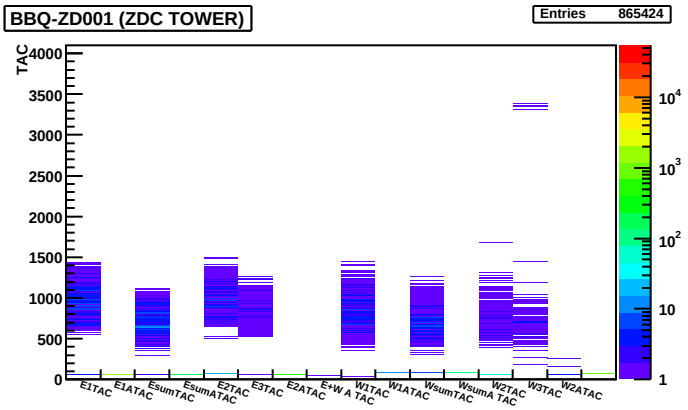

BBQ-BB002 (BBC west small tiles ADC)
Entries 865424
ADC
QT Input Channel

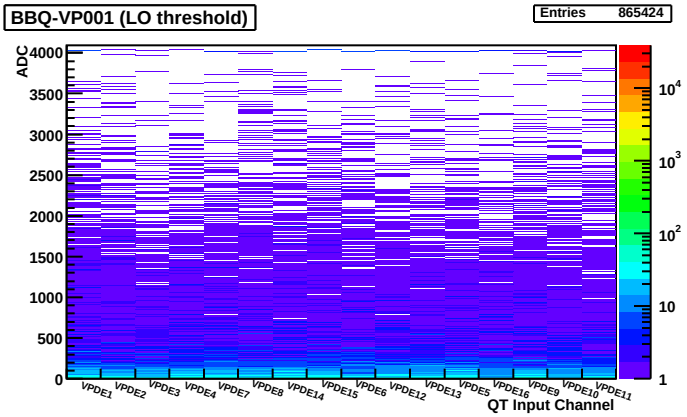

BBQ-BB002 (BBC west small tiles TAC)
Entries 865424
TAC
QT Input Channel

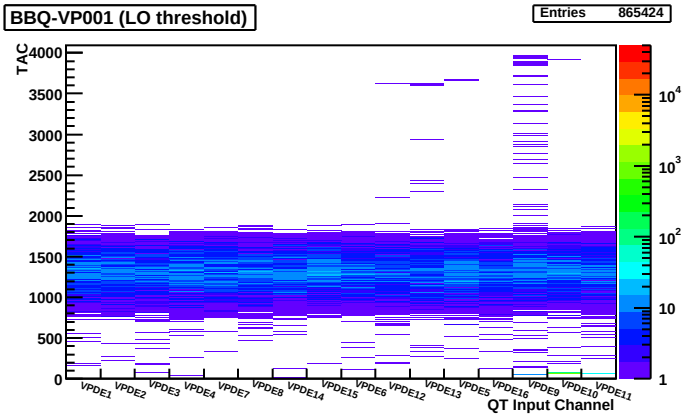

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 865424
ADC
QT Input Channel

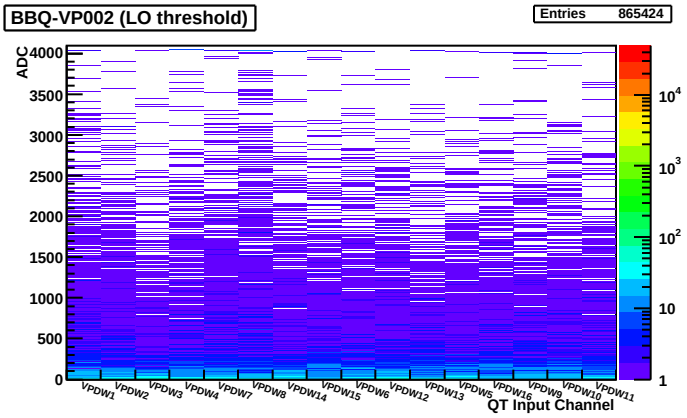

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 865424
TAC
QT Input Channel

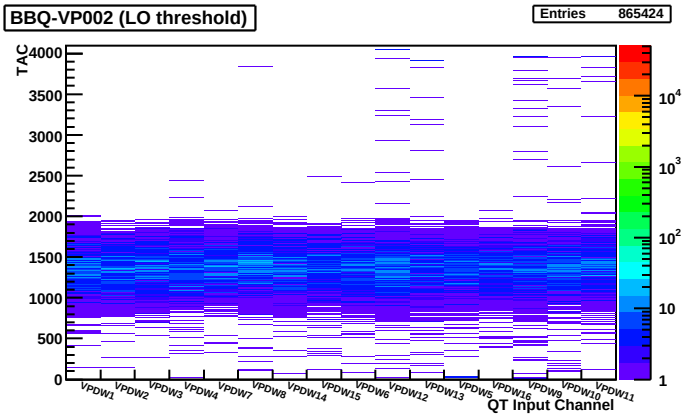

BBQ-ZD001 (ZDC TOWER)
Entries 865424
ADC

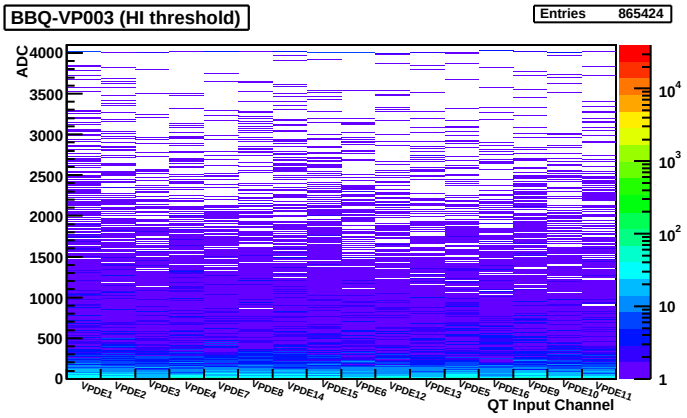

BBQ-ZD001 (ZDC TOWER)
Entries 865424
TAC

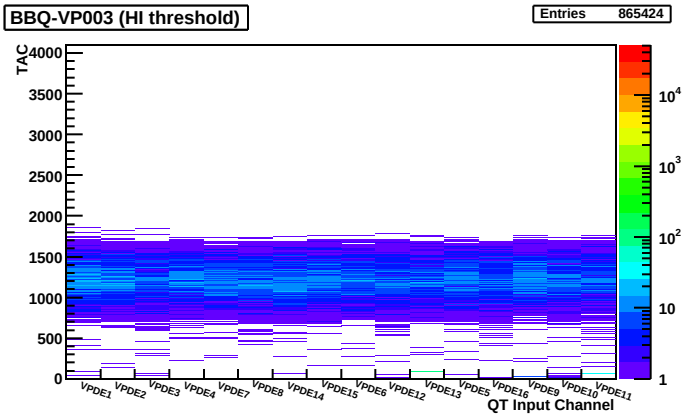

BBQ-VP001 (LO threshold)
Entries 865424
ADC
QT Input Channel

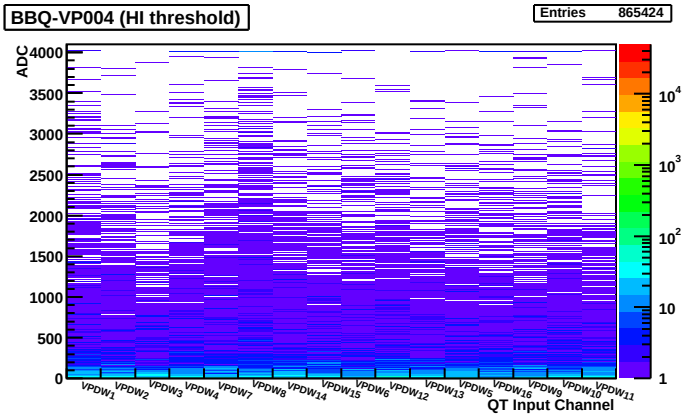

BBQ-VP001 (LO threshold)
Entries 865424
TAC
QT Input Channel

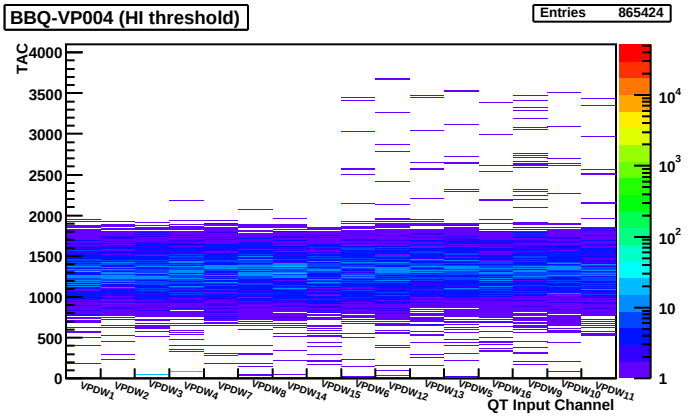

BBQ-VP002 (LO threshold)
Entries 865424
ADC
QT Input Channel

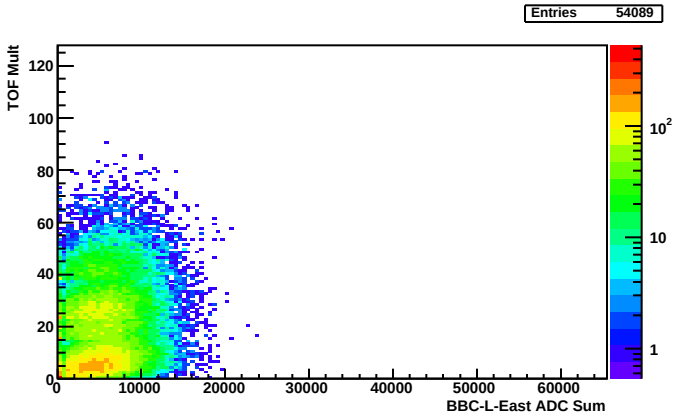

BBQ-VP002 (LO threshold)
Entries 865424
TAC
QT Input Channel

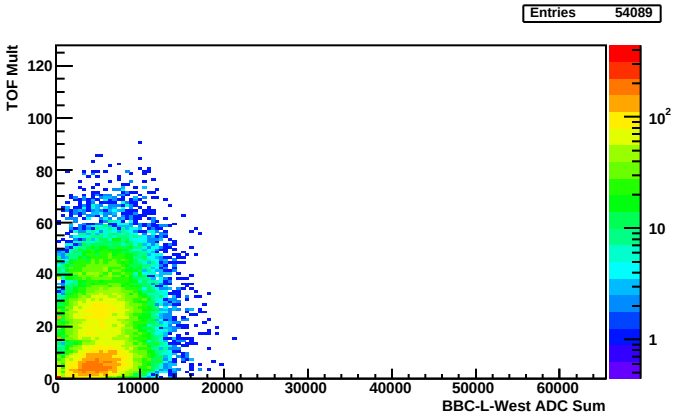

BBQ-VP003 (HI threshold)
Entries 865424
ADC
QT Input Channel

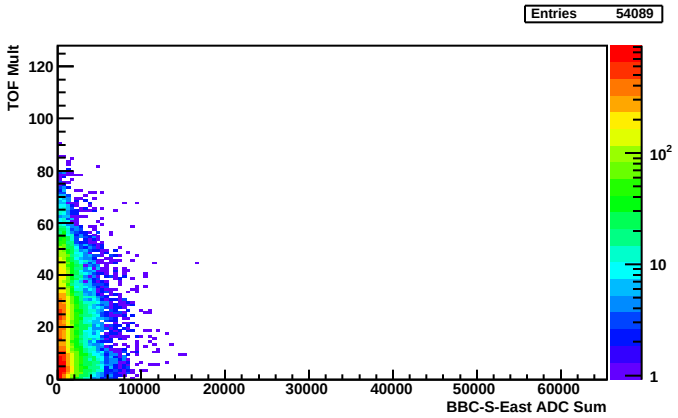

BBQ-VP003 (HI threshold)
Entries 865424
TAC
QT Input Channel

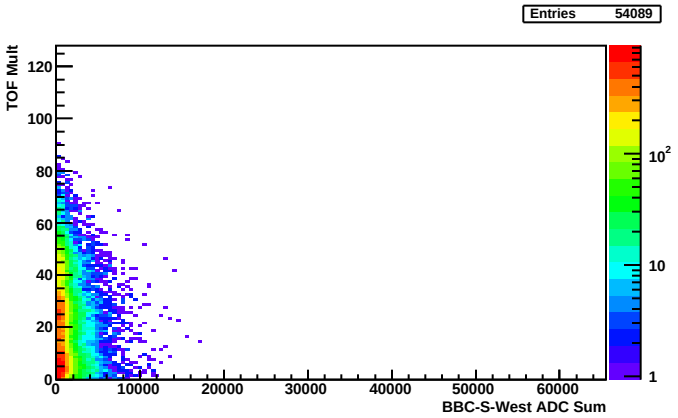

BBQ-VP004 (HI threshold)
Entries 865424
ADC
QT Input Channel

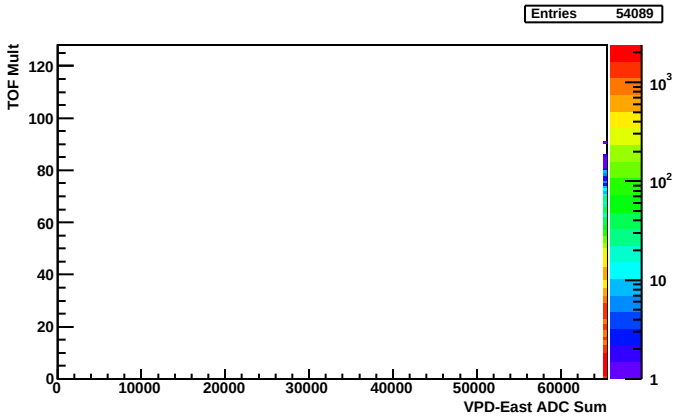

BBQ-VP004 (HI threshold)
Entries 865424
TAC
QT Input Channel

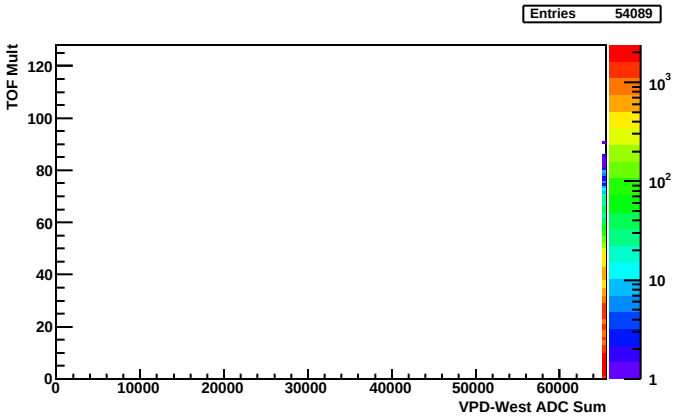

Entries 54089
TOF Mult
BBC-L-East ADC Sum

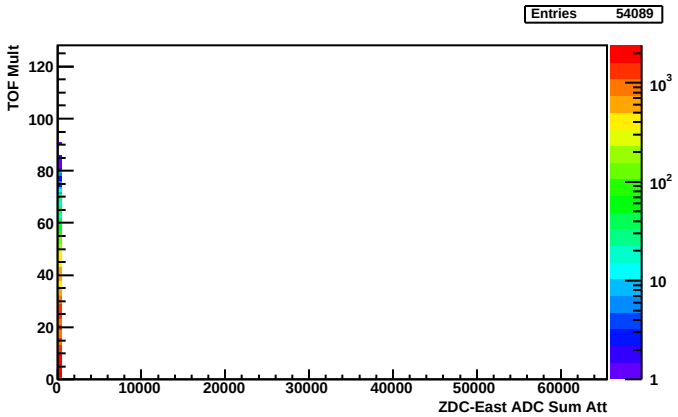

Entries 54089
TOF Mult
BBC-L-West ADC Sum

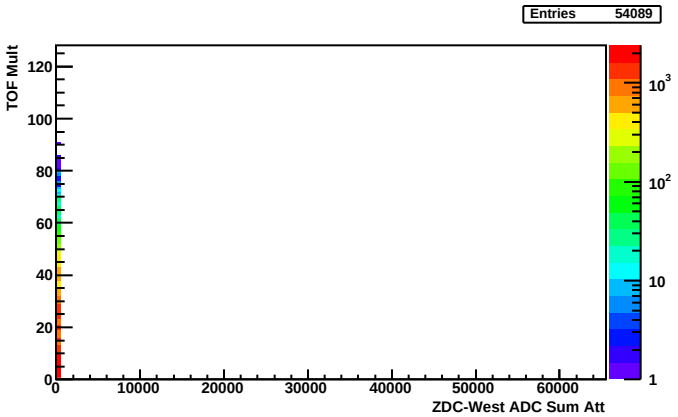

Entries 54089
TOF Mult
BBC-S-East ADC Sum

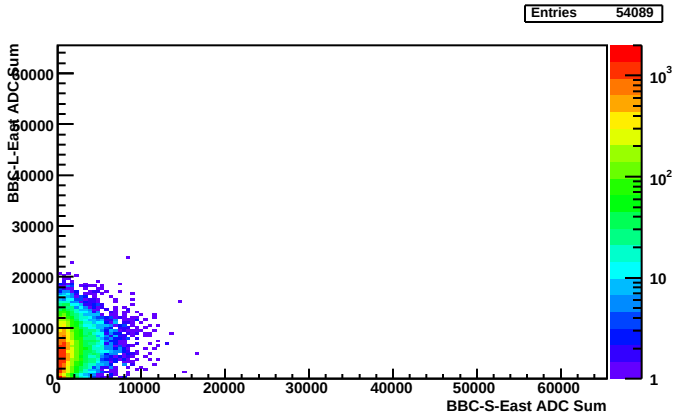

Entries 54089
TOF Mult
BBC-S-West ADC Sum

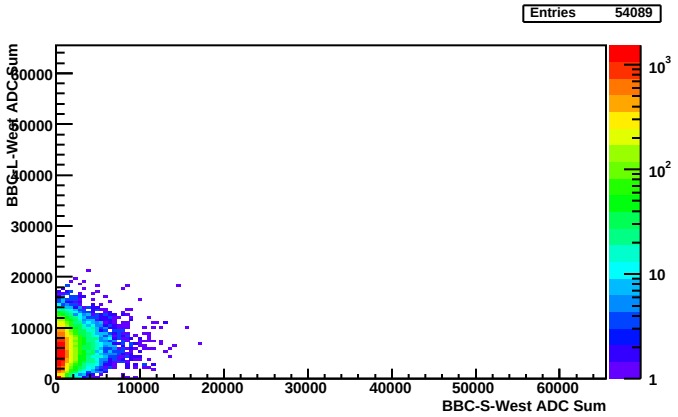

Entries 54089
TOF Mult
VPD-East ADC Sum

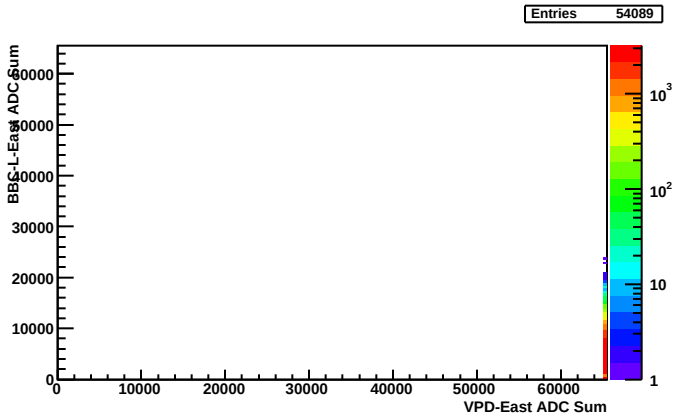

Entries 54089
TOF Mult
VPD-West ADC Sum

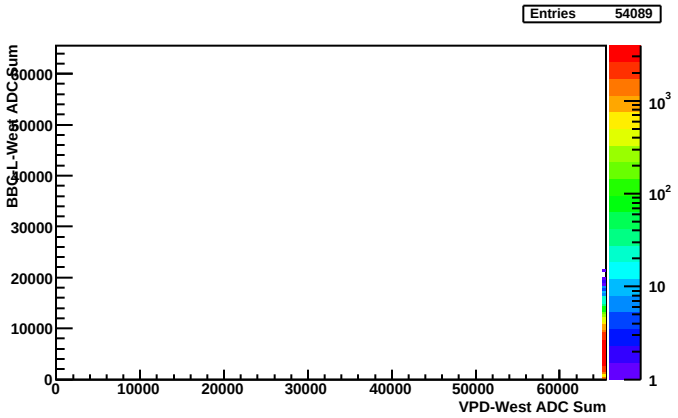

Entries 54089
TOF Mult
ZDC-East ADC Sum Att

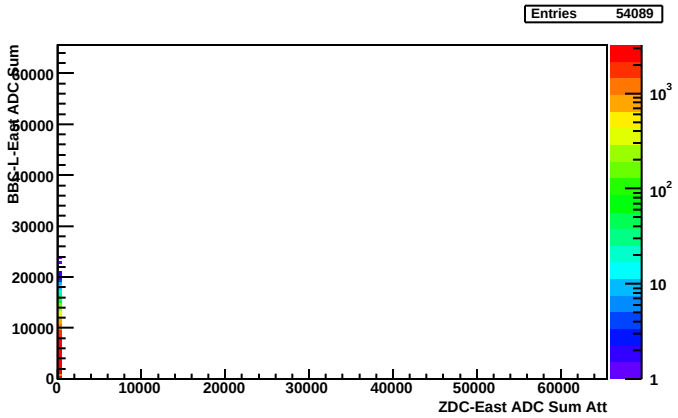

Entries 54089
TOF Mult
ZDC-West ADC Sum Att

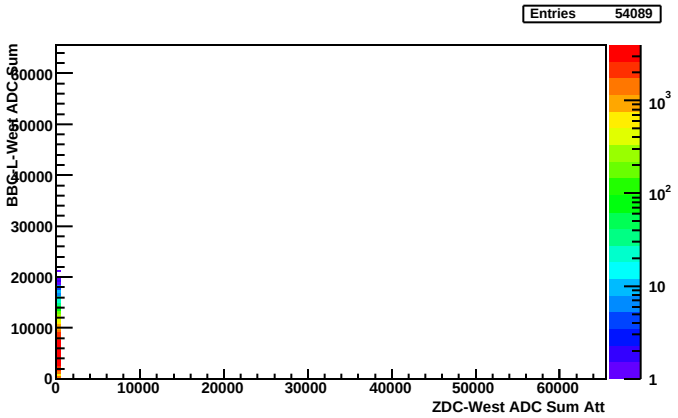

Entries 54089
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 54089
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 54089
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 54089
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 54089
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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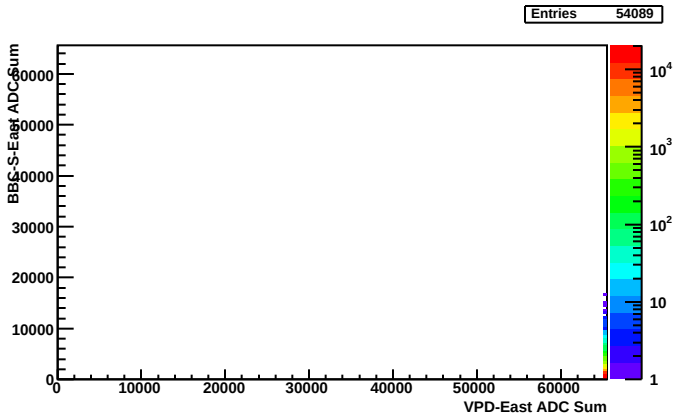
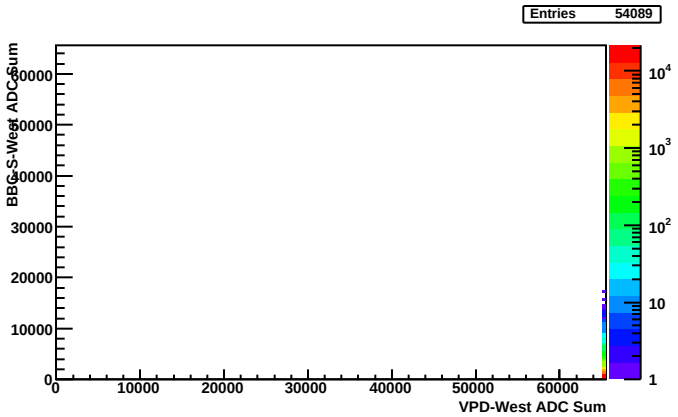
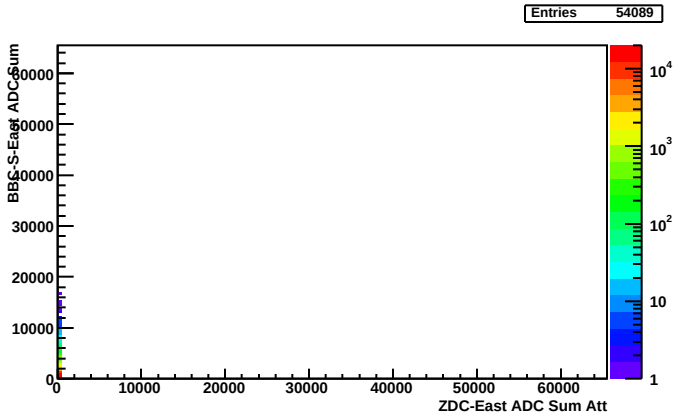
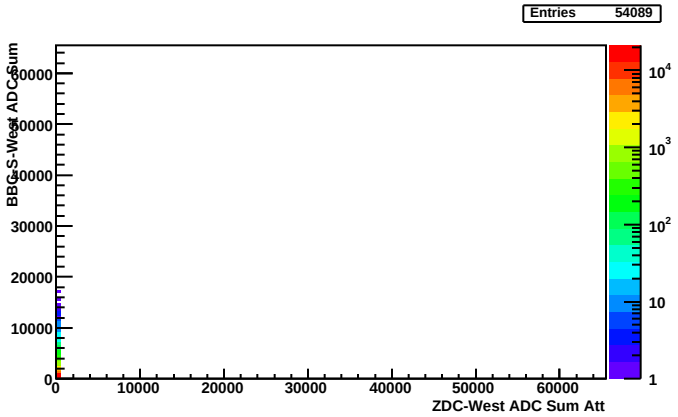
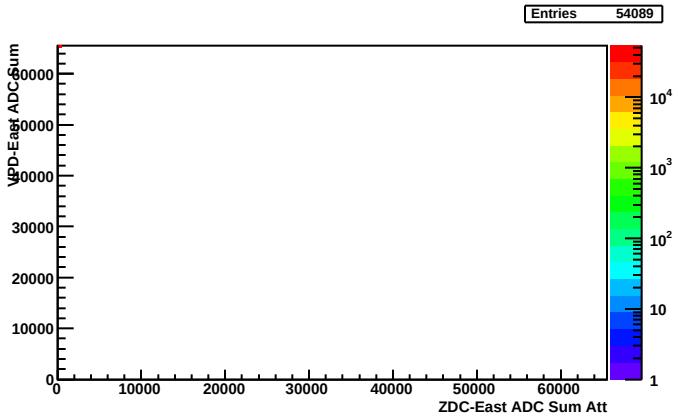
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

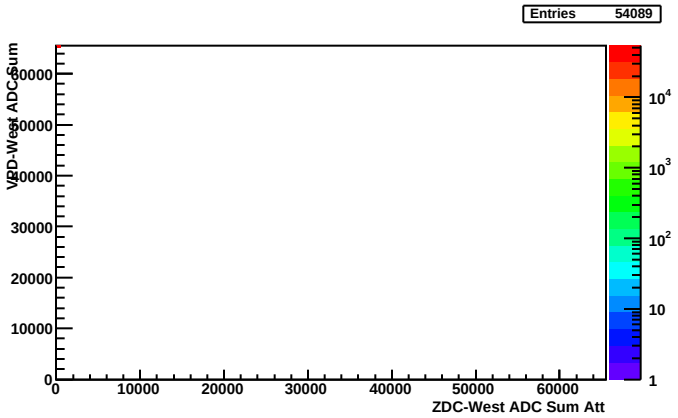

Entries 54089
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 54089
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 54089
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 54089
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 54089
VPD-East ADC Sum
ZDC-East ADC Sum Att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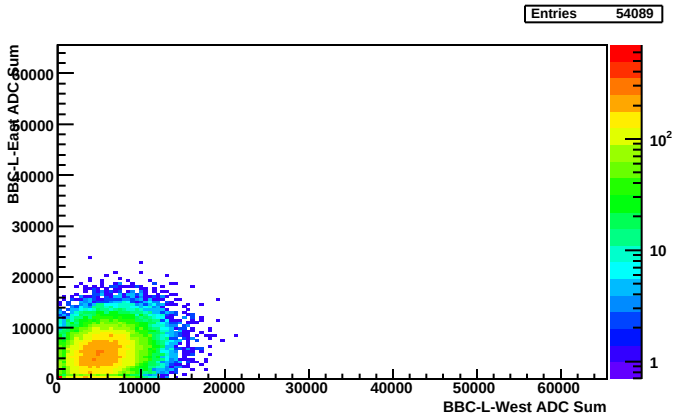
VPD-West ADC Sum
ZDC-West ADC Sum Att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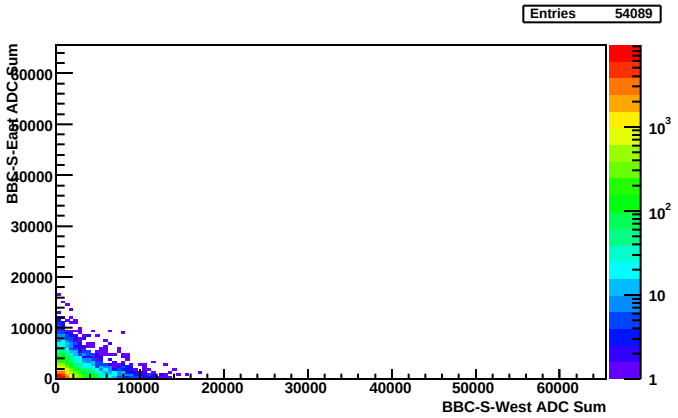
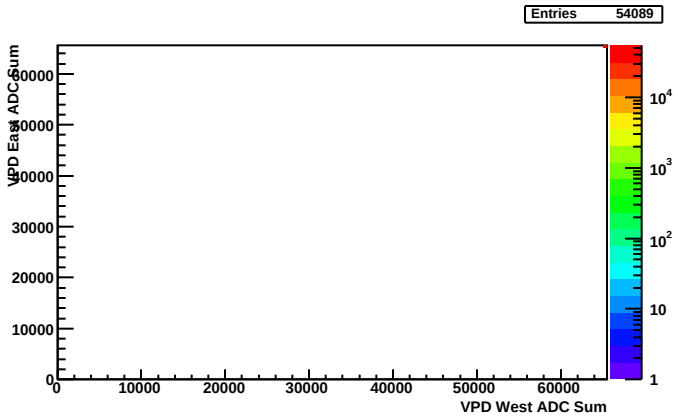
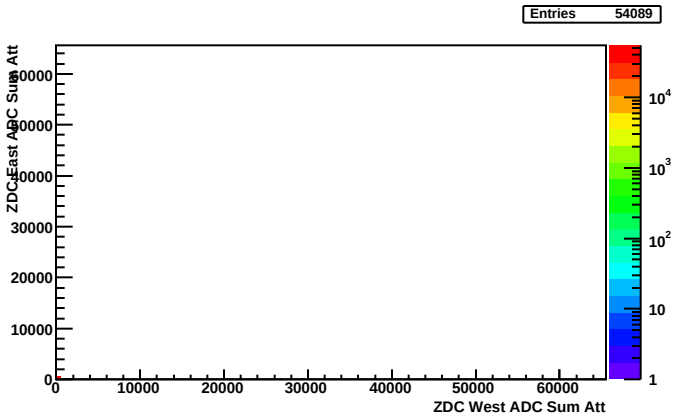
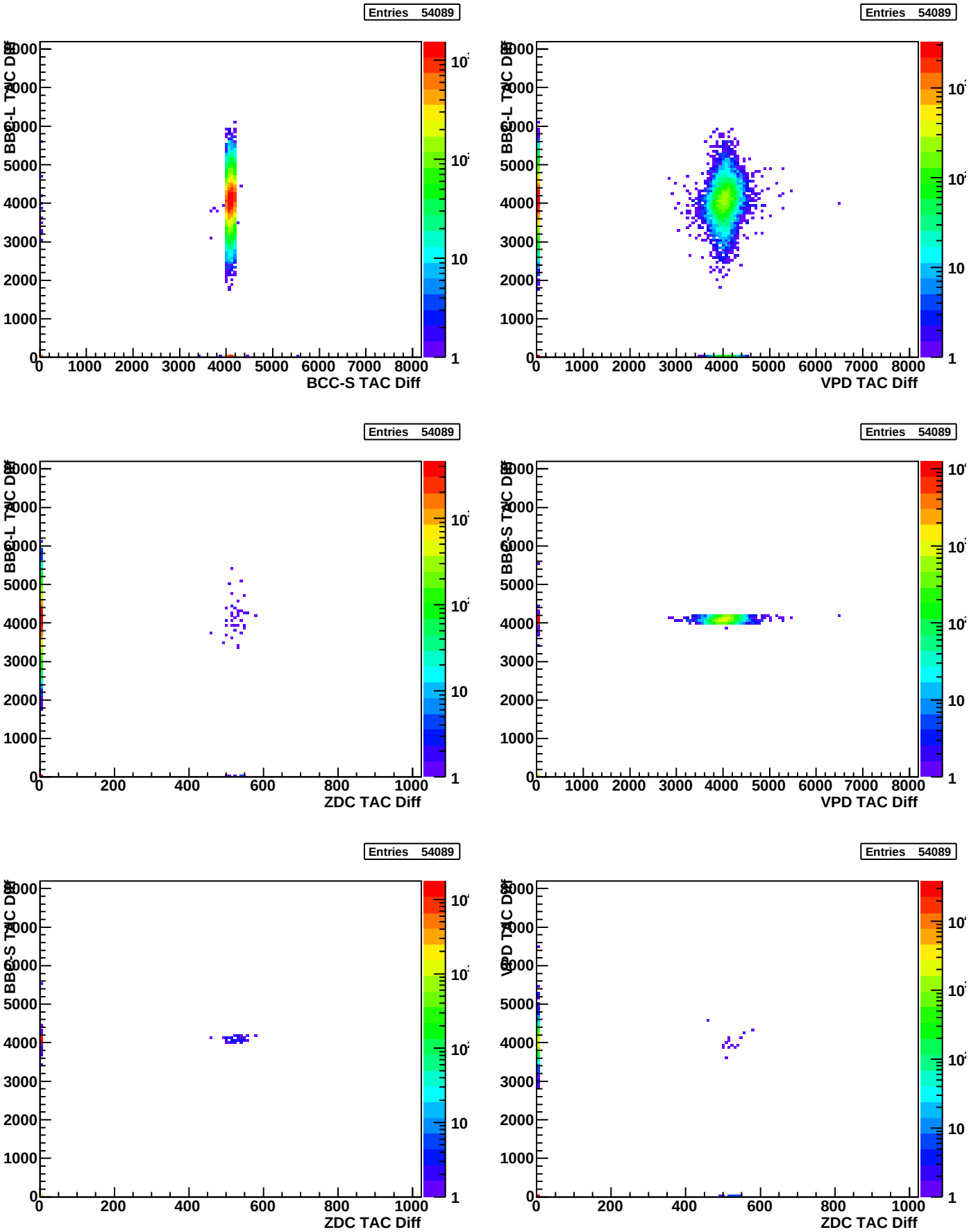
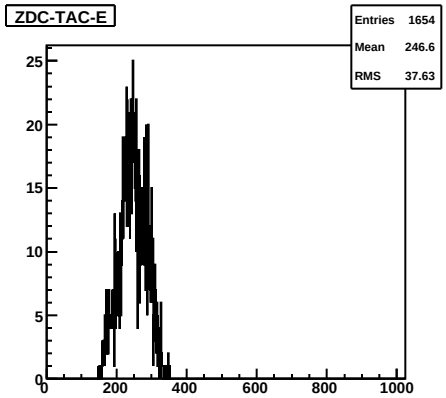
BBC-L-East ADC Sum
BBC-L-West ADC Sum

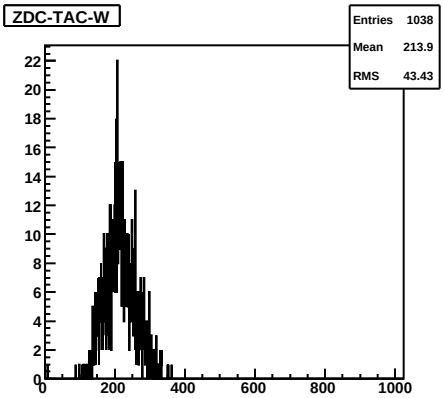

Entries 54089
BBC-S-East ADC Sum
BBC-S-West ADC Sum

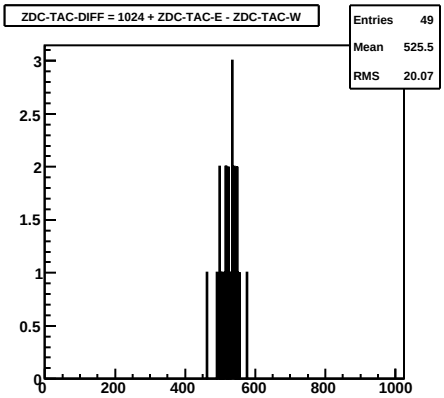

Entries 54089
VPD East ADC Sum
VPD West ADC Sum

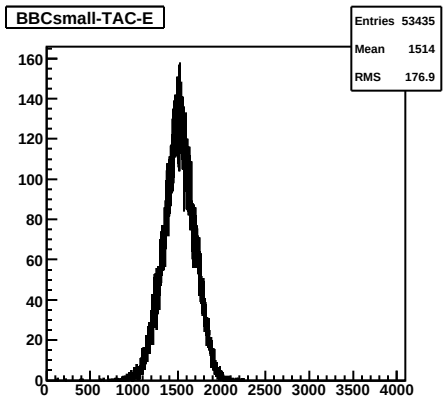

Entries 54089
ZDC East ADC Sum Att
ZDC West ADC Sum Att

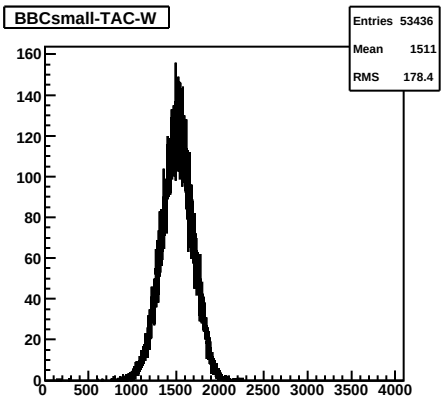

Entries 54089
BBC-L TAC Diff
BCC-S TAC Diff
Entries 54089
BBC-L TAC Diff
VPD TAC Diff
Entries 54089
BBC-L TAC Diff
ZDC TAC Diff
Entries 54089
BBC-S TAC Diff
VPD TAC Diff
Entries 54089
BBC-S TAC Diff
ZDC TAC Diff
Entries 54089
VPD TAC Diff
ZDC TAC Diff

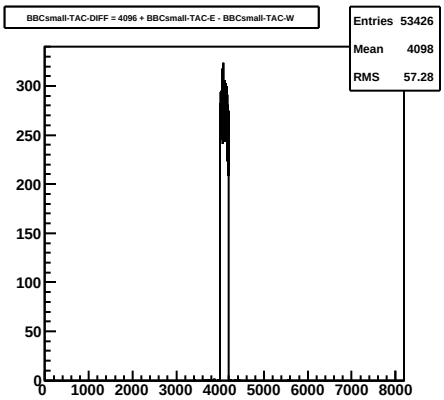

ZDC-TAC-E
Entries 1654
Mean 246.6
RMS 37.63

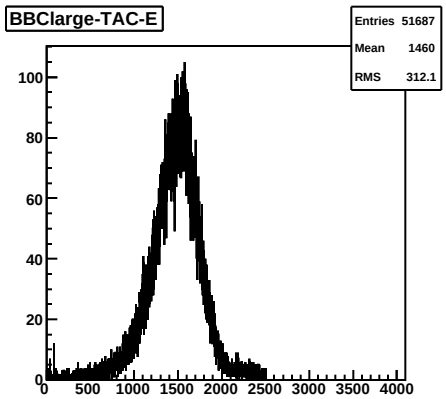

ZDC-TAC-W
Entries 1038
Mean 213.9
RMS 43.43

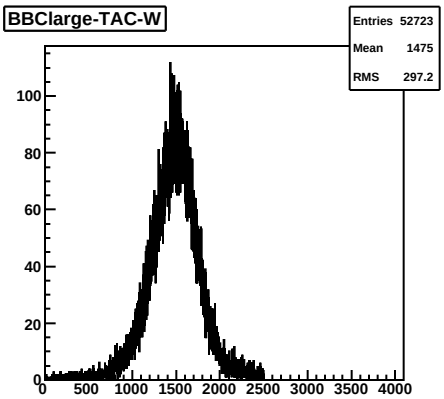

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 49
Mean 525.5
RMS 20.07

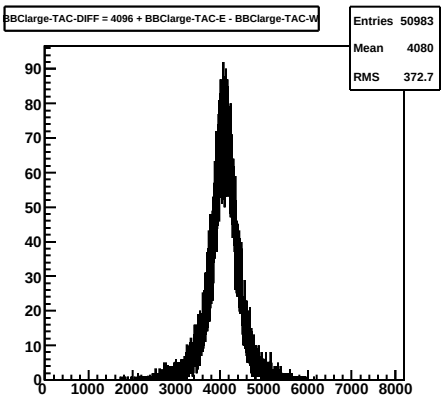

BBCsmall-TAC-E
Entries 53435
Mean 1514
RMS 176.9

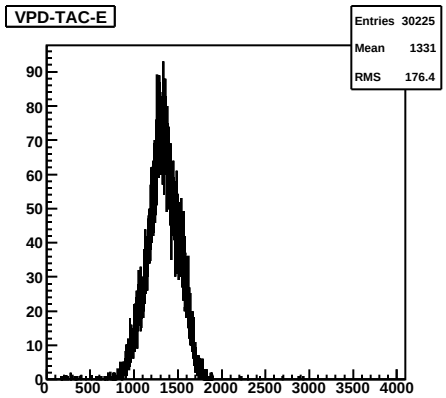

BBCsmall-TAC-W
Entries 53436
Mean 1511
RMS 178.4

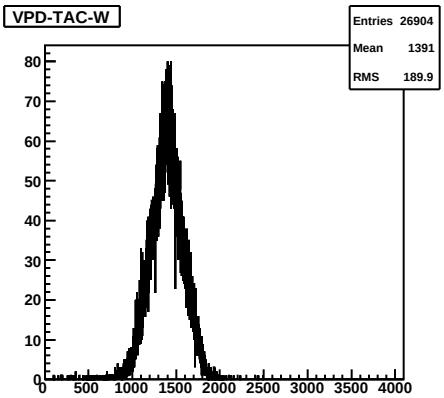

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 53426
Mean 4098
RMS 57.28

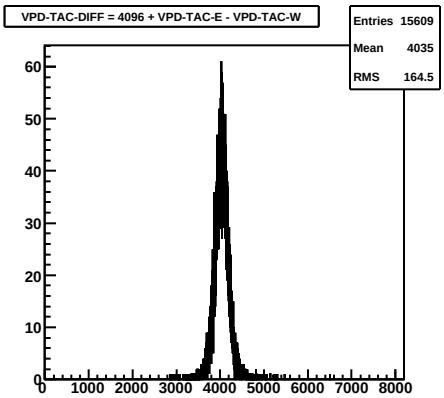

BBClarge-TAC-E
Entries 51687
Mean 1460
RMS 312.1

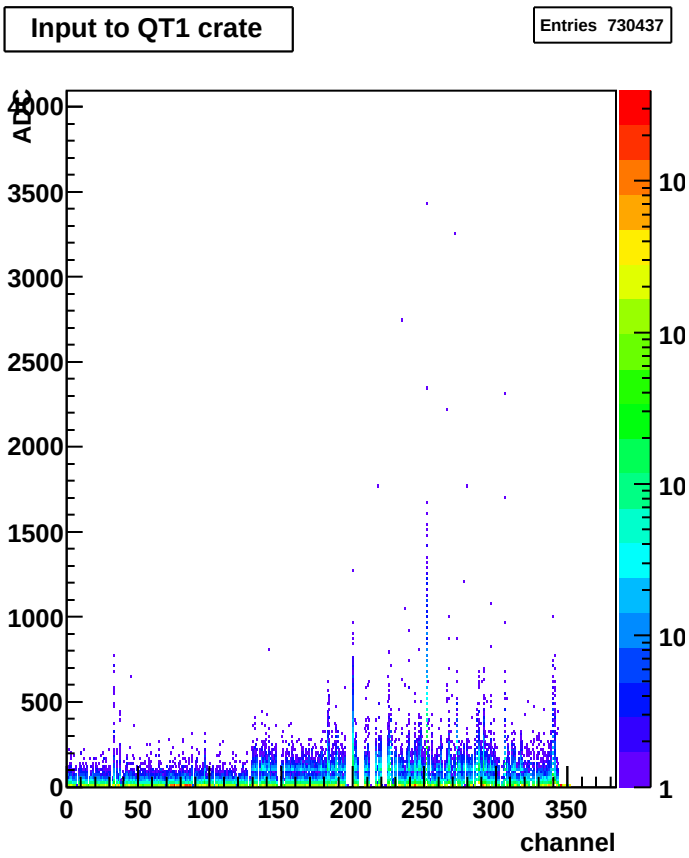

BBClarge-TAC-W
Entries 52723
Mean 1475
RMS 297.2

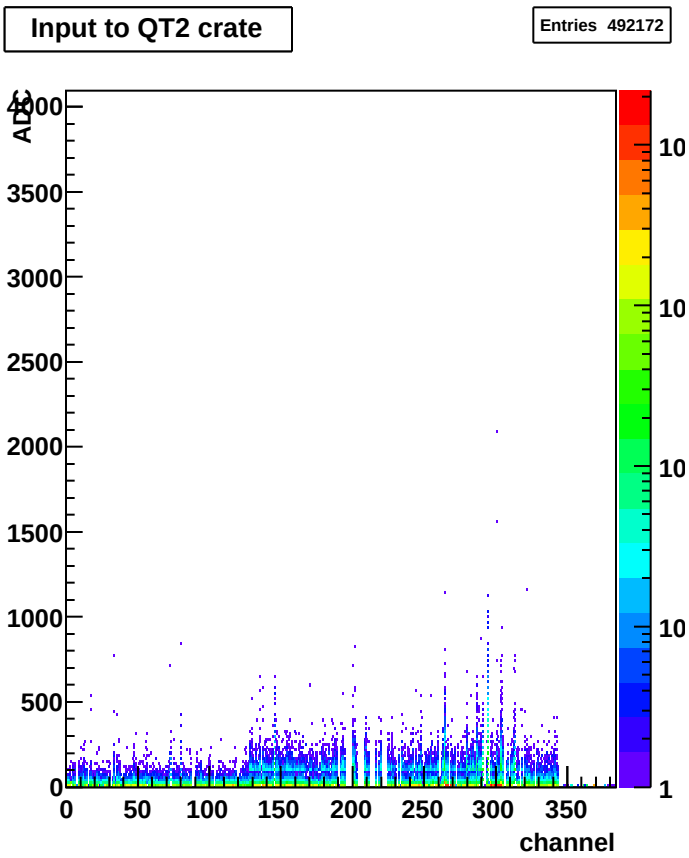

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 50983
Mean 4080
RMS 372.7

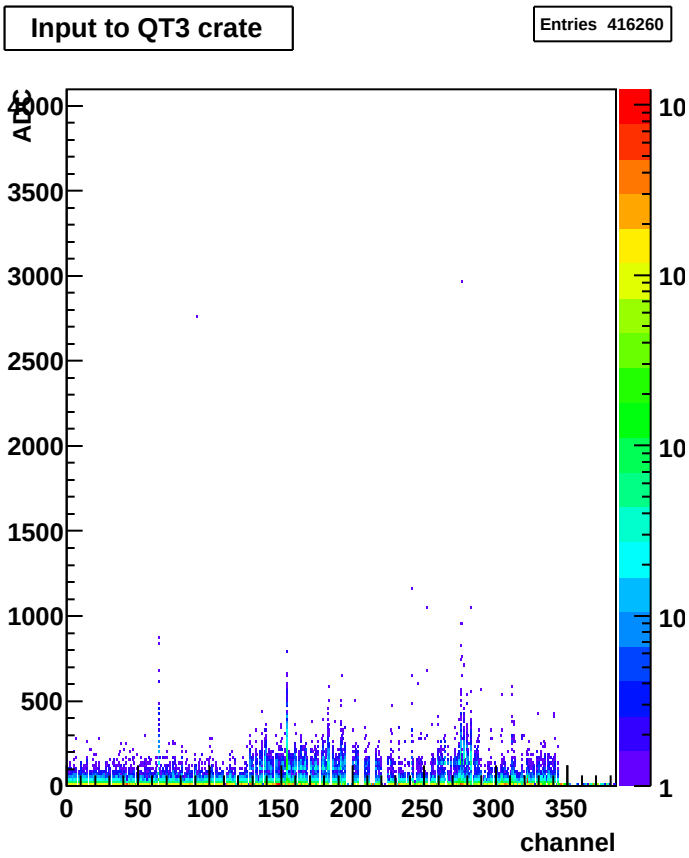

VPD-TAC-E
Entries 30225
Mean 1331
RMS 176.4

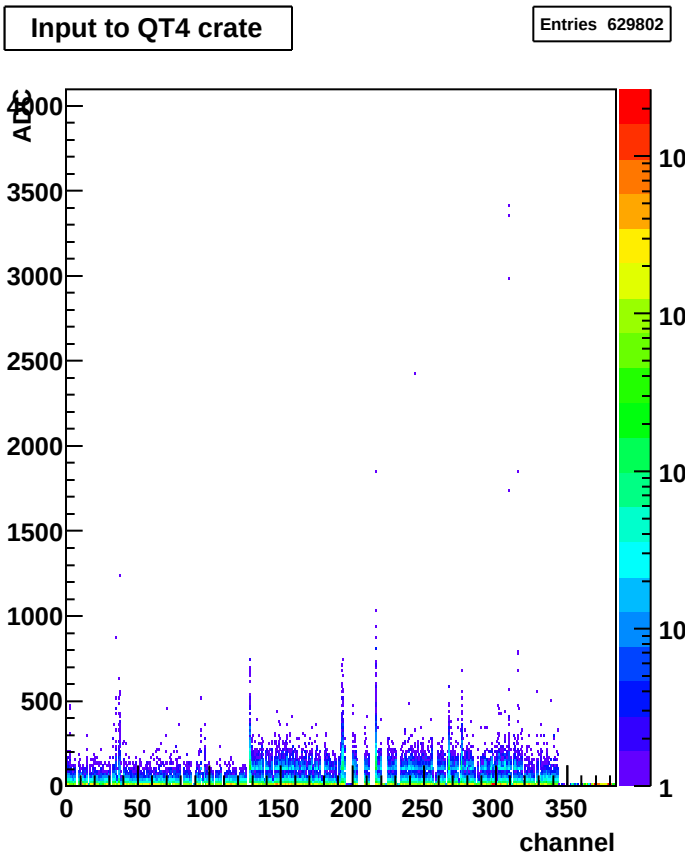

VPD-TAC-W
Entries 26904
Mean 1391
RMS 189.9

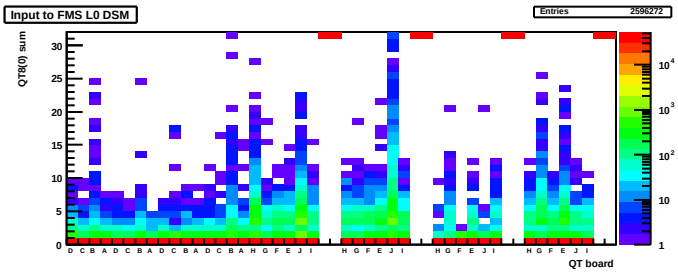

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 15609
Mean 4035
RMS 164.5

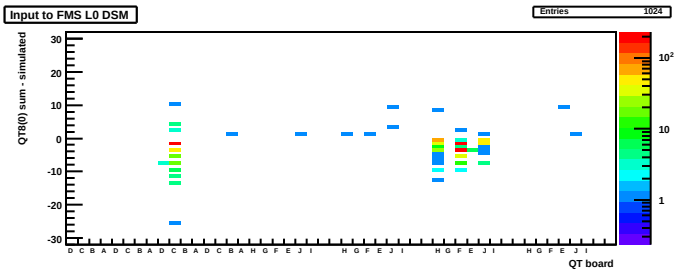

Input to QT1 crate
Entries 730437
ADC
channel

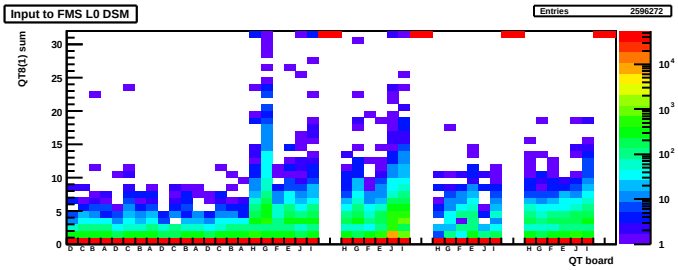

Input to QT2 crate
Entries 492172
ADC
channel

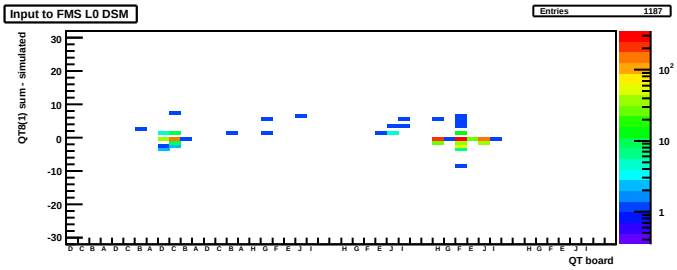

Input to QT3 crate
Entries 416260
ADC
channel

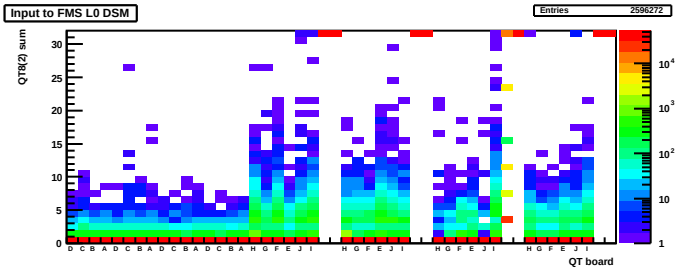

Input to QT4 crate
Entries 629802
ADC
channel

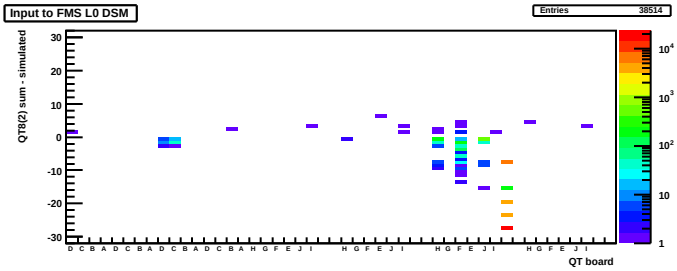

Input to FMS L0 DSM
Entries 2596272
QT8(0) sum
QT board

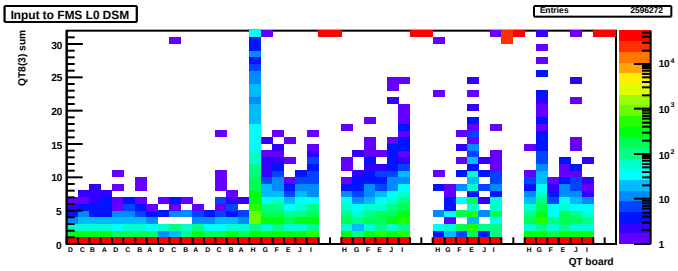

Input to FMS L0 DSM
Entries 1024
QT8(0) sum - simulated
QT board

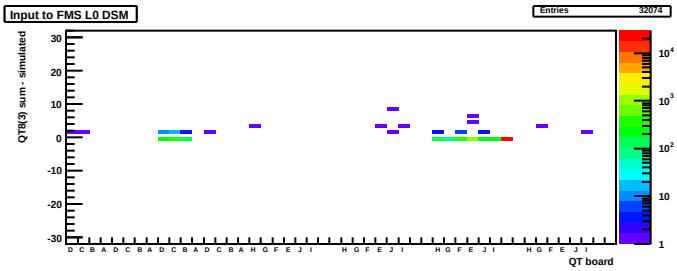

Input to FMS L0 DSM
Entries 2596272
QT8(1) sum
QT board

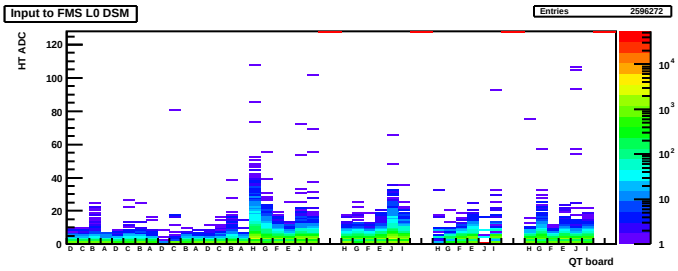

Input to FMS L0 DSM
Entries 1187
QT8(1) sum - simulated
QT board

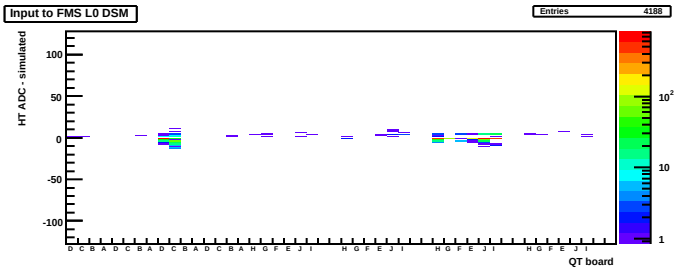

Input to FMS L0 DSM
Entries 2596272
QT8(2) sum
QT board

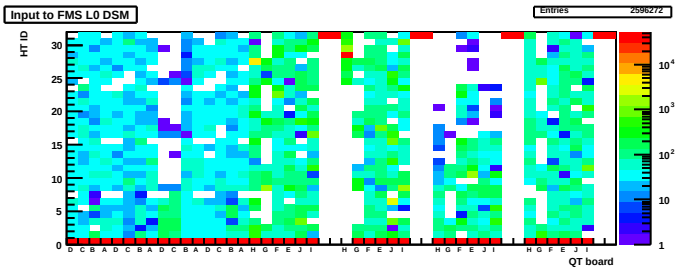

Input to FMS L0 DSM
Entries 38514
QT8(2) sum - simulated
QT board

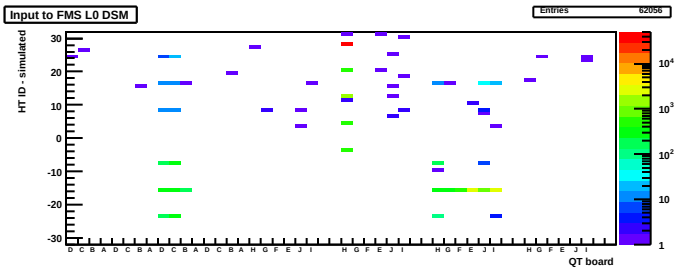

Input to FMS L0 DSM
Entries 2596272
QT8(3) sum
QT board

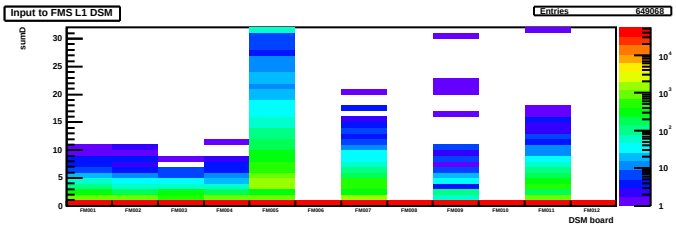

Input to FMS L0 DSM
Entries 32074
QT8(3) sum - simulated
QT board

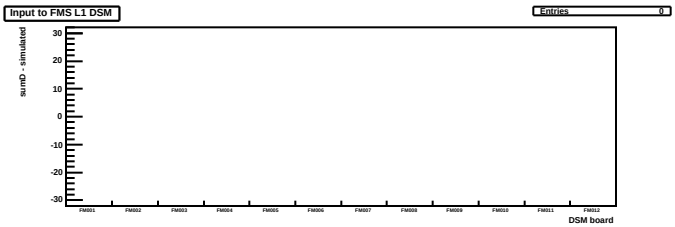

Input to FMS L0 DSM
Entries 2596272
HT ADC
QT board

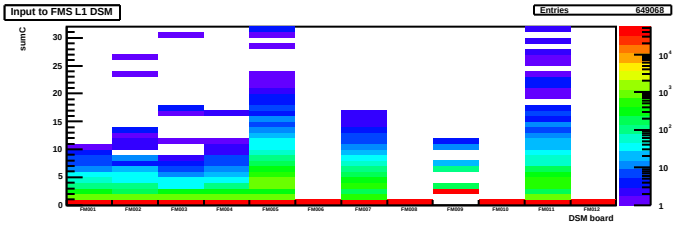

Input to FMS L0 DSM
Entries 4188
HT ADC - simulated
QT board

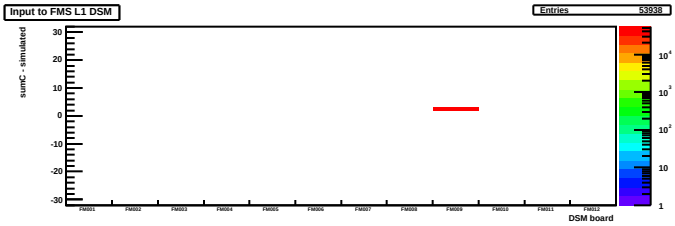

Input to FMS L0 DSM
Entries 2596272
HT ID
QT board

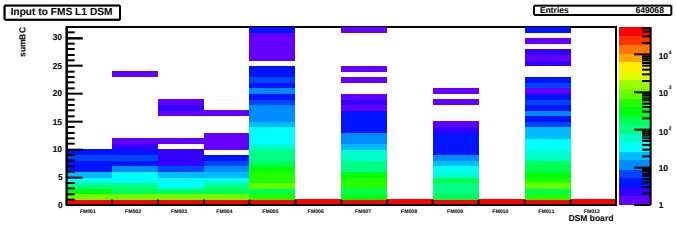

Input to FMS L0 DSM
Entries 62056
HT ID - simulated
QT board

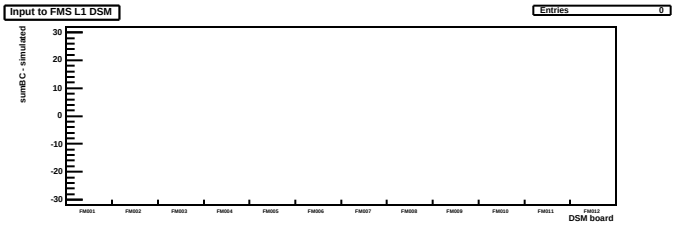

Input to FMS L1 DSM
Entries 649068
sumD
DSM board

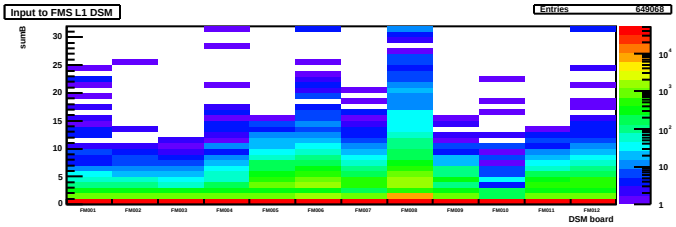

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

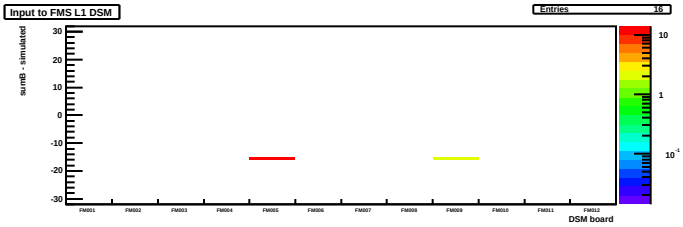

Input to FMS L1 DSM
Entries 649068
sumC
DSM board

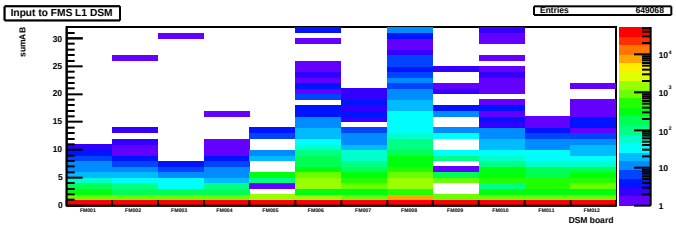

Input to FMS L1 DSM
Entries 53938
sumC - simulated
DSM board

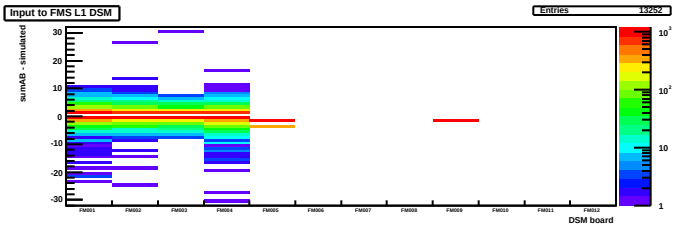

Input to FMS L1 DSM
Entries 649068
sumBC
DSM board

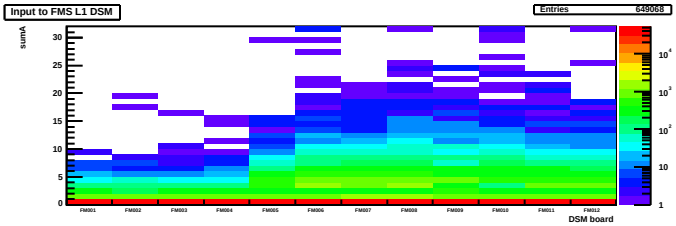

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

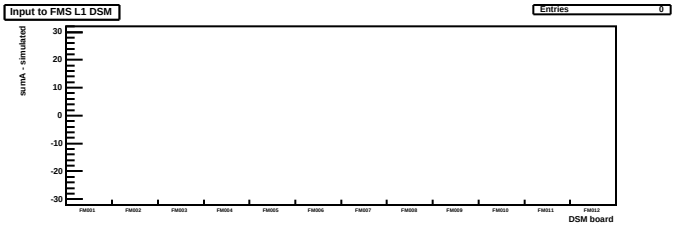

Input to FMS L1 DSM
Entries 649068
sumB
DSM board

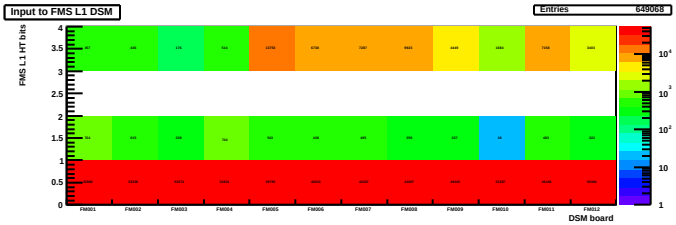

Input to FMS L1 DSM
Entries 16
sumB - simulated
DSM board

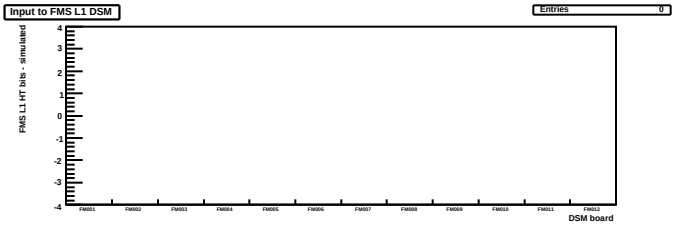

Input to FMS L1 DSM
Entries 649068
sumAB
DSM board

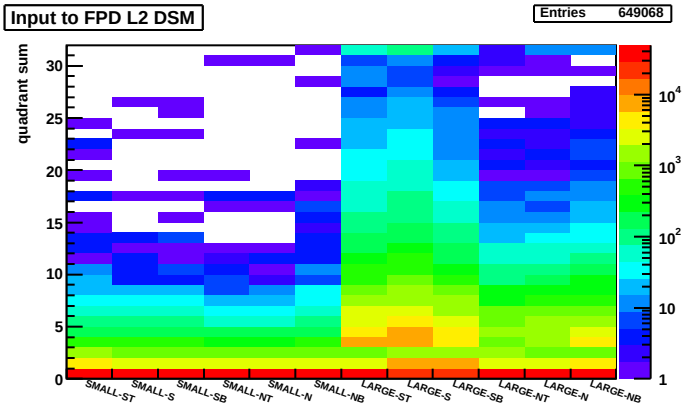

Input to FMS L1 DSM
Entries 13252
sumAB - simulated
DSM board

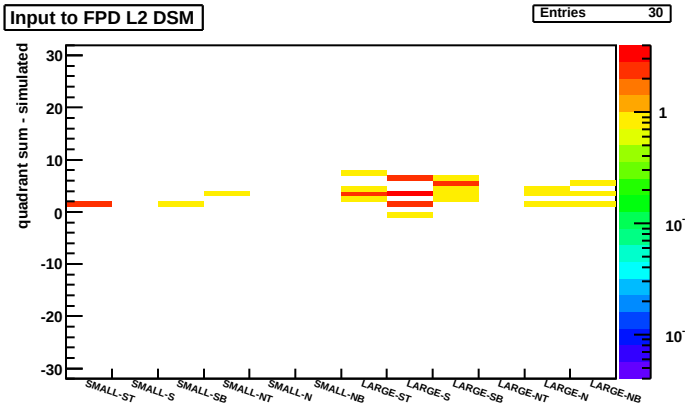

Input to FMS L1 DSM
Entries 649068
sumA
DSM board

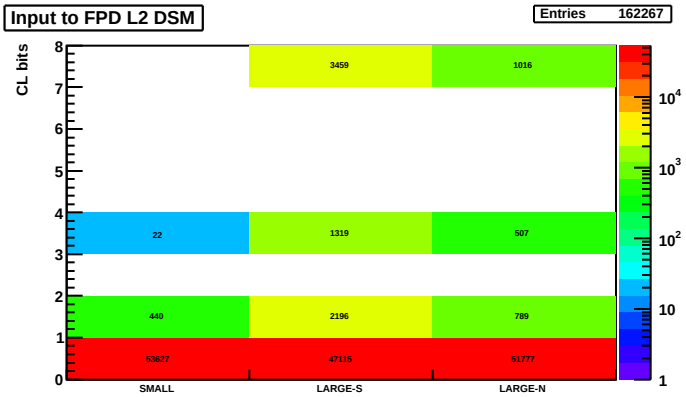

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

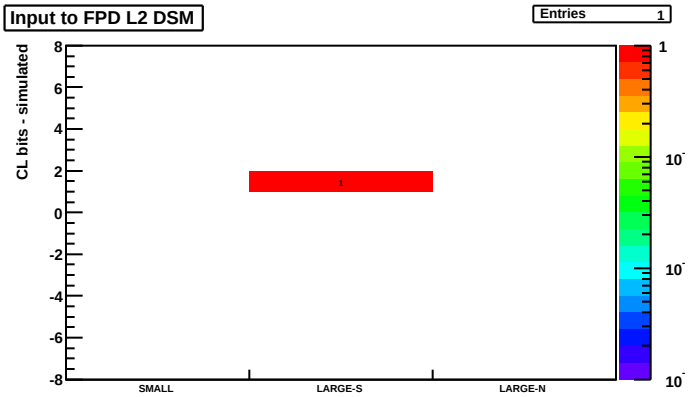

Input to FMS L1 DSM
Entries 649068
FMS L1 HT bits
DSM board

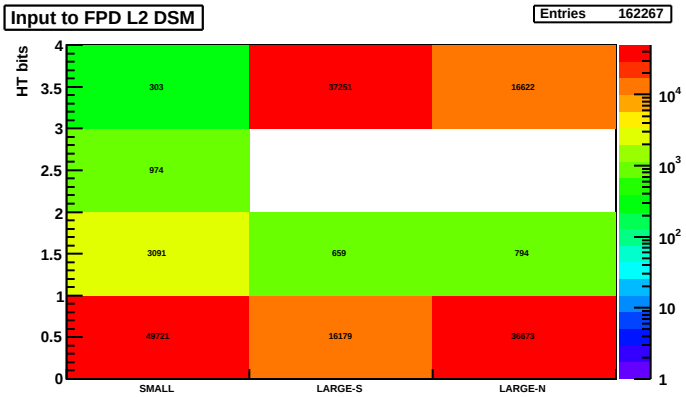

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

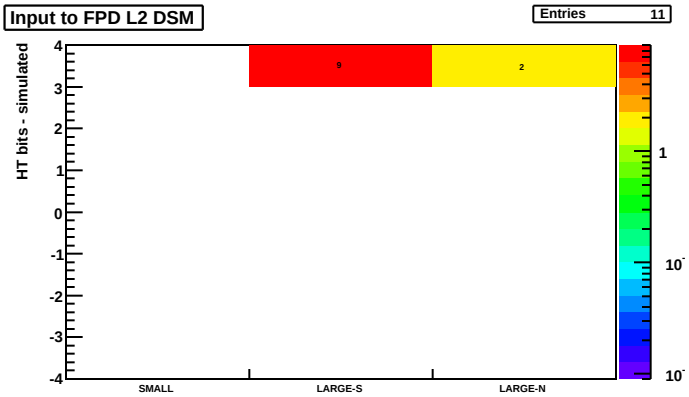

Input to FPD L2 DSM
Entries 649068
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

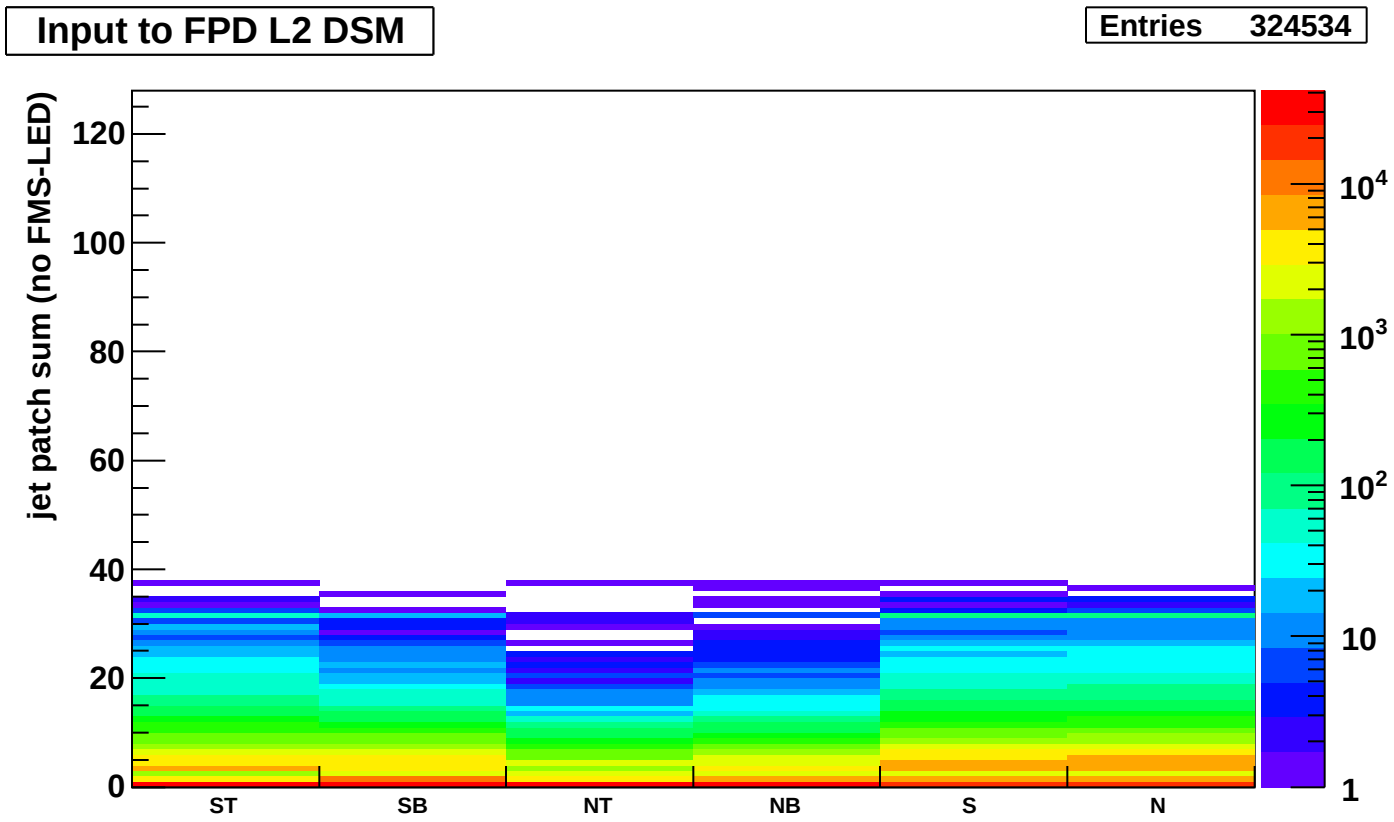

Input to FPD L2 DSM
Entries 30
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

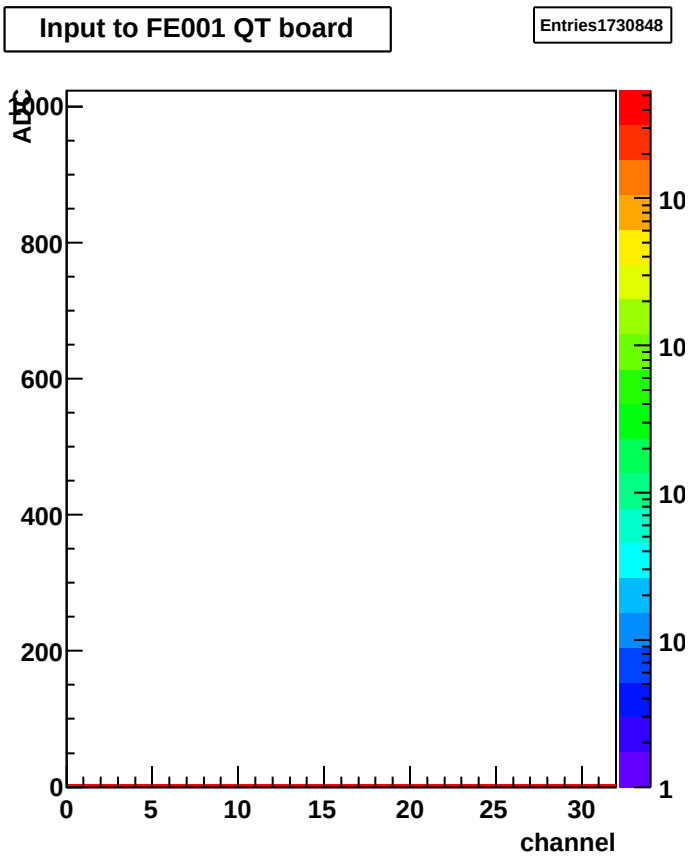

Input to FPD L2 DSM
Entries 162267
CL bits
3459
1016
22
1319
507
440
2196
789
53627
47115
51777
SMALL
LARGE-S
LARGE-N

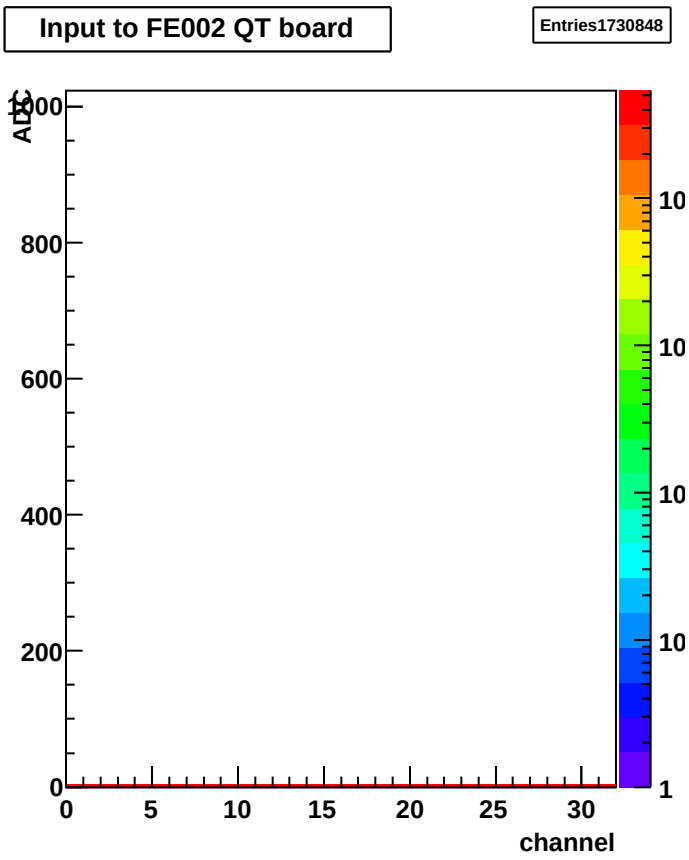

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

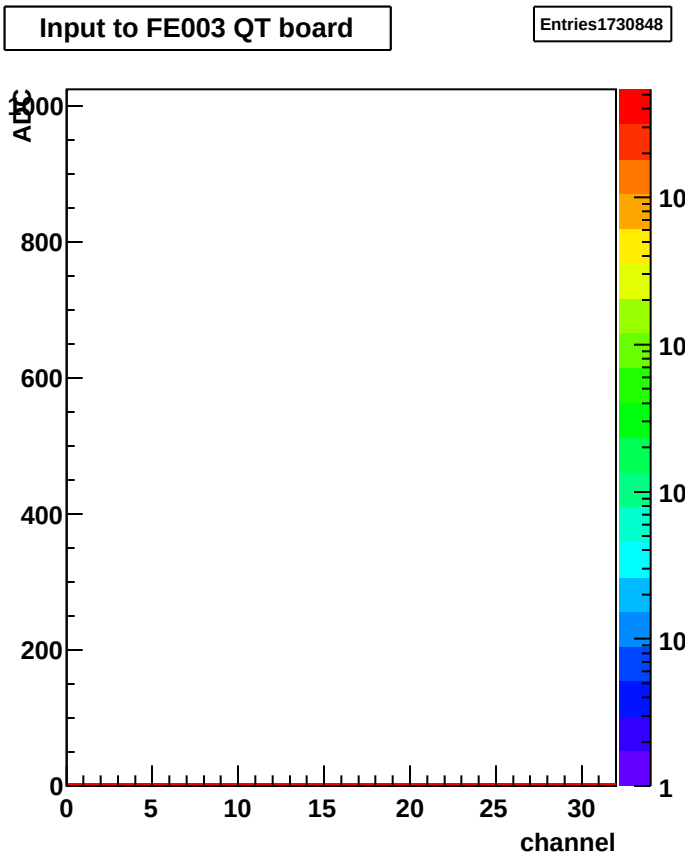

Input to FPD L2 DSM
Entries 162267
HT bits
303
37251
16622
974
3091
659
794
49721
16179
36673
SMALL
LARGE-S
LARGE-N

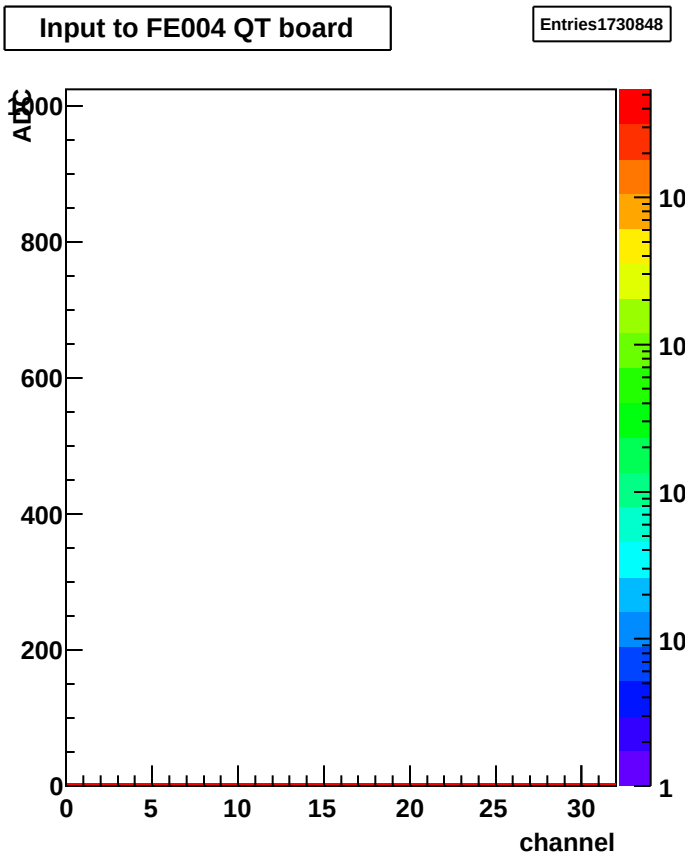

Input to FPD L2 DSM
Entries 11
HT bits - simulated
9
2
SMALL
LARGE-S
LARGE-N

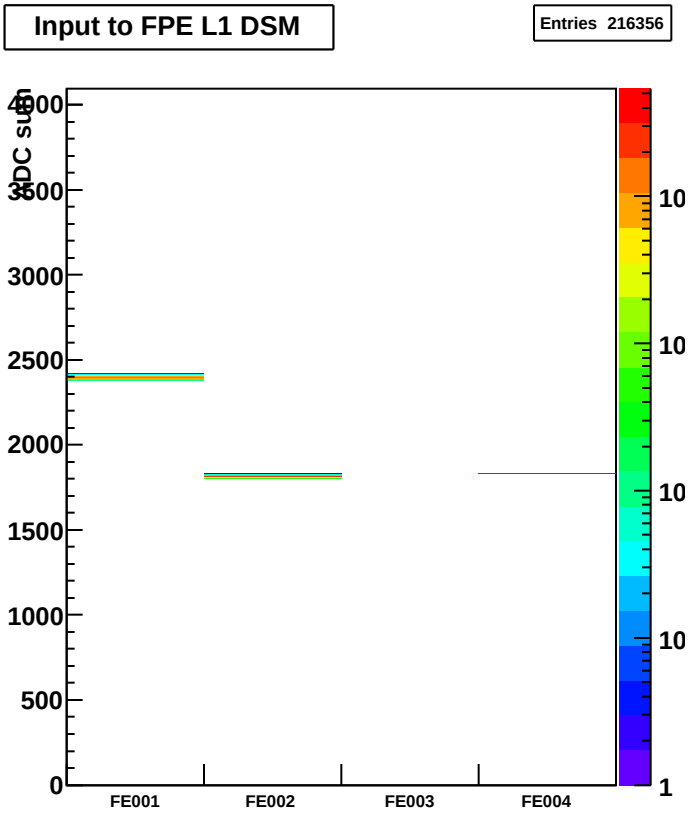

Input to FPD L2 DSM
Entries 324534
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4


Input to FE001 QT board
Entries1730848
ADC
channel

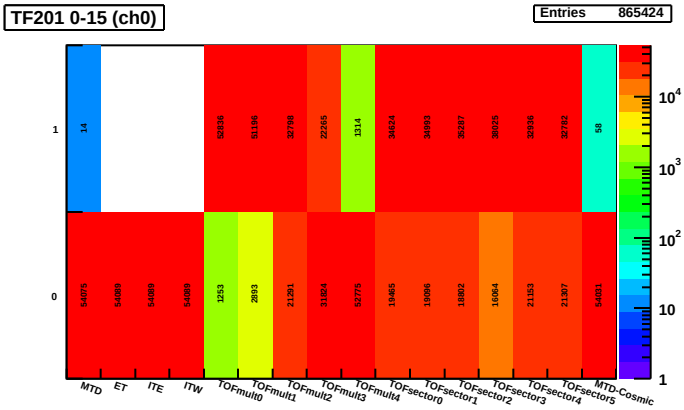

Input to FE002 QT board
Entries1730848
ADC
channel


Input to FE003 QT board
Entries1730848
ADC
channel

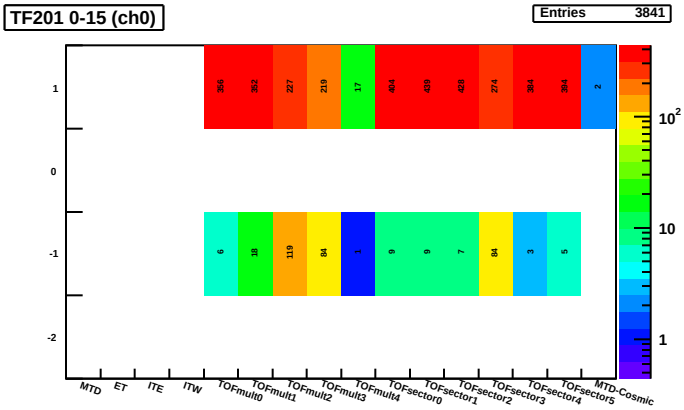

Input to FE004 QT board
Entries1730848
ADC
channel


Input to FPE L1 DSM
Entries 216356
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

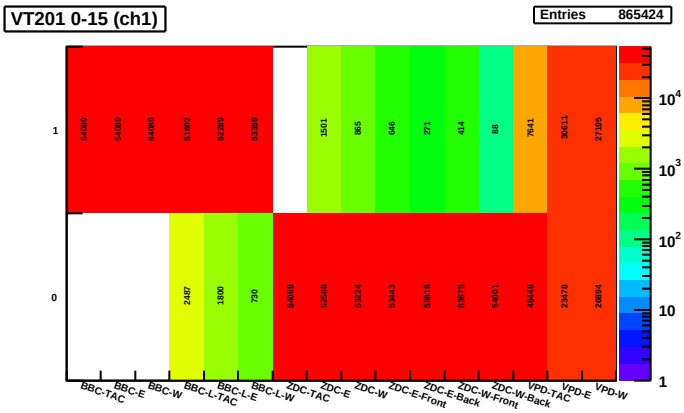
TF201 0-15 (ch0)



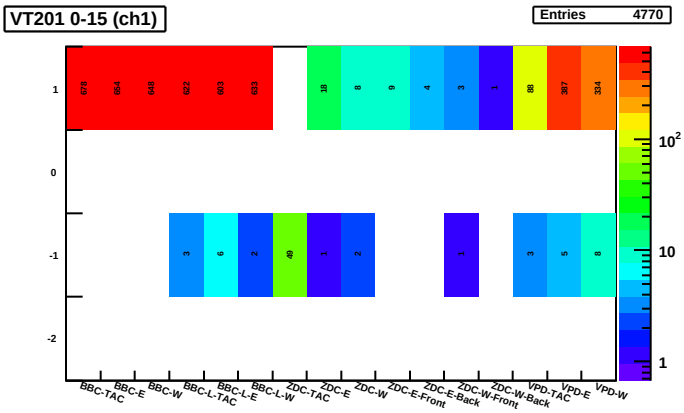
TF201 0-15 (ch0)



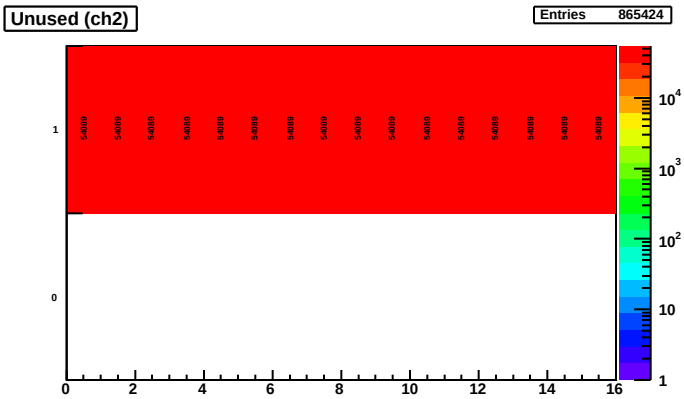
VT201 0-15 (ch1)

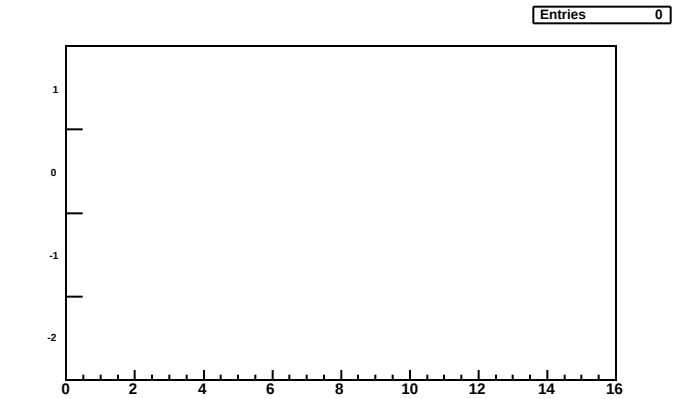


VT201 0-15 (ch1)

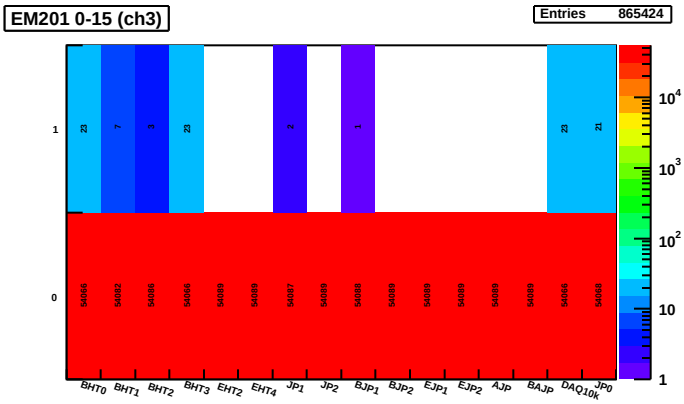


Unused (ch2)

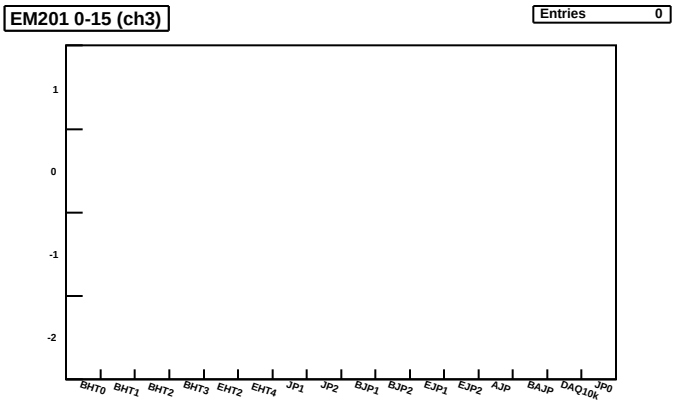




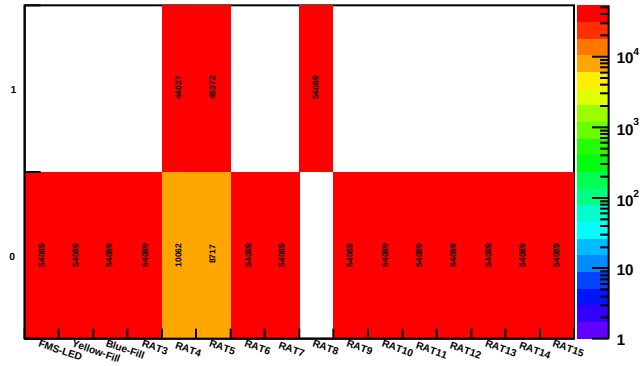
EM201 0-15 (ch3)

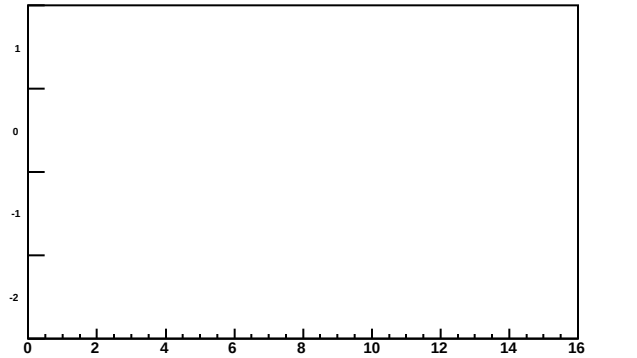


EM201 0-15 (ch3)

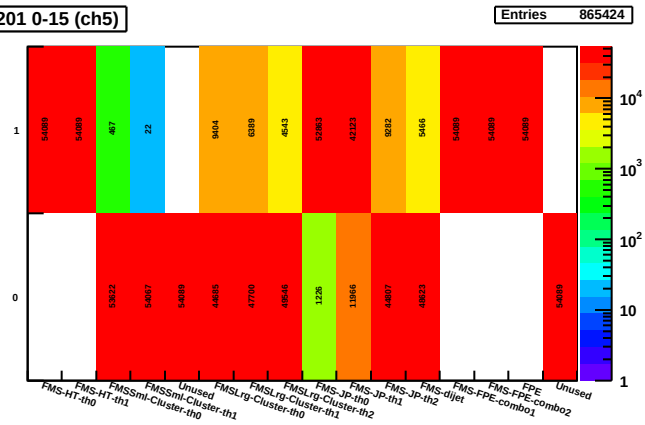


RAT board (ch4)





FP201 0-15 (ch5)



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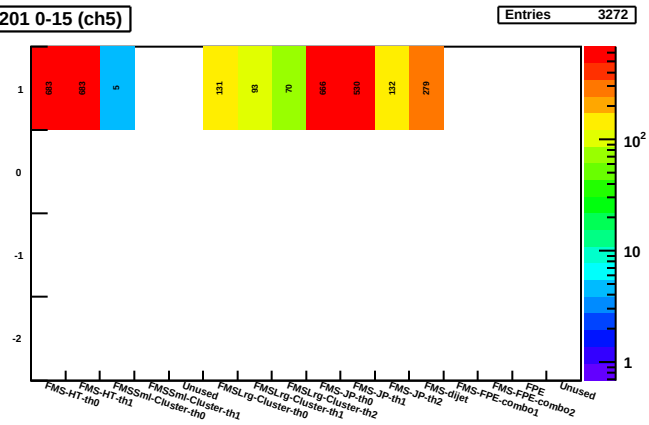

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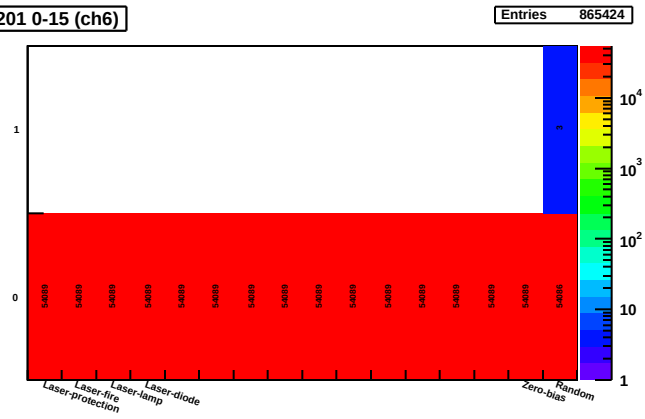

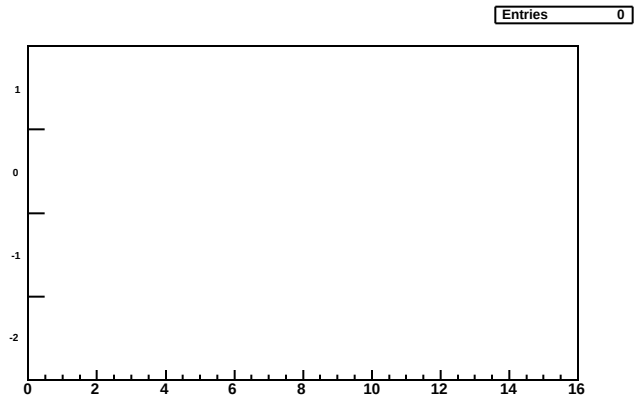
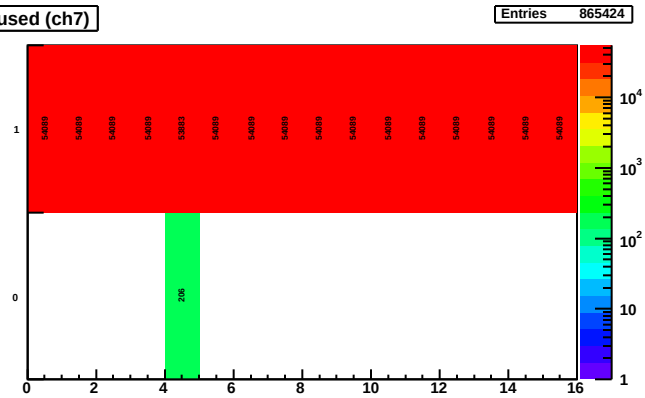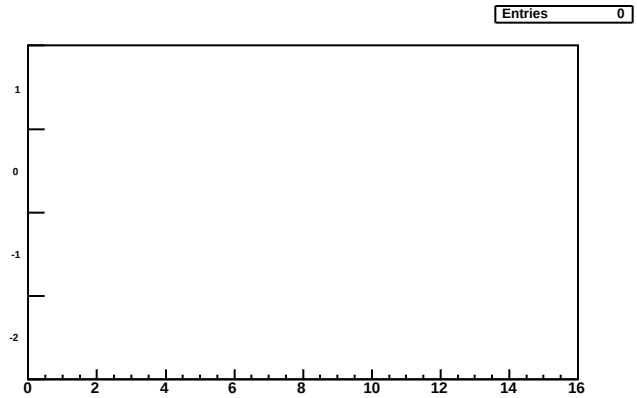
Unused (ch7)