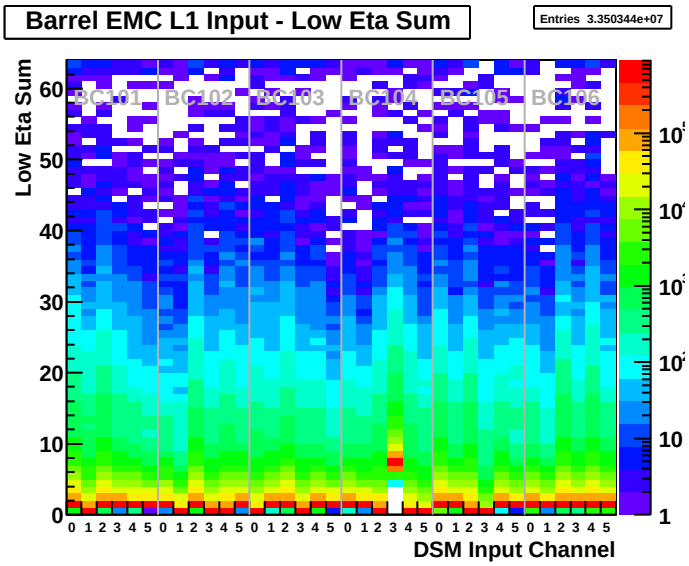
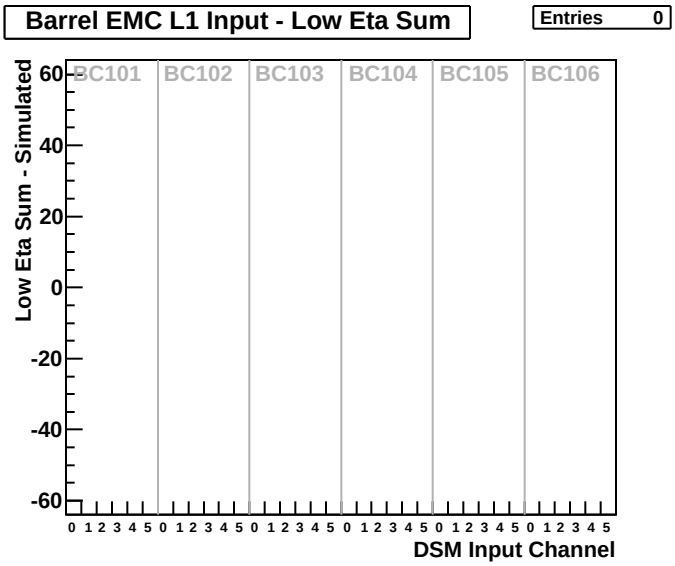
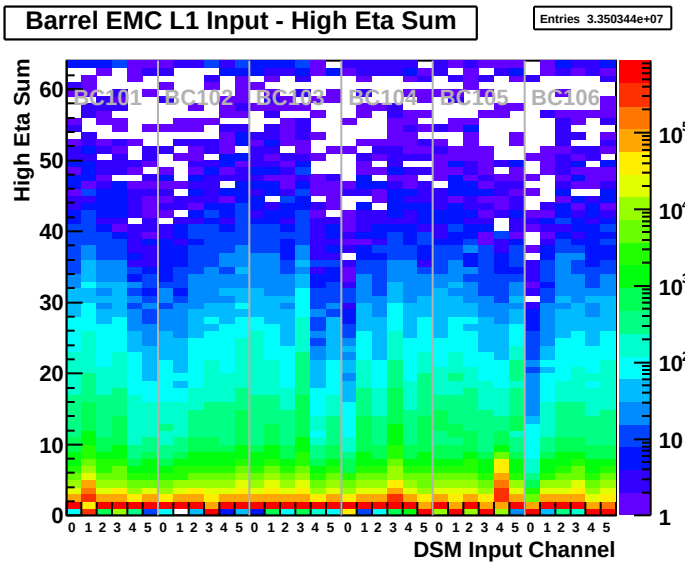
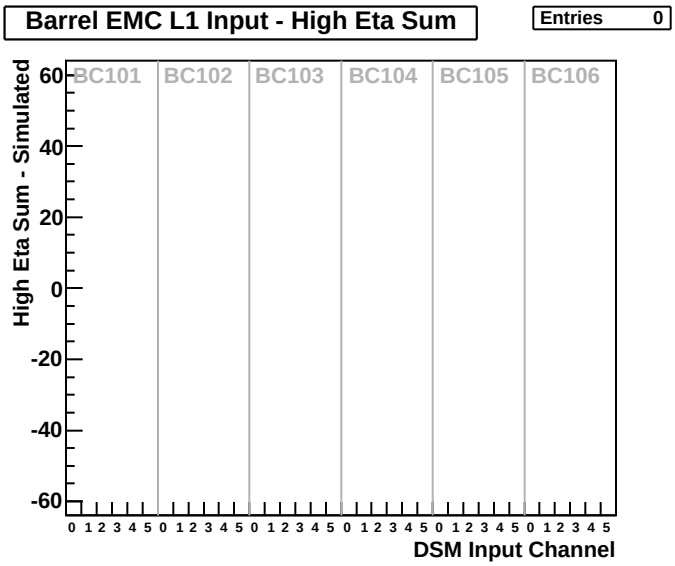
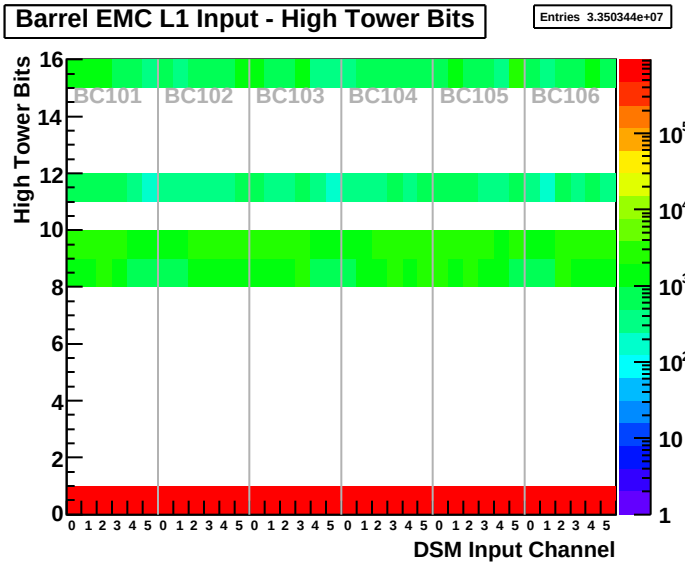
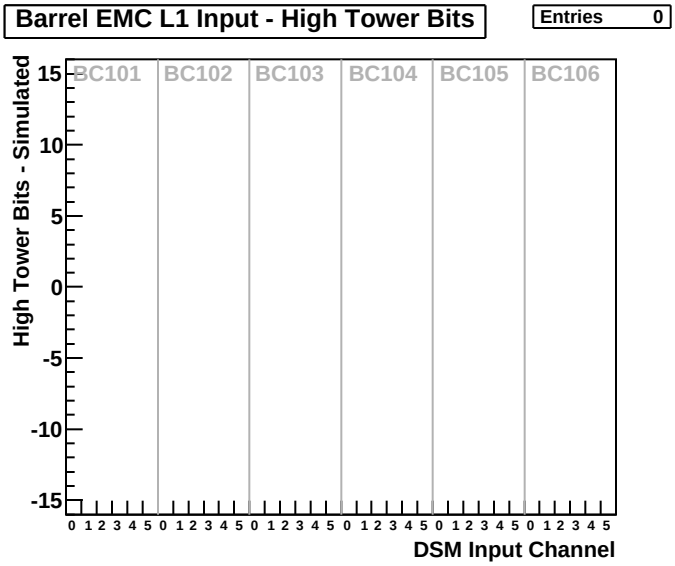

Barrel EMC L1 Input - Low Eta Sum
Entries 3.350344e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

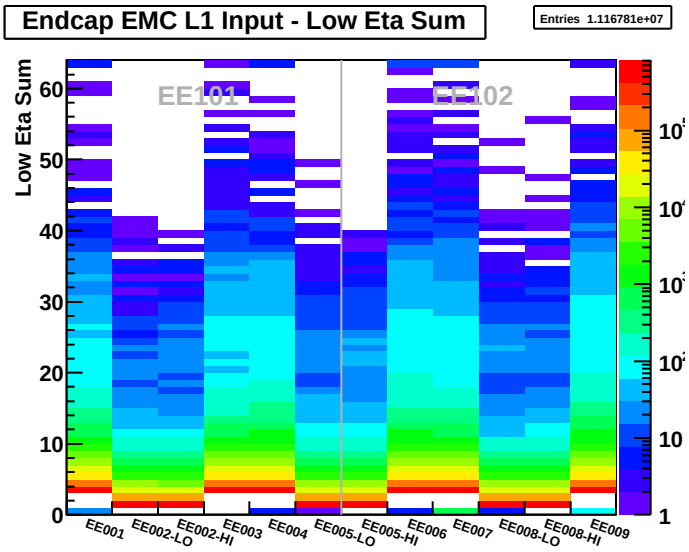

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

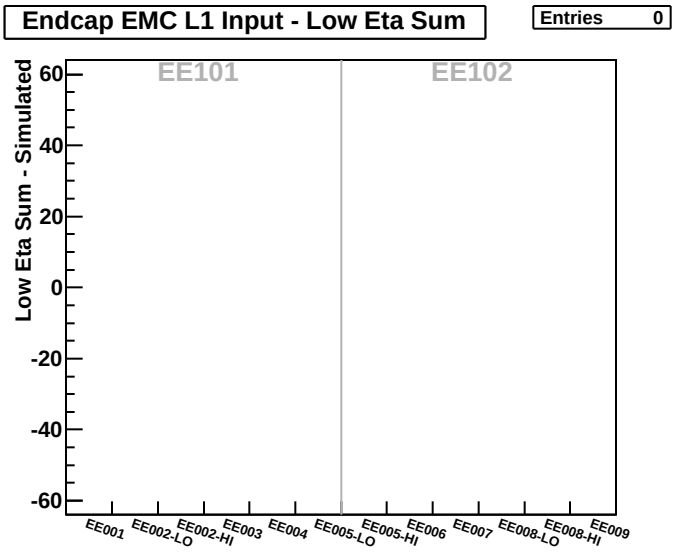

Barrel EMC L1 Input - High Eta Sum
Entries 3.350344e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

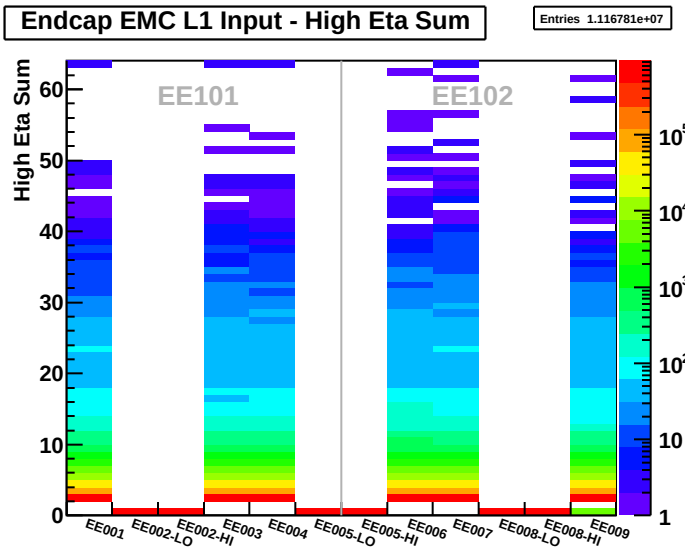

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

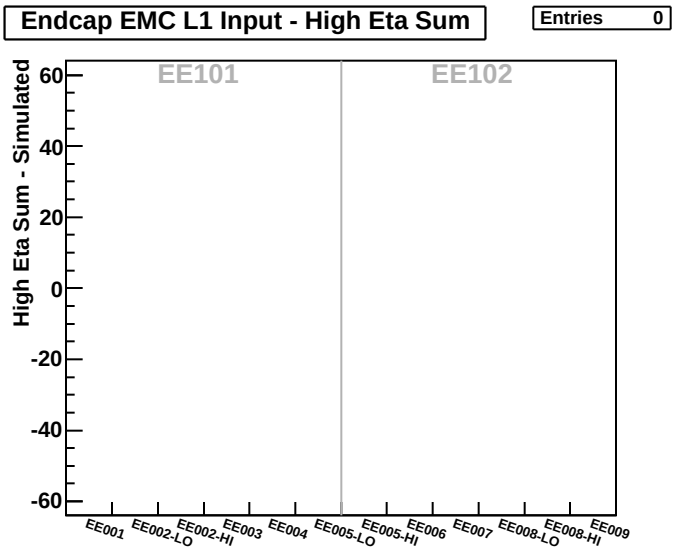

Barrel EMC L1 Input - High Tower Bits
Entries 3.350344e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

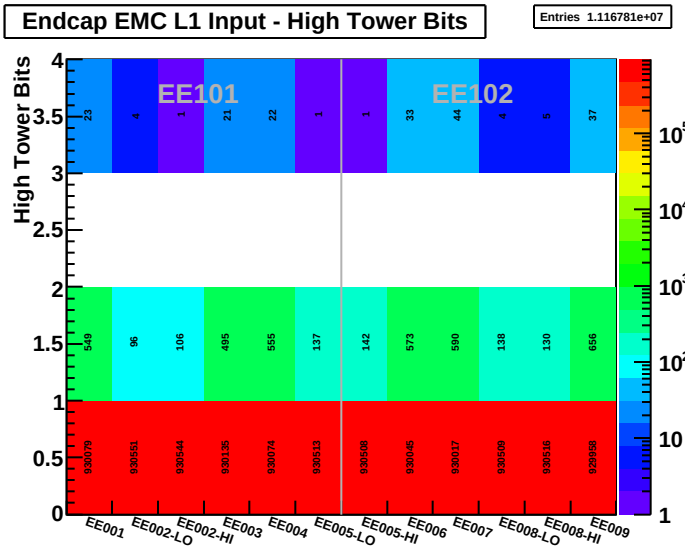

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

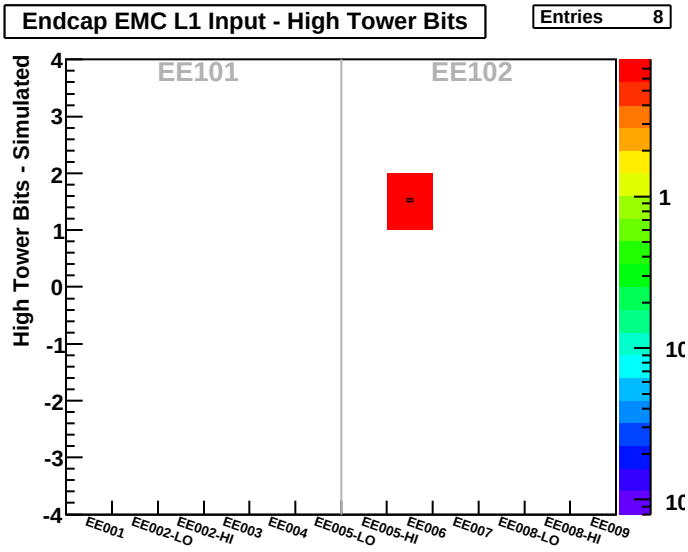

Endcap EMC L1 Input - Low Eta Sum
Entries 1.116781e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

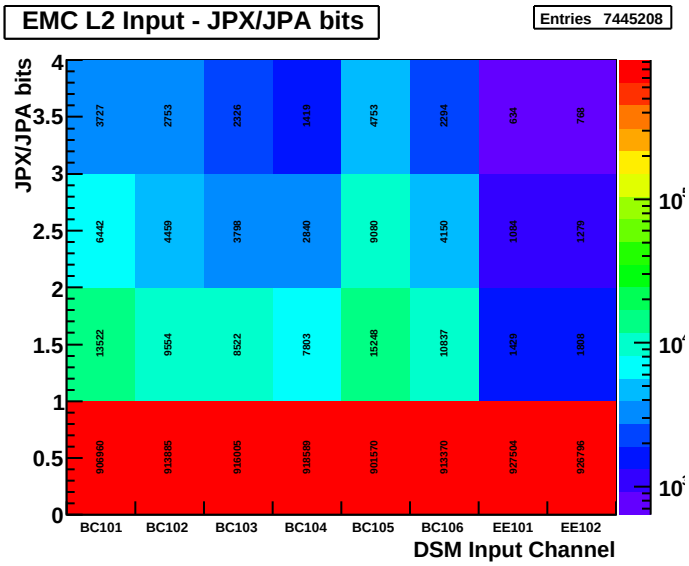

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

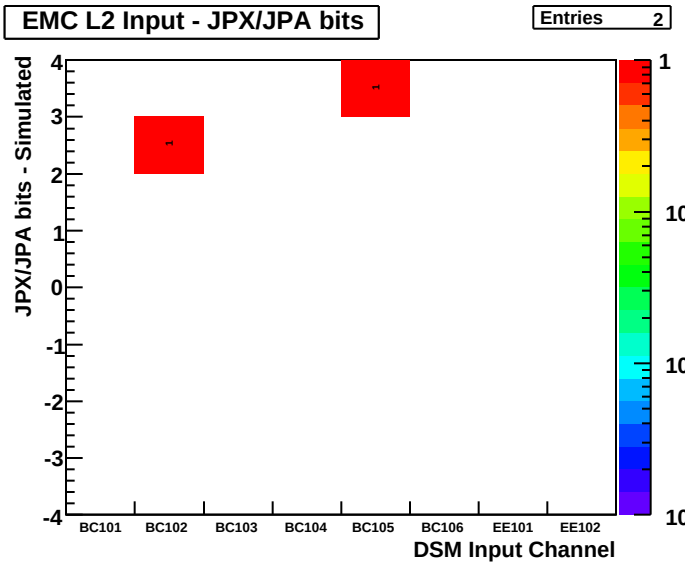

Endcap EMC L1 Input - High Eta Sum
Entries 1.116781e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

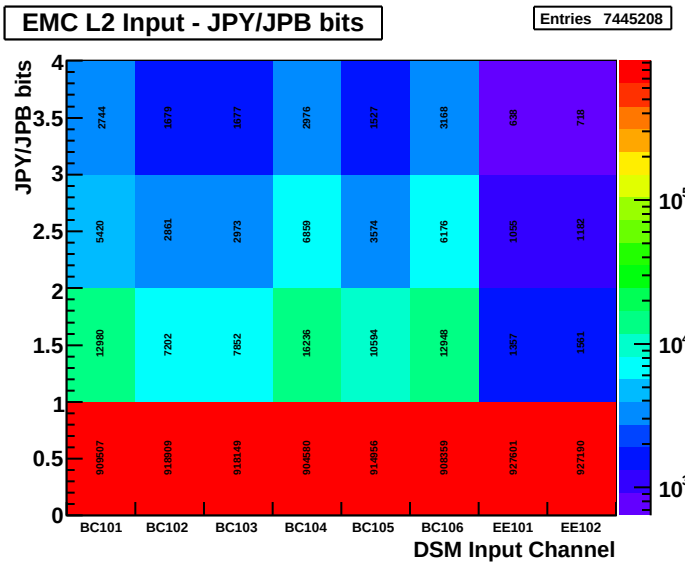

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

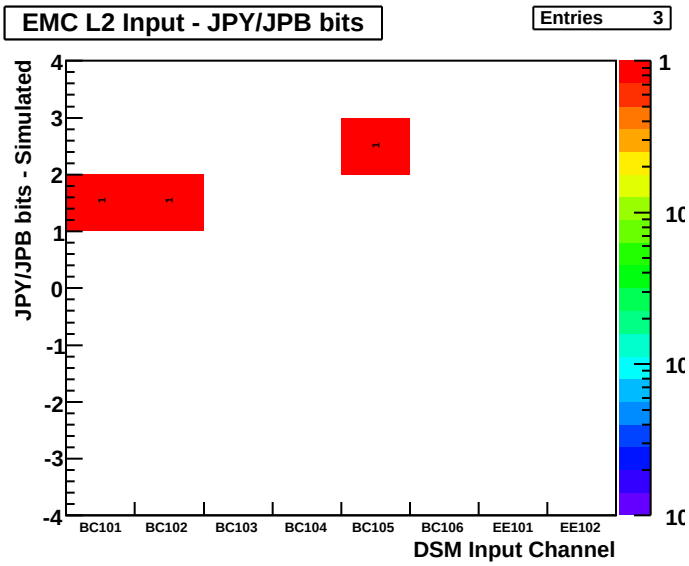

Endcap EMC L1 Input - High Tower Bits
Entries 1.116781e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

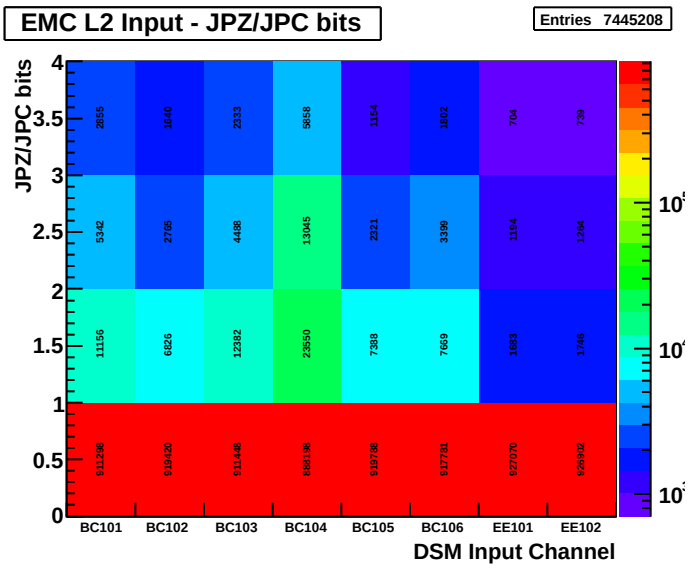

Endcap EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

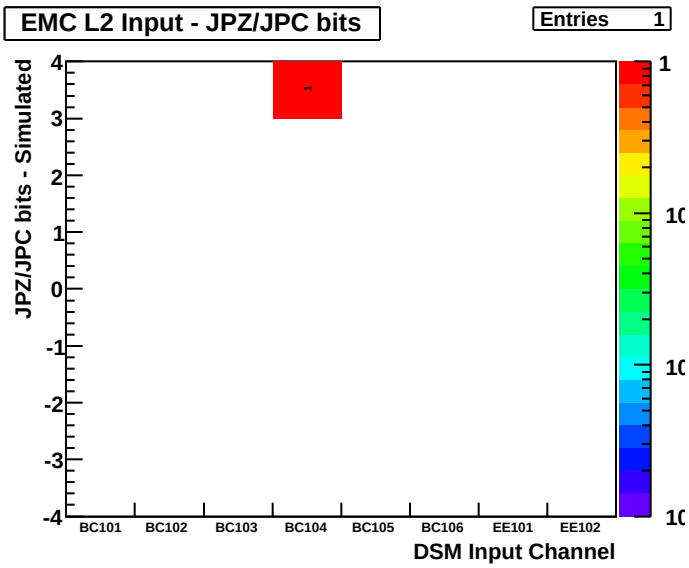

EMC L2 Input - JPX/JPA bits
Entries 7445208
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3727 2753 2236 1419 4753 2294 634 768
6442 4459 3798 2840 9080 4150 1084 1279
13522 9554 8522 7803 15248 10837 1429 1808
906960 913885 916005 918589 901570 913370 927504 926796

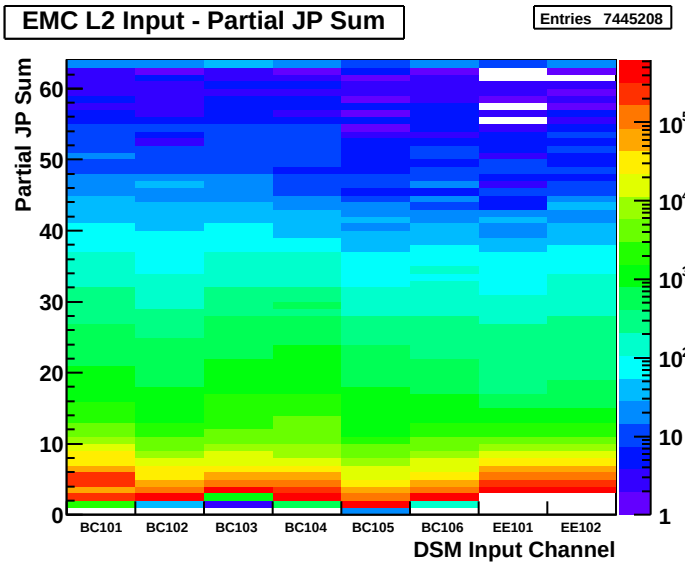

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1

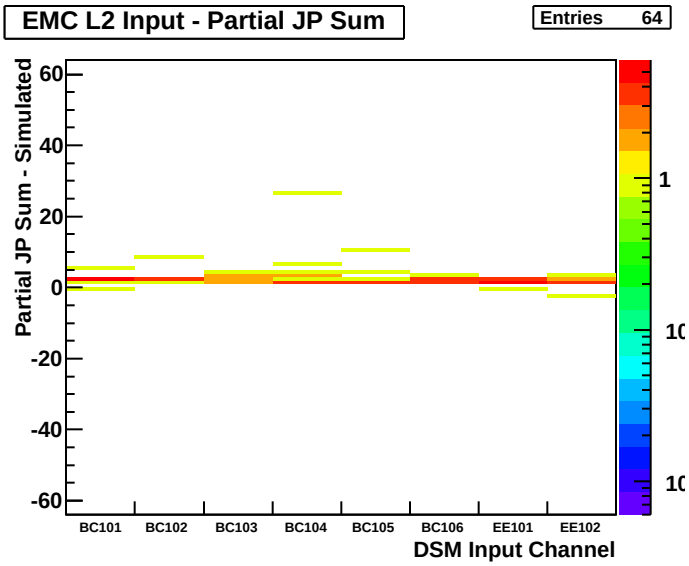

EMC L2 Input - JPY/JPB bits
Entries 7445208
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2744 1679 1677 2976 1527 3168 638 718
5420 2861 2973 6859 3574 6176 1055 1182
12980 7202 7852 16236 10594 12948 1357 1561
906507 918809 918149 904580 914956 908359 927601 922190

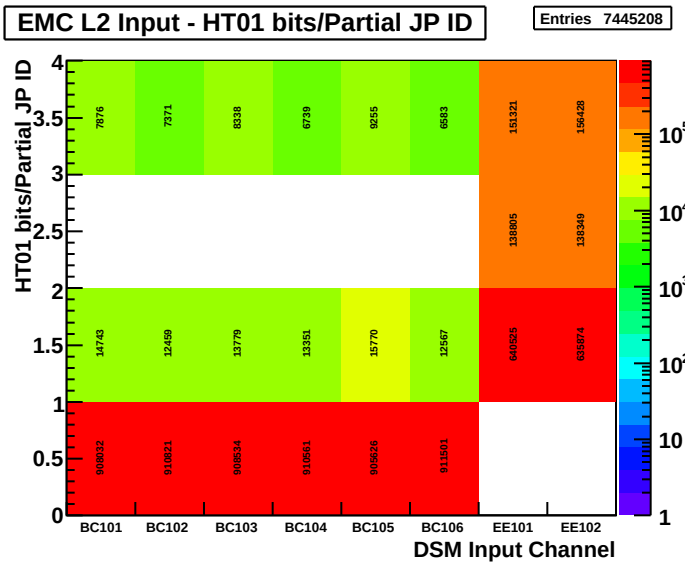

EMC L2 Input - JPY/JPB bits
Entries 3
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1
1

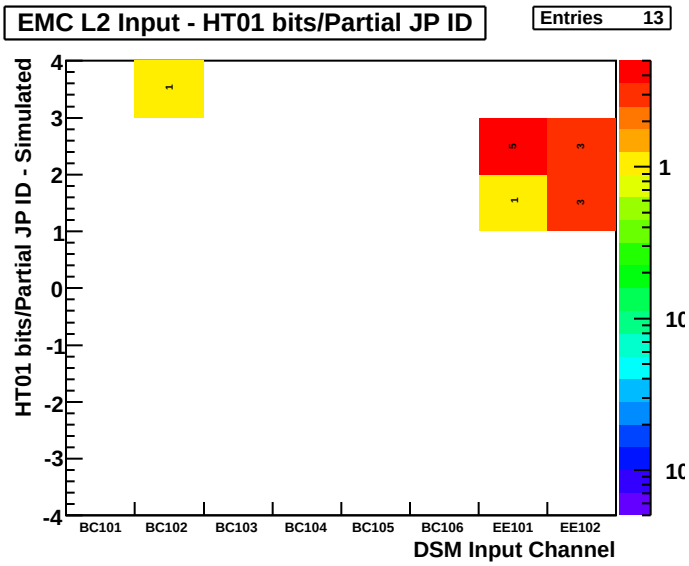

EMC L2 Input - JPZ/JPC bits
Entries 7445208
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2855 1640 2333 5858 1154 1602 704 739
5342 2765 4488 13045 2321 3399 1194 1264
11156 6826 12382 23550 7388 7669 1683 1746
911298 919420 911448 888198 919788 917781 927070 926902

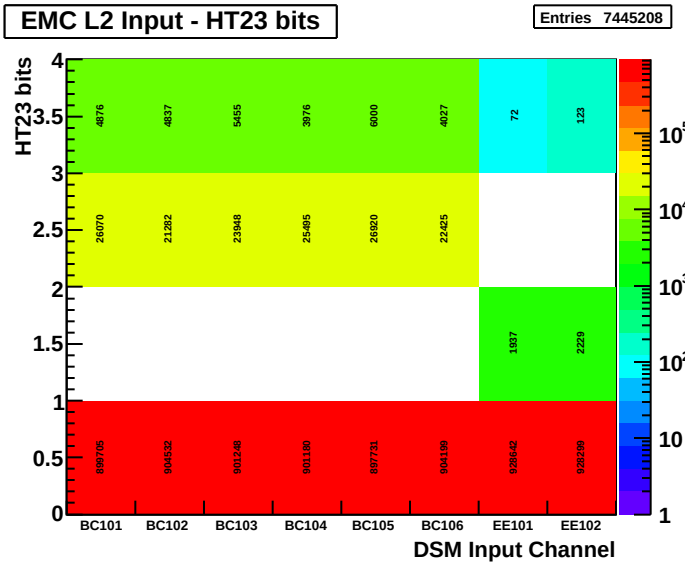

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

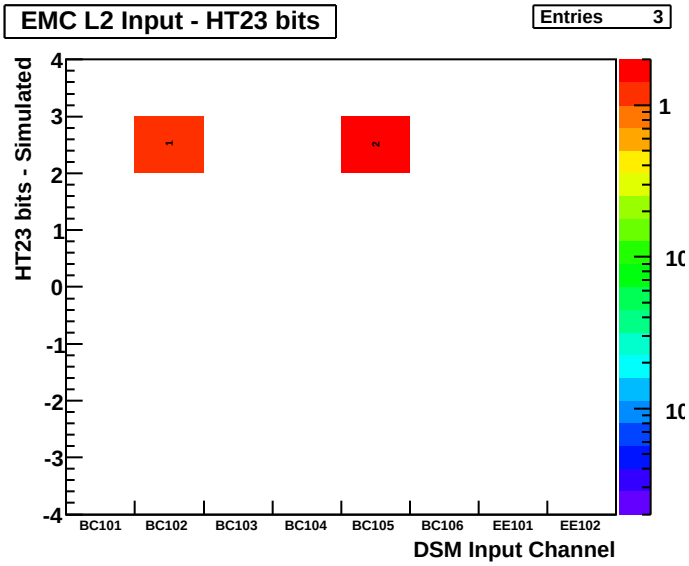

EMC L2 Input - Partial JP Sum
Entries 7445208
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

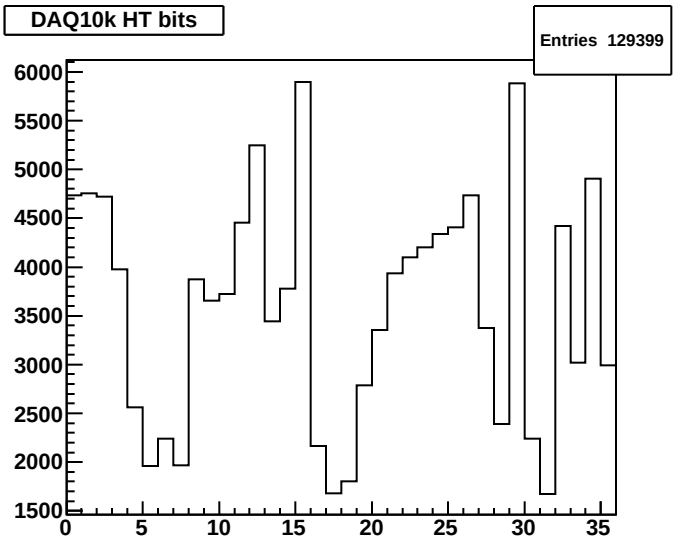

EMC L2 Input - Partial JP Sum
Entries 64
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

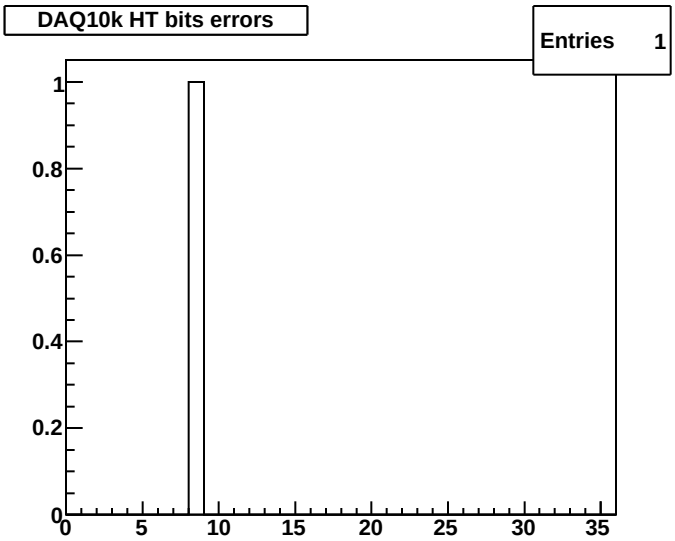

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7445208
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

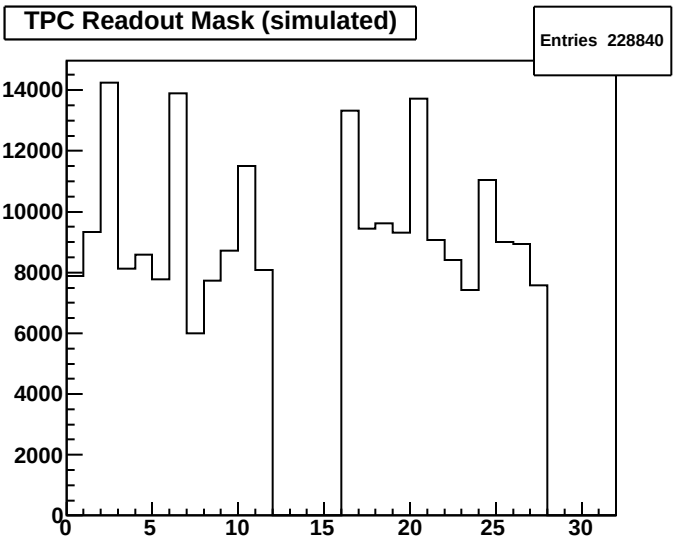

EMC L2 Input - HT01 bits/Partial JP ID
Entries 13
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

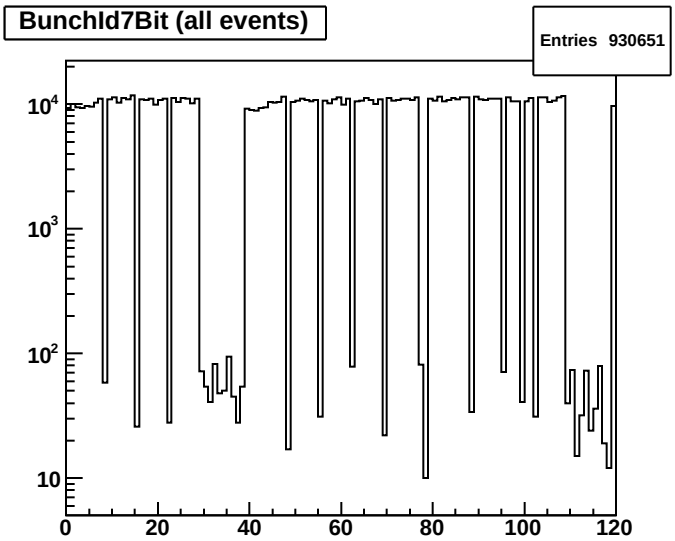

EMC L2 Input - HT23 bits
Entries 7445208
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

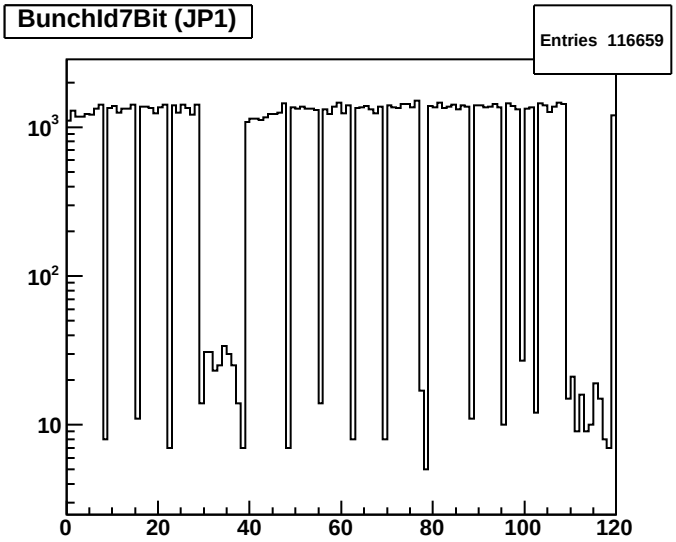

EMC L2 Input - HT23 bits
Entries 3
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

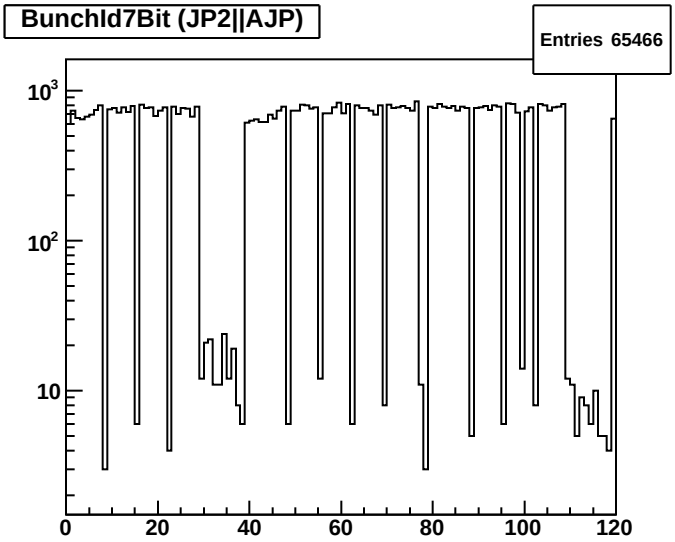

DAQ10k HT bits
Entries 129399

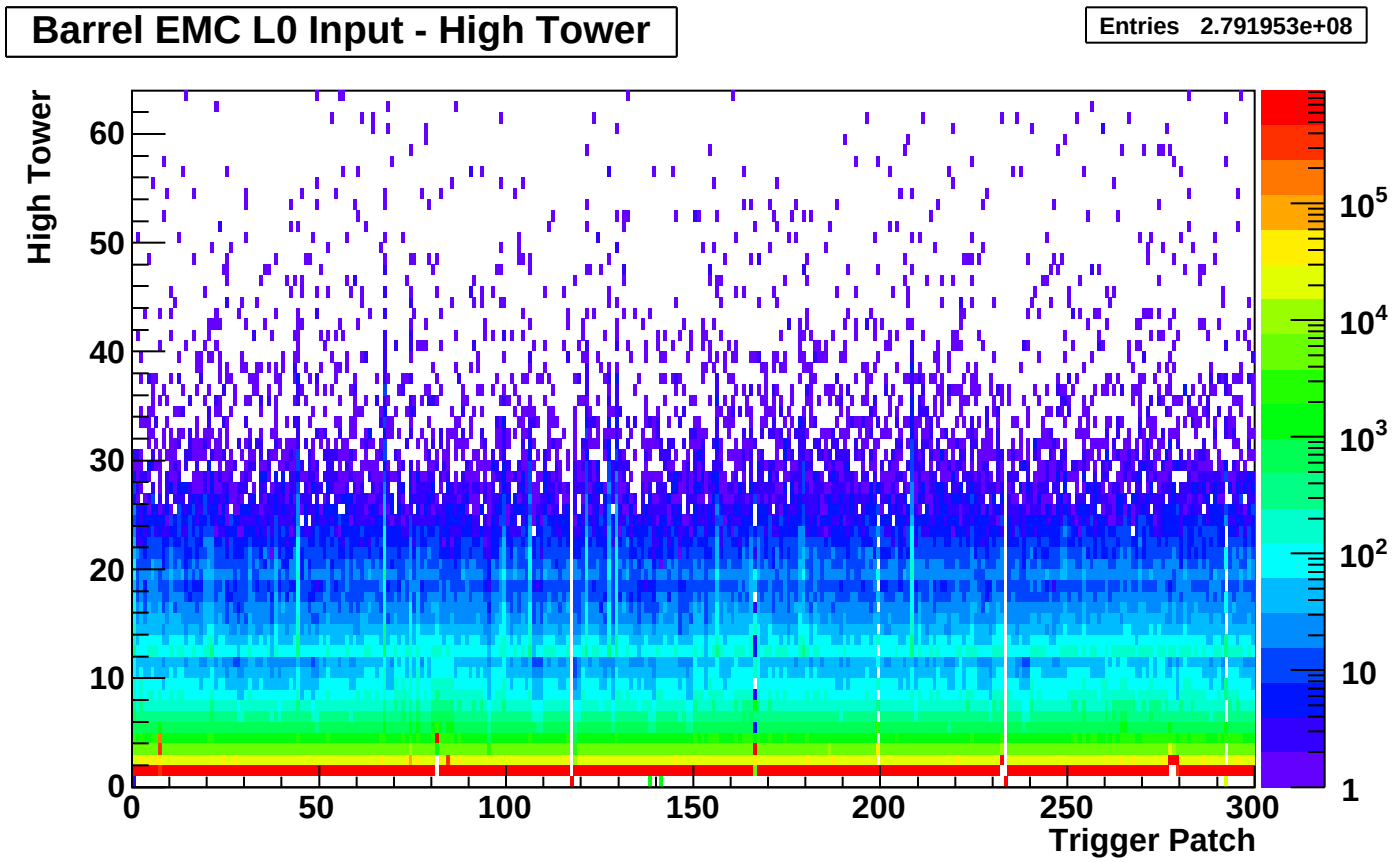

DAQ10k HT bits errors
Entries 1

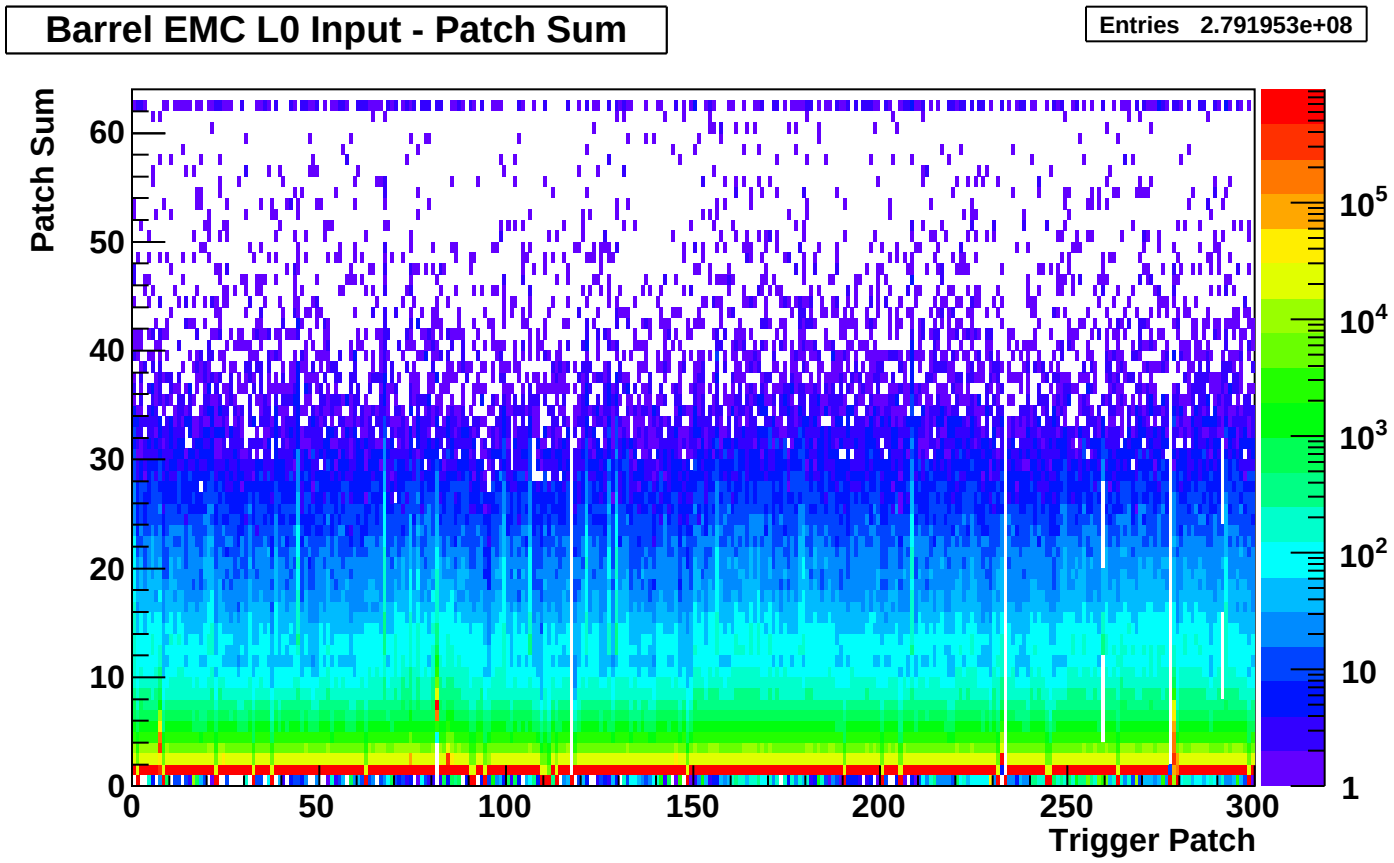

TPC Readout Mask (simulated)
Entries 228840

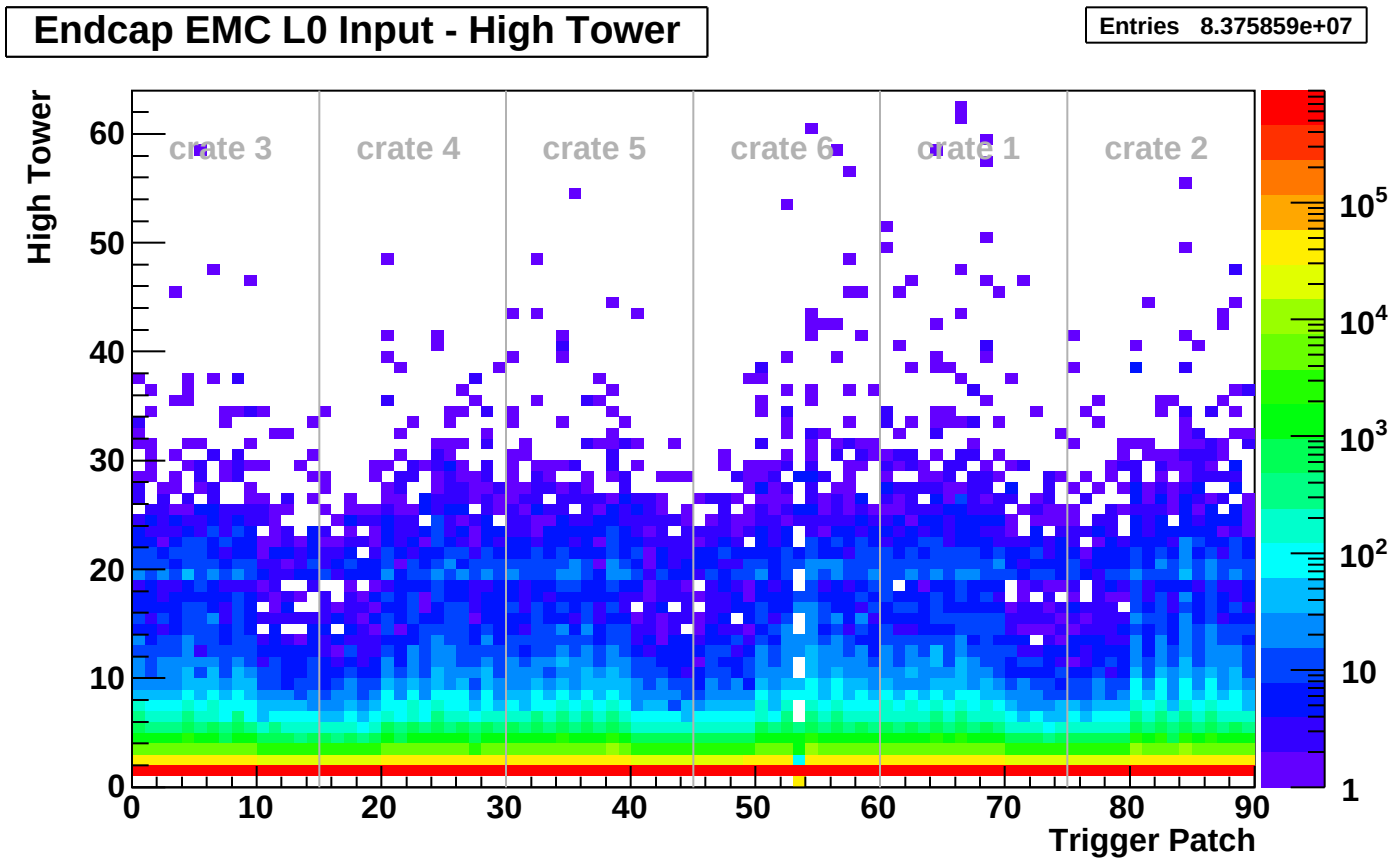

BunchId7Bit (all events)
Entries 930651

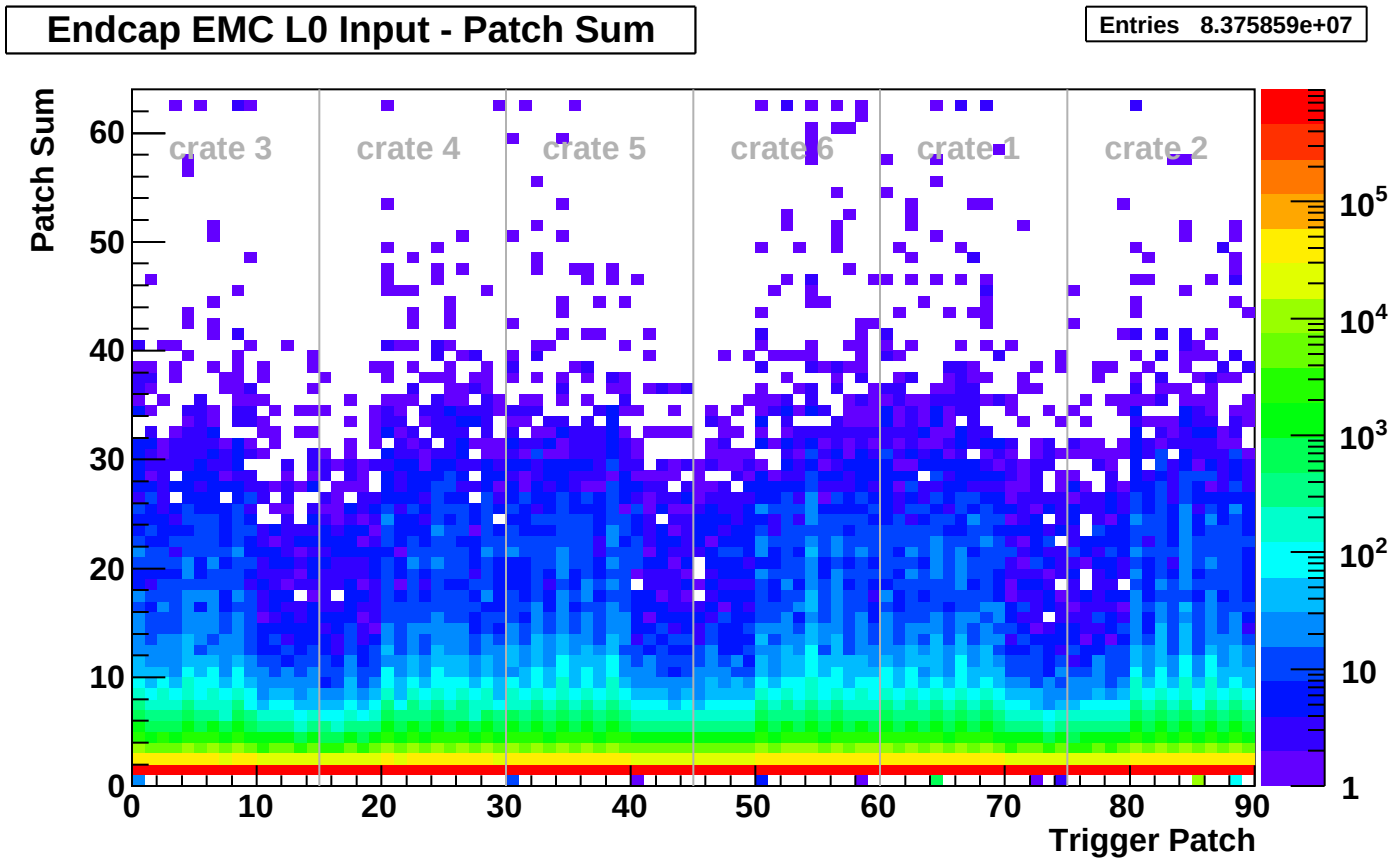

BunchId7Bit (JP1)
Entries 116659

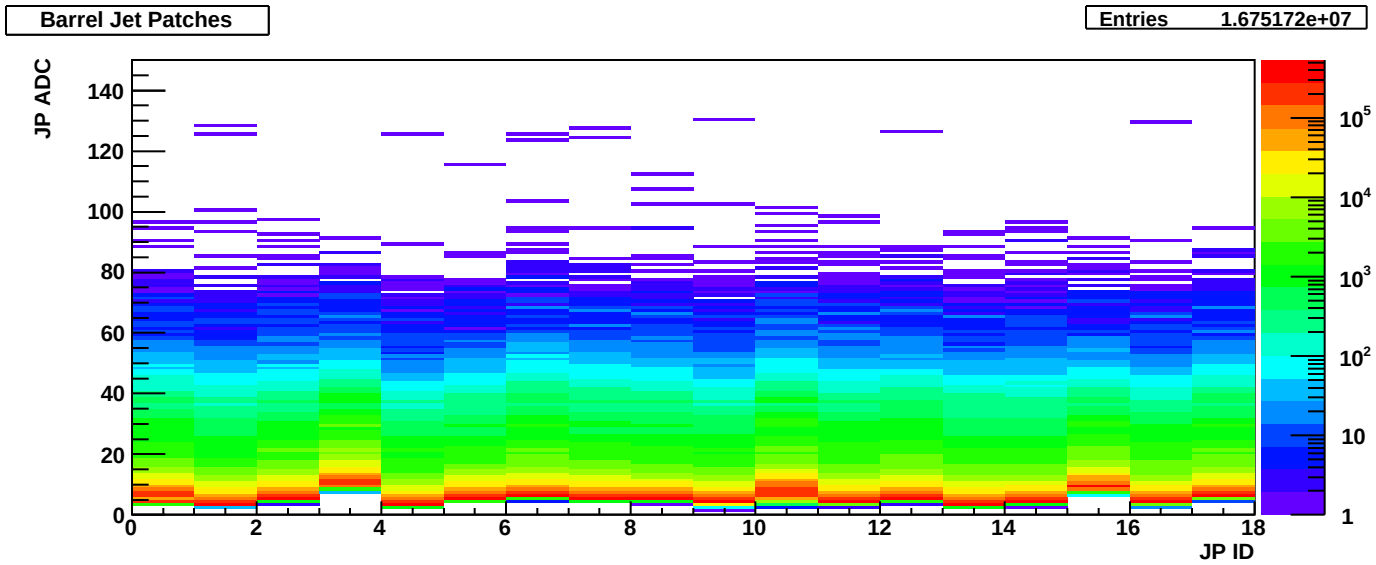

BunchId7Bit (JP2||AJP)
Entries 65466

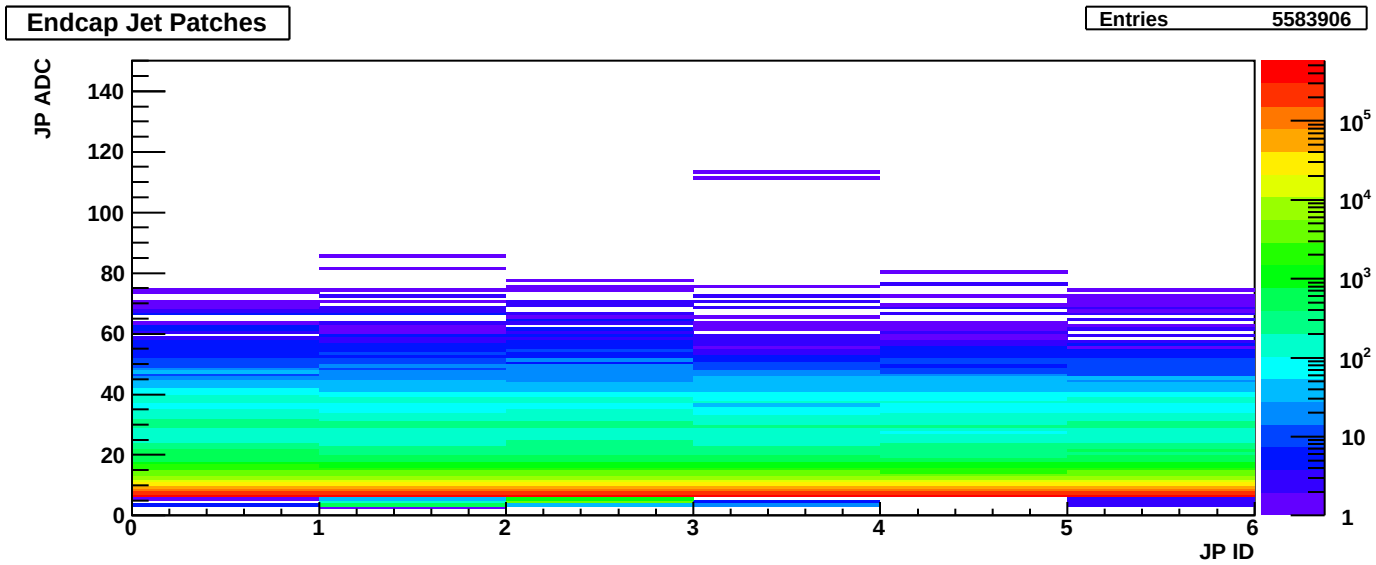

Barrel EMC L0 Input - High Tower
Entries 2.791953e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

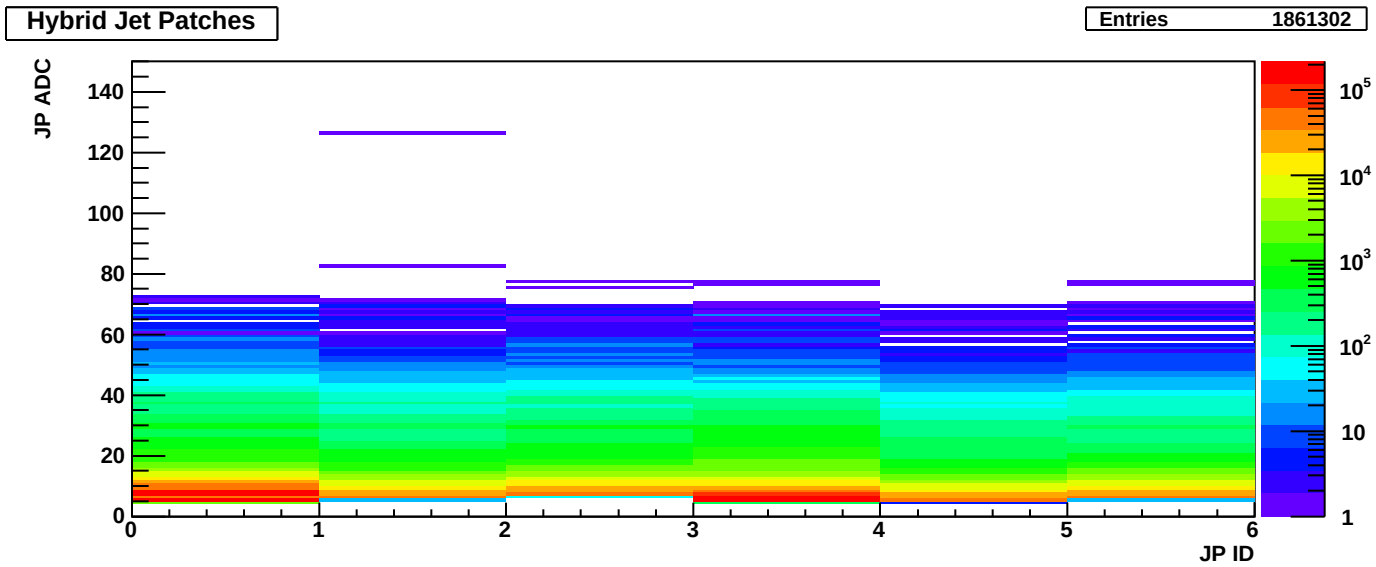

Barrel EMC L0 Input - Patch Sum
Entries 2.791953e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

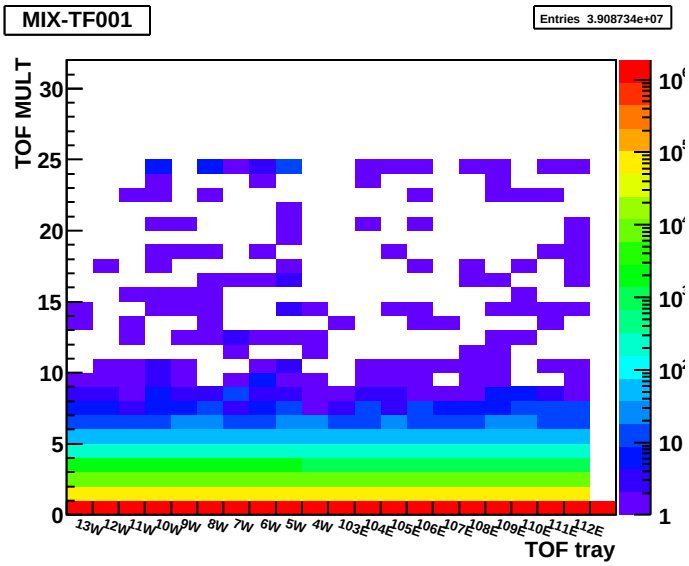

Endcap EMC L0 Input - High Tower
Entries 8.375859e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

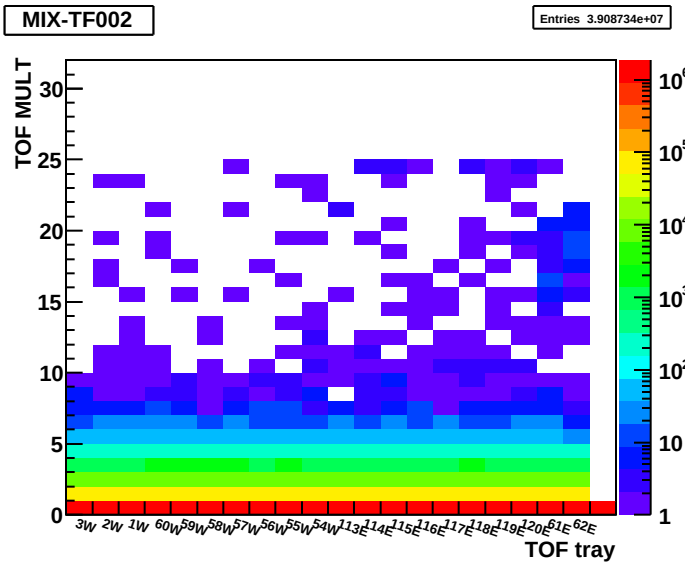

Endcap EMC L0 Input - Patch Sum
Entries 8.375859e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

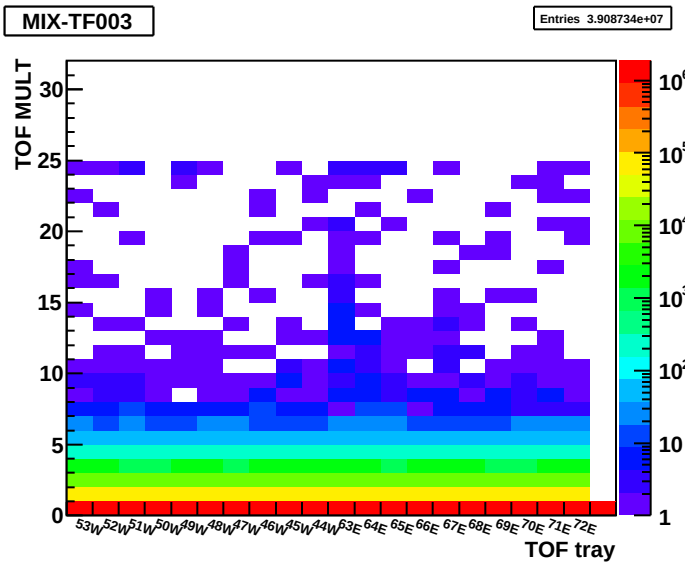

Barrel Jet Patches
Entries 1.675172e+07
JP ADC
JP ID

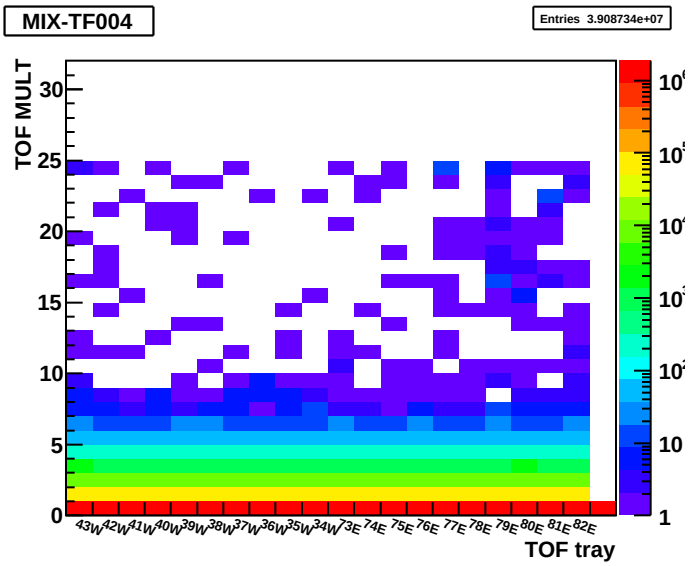

Endcap Jet Patches
Entries 5583906
JP ADC
JP ID

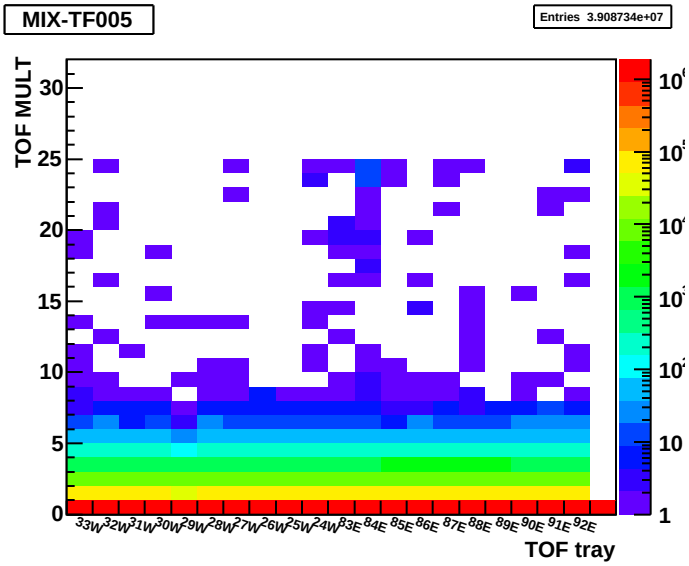

Hybrid Jet Patches
Entries 1861302
JP ADC
JP ID

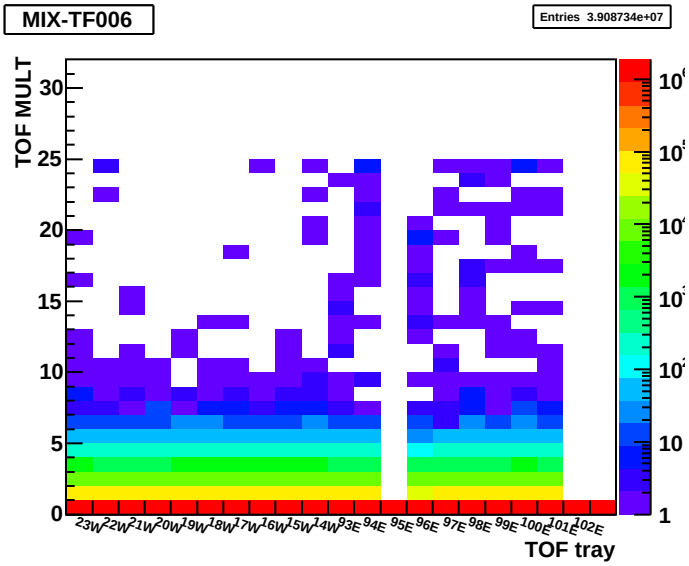

MIX-TF001
Entries 3.908734e+07
TOF MULT
TOF tray

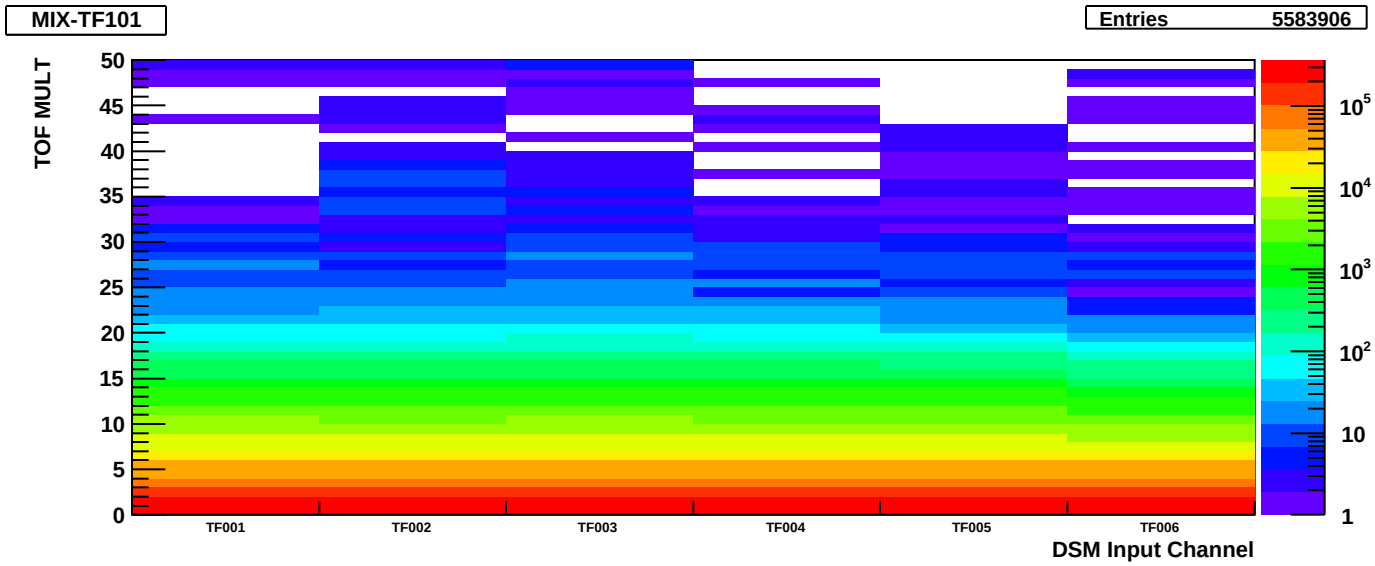

MIX-TF002
Entries 3.908734e+07
TOF MULT
TOF tray

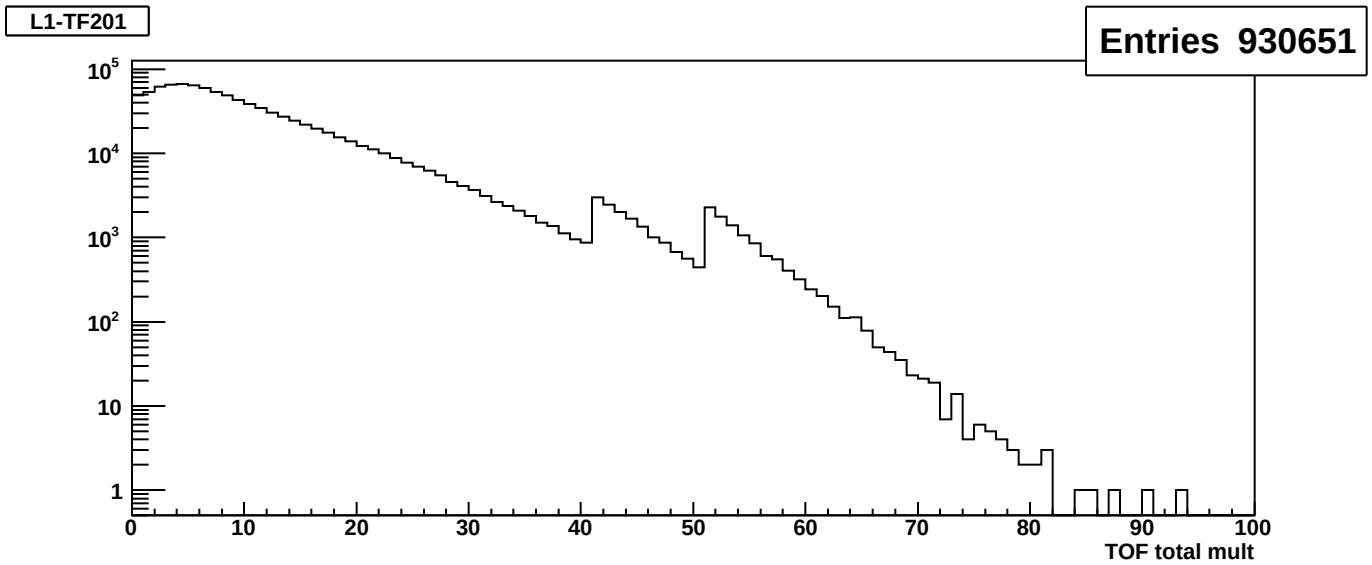

MIX-TF003
Entries 3.908734e+07
TOF MULT
TOF tray

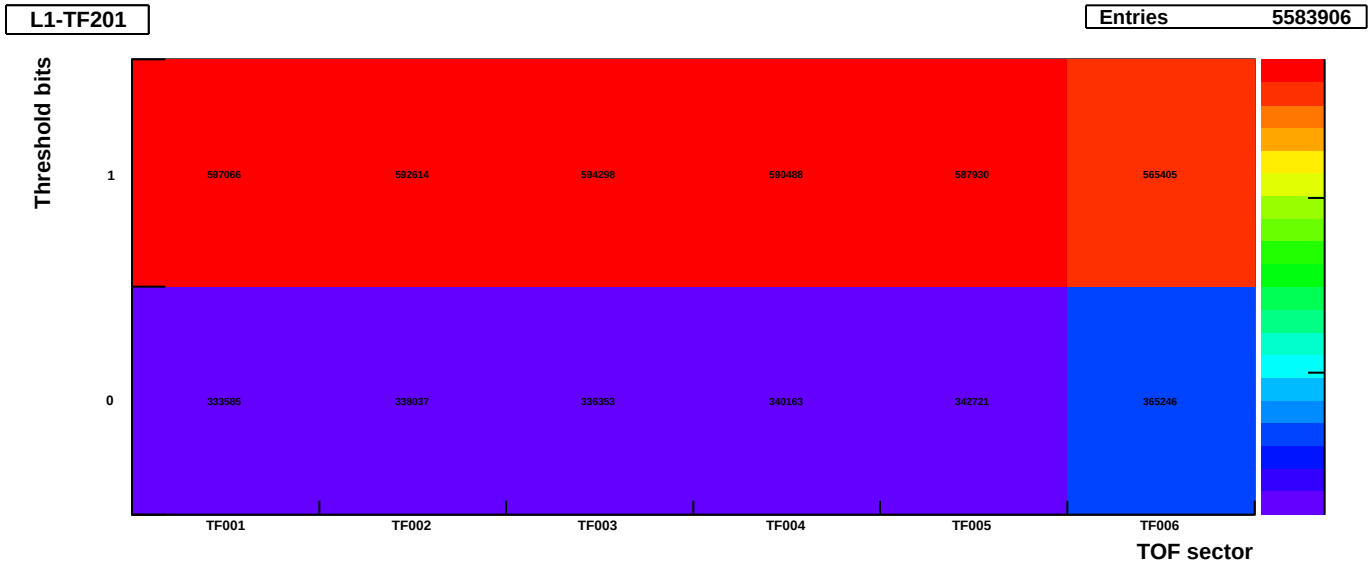

MIX-TF004
Entries 3.908734e+07
TOF MULT
TOF tray

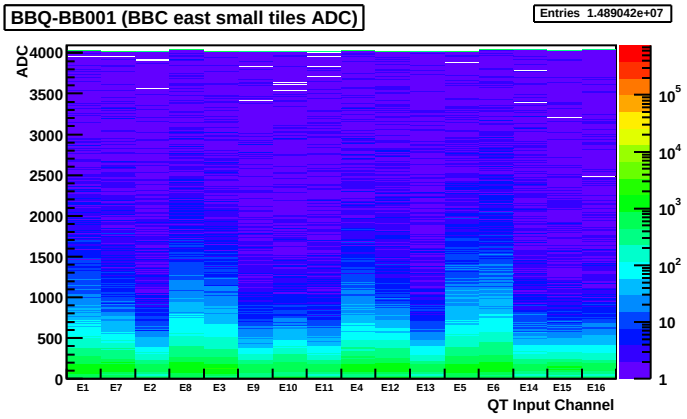

MIX-TF005
Entries 3.908734e+07
TOF MULT
TOF tray

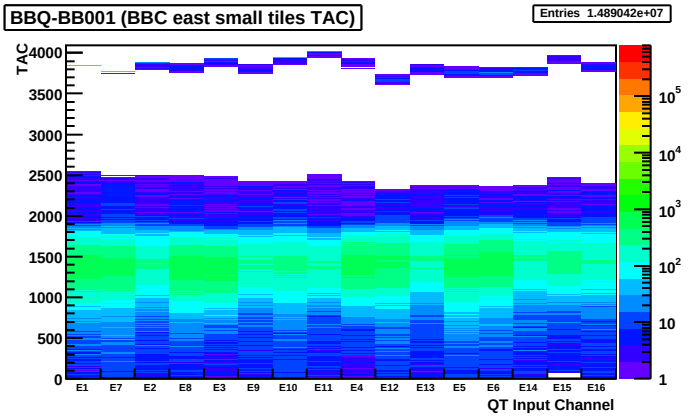

MIX-TF006
Entries 3.908734e+07
TOF MULT
TOF tray

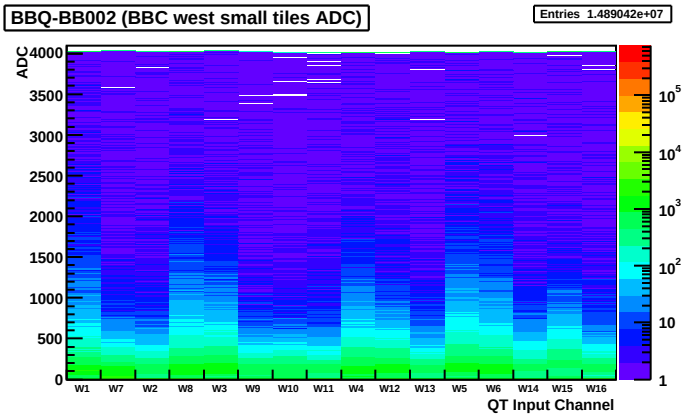

MIX-TF101
Entries 5583906
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

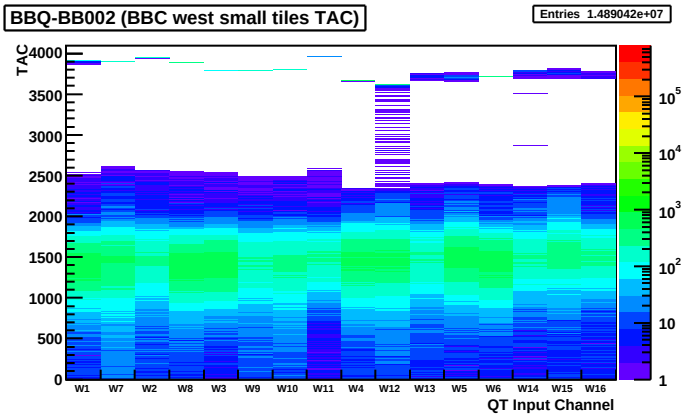

L1-TF201
Entries 930651
TOF total mult

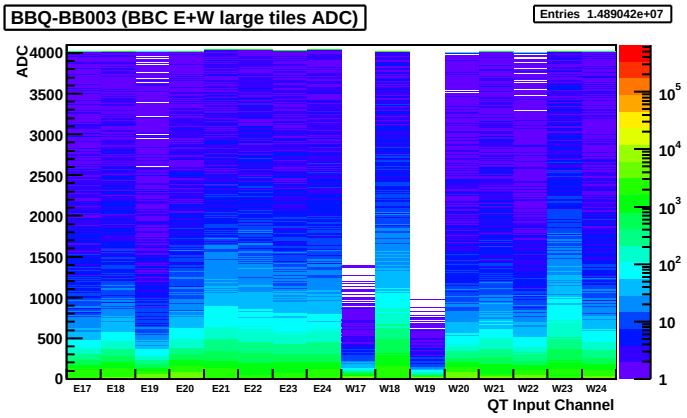

L1-TF201
Entries 5583906
Threshold bits
597066
592614
594298
590488
587930
565405
333585
338037
336353
340163
342721
365246
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

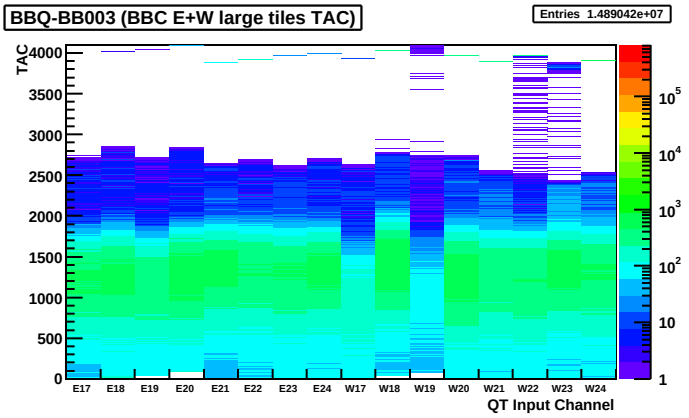

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.489042e+07
ADC
QT Input Channel

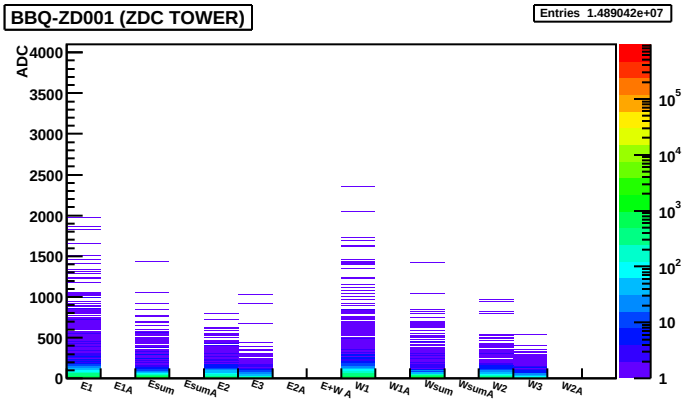

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.489042e+07
TAC
QT Input Channel

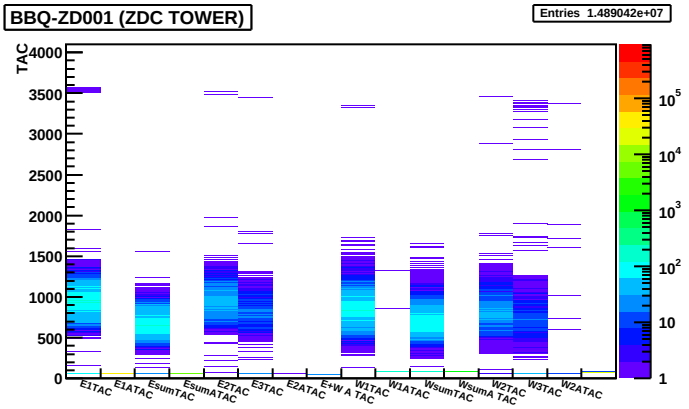

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.489042e+07
ADC
QT Input Channel

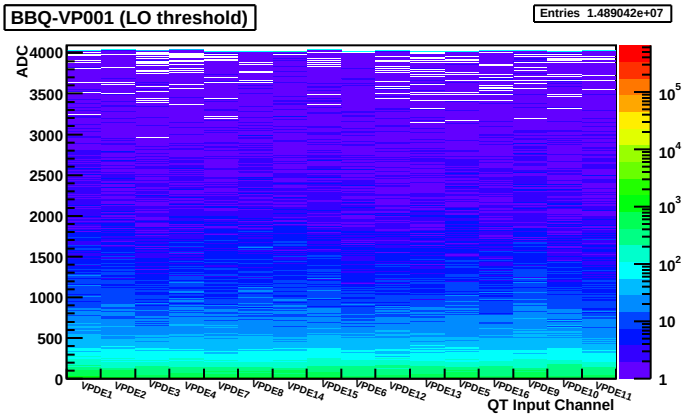

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.489042e+07
TAC
QT Input Channel

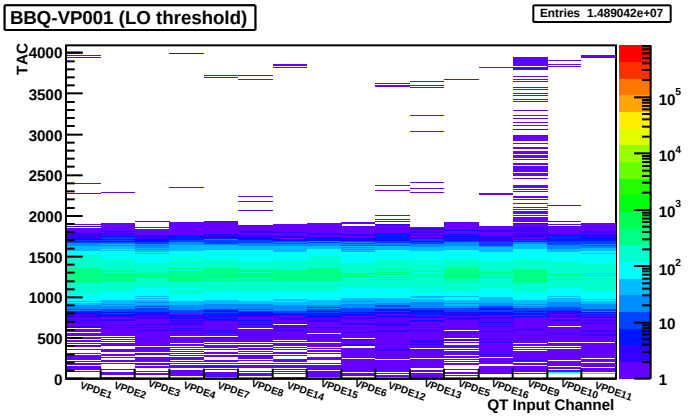

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.489042e+07
ADC
QT Input Channel

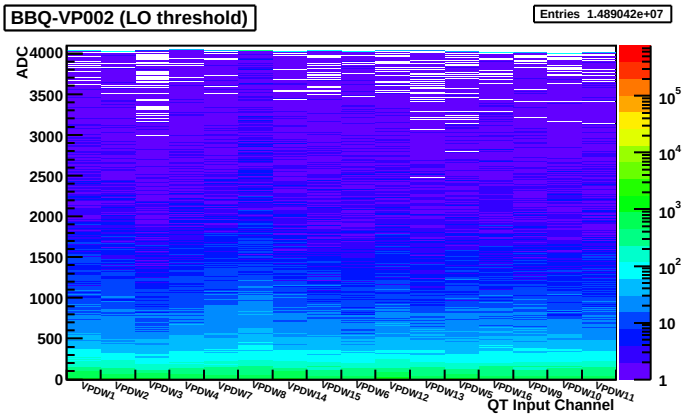

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.489042e+07
TAC
QT Input Channel

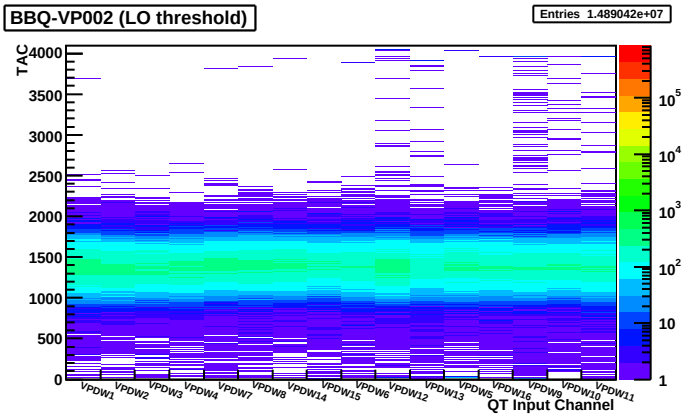

BBQ-ZD001 (ZDC TOWER)
Entries 1.489042e+07
ADC

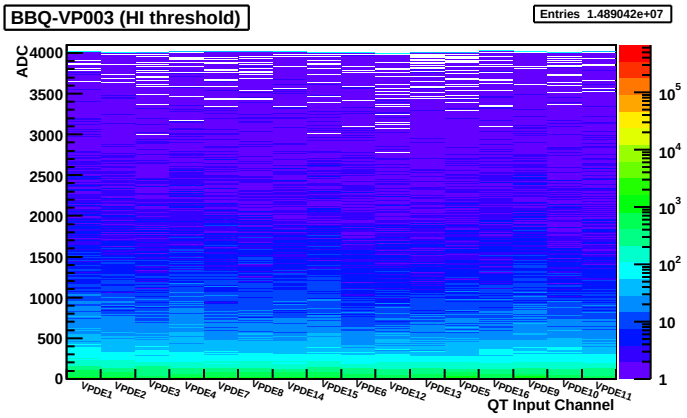

BBQ-ZD001 (ZDC TOWER)
Entries 1.489042e+07
TAC

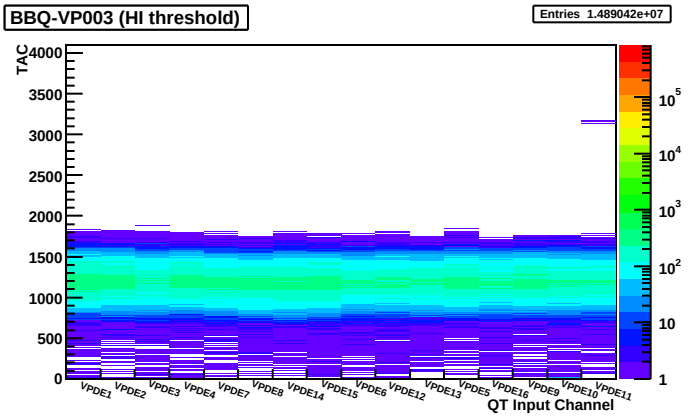

BBQ-VP001 (LO threshold)
Entries 1.489042e+07
ADC
QT Input Channel

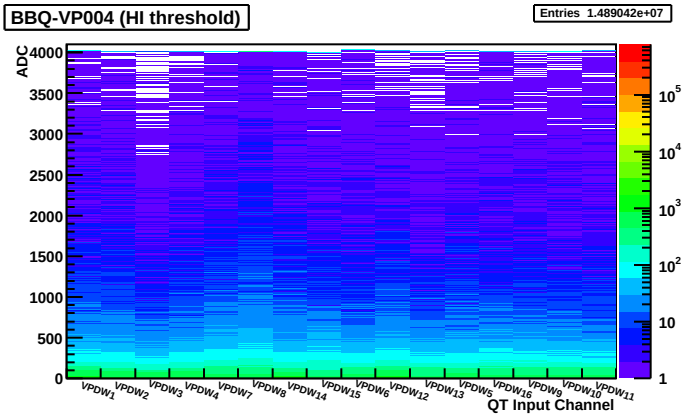

BBQ-VP001 (LO threshold)
Entries 1.489042e+07
TAC
QT Input Channel

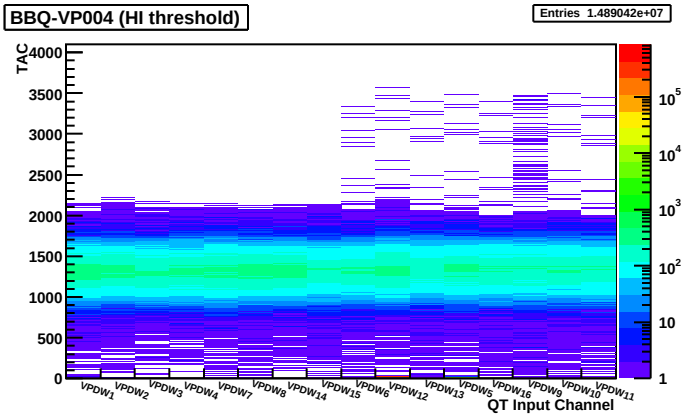

BBQ-VP002 (LO threshold)
Entries 1.489042e+07
ADC
QT Input Channel

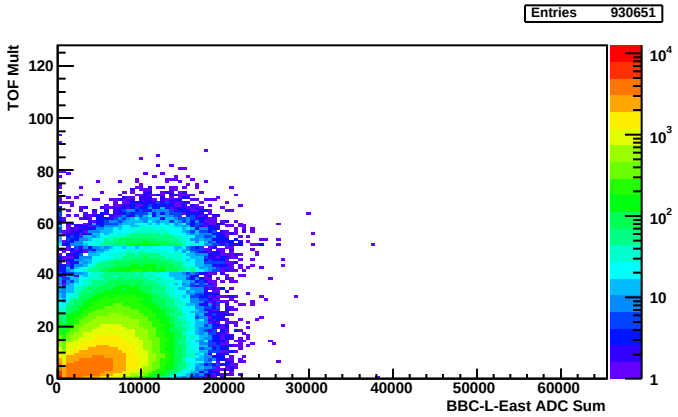

BBQ-VP002 (LO threshold)
Entries 1.489042e+07
TAC
QT Input Channel

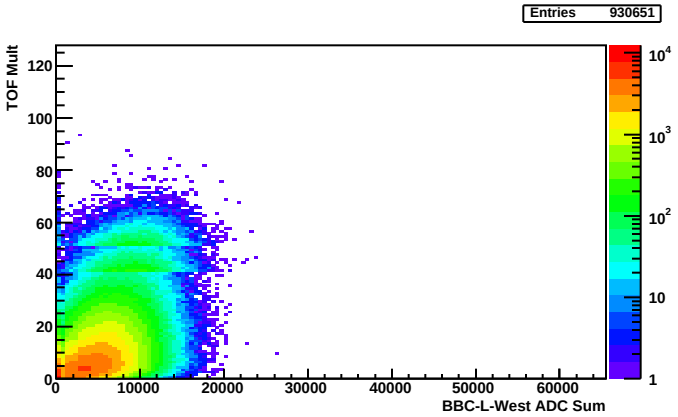

BBQ-VP003 (HI threshold)
Entries 1.489042e+07
ADC
QT Input Channel

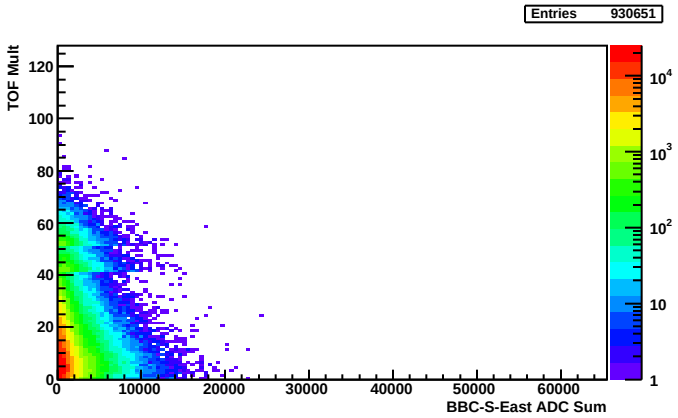

BBQ-VP003 (HI threshold)
Entries 1.489042e+07
TAC
QT Input Channel

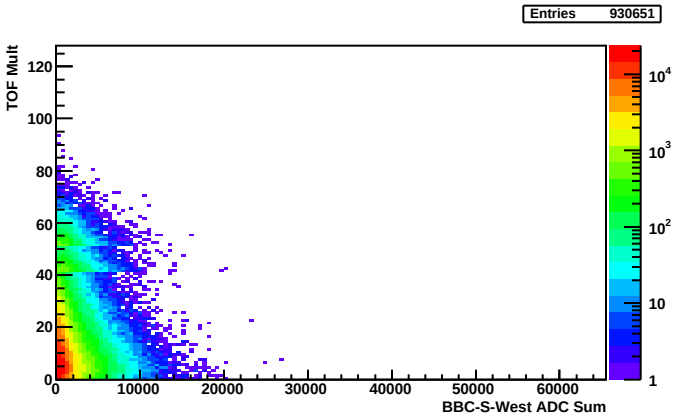

BBQ-VP004 (HI threshold)
Entries 1.489042e+07
ADC
QT Input Channel

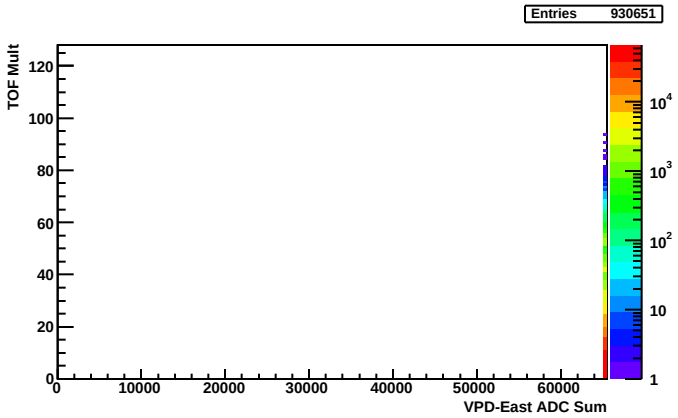

BBQ-VP004 (HI threshold)
Entries 1.489042e+07
TAC
QT Input Channel

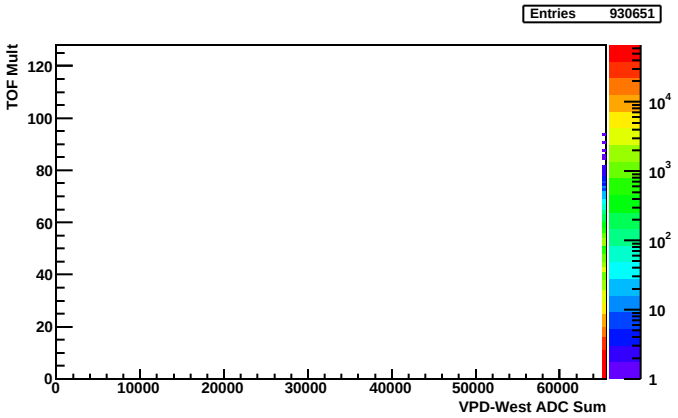

Entries 930651
TOF Mult
BBC-L-East ADC Sum

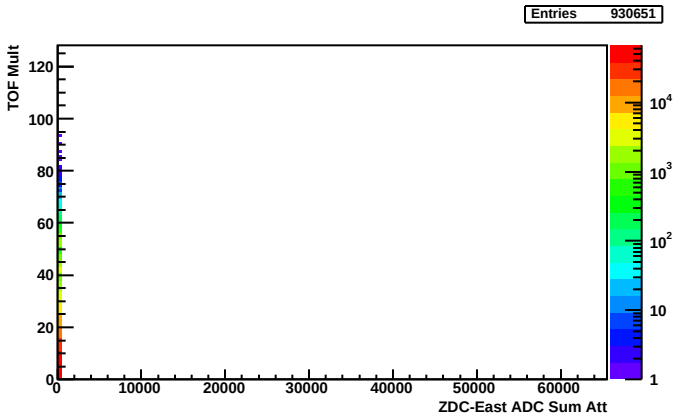

Entries 930651
TOF Mult
BBC-L-West ADC Sum

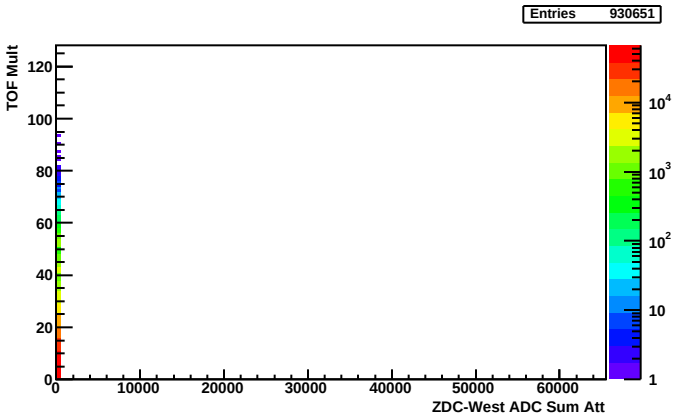

Entries 930651
TOF Mult
BBC-S-East ADC Sum

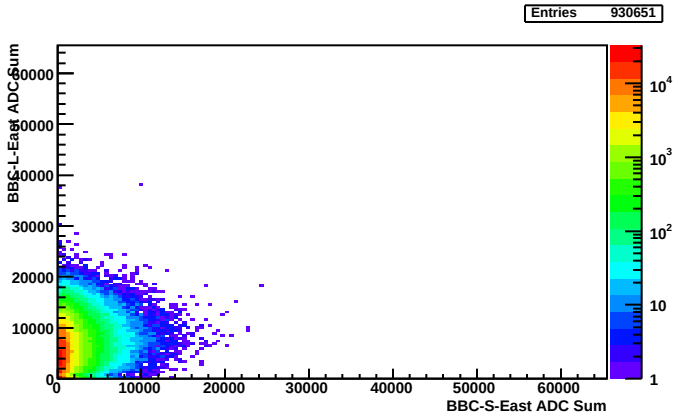

Entries 930651
TOF Mult
BBC-S-West ADC Sum

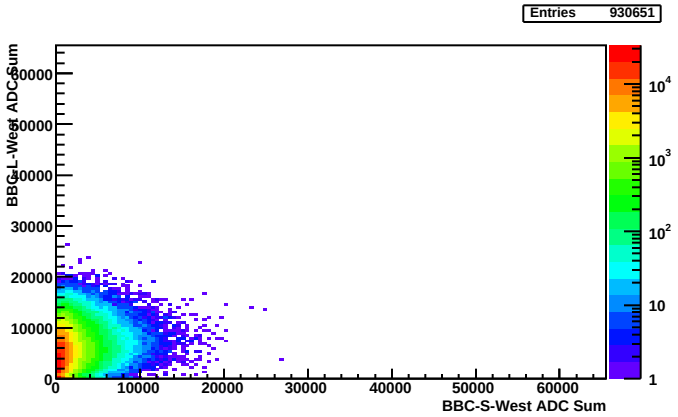

Entries 930651
TOF Mult
VPD-East ADC Sum

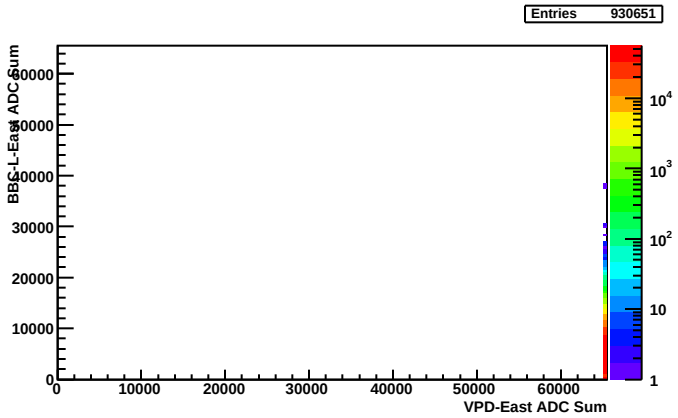

Entries 930651
TOF Mult
VPD-West ADC Sum


Entries 930651
TOF Mult
ZDC-East ADC Sum Att


Entries 930651
TOF Mult
ZDC-West ADC Sum Att


Entries 930651
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 930651
BBC-L-West ADC Sum
BBC-S-West ADC Sum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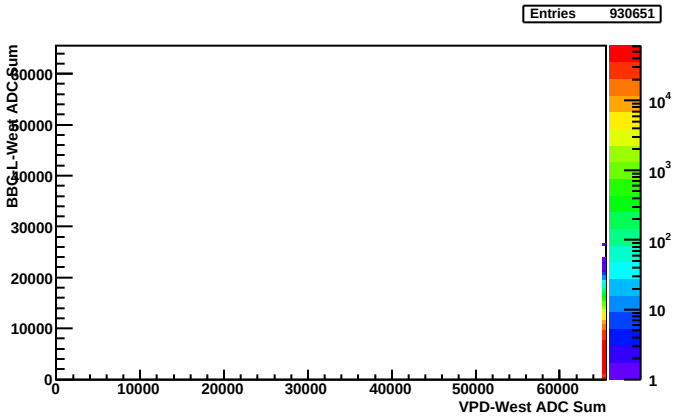
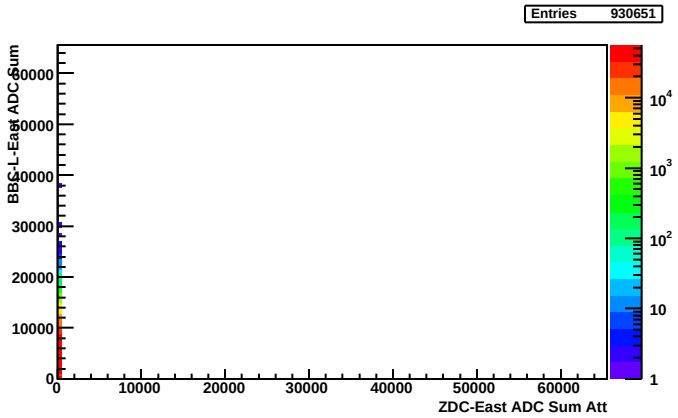
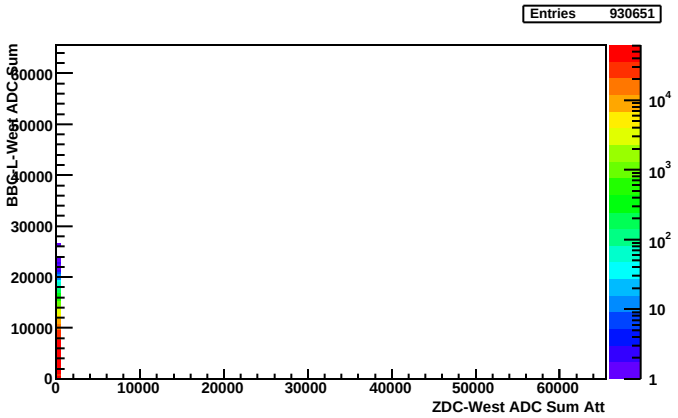
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 930651
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 930651
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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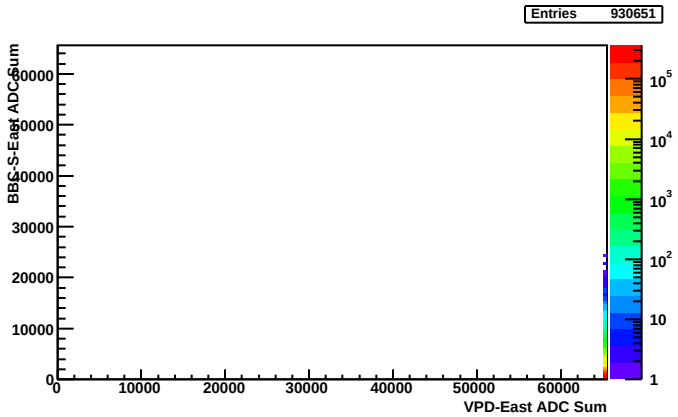
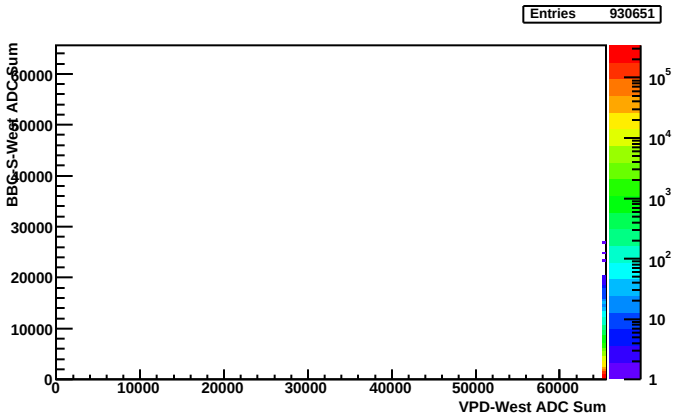
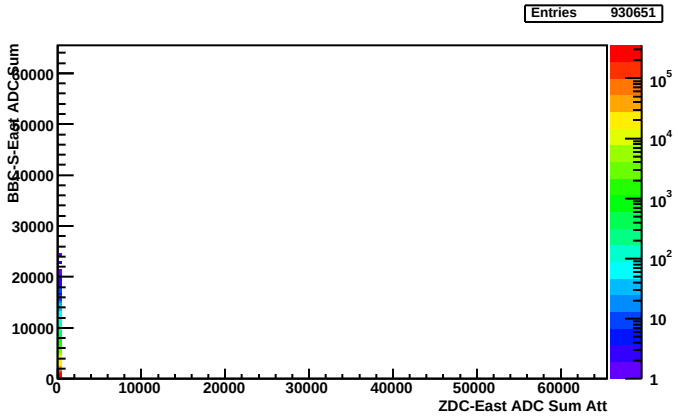
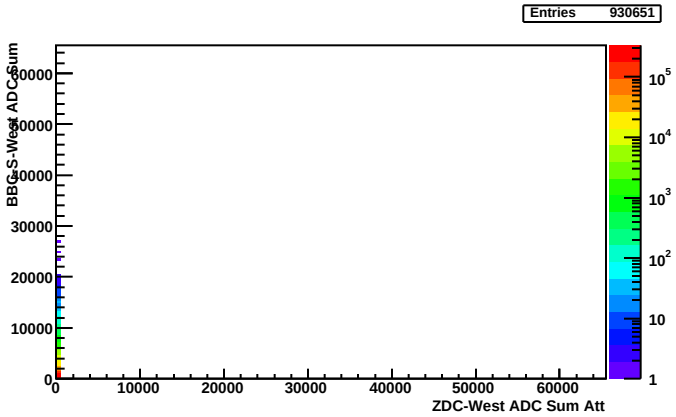
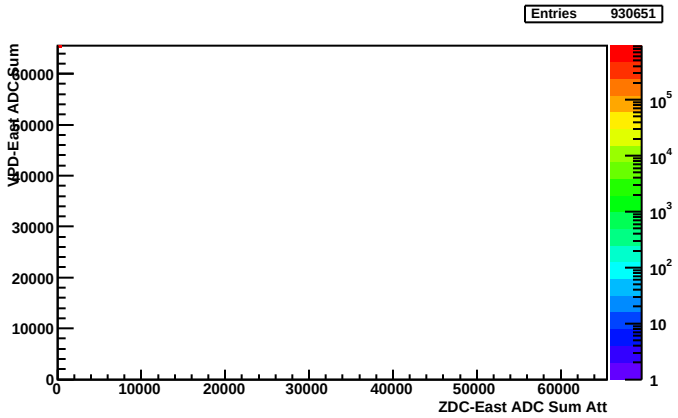
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

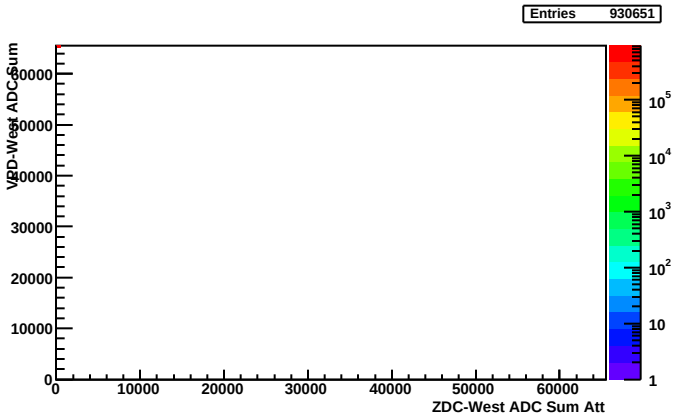

Entries 930651
BBC-S-East ADC Sum
VPD-East ADC Sum

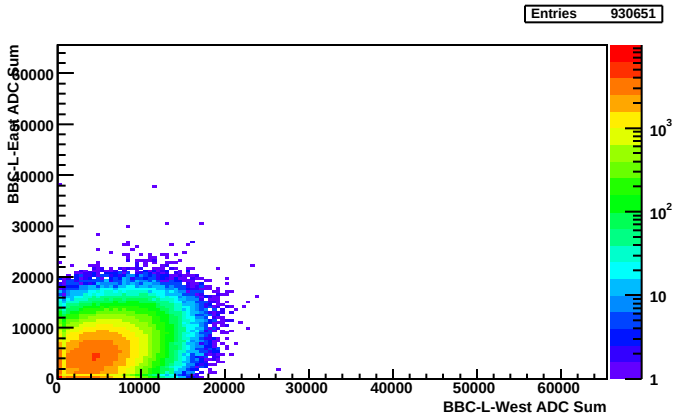

Entries 930651
BBC-S-West ADC Sum
VPD-West ADC Sum

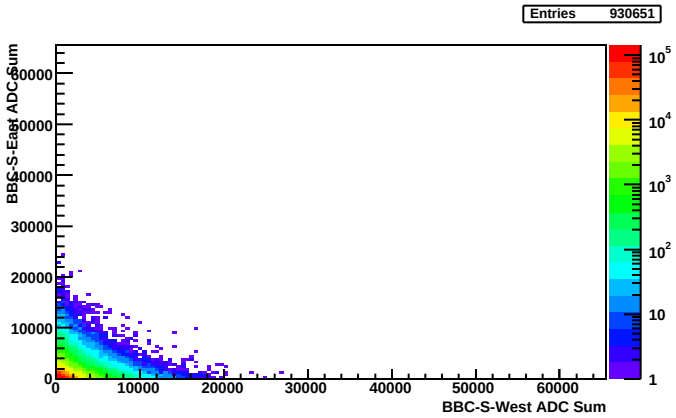

Entries 930651
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

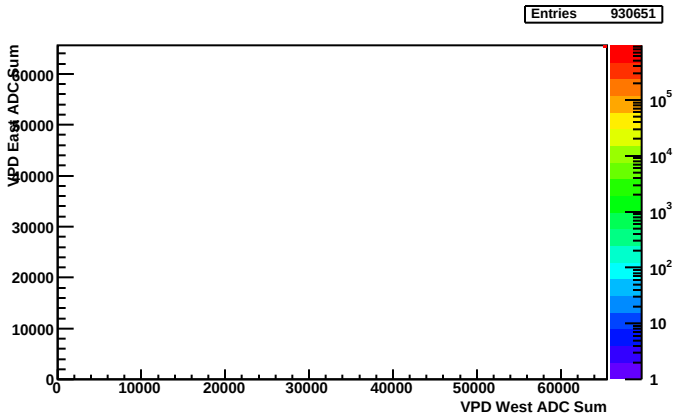

Entries 930651
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

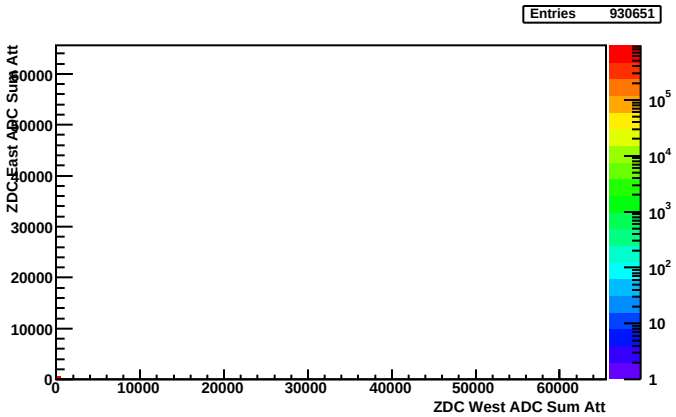

Entries 930651
VPD-East ADC Sum
ZDC-East ADC Sum Att

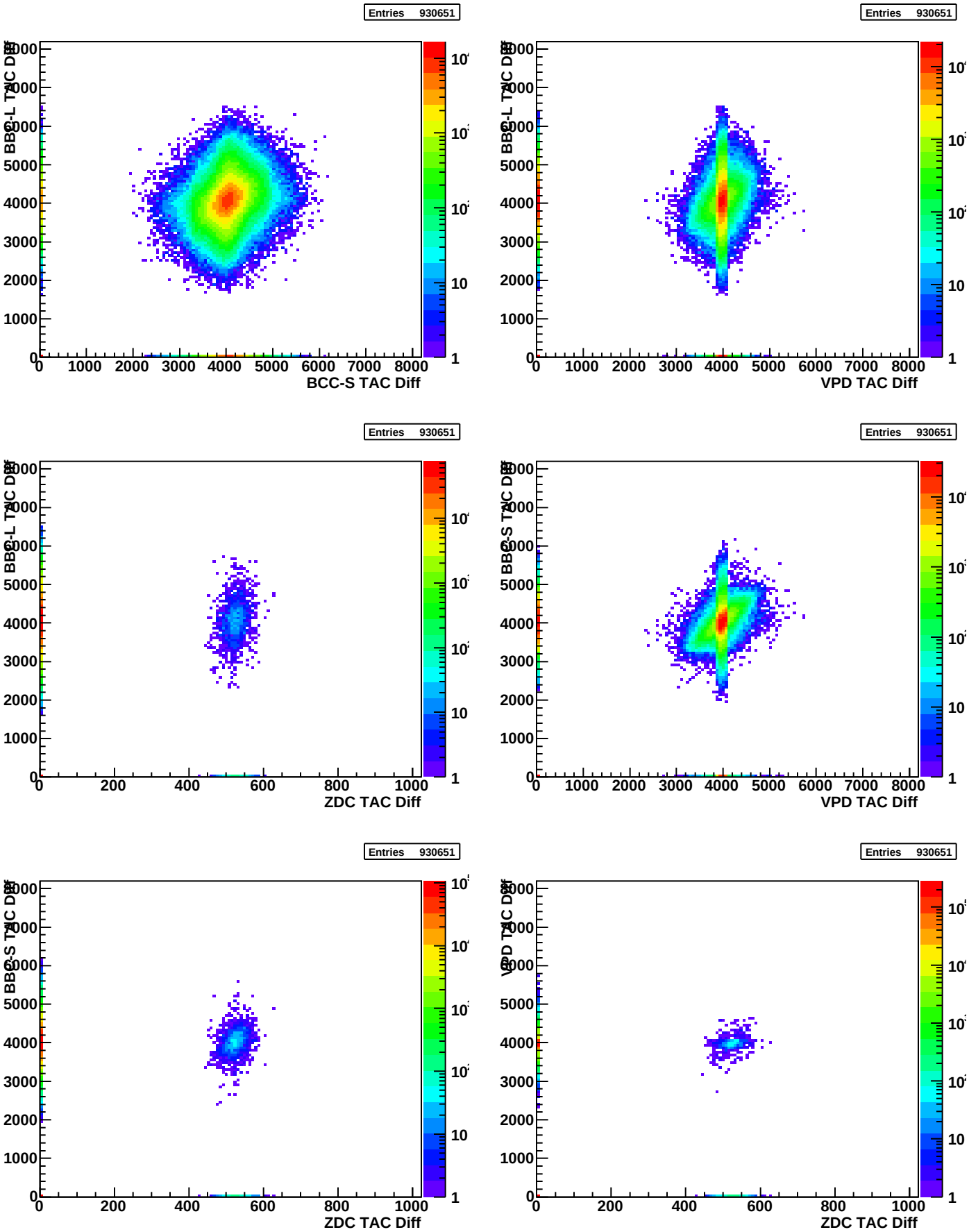

Entries 930651
VPD-West ADC Sum
ZDC-West ADC Sum Att

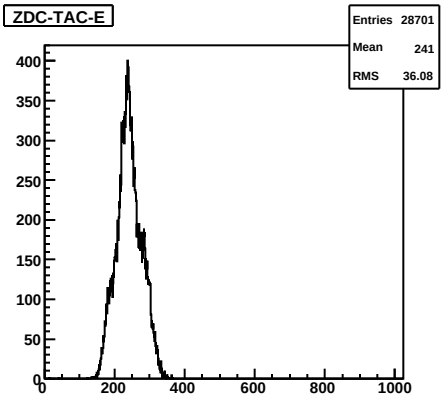

Entries 930651
BBC-L-East ADC Sum
BBC-L-West ADC Sum

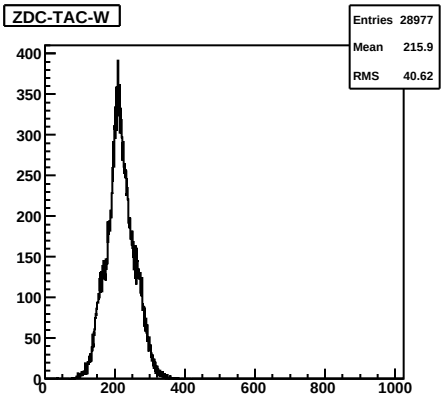

Entries 930651
BBC-S-East ADC Sum
BBC-S-West ADC Sum

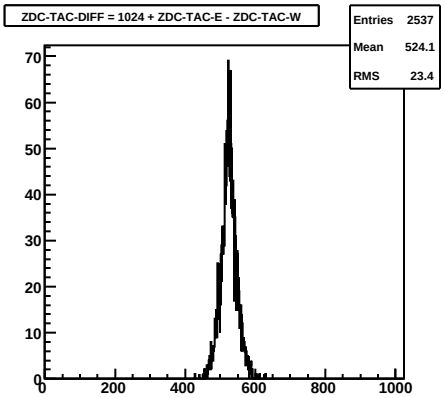

Entries 930651
VPD East ADC Sum
VPD West ADC Sum

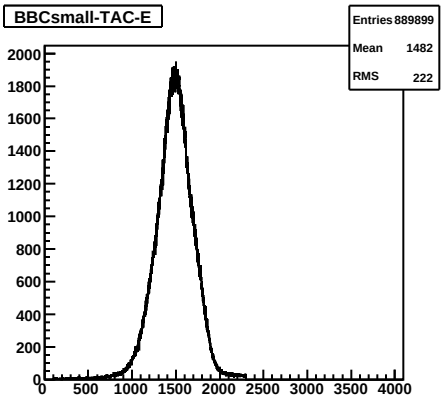

Entries 930651
ZDC East ADC Sum Att
ZDC West ADC Sum Att

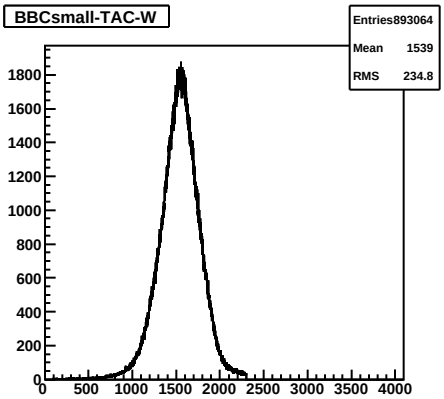

Entries 930651
BBC-L TAC Diff
BCC-S TAC Diff
Entries 930651
BBC-L TAC Diff
VPD TAC Diff
Entries 930651
BBC-L TAC Diff
ZDC TAC Diff
Entries 930651
BBC-S TAC Diff
VPD TAC Diff
Entries 930651
BBC-S TAC Diff
ZDC TAC Diff
Entries 930651
VPD TAC Diff
ZDC TAC Diff

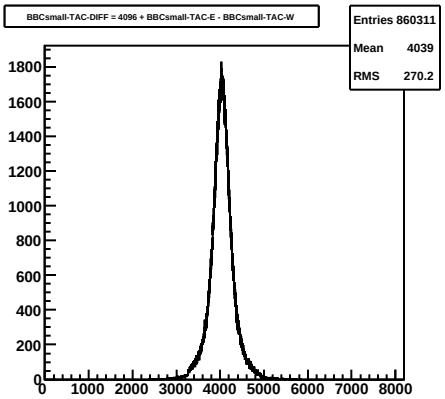

ZDC-TAC-E
Entries 28701
Mean 241
RMS 36.08

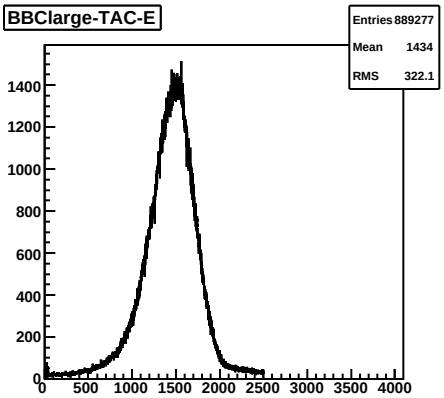

ZDC-TAC-W
Entries 28977
Mean 215.9
RMS 40.62

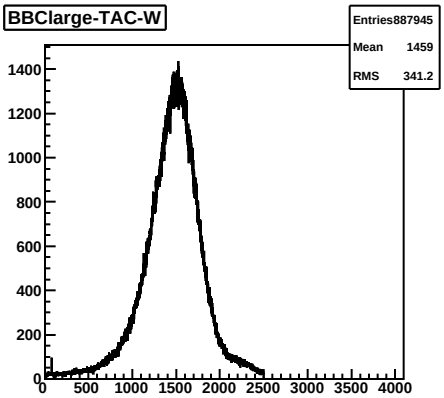

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2537
Mean 524.1
RMS 23.4

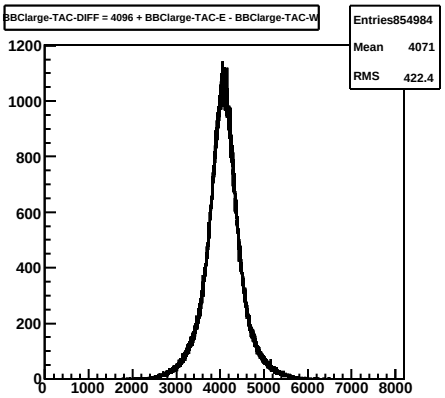

BBCsmall-TAC-E
Entries 889899
Mean 1482
RMS 222

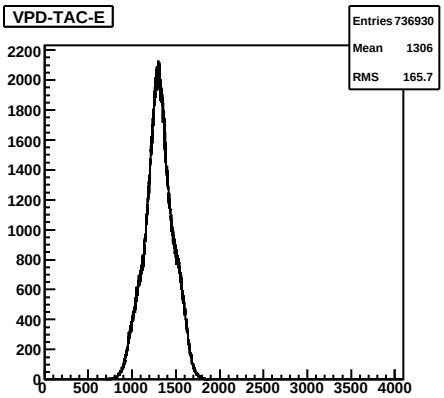

BBCsmall-TAC-W
Entries 893064
Mean 1539
RMS 234.8

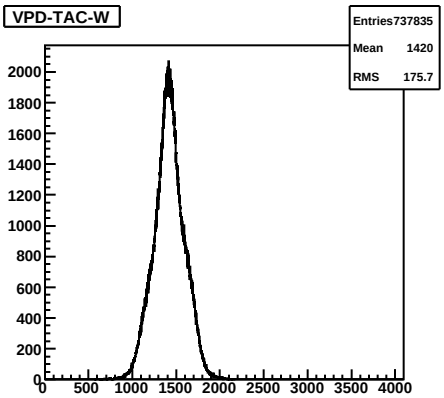

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 860311
Mean 4039
RMS 270.2

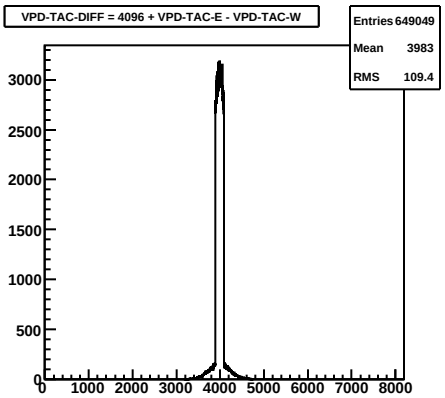

BBClarge-TAC-E
Entries 889277
Mean 1434
RMS 322.1

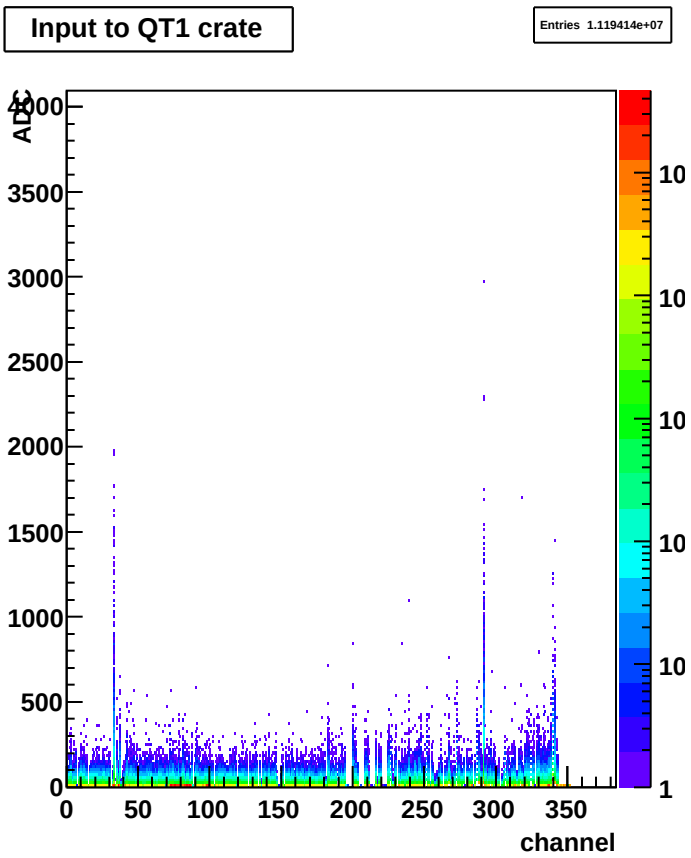

BBClarge-TAC-W
Entries 887945
Mean 1459
RMS 341.2

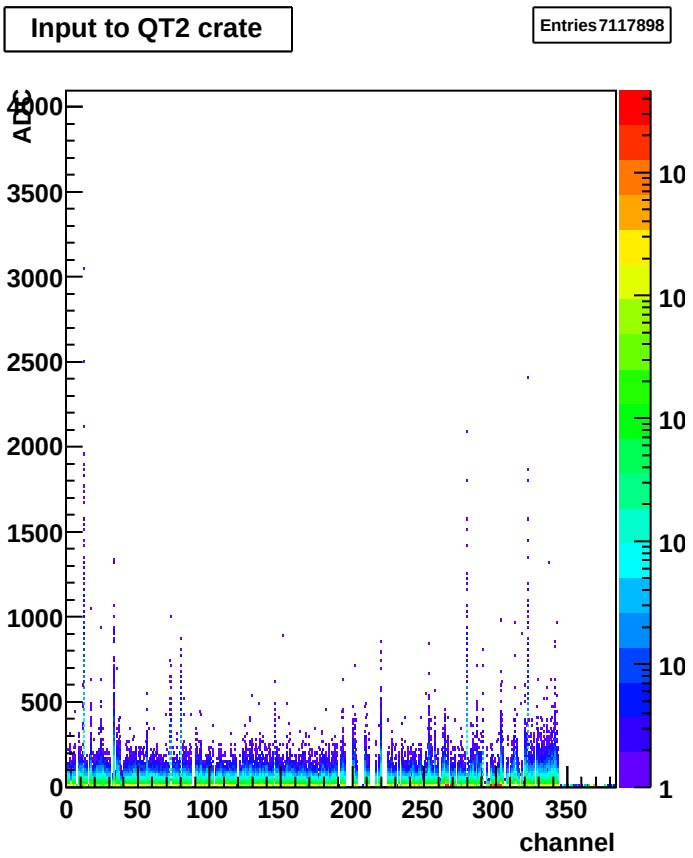

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 854984
Mean 4071
RMS 422.4

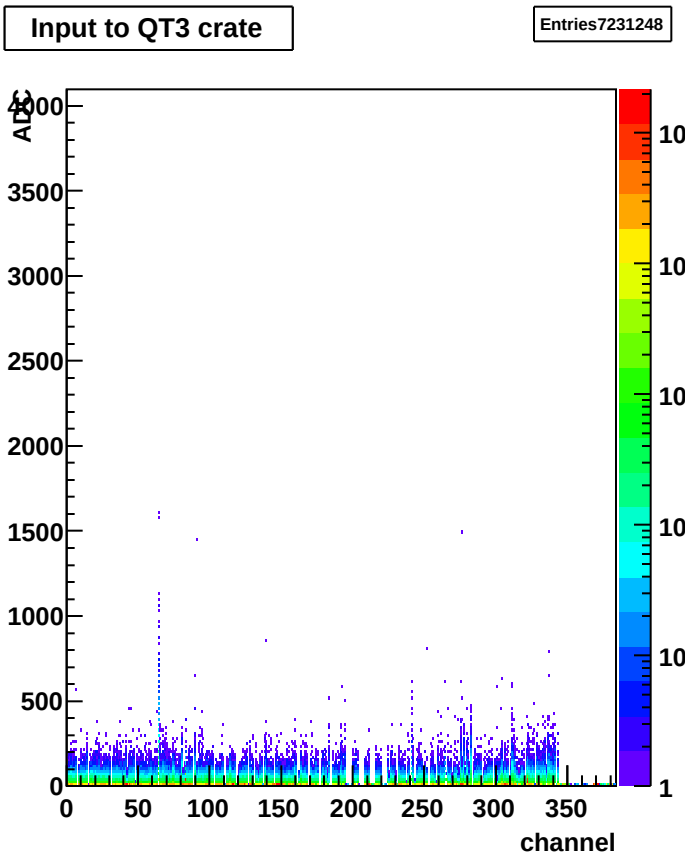

VPD-TAC-E
Entries 736930
Mean 1306
RMS 165.7

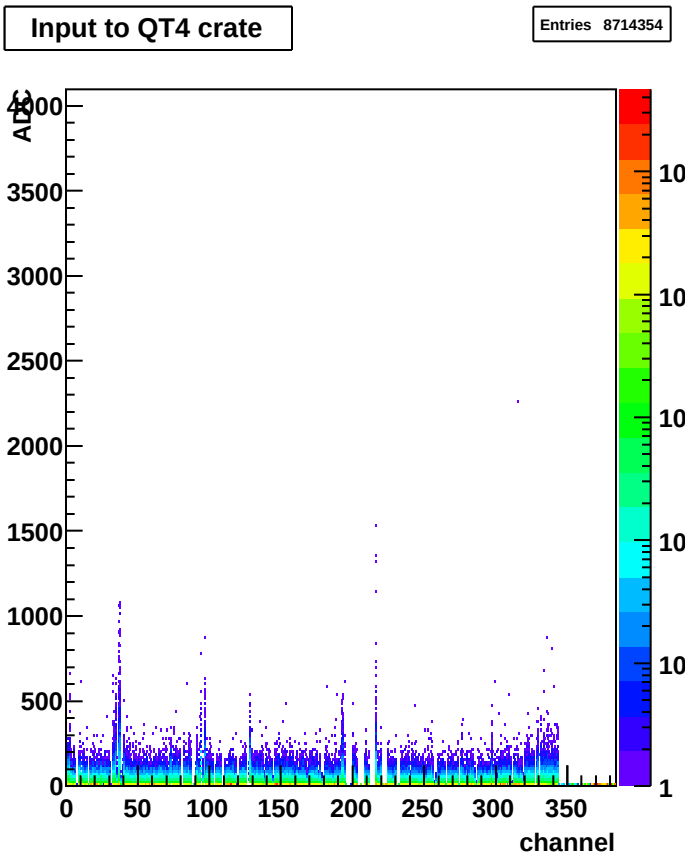

VPD-TAC-W
Entries 737835
Mean 1420
RMS 175.7

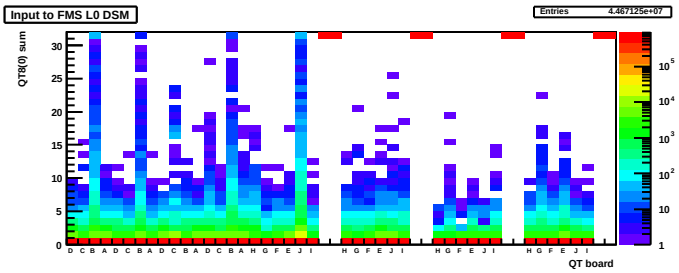

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 649049
Mean 3983
RMS 109.4

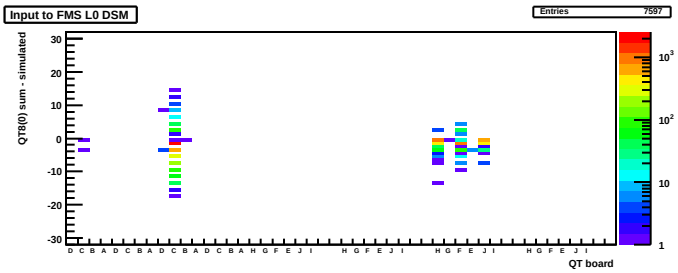

Input to QT1 crate
Entries 1.119414e+07
ADC
channel

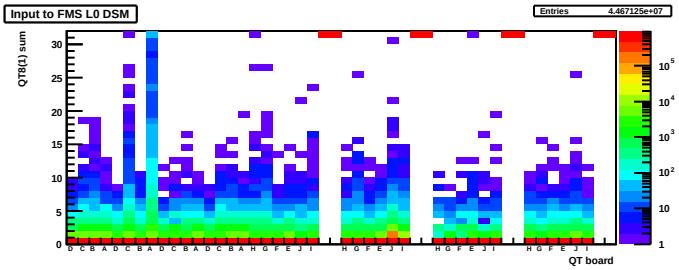

Input to QT2 crate
Entries 7117898
ADC
channel

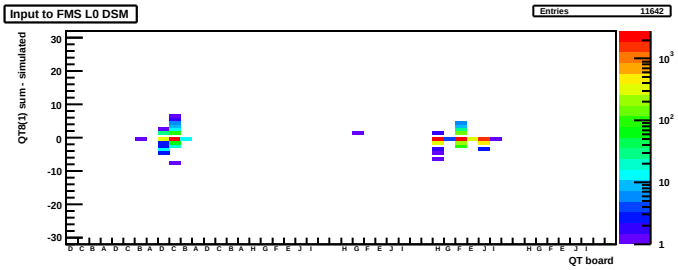

Input to QT3 crate
Entries 7231248
ADC
channel

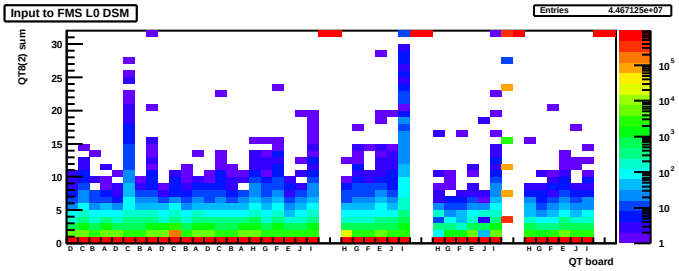

Input to QT4 crate
Entries 8714354
ADC
channel

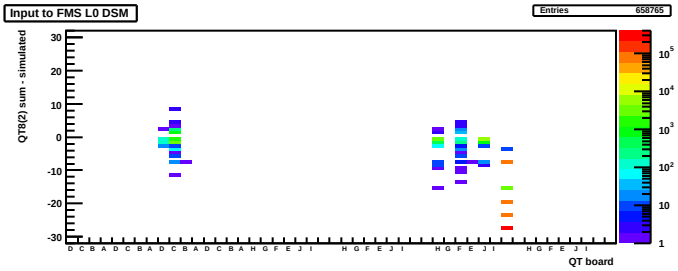

Input to FMS L0 DSM
Entries 4.467125e+07
QT8(0) sum
QT board

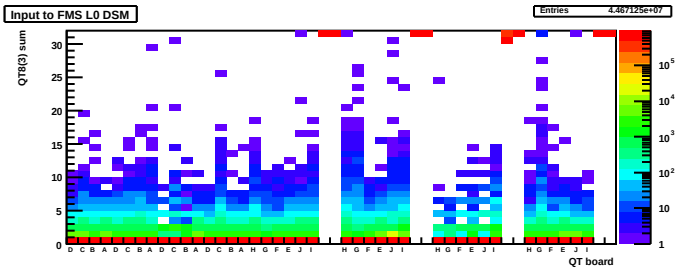

Input to FMS L0 DSM
Entries 7597
QT8(0) sum - simulated
QT board

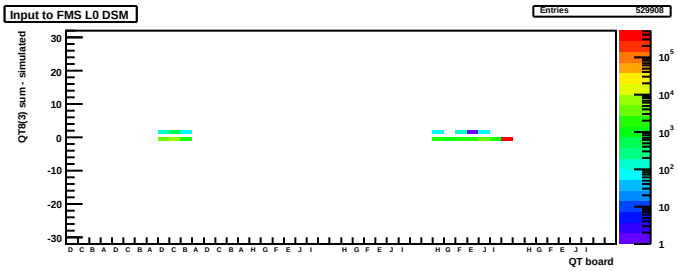

Input to FMS L0 DSM
Entries 4.467125e+07
QT8(1) sum
QT board

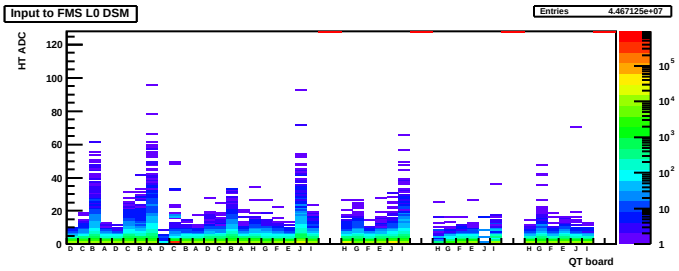

Input to FMS L0 DSM
Entries 11642
QT8(1) sum - simulated
QT board

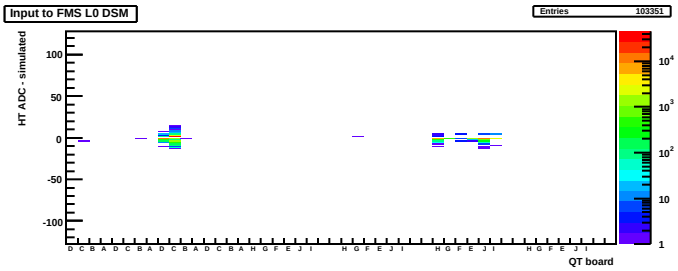

Input to FMS L0 DSM
Entries 4.467125e+07
QT8(2) sum
QT board

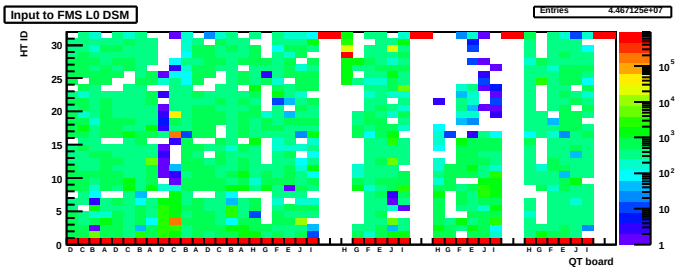

Input to FMS L0 DSM
Entries 658765
QT8(2) sum - simulated
QT board

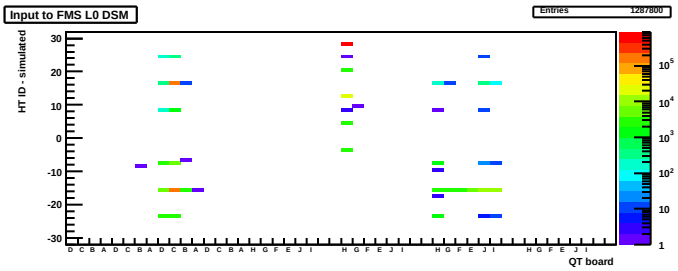

Input to FMS L0 DSM
Entries 4.467125e+07
QT8(3) sum
QT board

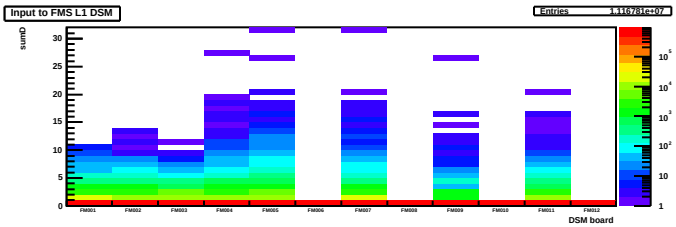

Input to FMS L0 DSM
Entries 529908
QT8(3) sum - simulated
QT board

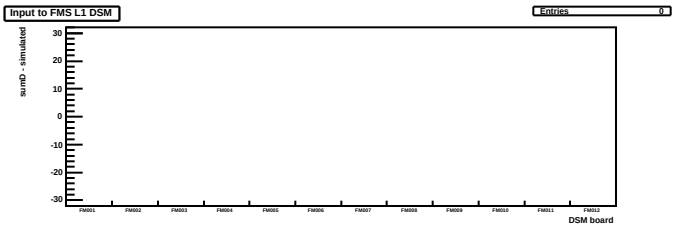

Input to FMS L0 DSM
Entries 4.467125e+07
HT ADC
QT board

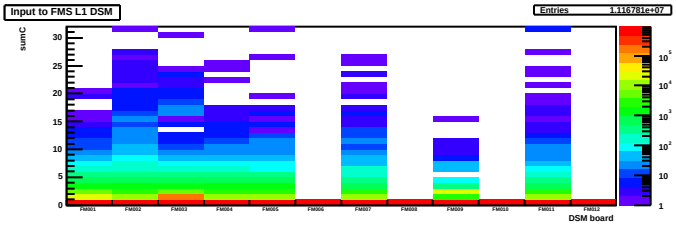

Input to FMS L0 DSM
Entries 103351
HT ADC - simulated
QT board

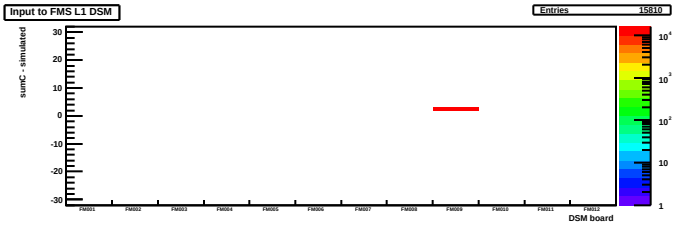

Input to FMS L0 DSM
Entries 4.467125e+07
HT ID
QT board

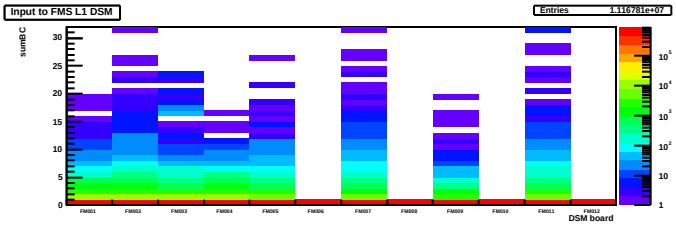

Input to FMS L0 DSM
Entries 1287800
HT ID - simulated
QT board

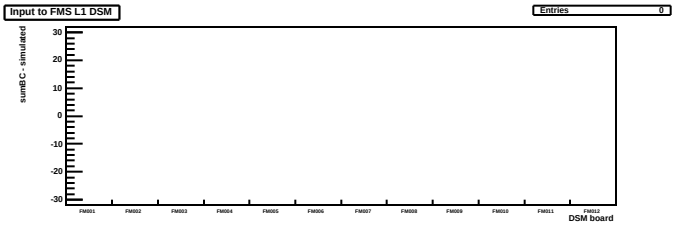

Input to FMS L1 DSM
Entries 1.116781e+07
sumD
DSM board

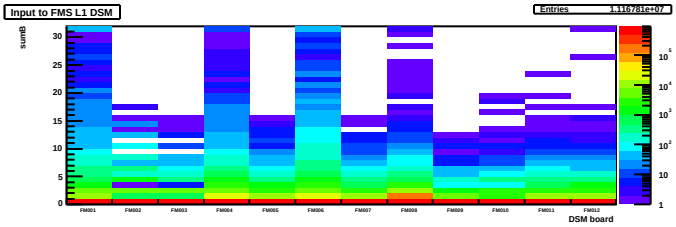

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

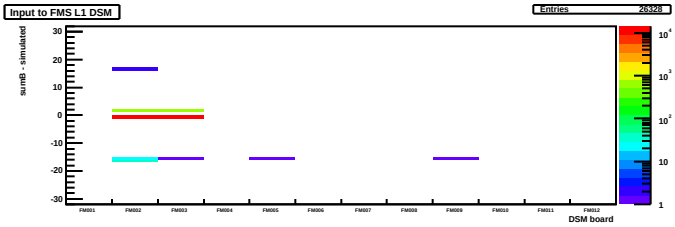

Input to FMS L1 DSM
Entries 1.116781e+07
sumC
DSM board

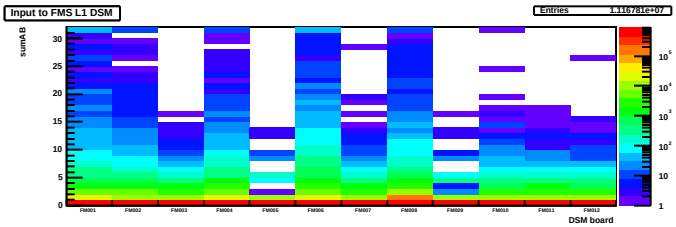

Input to FMS L1 DSM
Entries 15810
sumC - simulated
DSM board

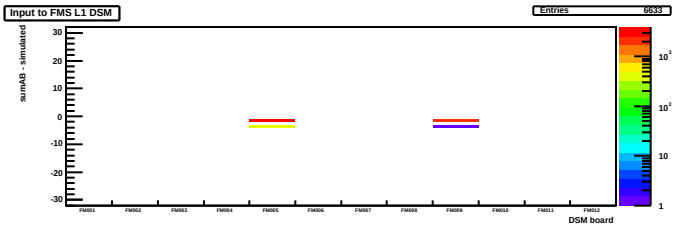

Input to FMS L1 DSM
Entries 1.116781e+07
sumBC
DSM board

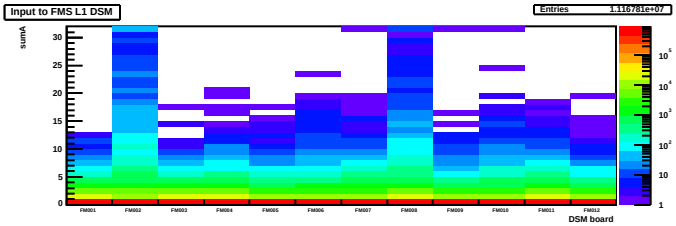

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

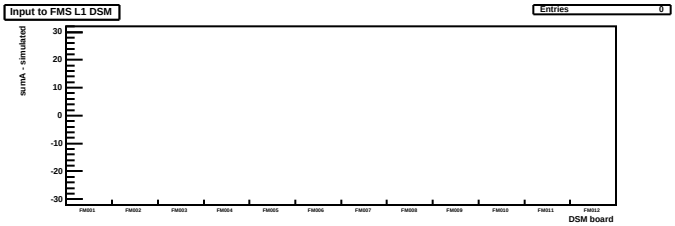

Input to FMS L1 DSM
Entries 1.116781e+07
sumB
DSM board

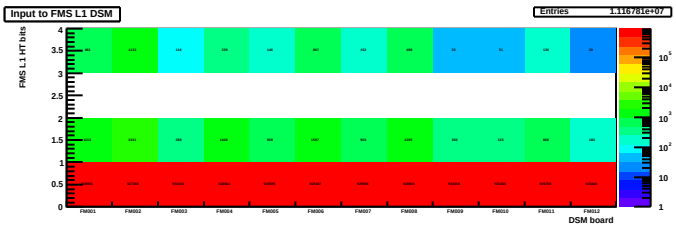

Input to FMS L1 DSM
Entries 26328
sumB - simulated
DSM board

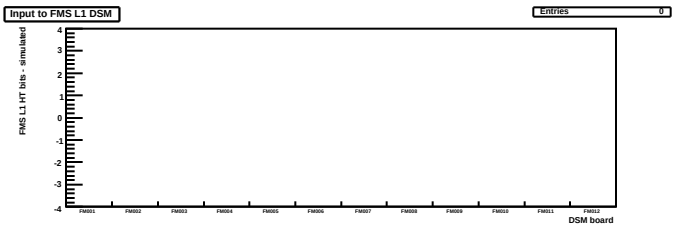

Input to FMS L1 DSM
Entries 1.116781e+07
sumAB
DSM board

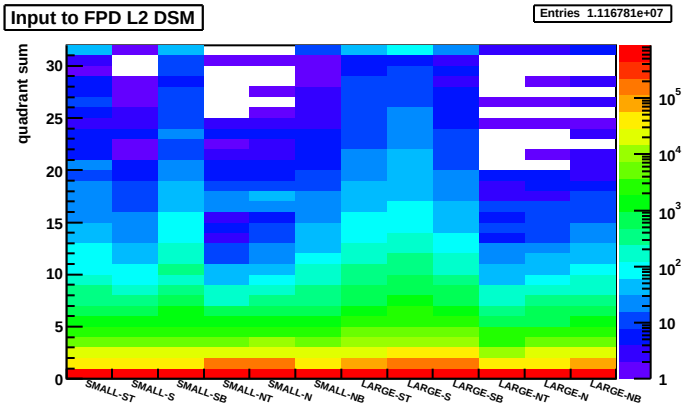

Input to FMS L1 DSM
Entries 6633
sumAB - simulated
DSM board

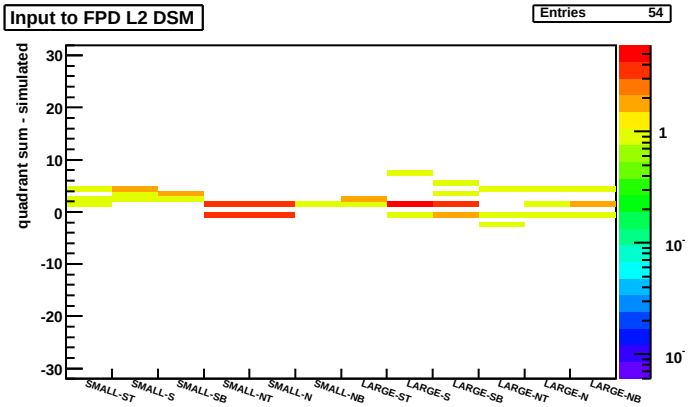

Input to FMS L1 DSM
Entries 1.116781e+07
sumA
DSM board

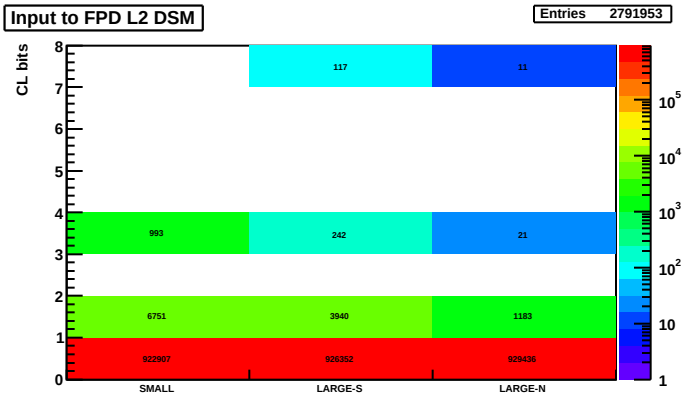

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

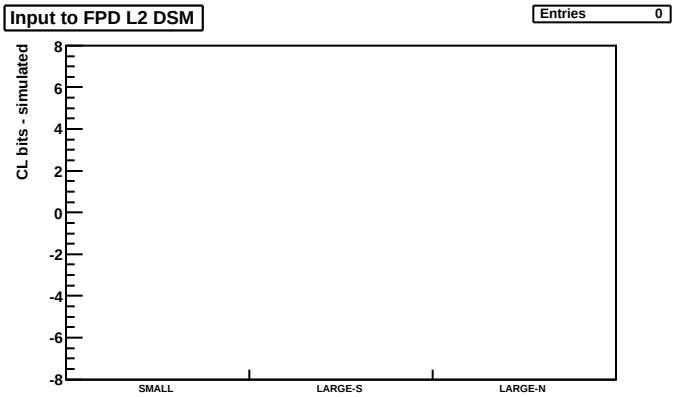

Input to FMS L1 DSM
Entries 1.116781e+07
FMS L1 HT bits
DSM board

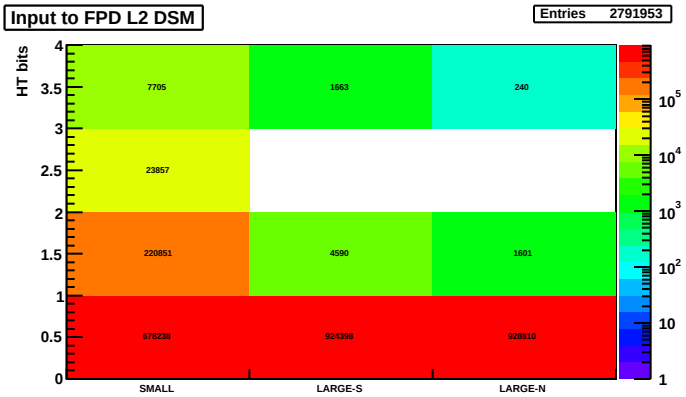

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

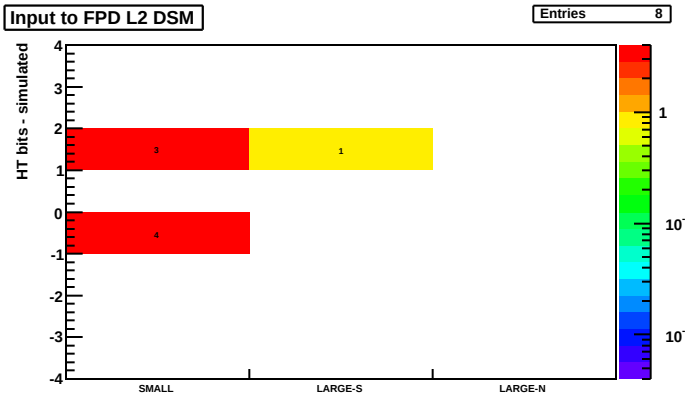

Input to FPD L2 DSM
Entries 1.116781e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

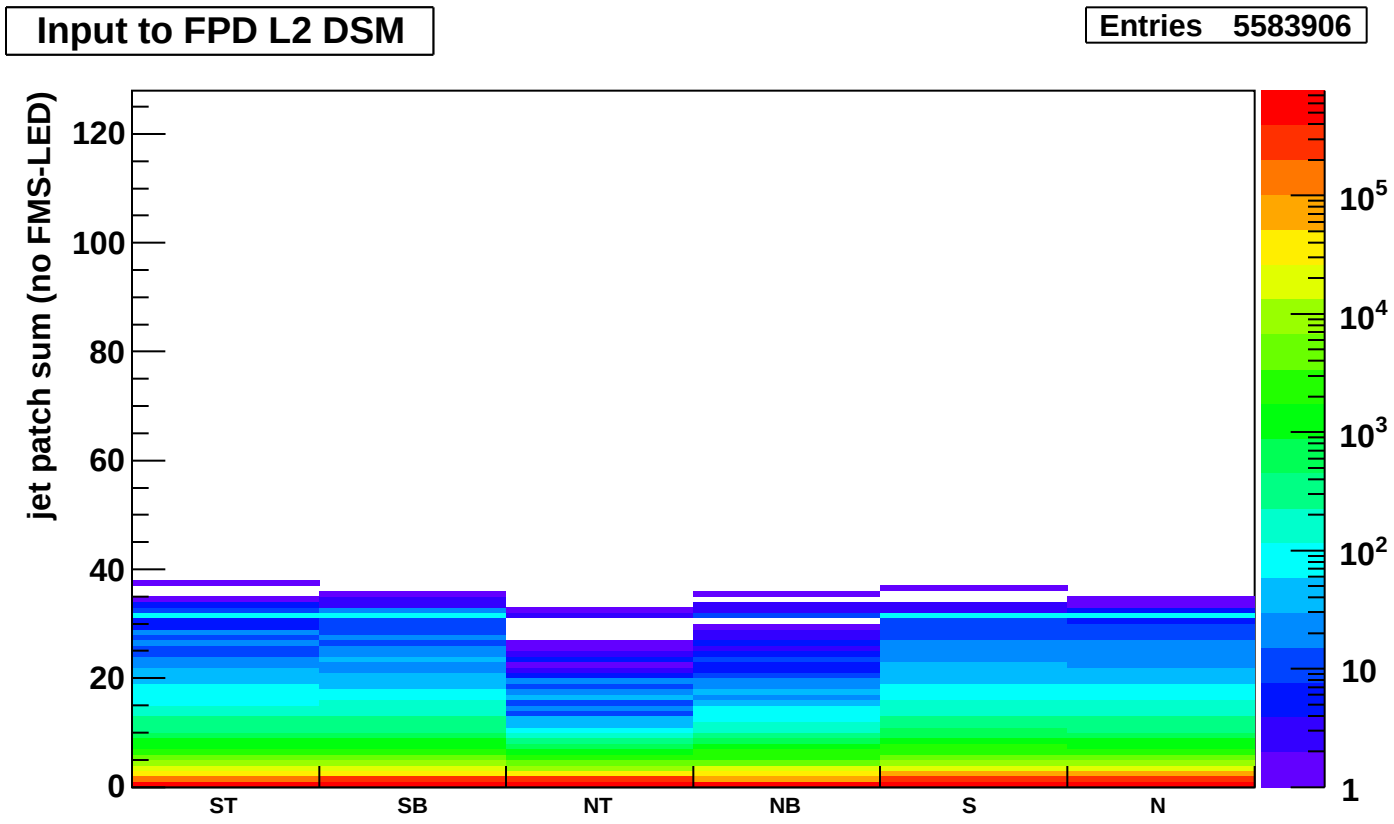

Input to FPD L2 DSM
Entries 54
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

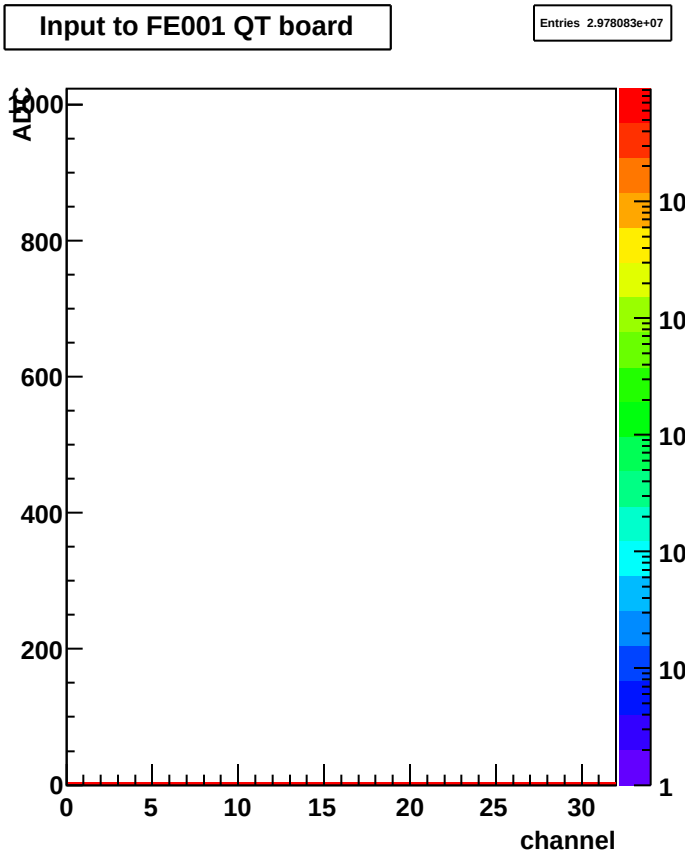

Input to FPD L2 DSM
Entries 2791953
CL bits
117
11
993
242
21
6751
3940
1183
922907
926352
929436
SMALL
LARGE-S
LARGE-N

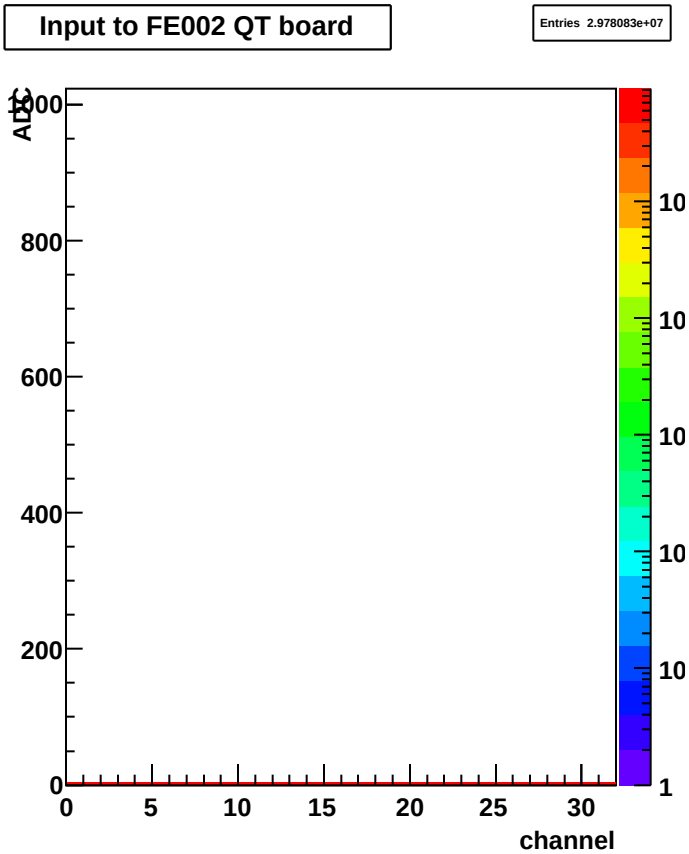

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

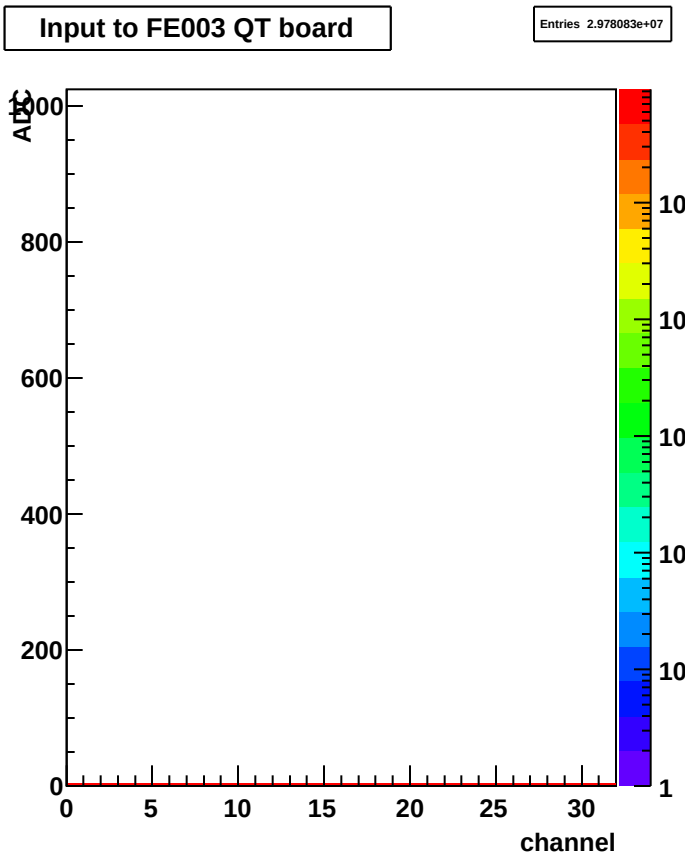

Input to FPD L2 DSM
Entries 2791953
HT bits
7705
1663
240
23857
220851
4590
1601
678238
924398
928810
SMALL
LARGE-S
LARGE-N

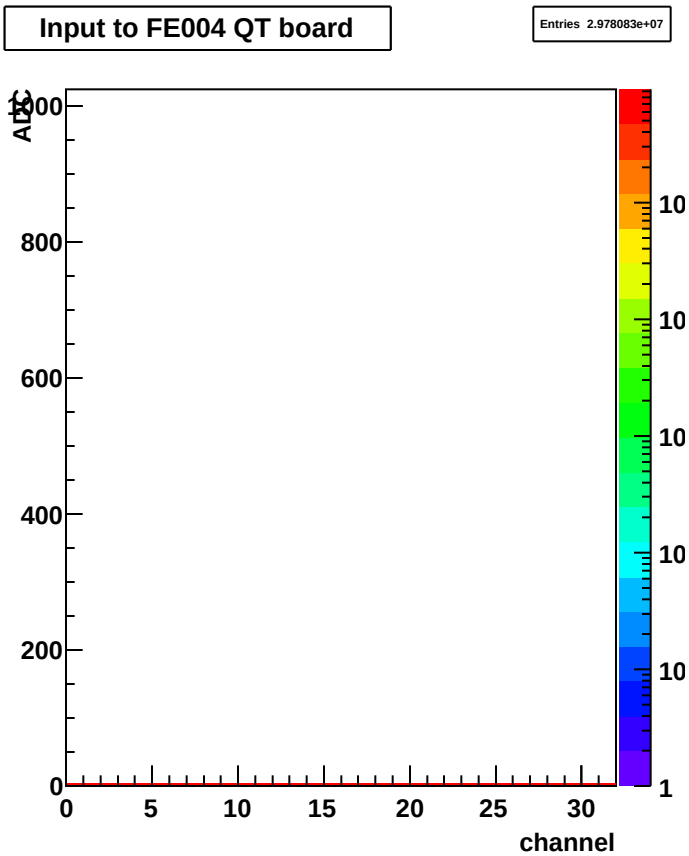

Input to FPD L2 DSM
Entries 8
HT bits - simulated
3
1
4
SMALL
LARGE-S
LARGE-N

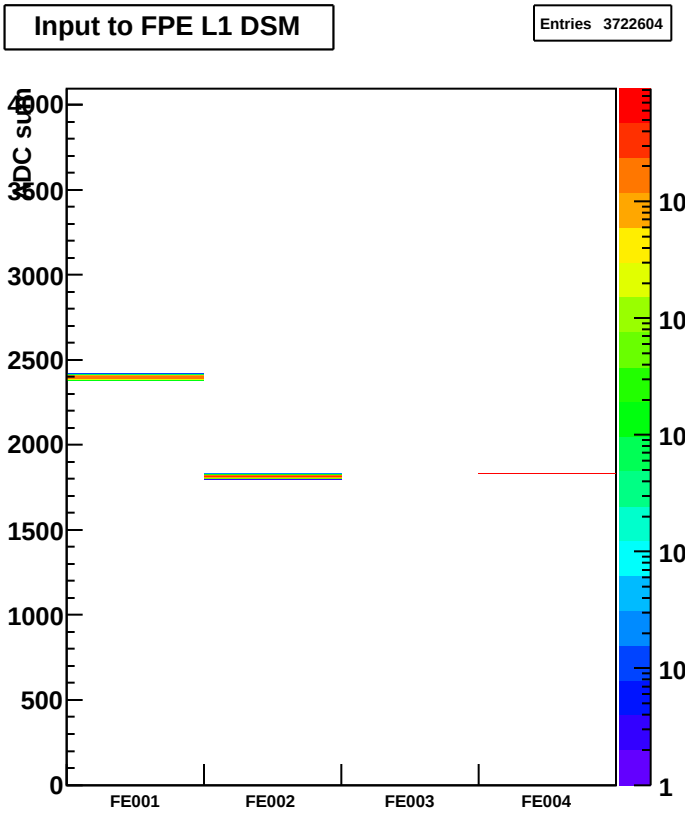

Input to FPD L2 DSM
Entries 5583906
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
$10^2$
$10^3$
$10^4$
$10^5$

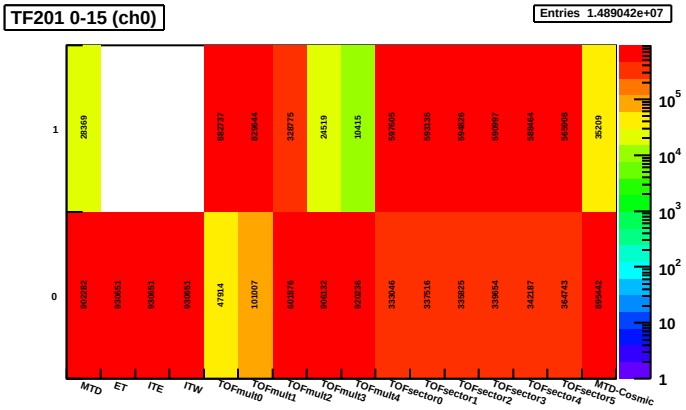

Input to FE001 QT board
Entries 2.978083e+07
ADC
channel

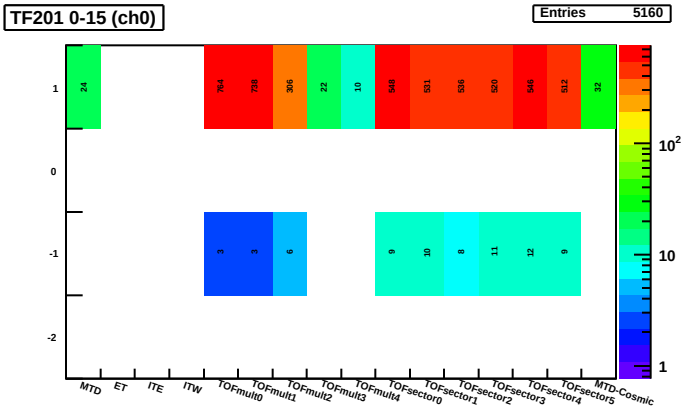

Input to FE002 QT board
Entries 2.978083e+07
ADC
channel

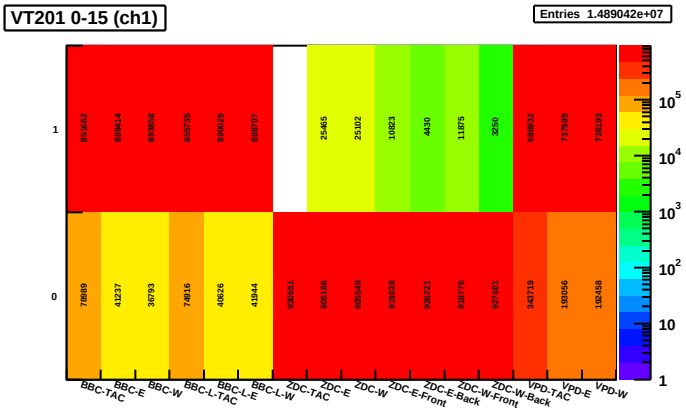

Input to FE003 QT board
Entries 2.978083e+07
ADC
channel

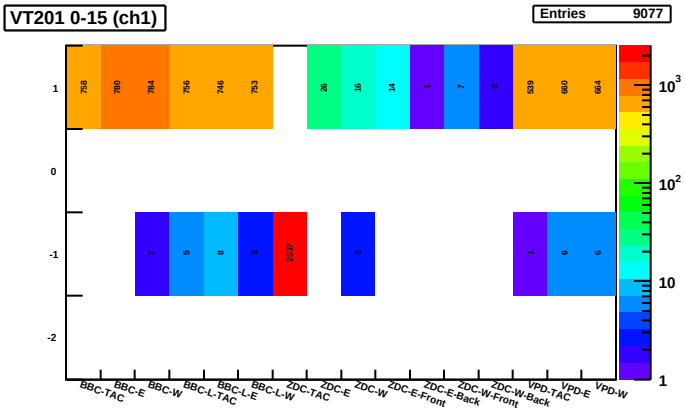

Input to FE004 QT board
Entries 2.978083e+07
ADC
channel


Input to FPE L1 DSM
Entries 3722604
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.489042e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 5160
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1.489042e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 9077
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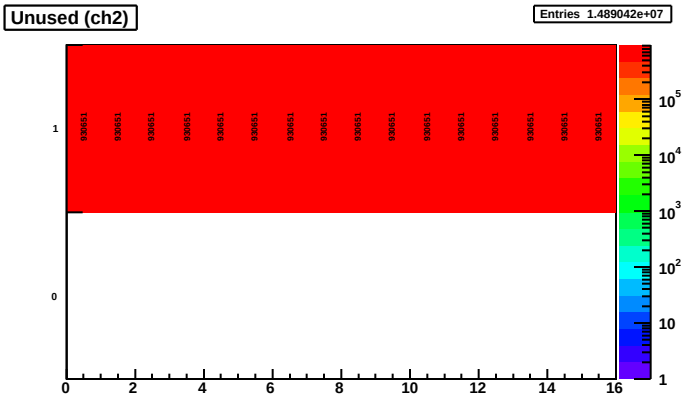
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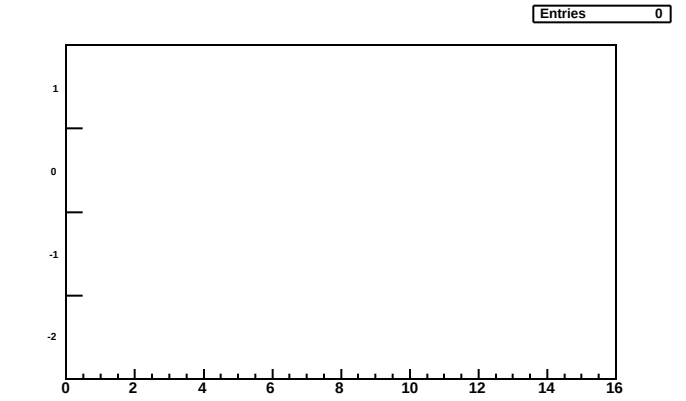
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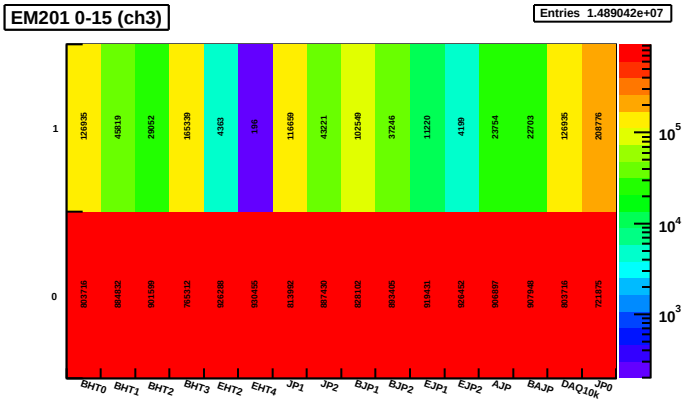
EM201 0-15 (ch3)
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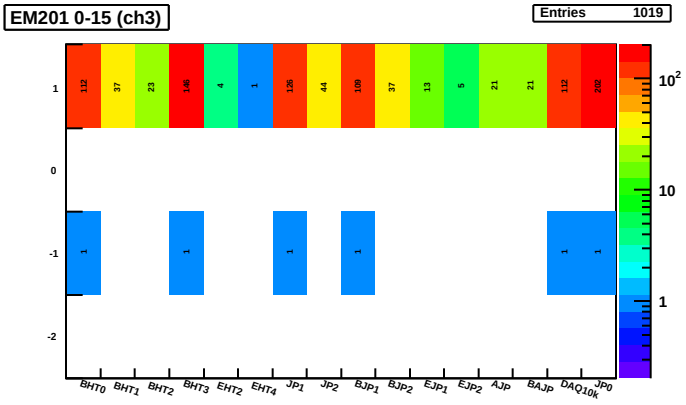
EM201 0-15 (ch3)
Entries 1019
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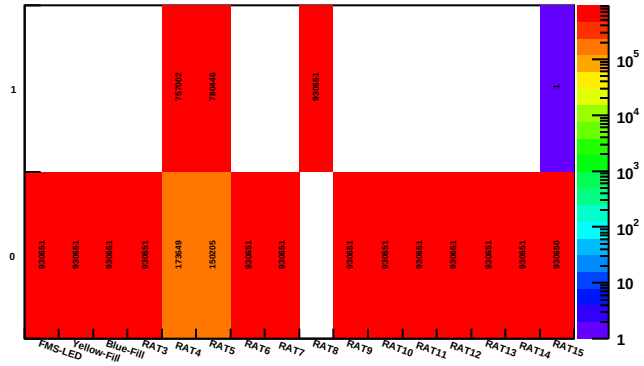
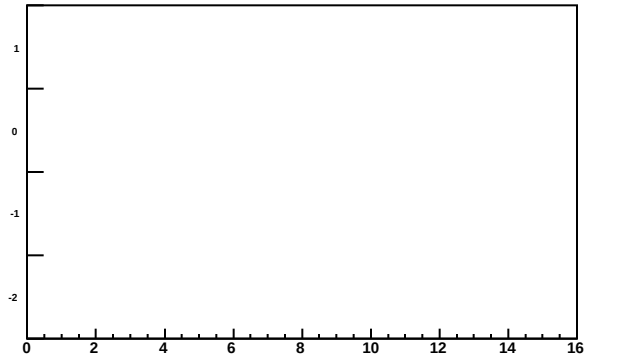
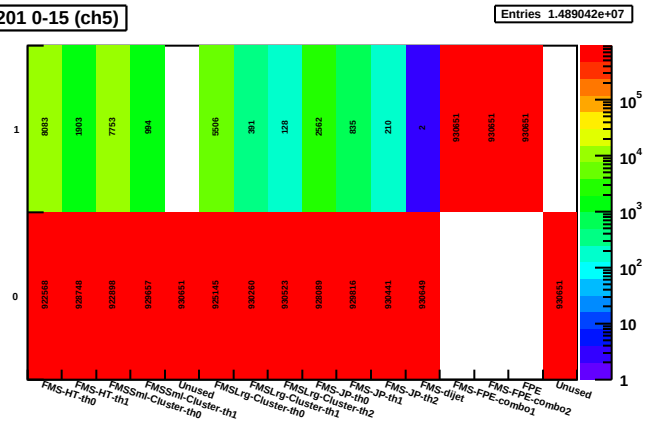
**RAT board (ch4)**

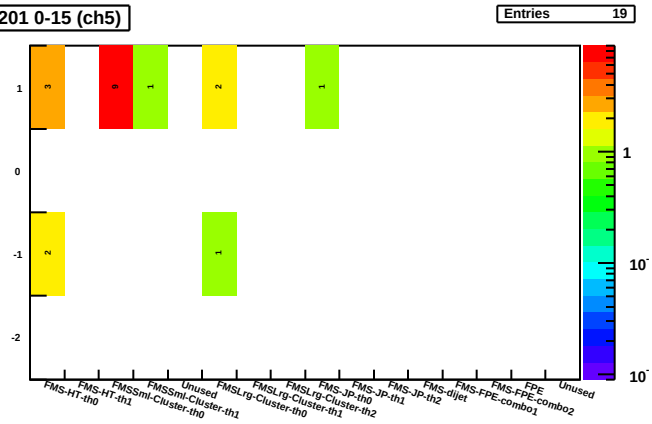


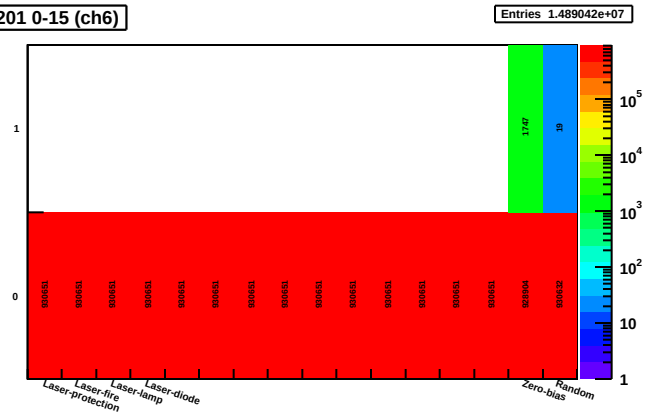

**FP201 0-15 (ch5)**

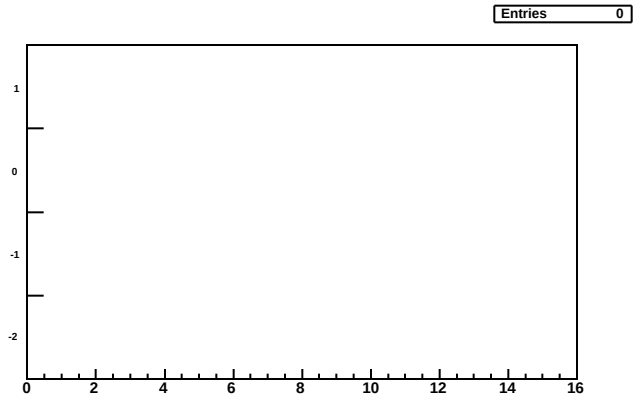


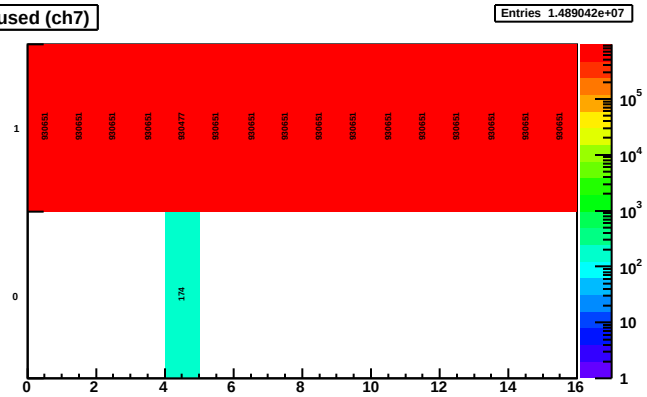
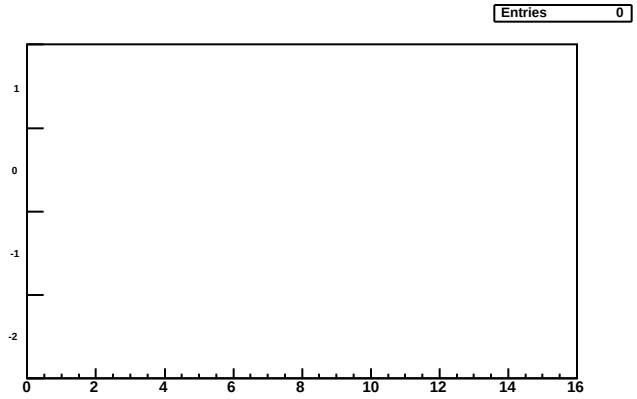

**FP201 0-15 (ch5)**




**ST201 0-15 (ch6)**




**Unused (ch7)**