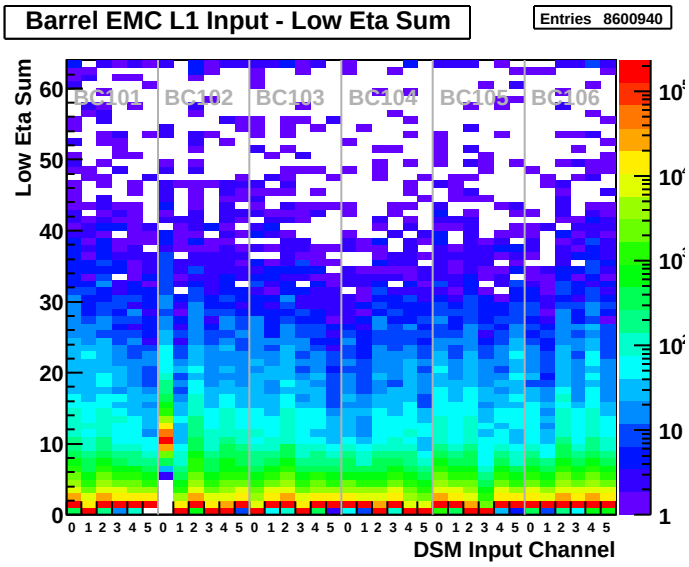
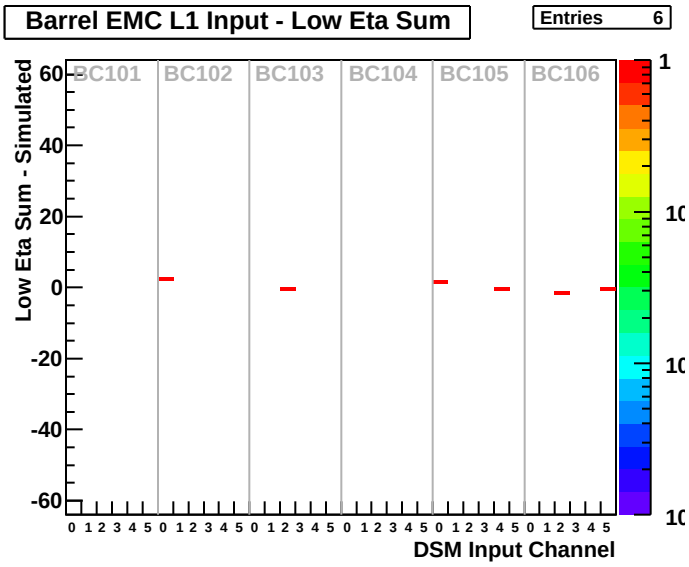
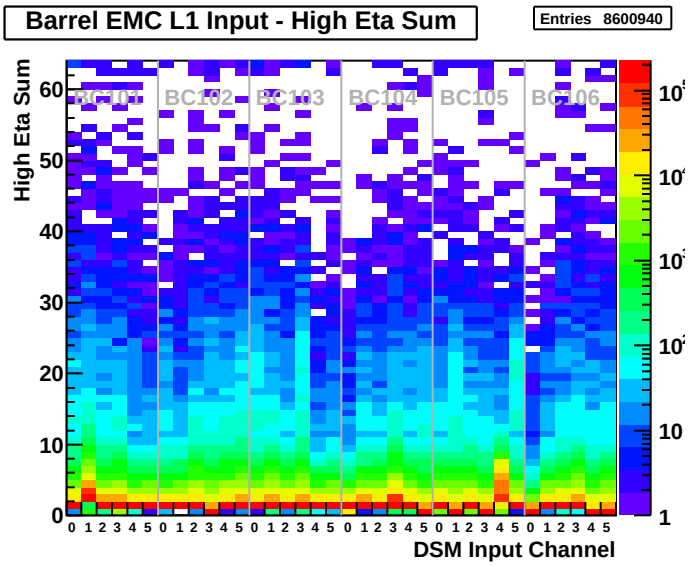
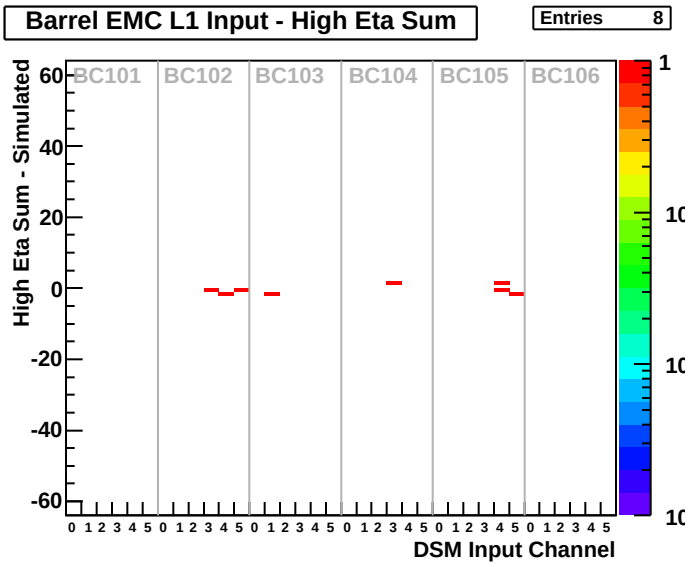
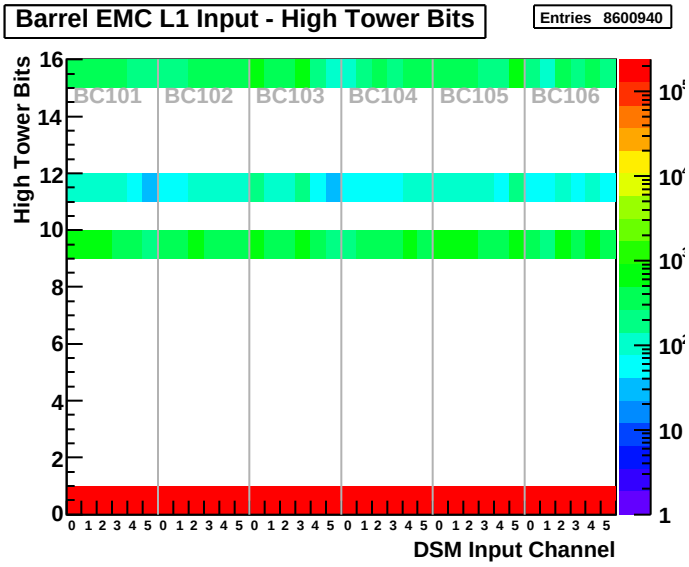
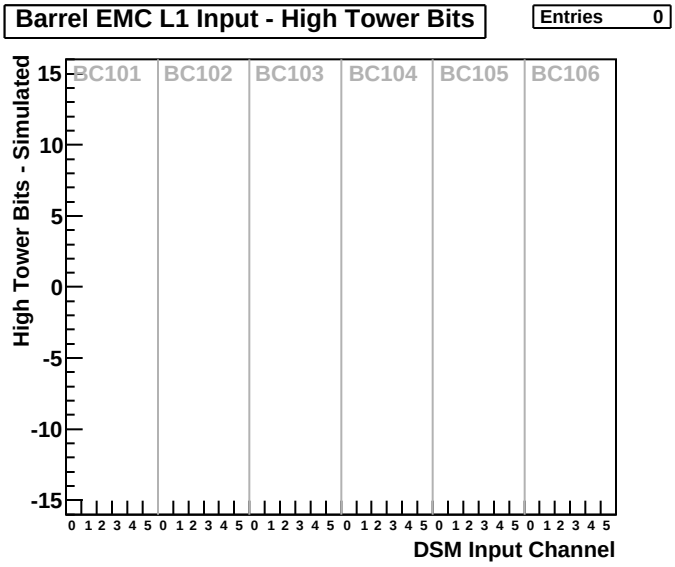

Barrel EMC L1 Input - Low Eta Sum
Entries 8600940
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

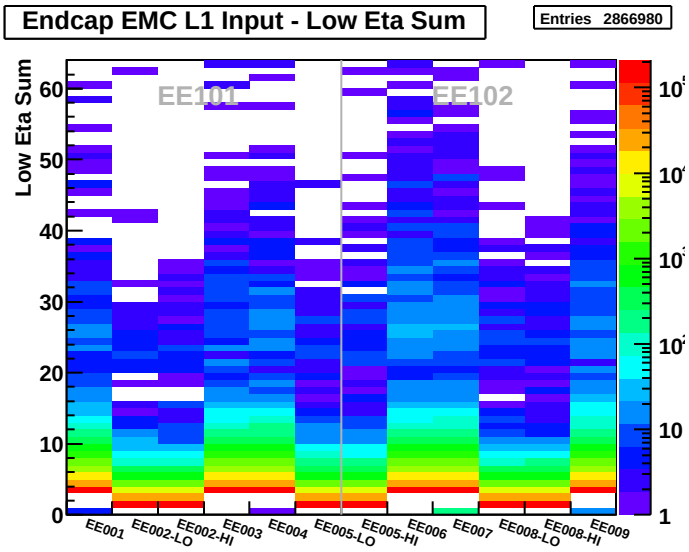

Barrel EMC L1 Input - Low Eta Sum
Entries 6
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

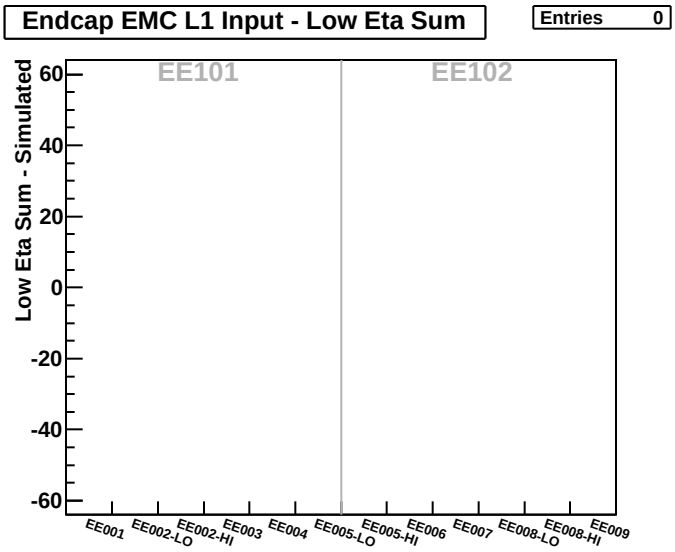

Barrel EMC L1 Input - High Eta Sum
Entries 8600940
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

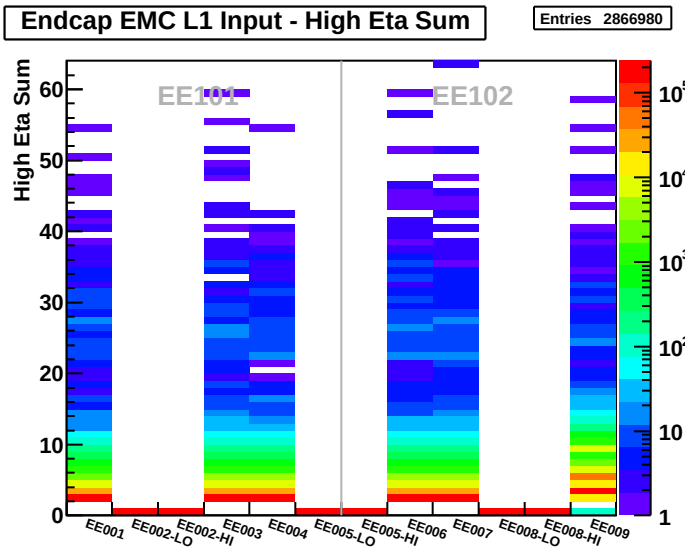

Barrel EMC L1 Input - High Eta Sum
Entries 8
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

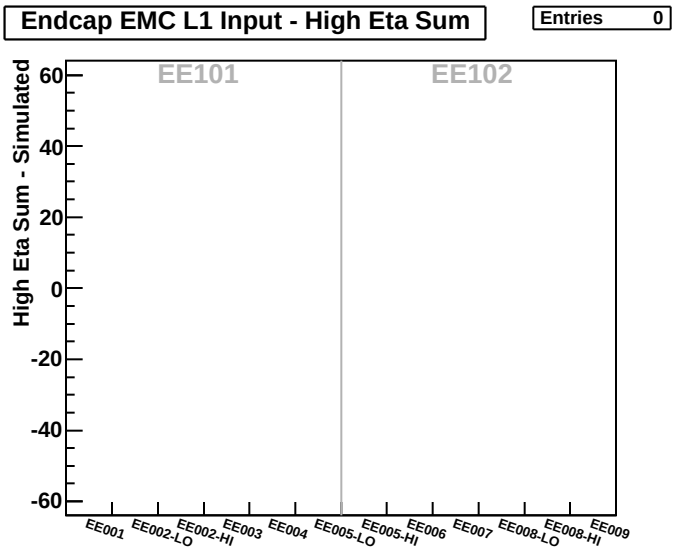

Barrel EMC L1 Input - High Tower Bits
Entries 8600940
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

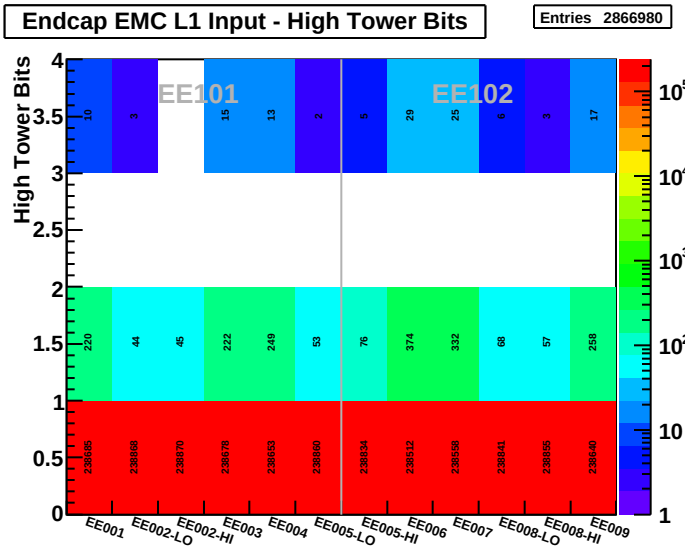

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

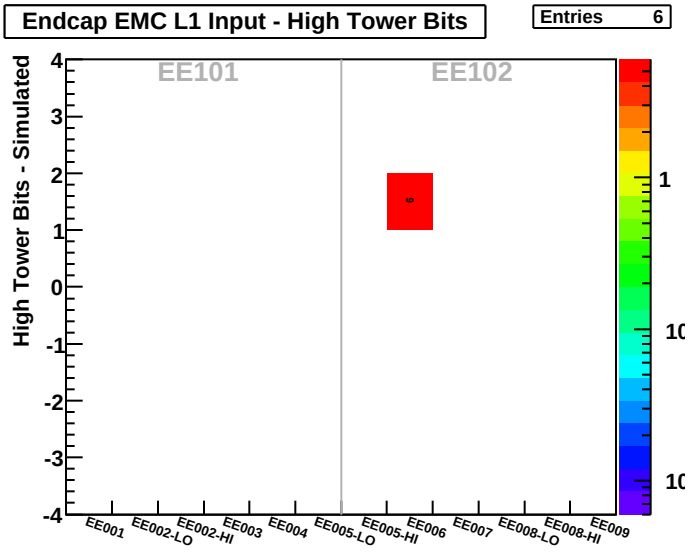

Endcap EMC L1 Input - Low Eta Sum
Entries 2866980
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

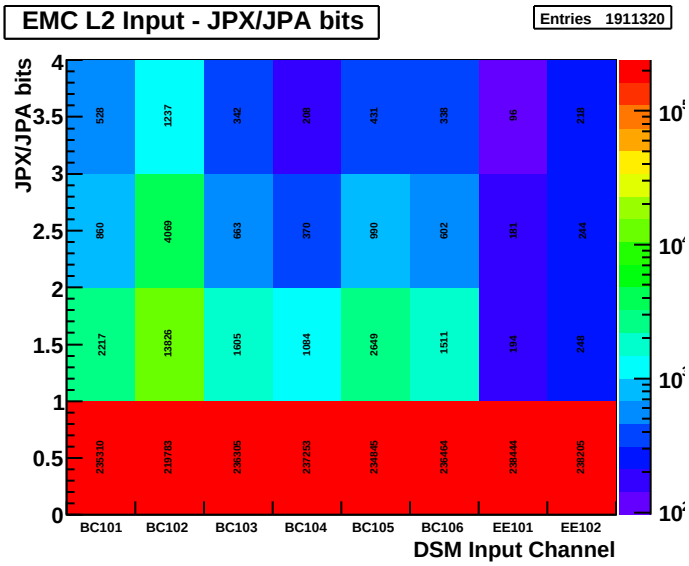

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

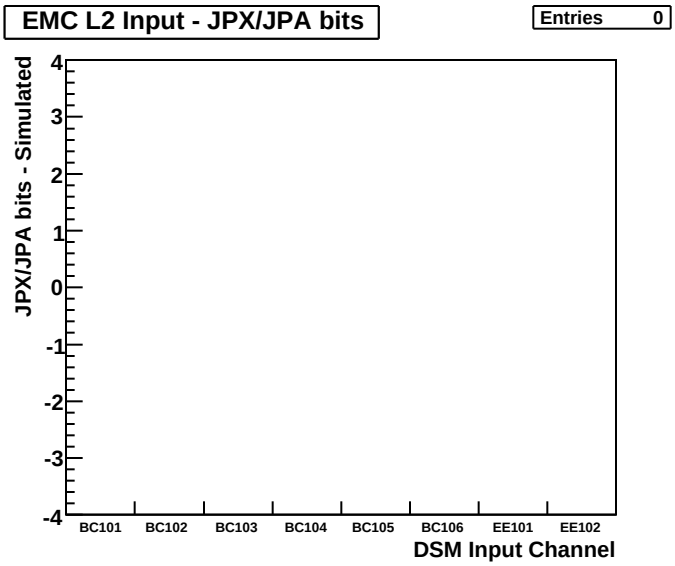

Endcap EMC L1 Input - High Eta Sum
Entries 2866980
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

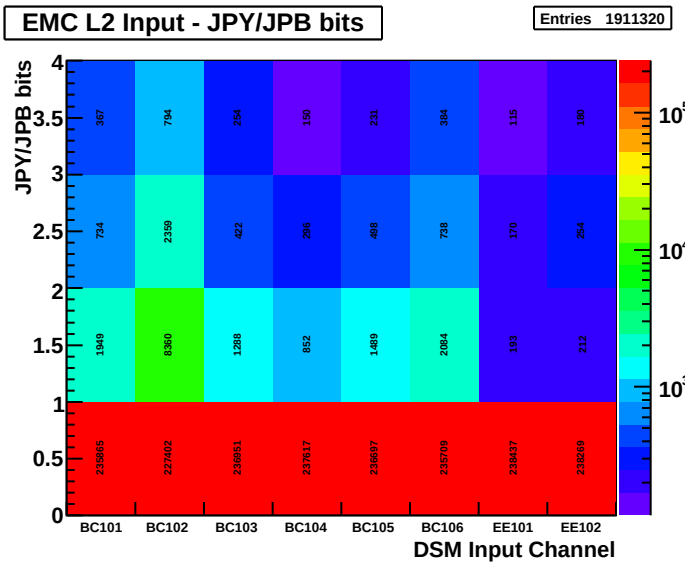

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

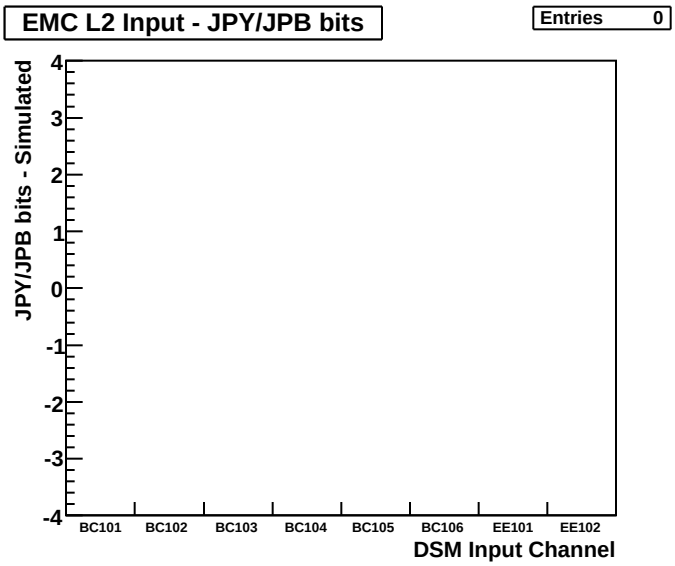

Endcap EMC L1 Input - High Tower Bits
Entries 2866980
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

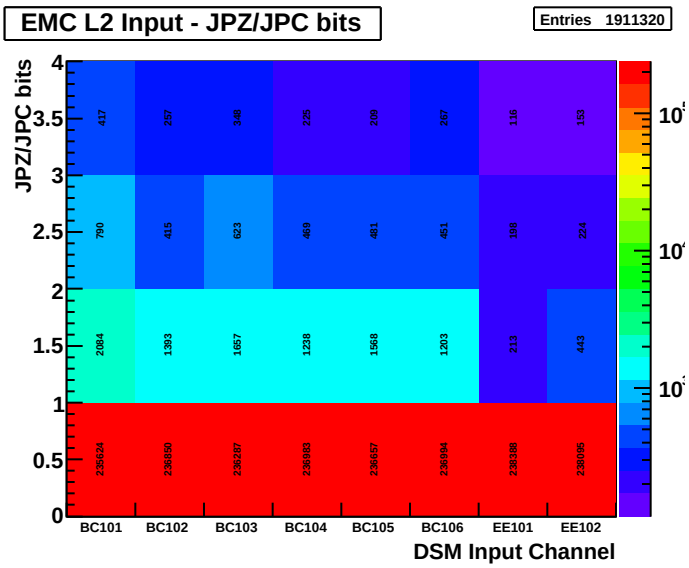

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

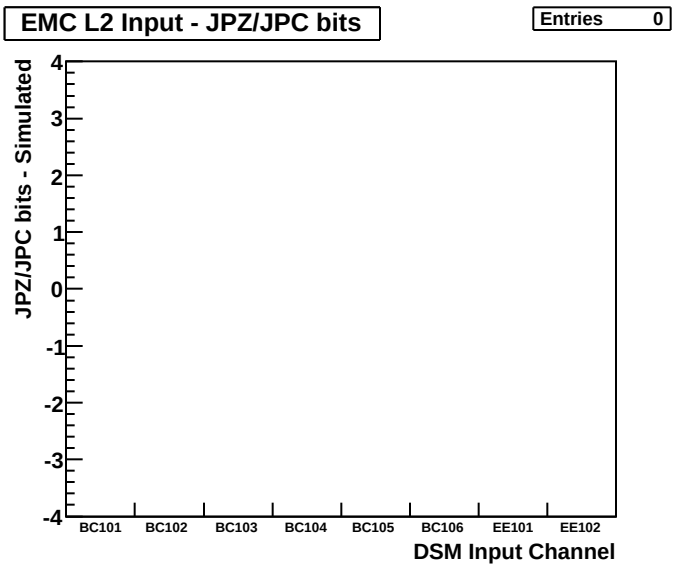

EMC L2 Input - JPX/JPA bits
Entries 1911320
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
528 1237 342 208 431 338 96 218
860 4069 663 370 990 602 181 244
2217 13826 1605 1084 2649 1511 194 248
235310 219783 238305 237253 234845 235464 239444 239205

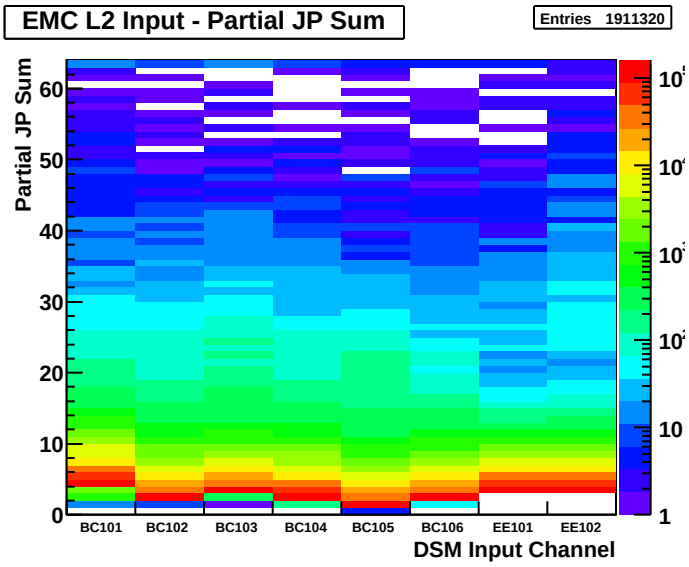

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

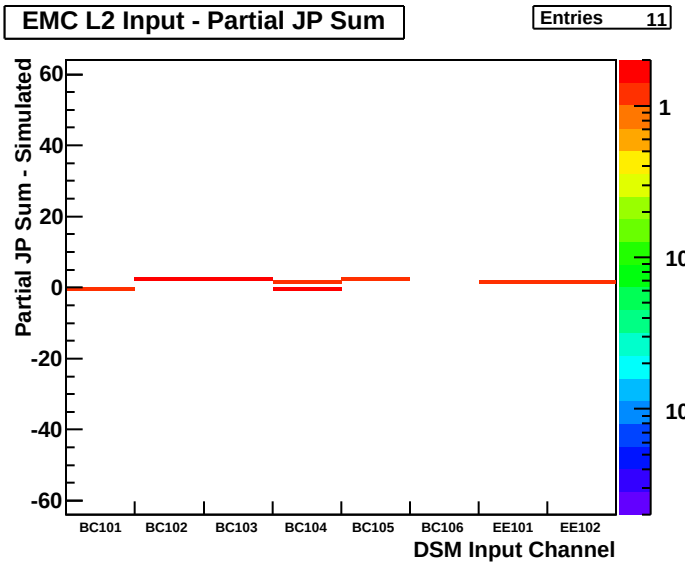

EMC L2 Input - JPY/JPB bits
Entries 1911320
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
367 794 254 150 231 384 115 180
734 2359 422 286 498 738 170 254
1949 8360 1238 852 1489 2084 193 212
233865 227402 238051 237617 236697 235709 239437 239269

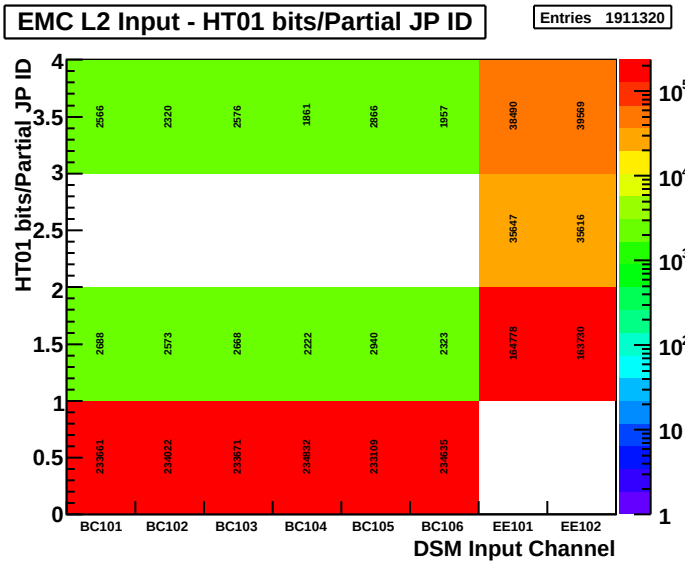

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

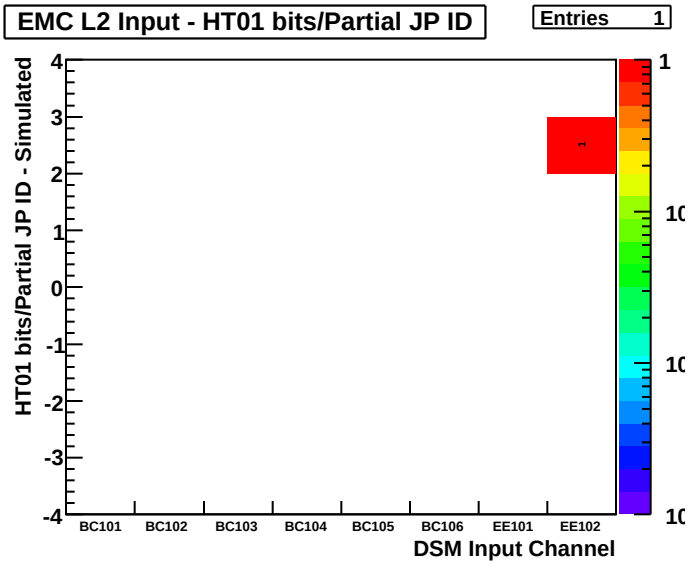

EMC L2 Input - JPZ/JPC bits
Entries 1911320
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
417 257 248 225 209 267 116 153
790 415 623 469 481 451 198 224
2084 1393 1657 1238 1568 1203 213 443
235524 236850 236287 236983 236657 236994 238388 238095

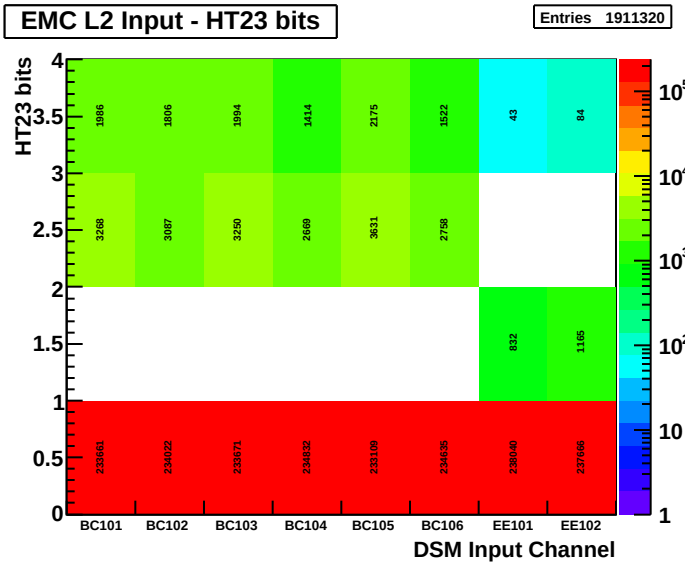

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

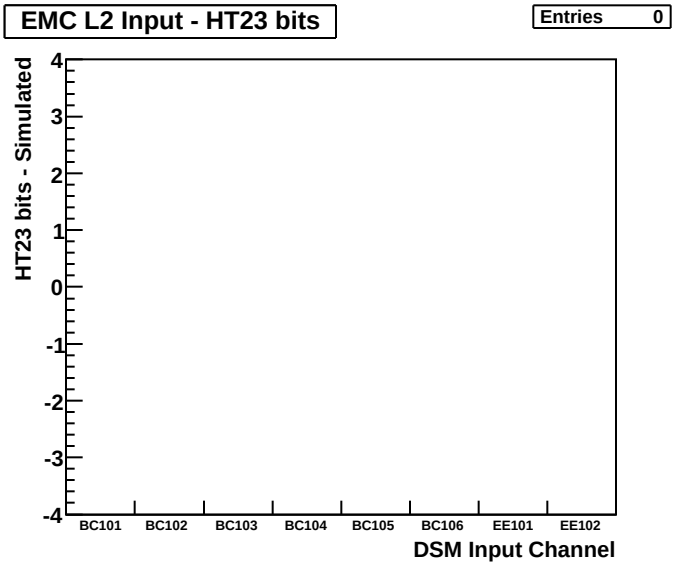

EMC L2 Input - Partial JP Sum
Entries 1911320
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

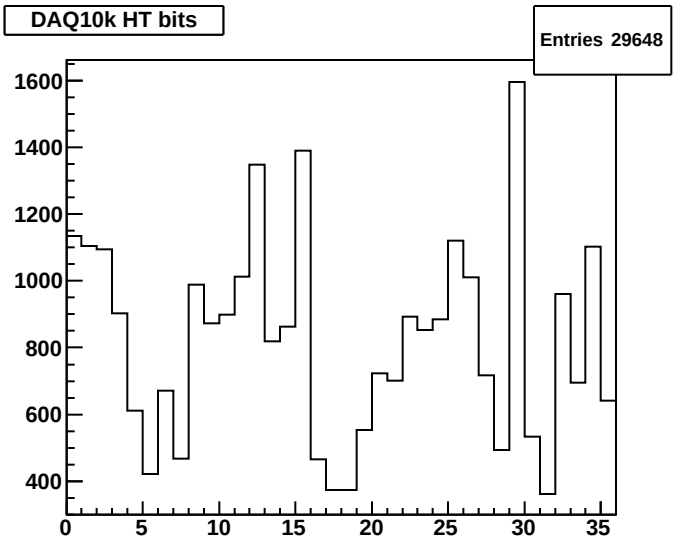

EMC L2 Input - Partial JP Sum
Entries 11
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

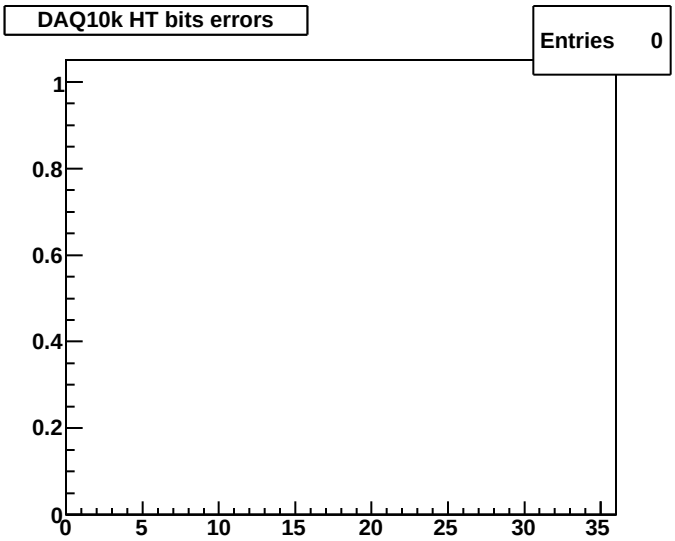

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1911320
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

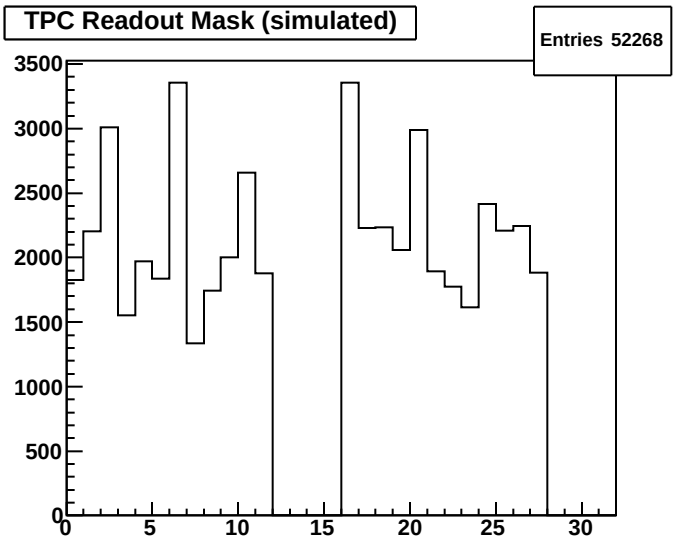

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

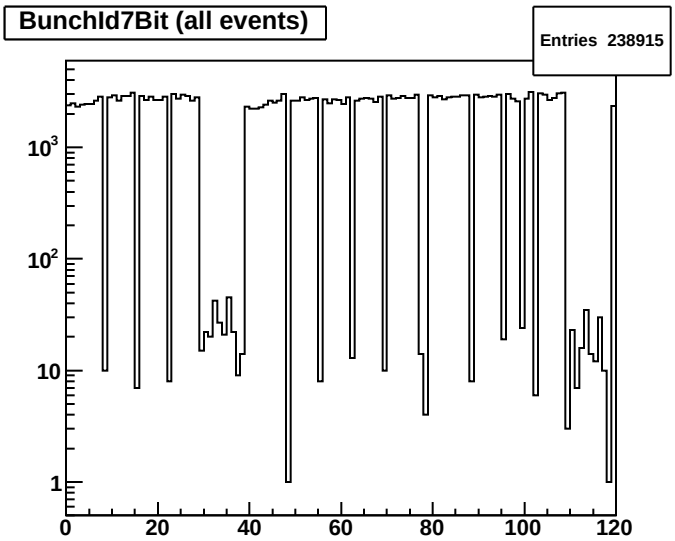

EMC L2 Input - HT23 bits
Entries 1911320
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

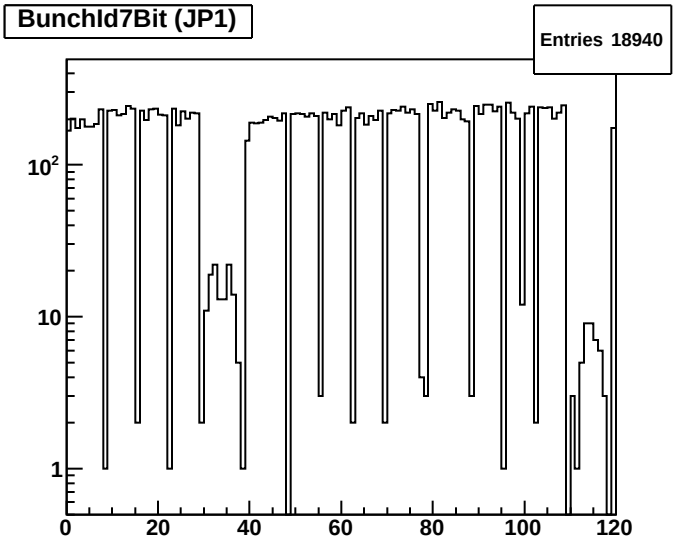

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

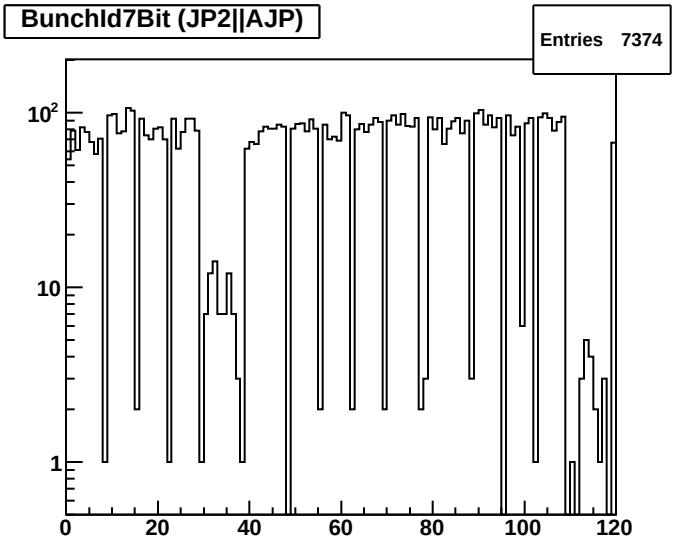

DAQ10k HT bits
Entries 29648

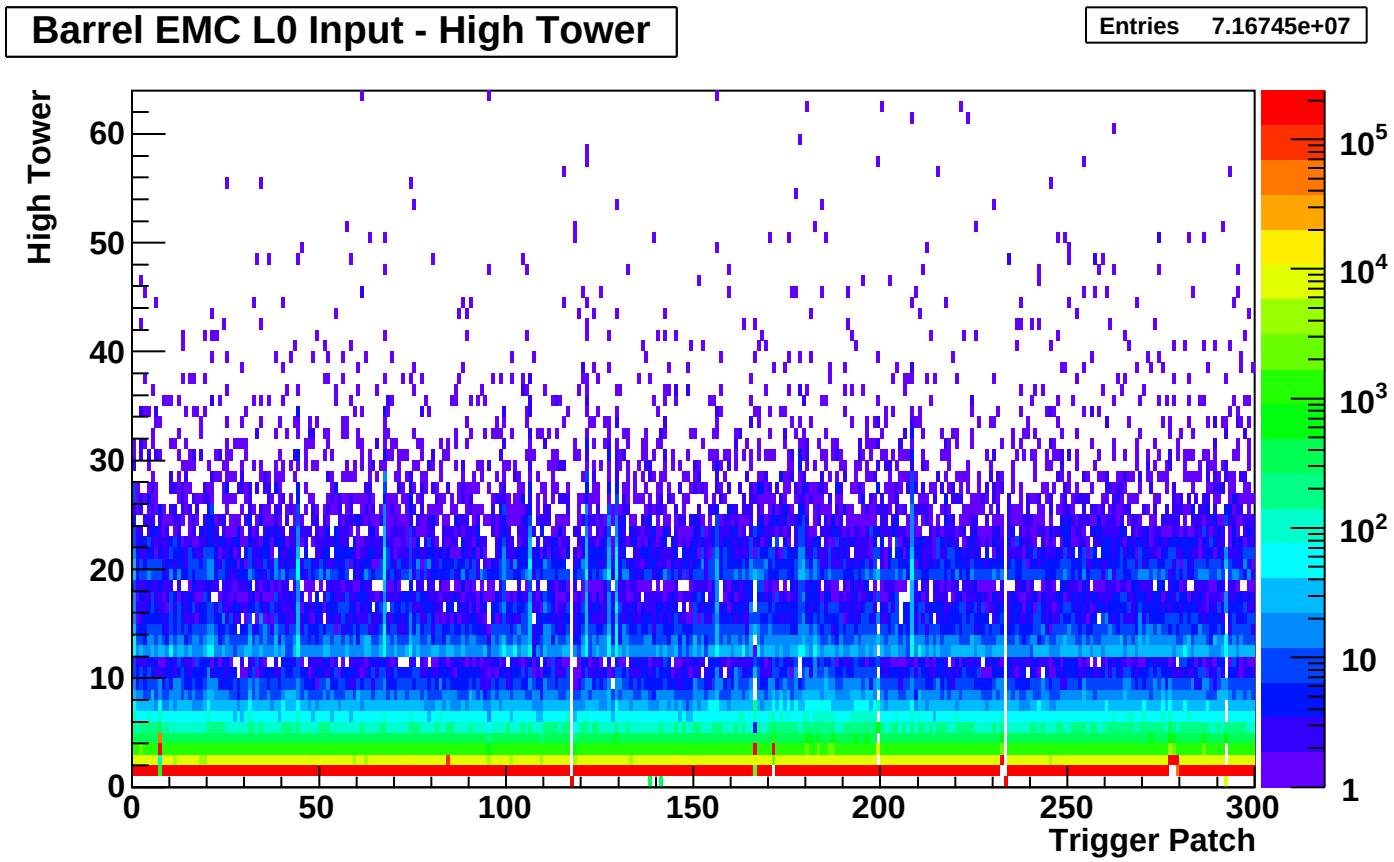

DAQ10k HT bits errors
Entries 0

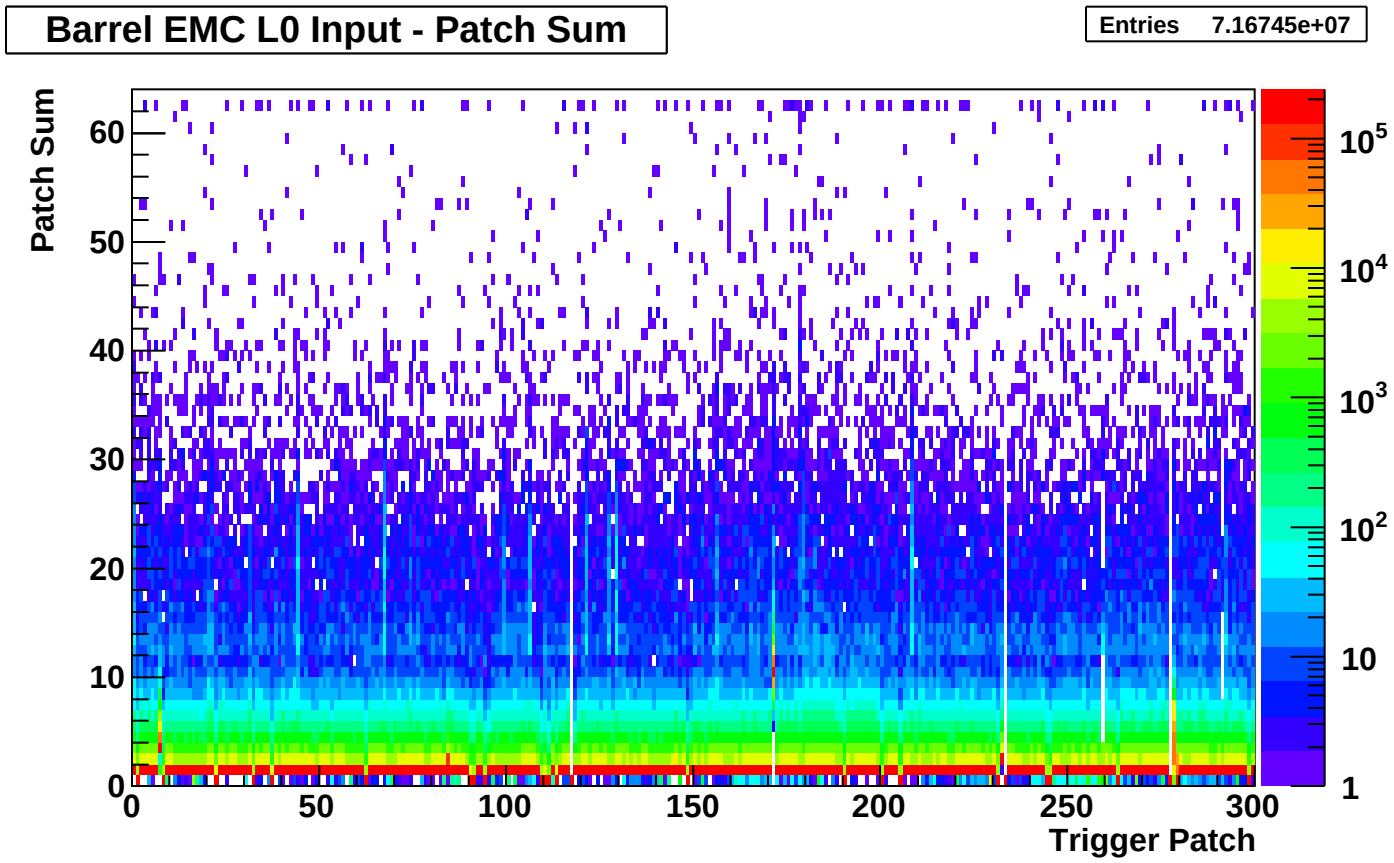

TPC Readout Mask (simulated)
Entries 52268

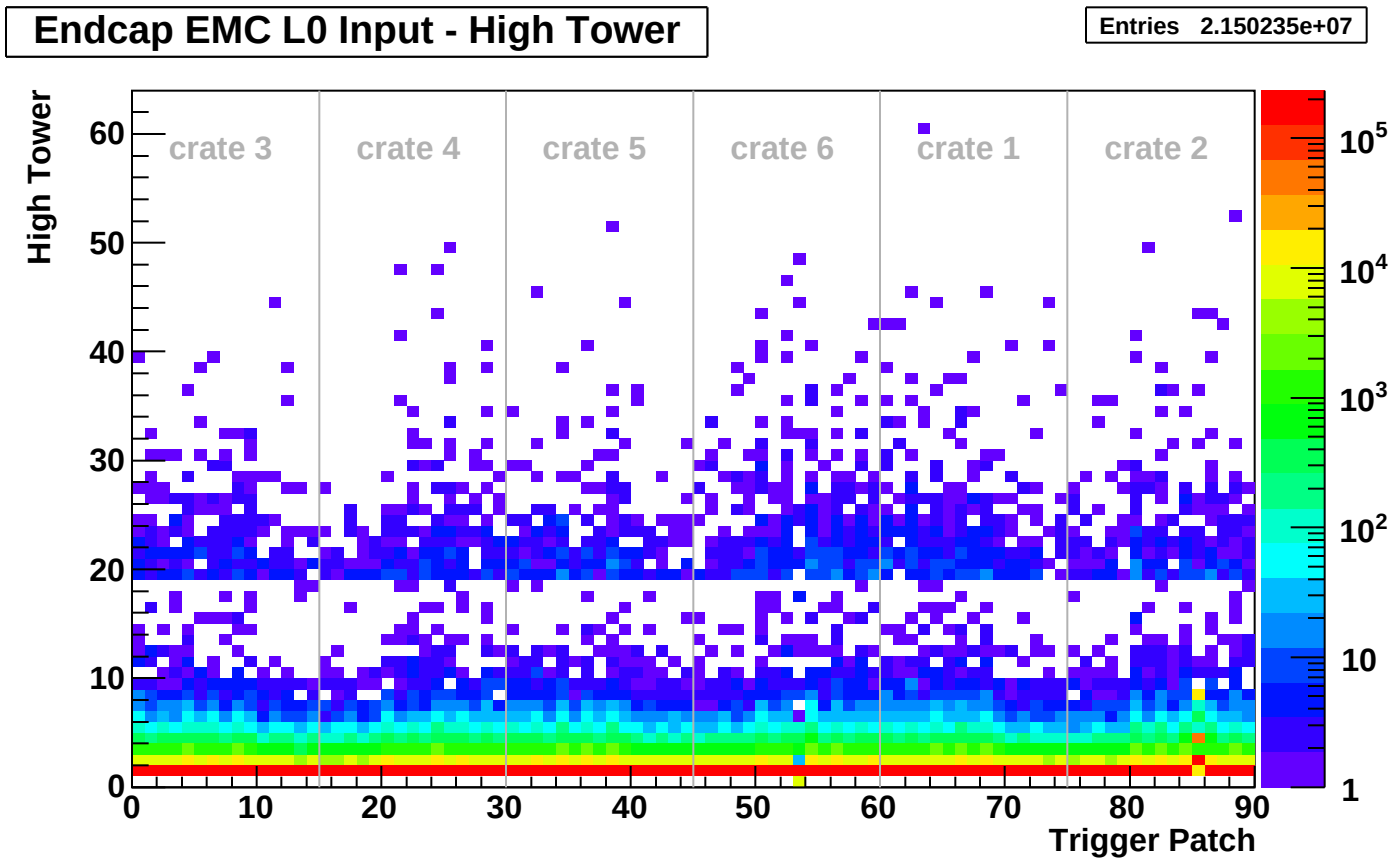

BunchId7Bit (all events)
Entries 238915

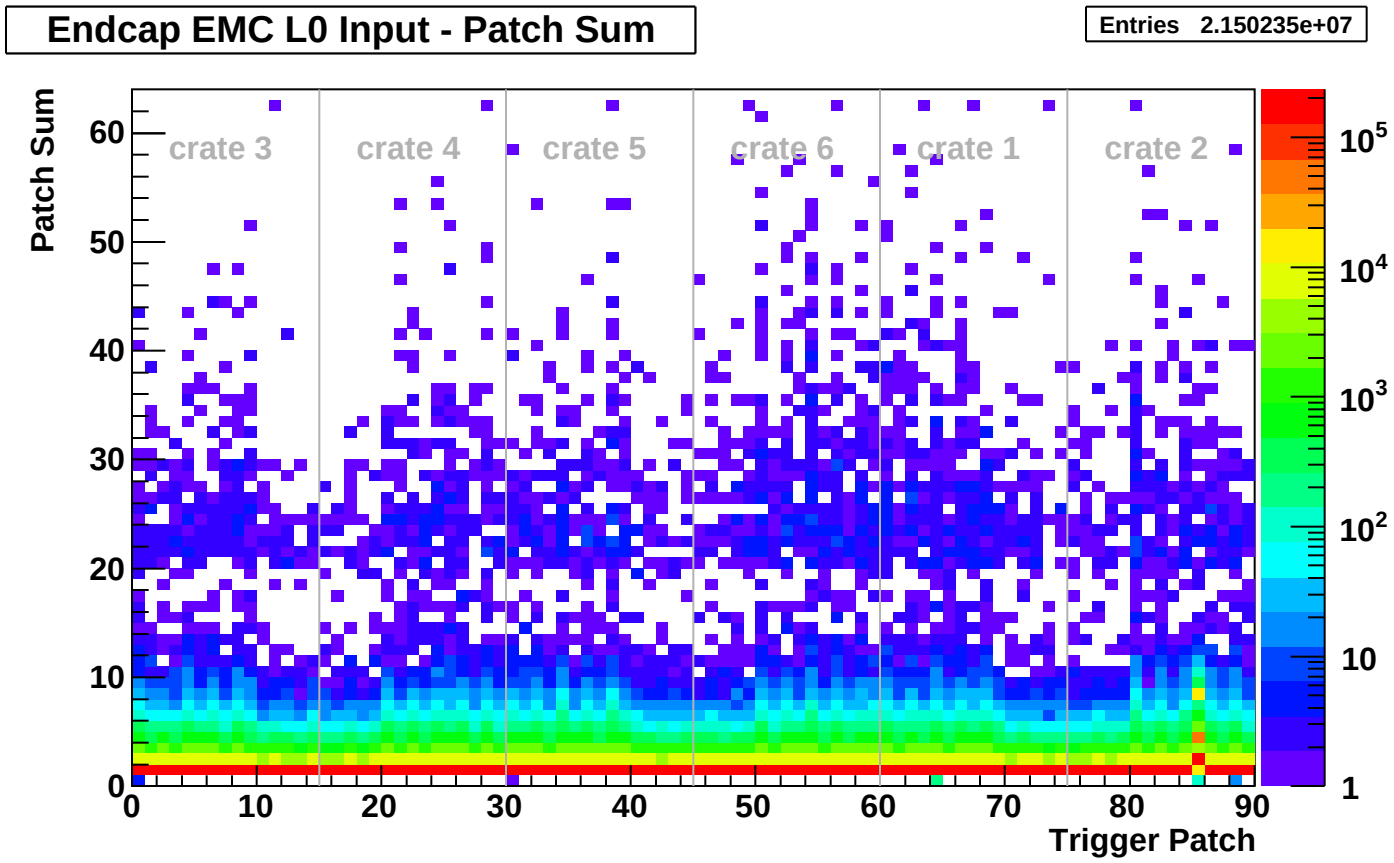

BunchId7Bit (JP1)
Entries 18940

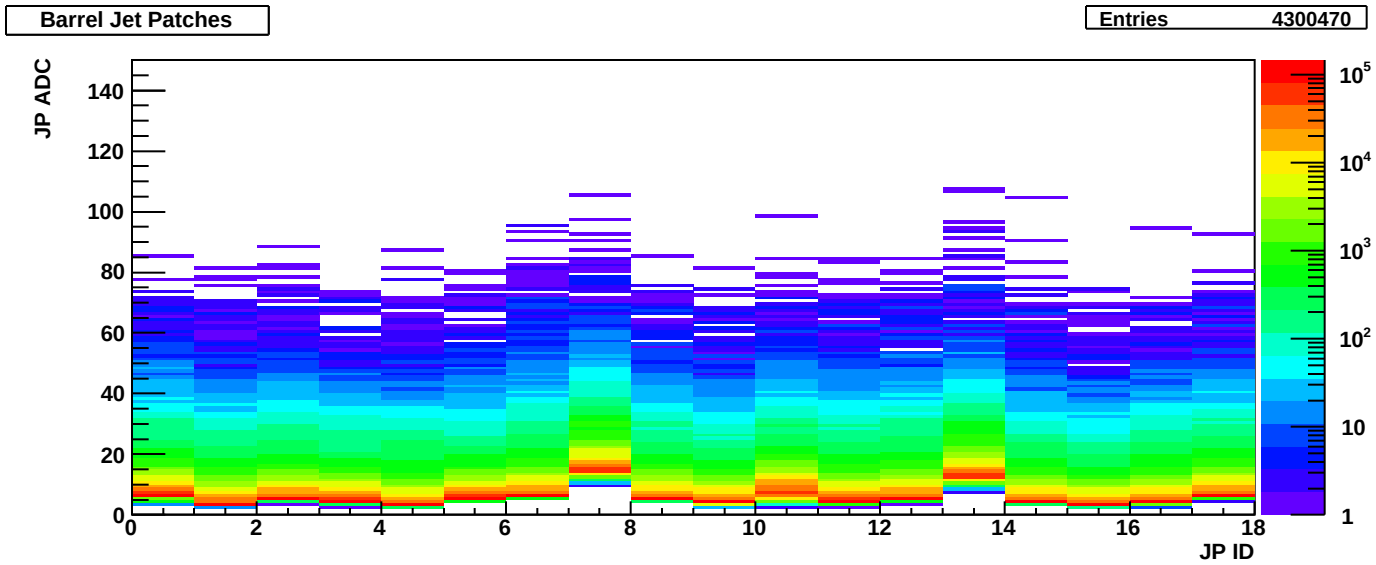

BunchId7Bit (JP2||AJP)
Entries 7374

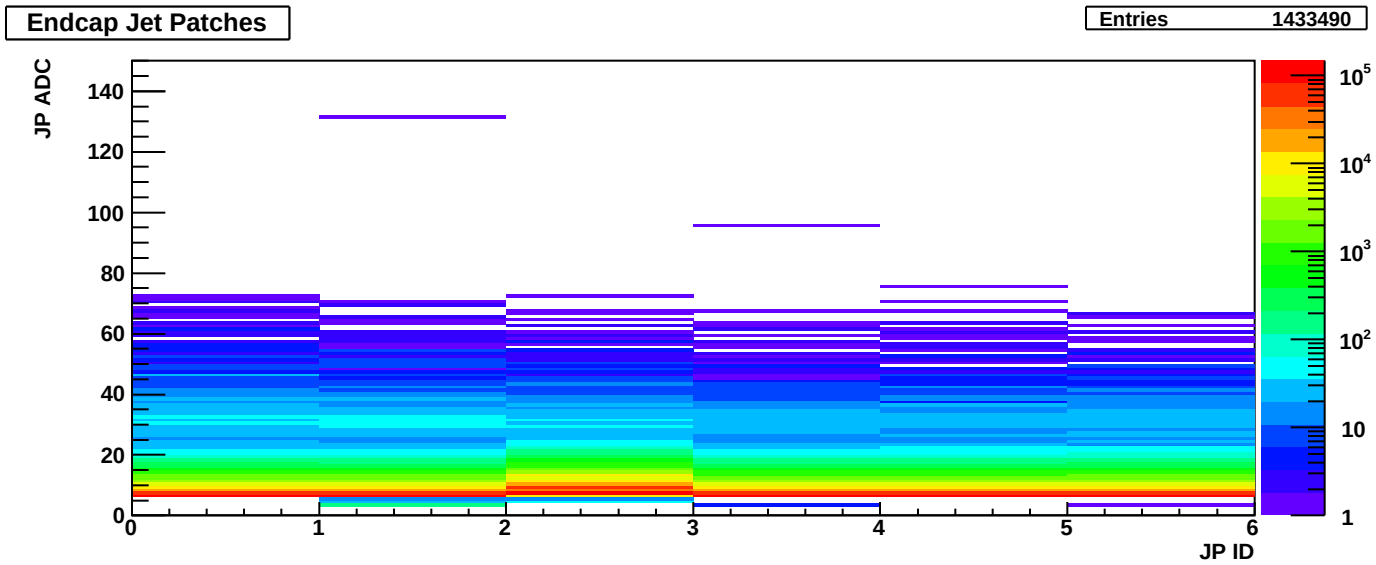

Barrel EMC L0 Input - High Tower
Entries 7.16745e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

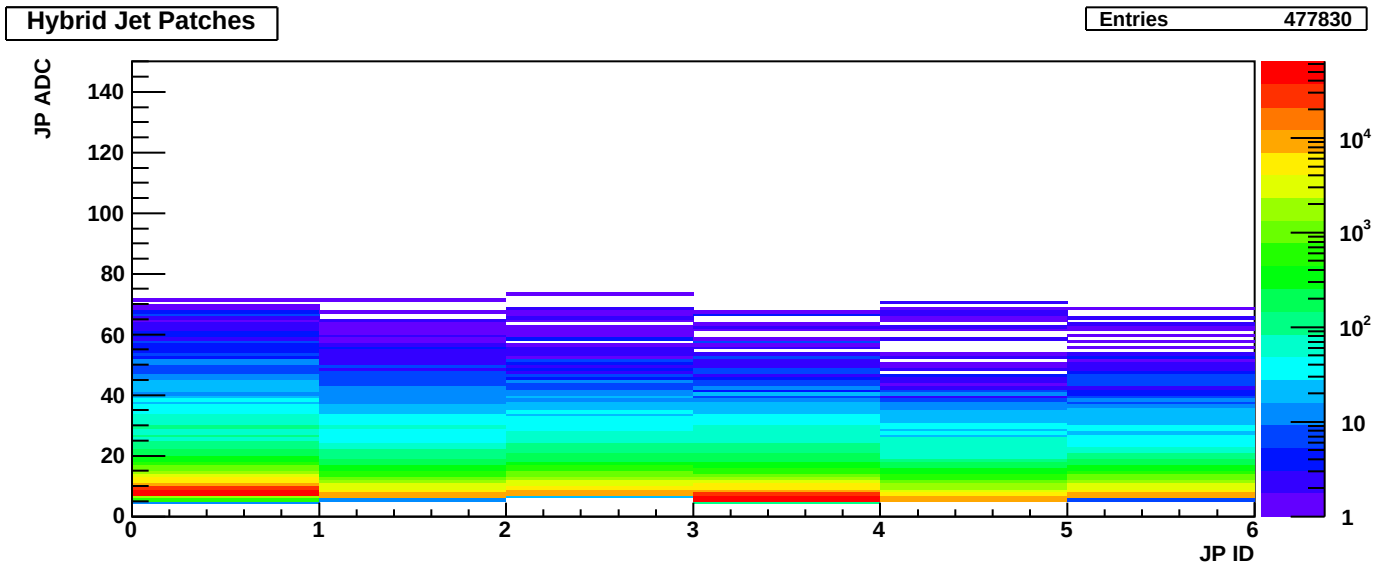

Barrel EMC L0 Input - Patch Sum
Entries 7.16745e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

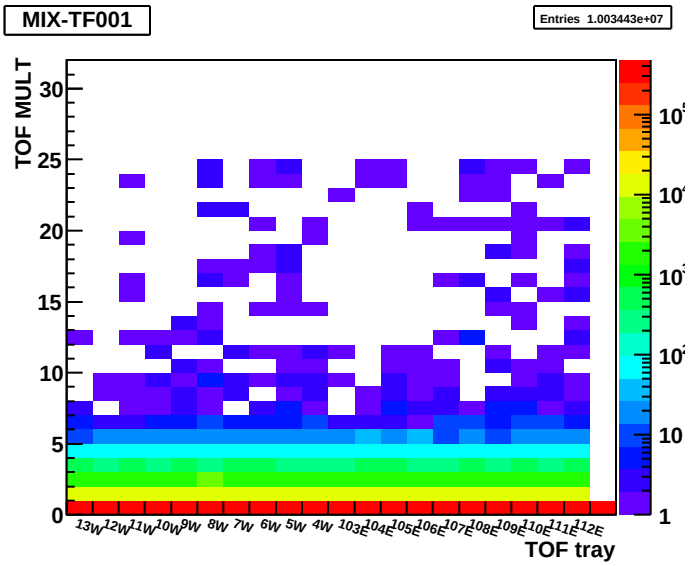

Endcap EMC L0 Input - High Tower
Entries 2.150235e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

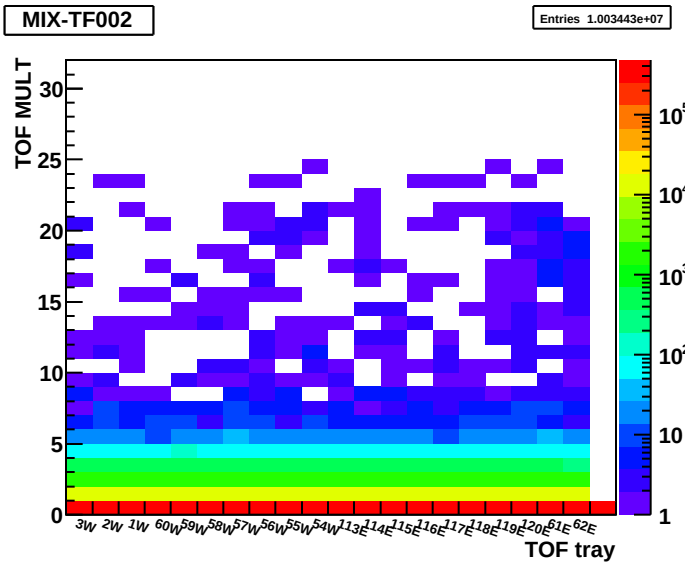

Endcap EMC L0 Input - Patch Sum
Entries 2.150235e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

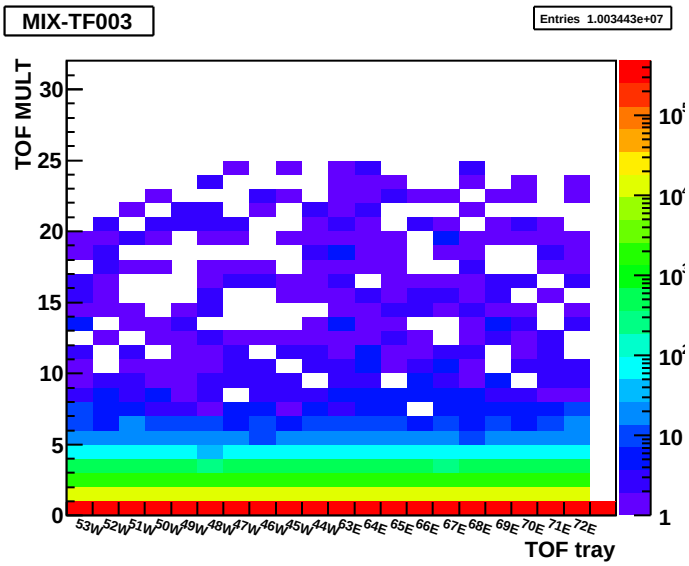

Barrel Jet Patches
Entries 4300470
JP ADC
JP ID

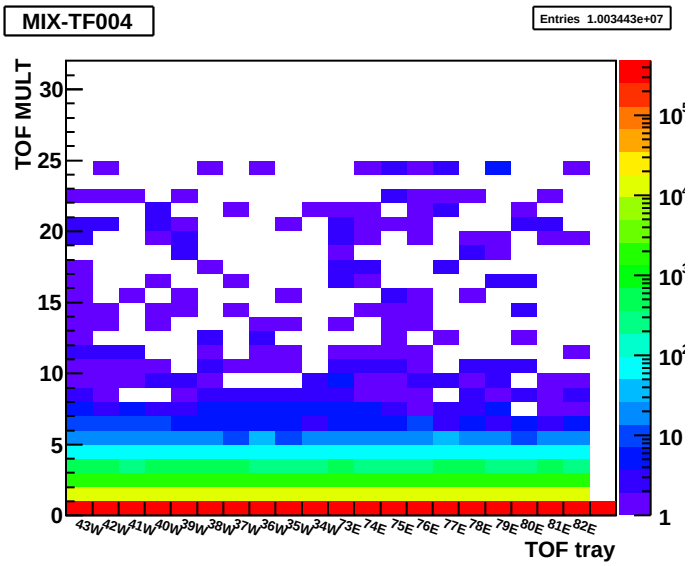

Endcap Jet Patches
Entries 1433490
JP ADC
JP ID

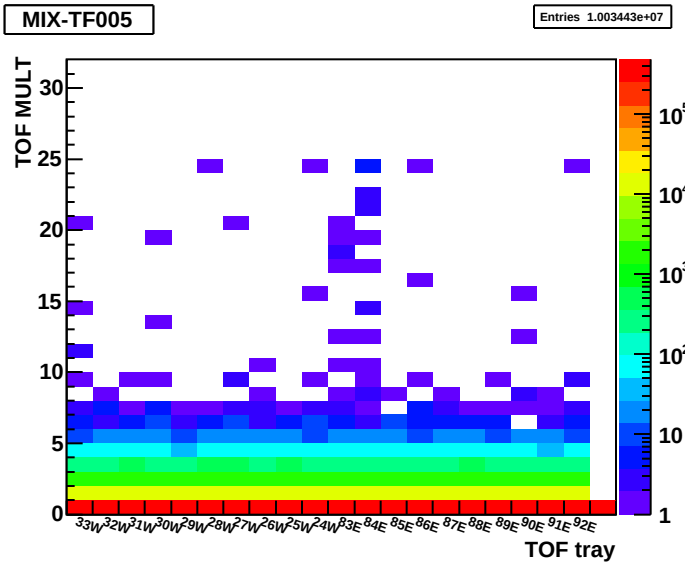

Hybrid Jet Patches
Entries 477830
JP ADC
JP ID

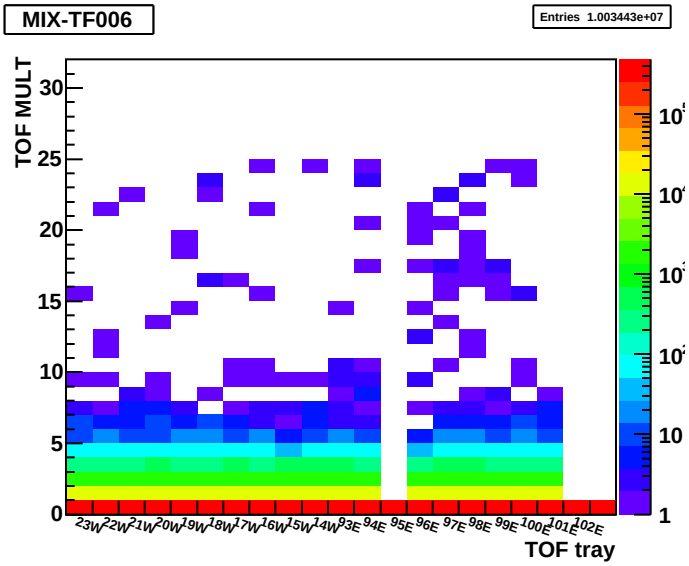

MIX-TF001
Entries 1.003443e+07
TOF MULT
TOF tray

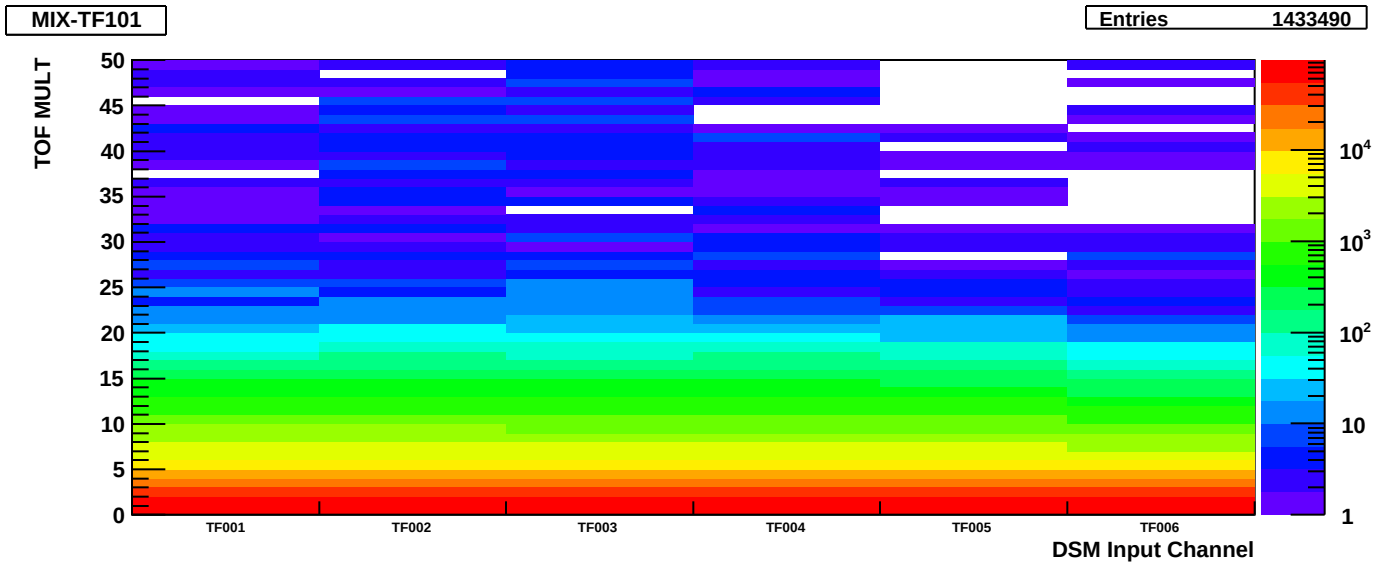

MIX-TF002
Entries 1.003443e+07
TOF MULT
TOF tray

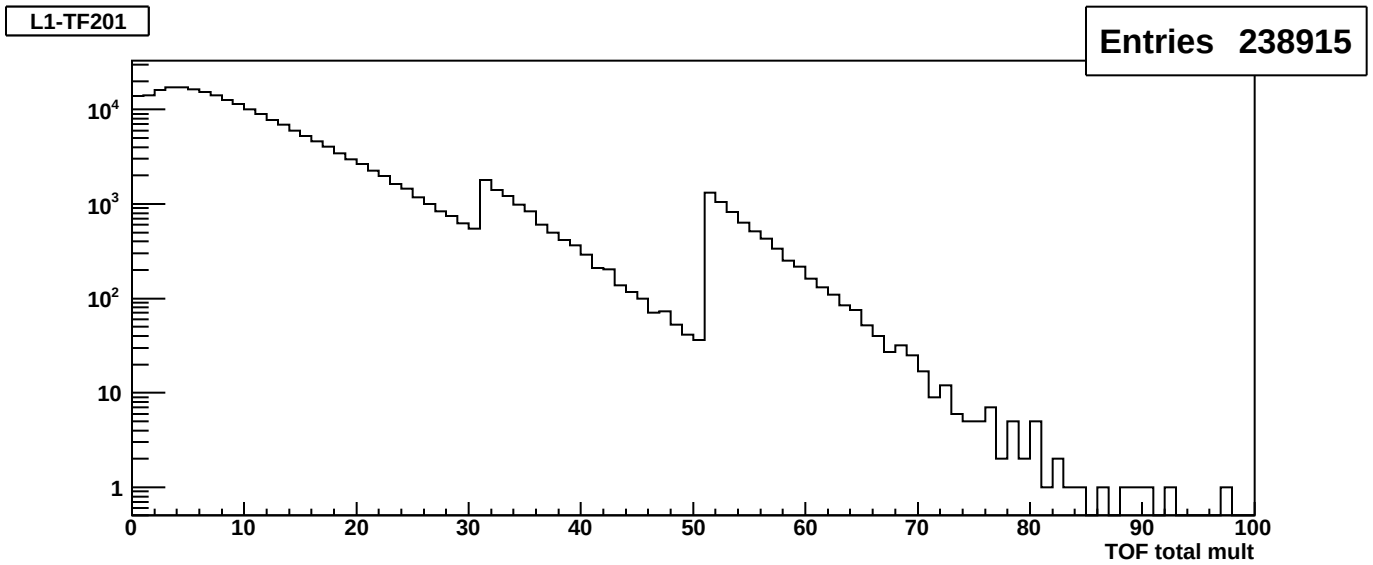

MIX-TF003
Entries 1.003443e+07
TOF MULT
TOF tray

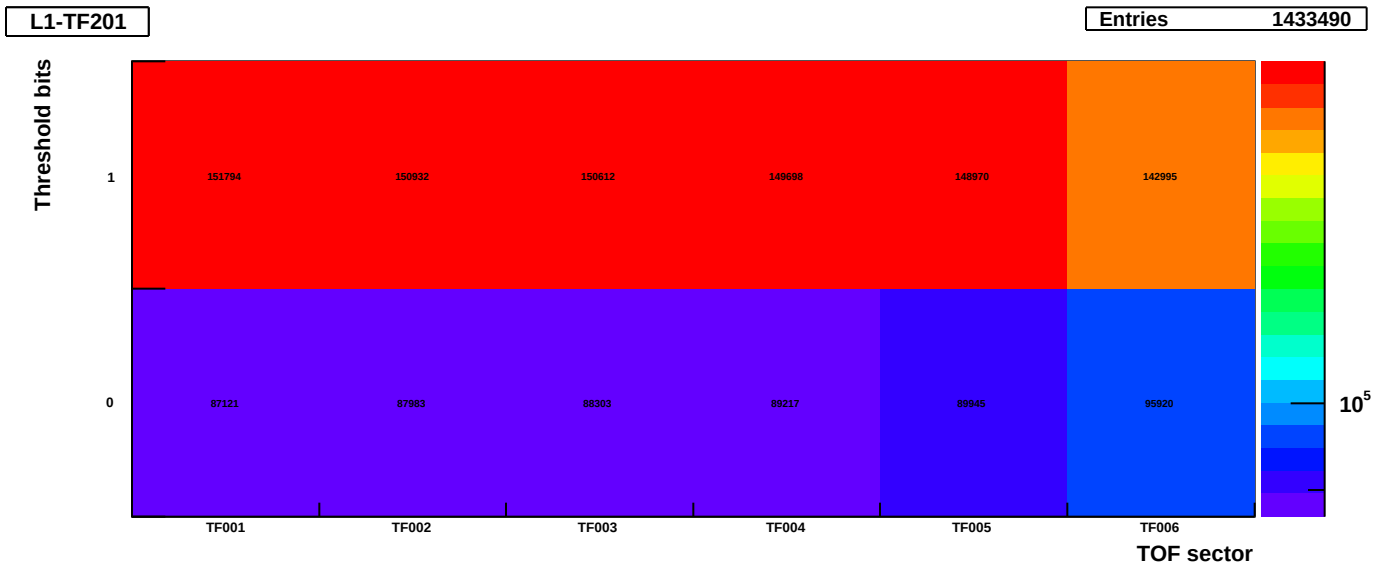

MIX-TF004
Entries 1.003443e+07
TOF MULT
TOF tray

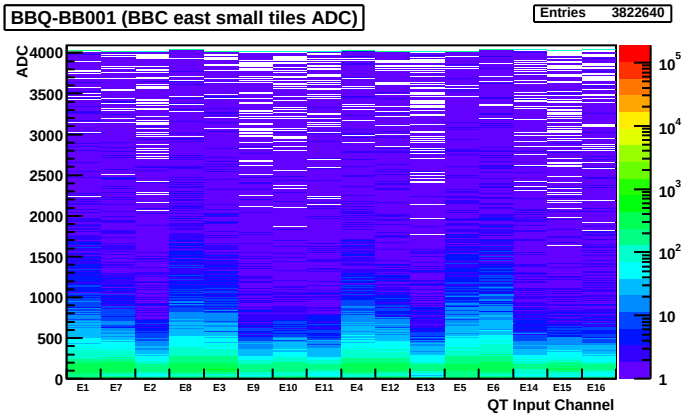

MIX-TF005
Entries 1.003443e+07
TOF MULT
TOF tray

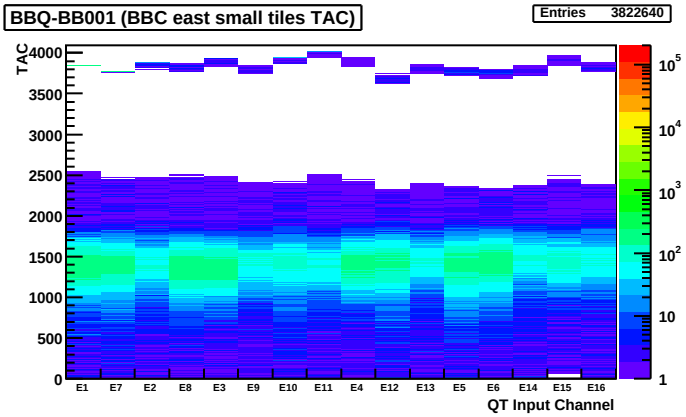

MIX-TF006
Entries 1.003443e+07
TOF MULT
TOF tray

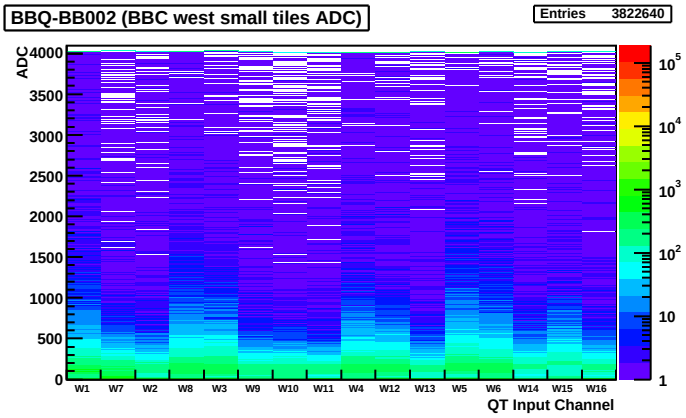

MIX-TF101
Entries 1433490
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

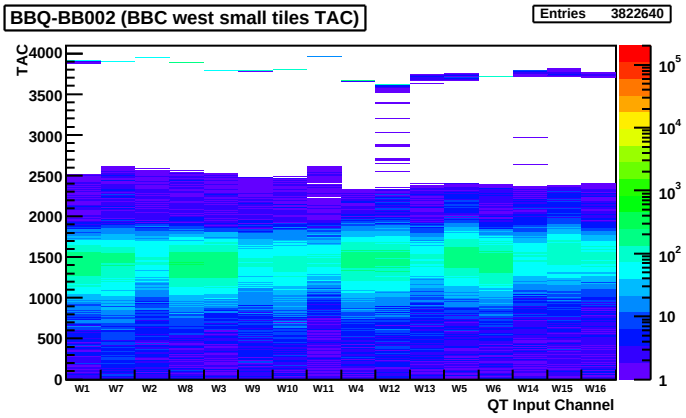

L1-TF201
Entries 238915
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

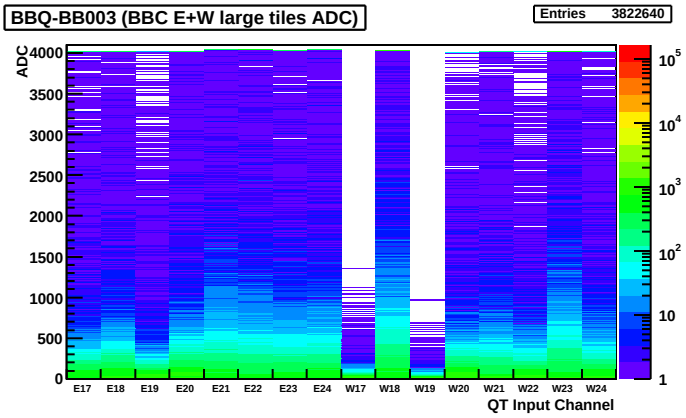

L1-TF201
Entries 1433490
Threshold bits
1
0
151794
150932
150612
149698
148970
142995
87121
87983
88303
89217
89945
95920
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

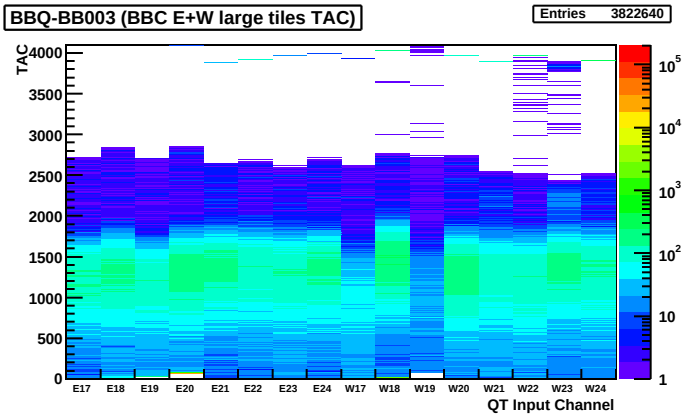

BBQ-BB001 (BBC east small tiles ADC)
Entries 3822640
ADC
QT Input Channel

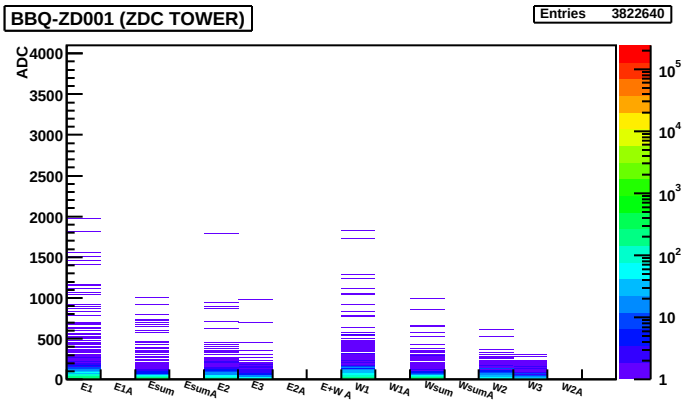

BBQ-BB001 (BBC east small tiles TAC)
Entries 3822640
TAC
QT Input Channel

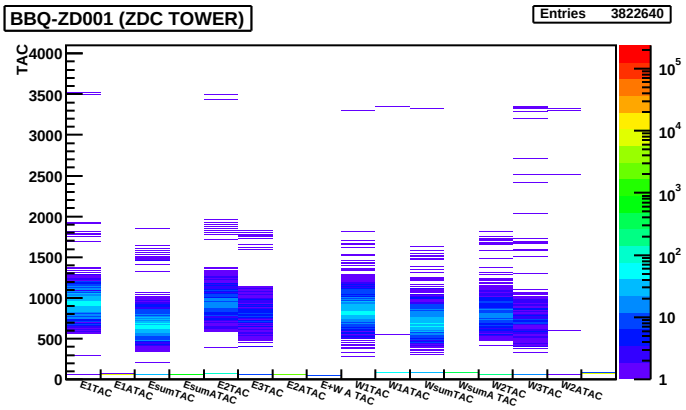

BBQ-BB002 (BBC west small tiles ADC)
Entries 3822640
ADC
QT Input Channel

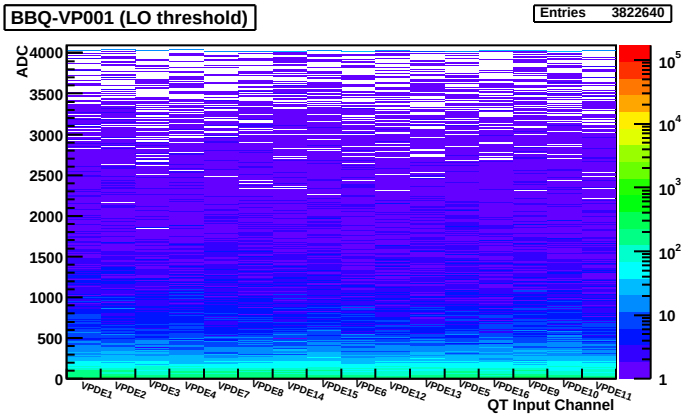

BBQ-BB002 (BBC west small tiles TAC)
Entries 3822640
TAC
QT Input Channel

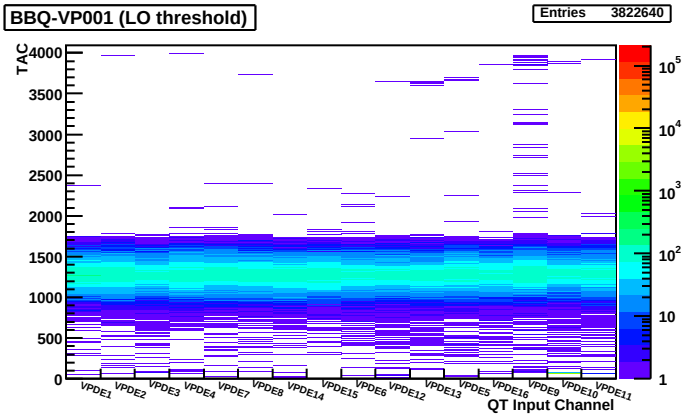

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3822640
ADC
QT Input Channel

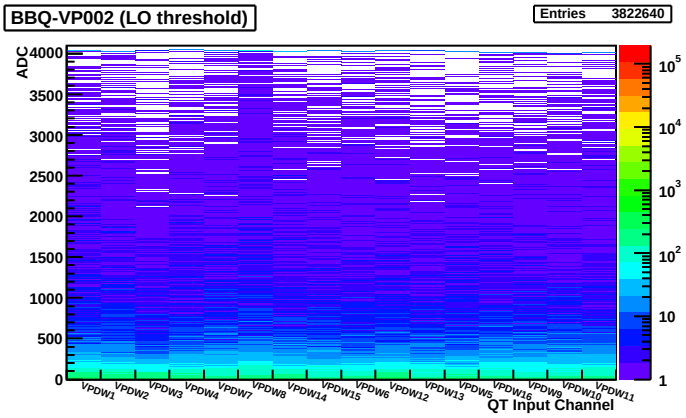

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3822640
TAC
QT Input Channel

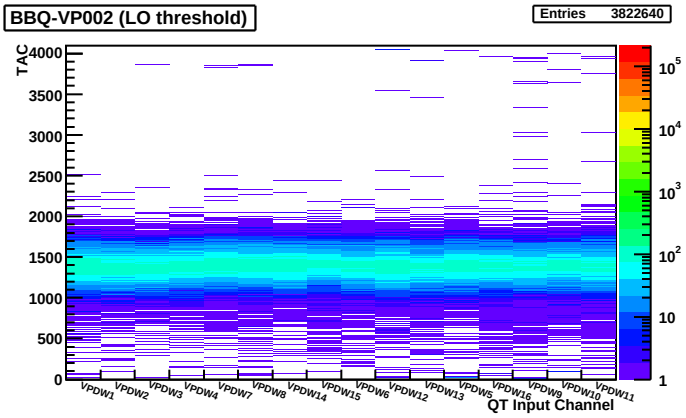

BBQ-ZD001 (ZDC TOWER)
Entries 3822640
ADC

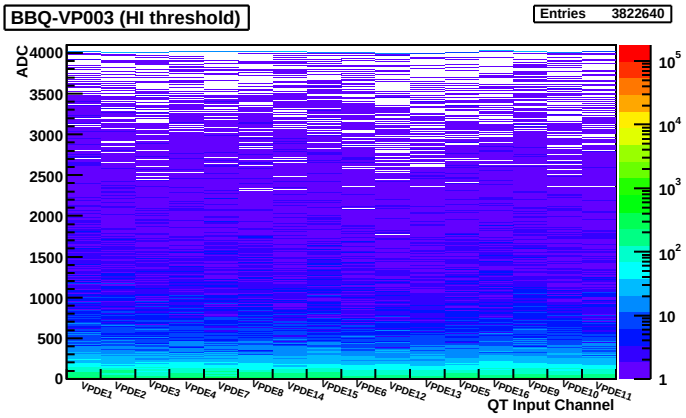

BBQ-ZD001 (ZDC TOWER)
Entries 3822640
TAC

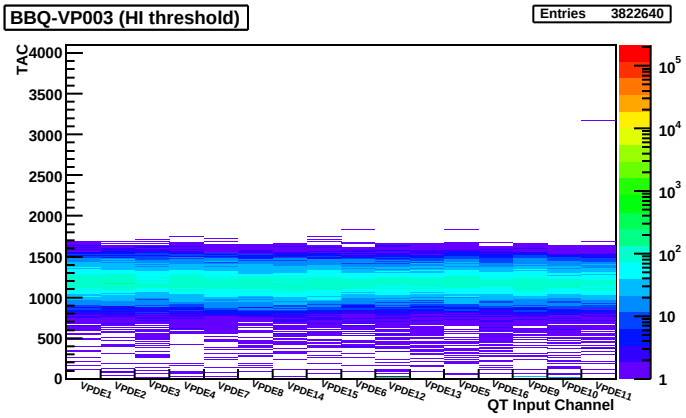

BBQ-VP001 (LO threshold)
Entries 3822640
ADC
QT Input Channel

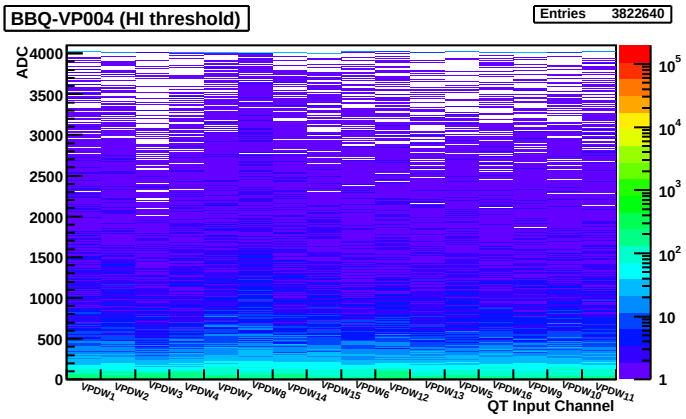

BBQ-VP001 (LO threshold)
Entries 3822640
TAC
QT Input Channel

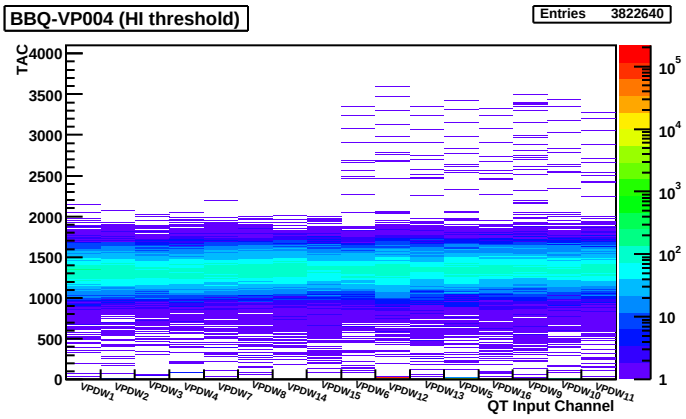

BBQ-VP002 (LO threshold)
Entries 3822640
ADC
QT Input Channel

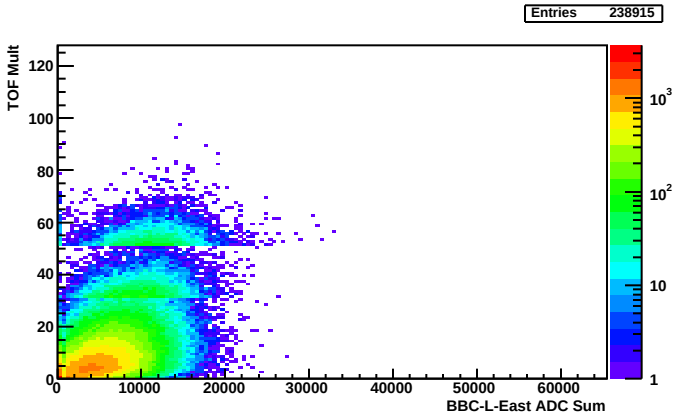

BBQ-VP002 (LO threshold)
Entries 3822640
TAC
QT Input Channel

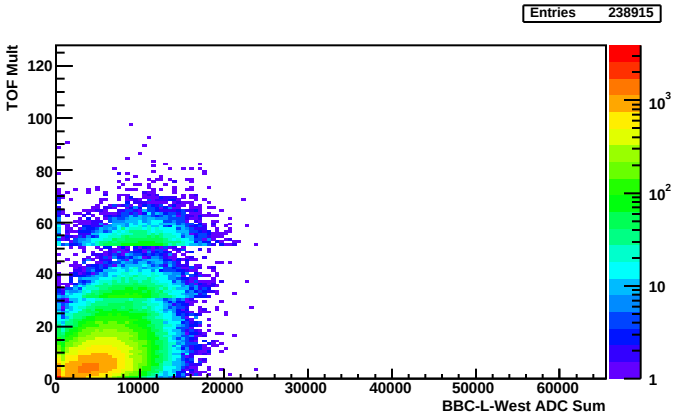

BBQ-VP003 (HI threshold)
Entries 3822640
ADC
QT Input Channel

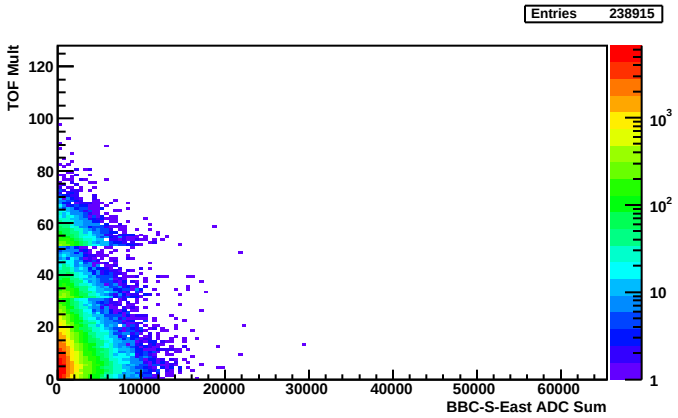

BBQ-VP003 (HI threshold)
Entries 3822640
TAC
QT Input Channel

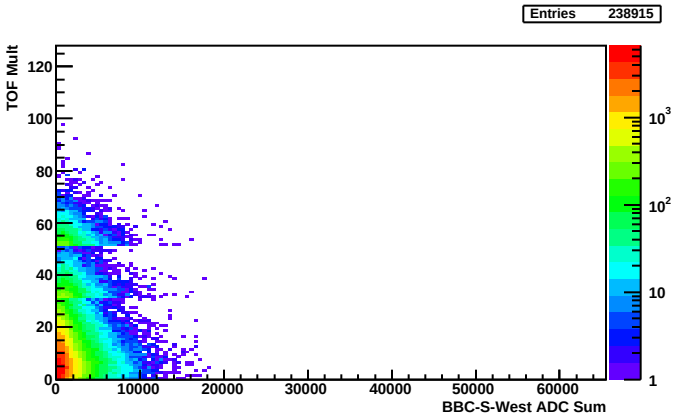

BBQ-VP004 (HI threshold)
Entries 3822640
ADC
QT Input Channel

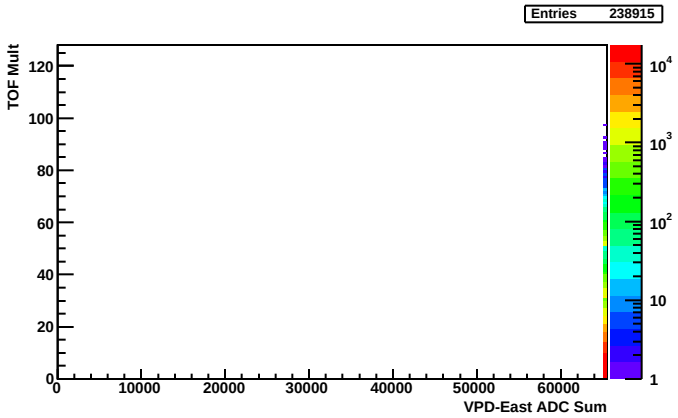

BBQ-VP004 (HI threshold)
Entries 3822640
TAC
QT Input Channel

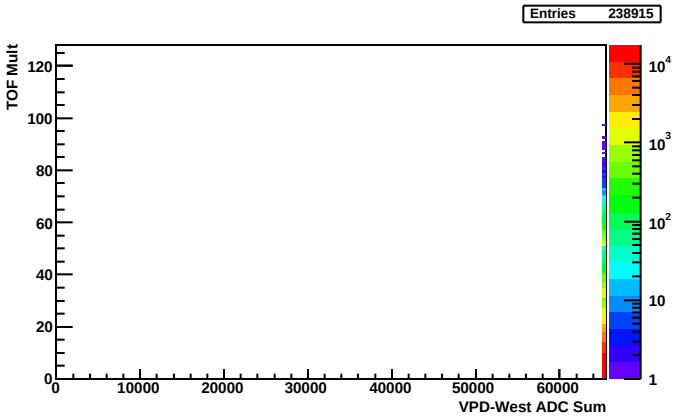

Entries 238915
TOF Mult
BBC-L-East ADC Sum

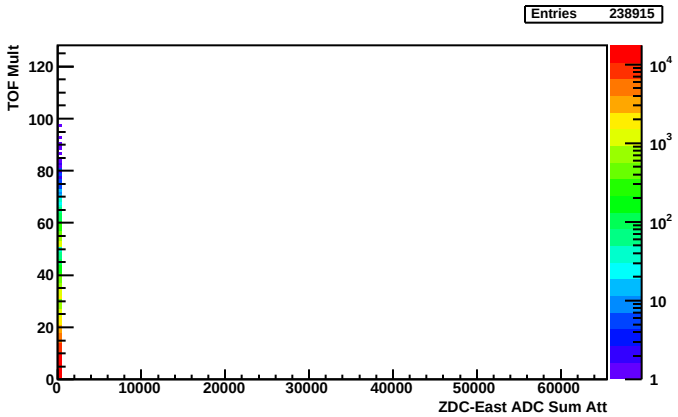

Entries 238915
TOF Mult
BBC-L-West ADC Sum

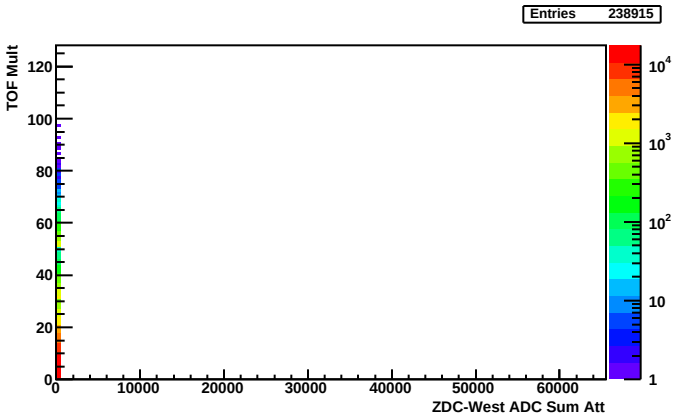

Entries 238915
TOF Mult
BBC-S-East ADC Sum

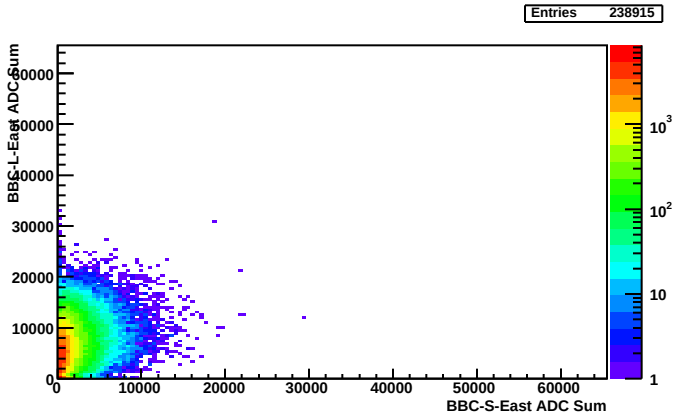

Entries 238915
TOF Mult
BBC-S-West ADC Sum

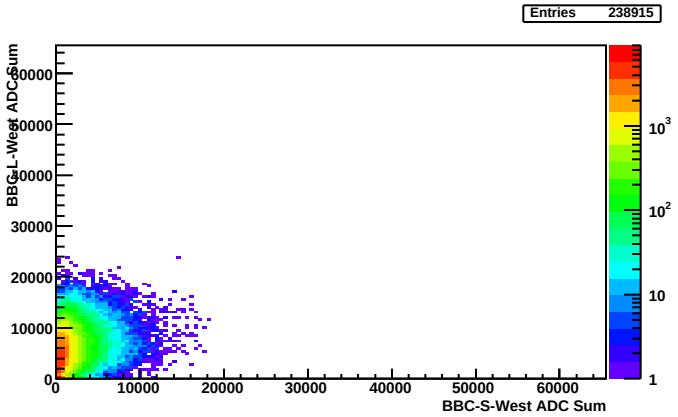

Entries 238915
TOF Mult
VPD-East ADC Sum

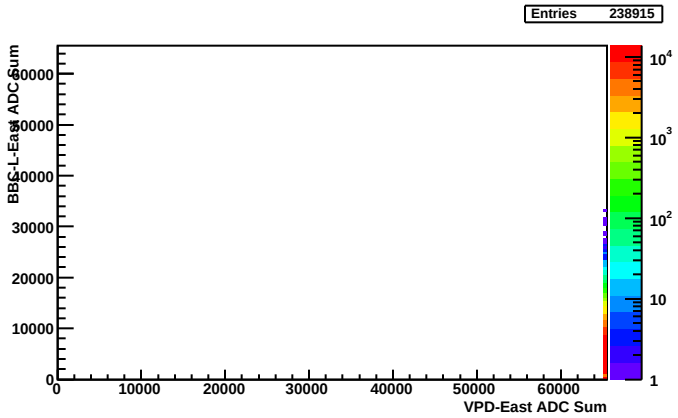

Entries 238915
TOF Mult
VPD-West ADC Sum

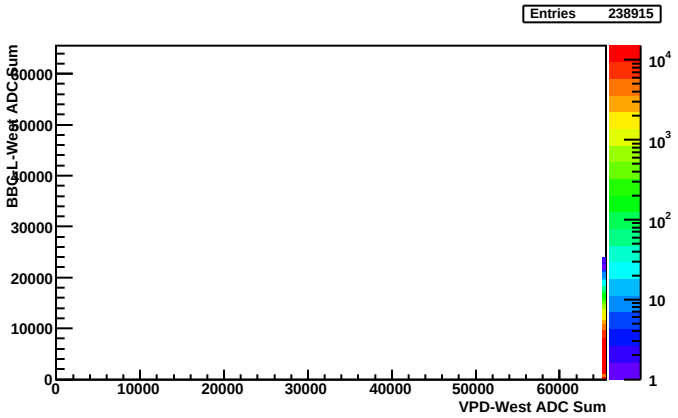

Entries 238915
TOF Mult
ZDC-East ADC Sum Att

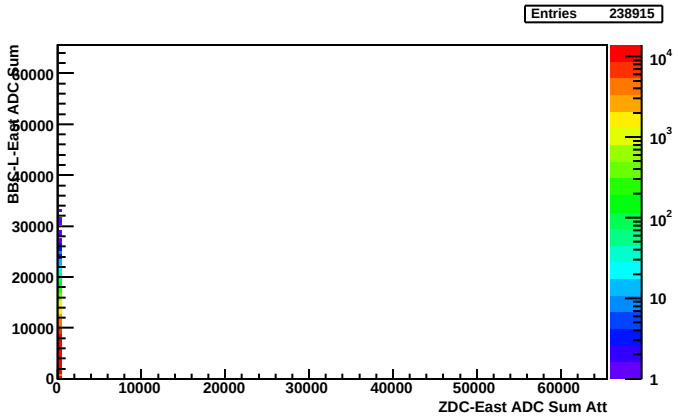

Entries 238915
TOF Mult
ZDC-West ADC Sum Att

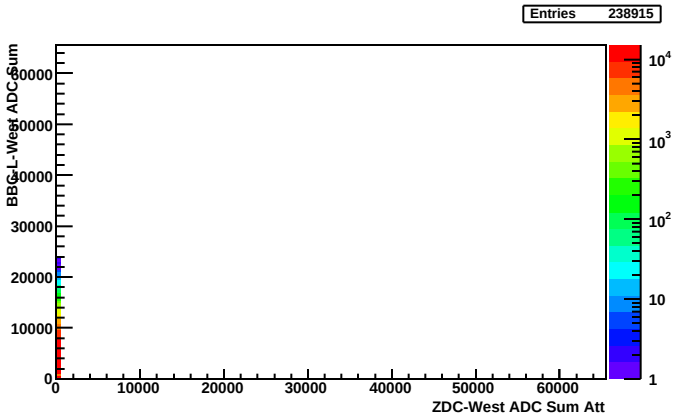

Entries 238915
BBC-L-East ADC Sum
BBC-S-East ADC Sum

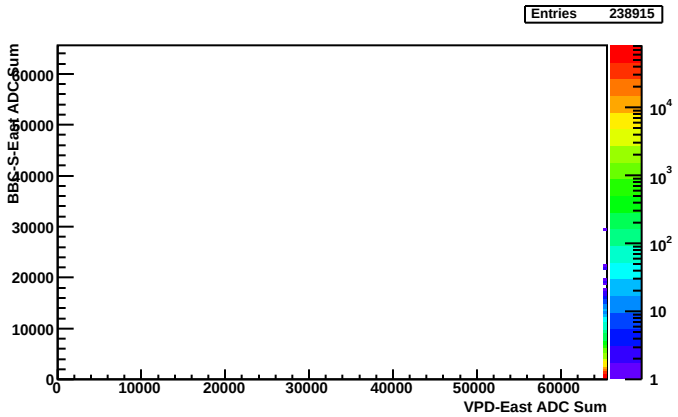

Entries 238915
BBC-L-West ADC Sum
BBC-S-West ADC Sum

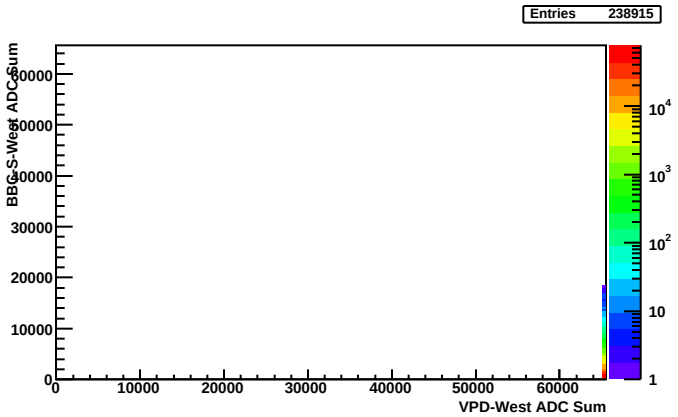

Entries 238915
BBC-L-East ADC Sum
VPD-East ADC Sum

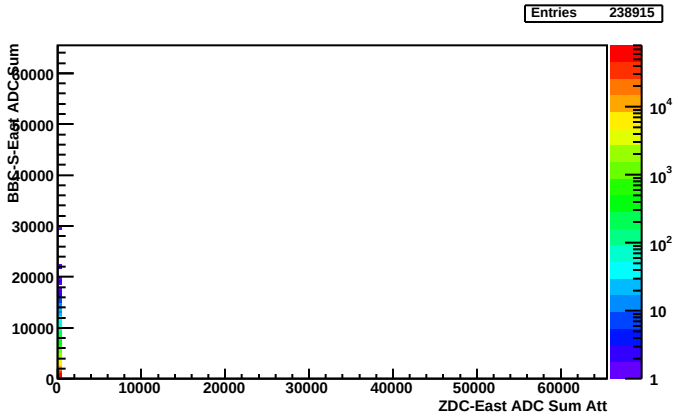

Entries 238915
BBC-L-West ADC Sum
VPD-West ADC Sum

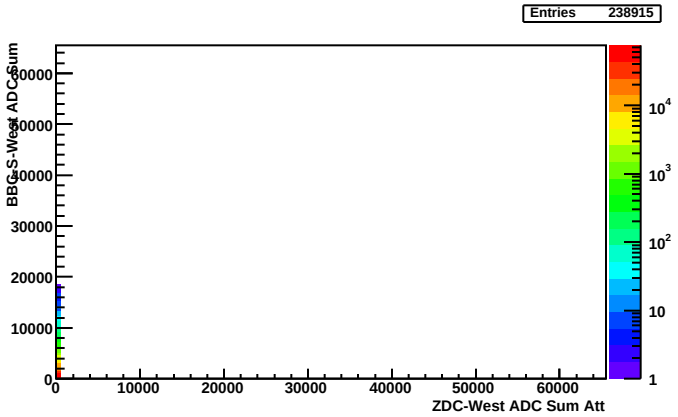

Entries 238915
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

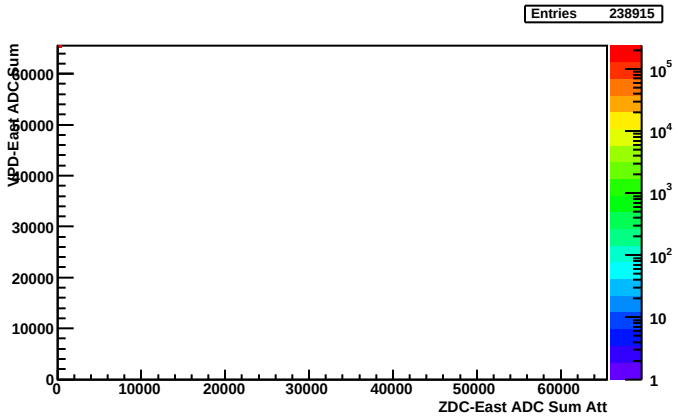

Entries 238915
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

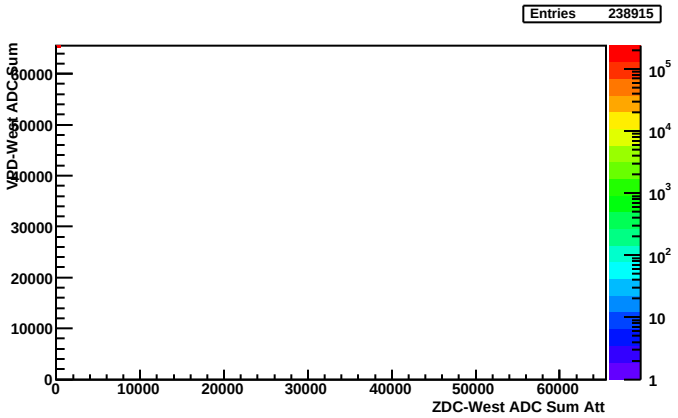

Entries 238915
BBC-S-East ADC Sum
VPD-East ADC Sum

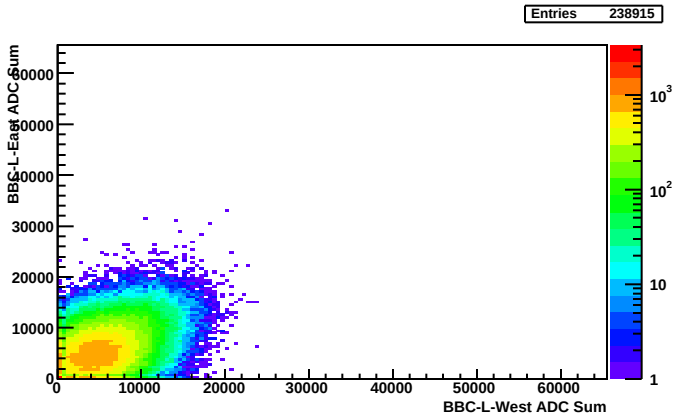

Entries 238915
BBC-S-West ADC Sum
VPD-West ADC Sum

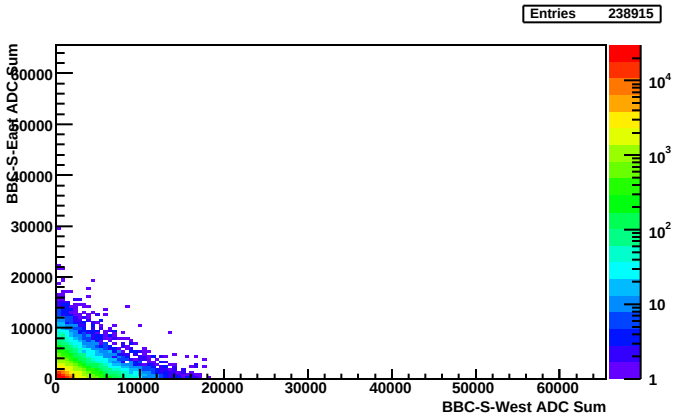

Entries 238915
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

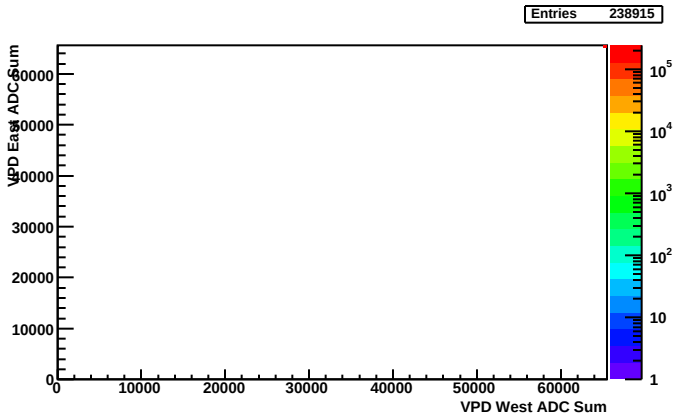

Entries 238915
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

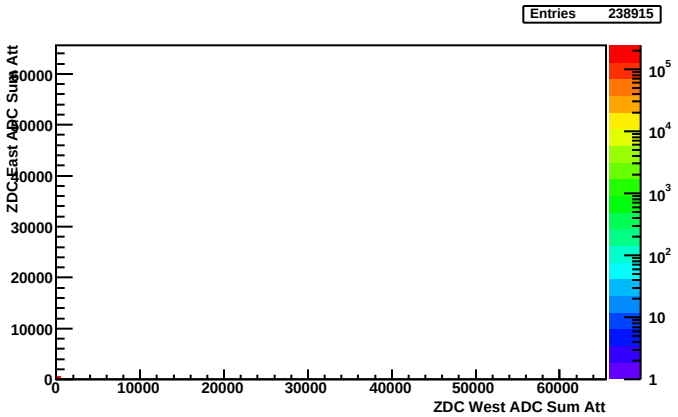

Entries 238915
VPD-East ADC Sum
ZDC-East ADC Sum Att

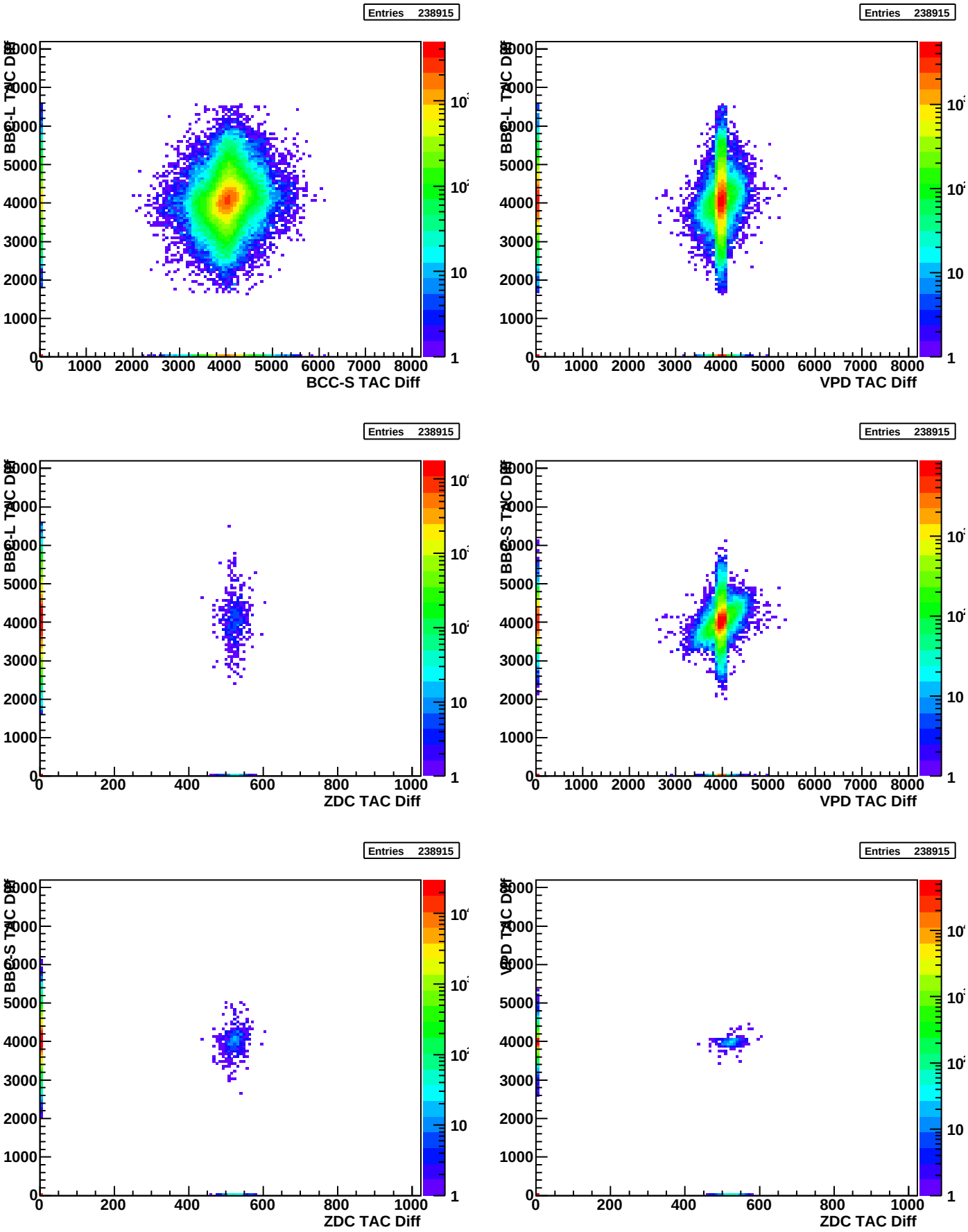

Entries 238915
VPD-West ADC Sum
ZDC-West ADC Sum Att

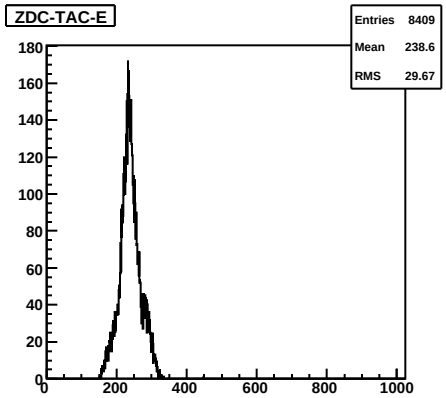

Entries 238915
BBC-L-East ADC Sum
BBC-L-West ADC Sum

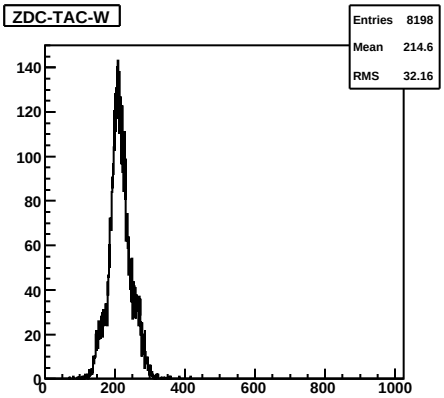

Entries 238915
BBC-S-East ADC Sum
BBC-S-West ADC Sum

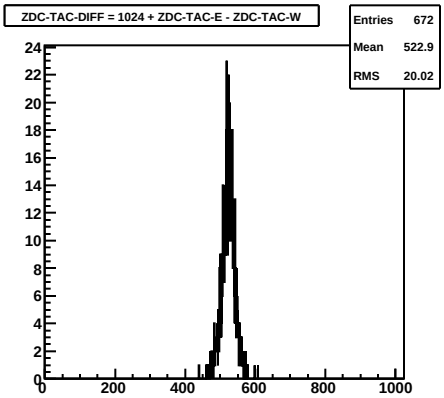

Entries 238915
VPD East ADC Sum
VPD West ADC Sum

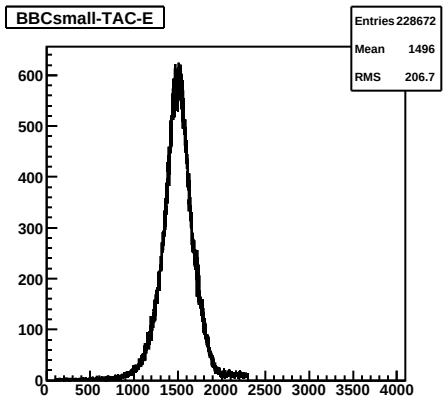

Entries 238915
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

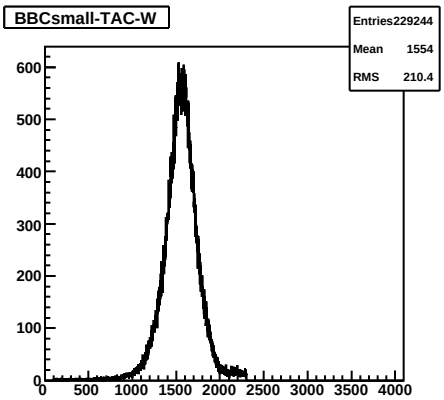

Entries 238915
BBC-L TAC Diff
BCC-S TAC Diff
Entries 238915
BBC-L TAC Diff
VPD TAC Diff
Entries 238915
BBC-L TAC Diff
ZDC TAC Diff
Entries 238915
BBC-S TAC Diff
VPD TAC Diff
Entries 238915
BBC-S TAC Diff
ZDC TAC Diff
Entries 238915
VPD TAC Diff
ZDC TAC Diff

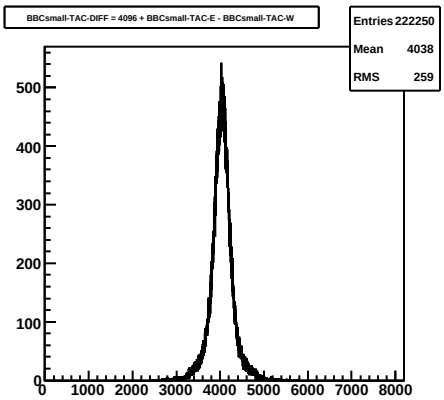

ZDC-TAC-E
Entries 8409
Mean 238.6
RMS 29.67

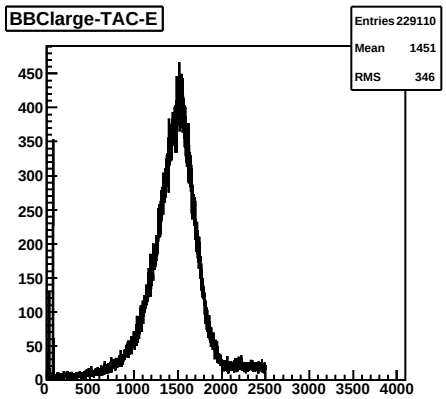

ZDC-TAC-W
Entries 8198
Mean 214.6
RMS 32.16

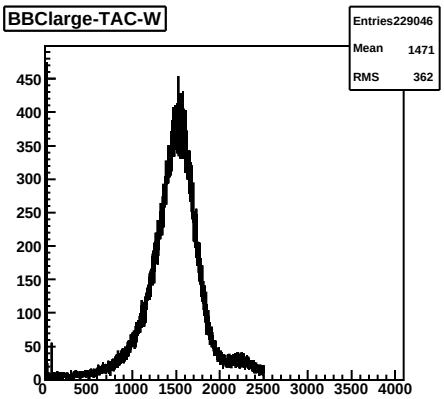

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 672
Mean 522.9
RMS 20.02

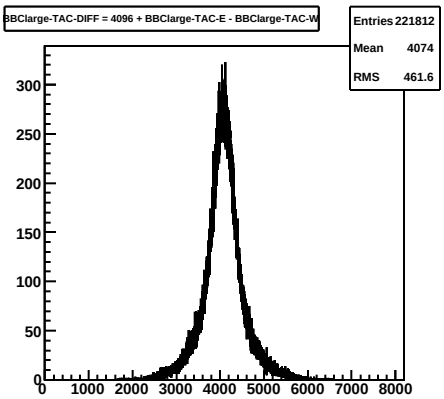

BBCsmall-TAC-E
Entries 228672
Mean 1496
RMS 206.7

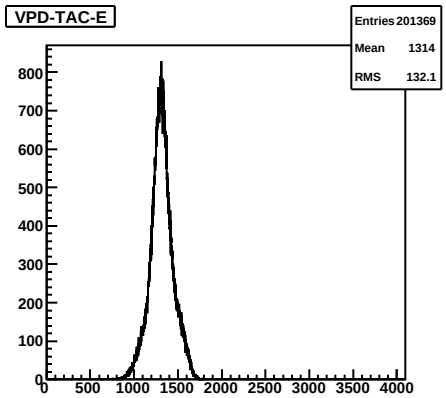

BBCsmall-TAC-W
Entries 229244
Mean 1554
RMS 210.4

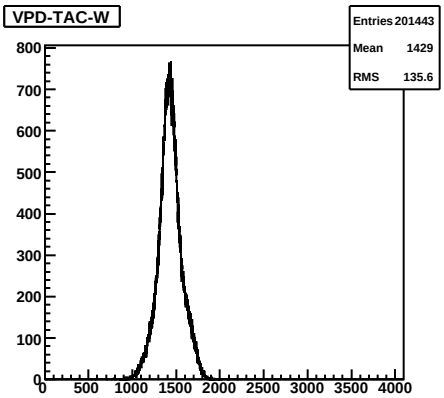

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 222250
Mean 4038
RMS 259

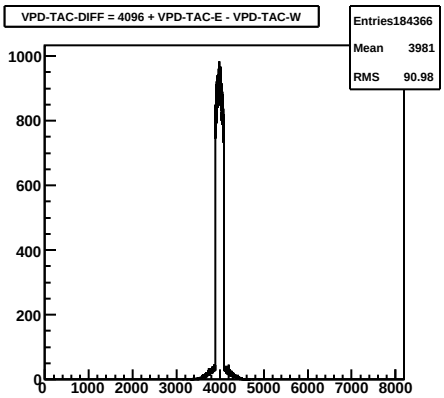

BBClarge-TAC-E
Entries 229110
Mean 1451
RMS 346

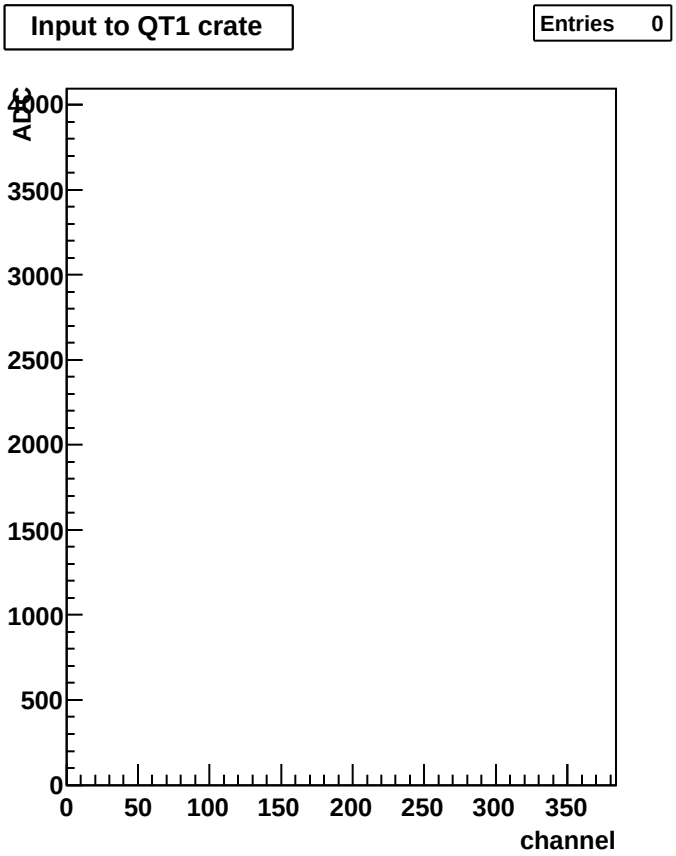

BBClarge-TAC-W
Entries 229046
Mean 1471
RMS 362

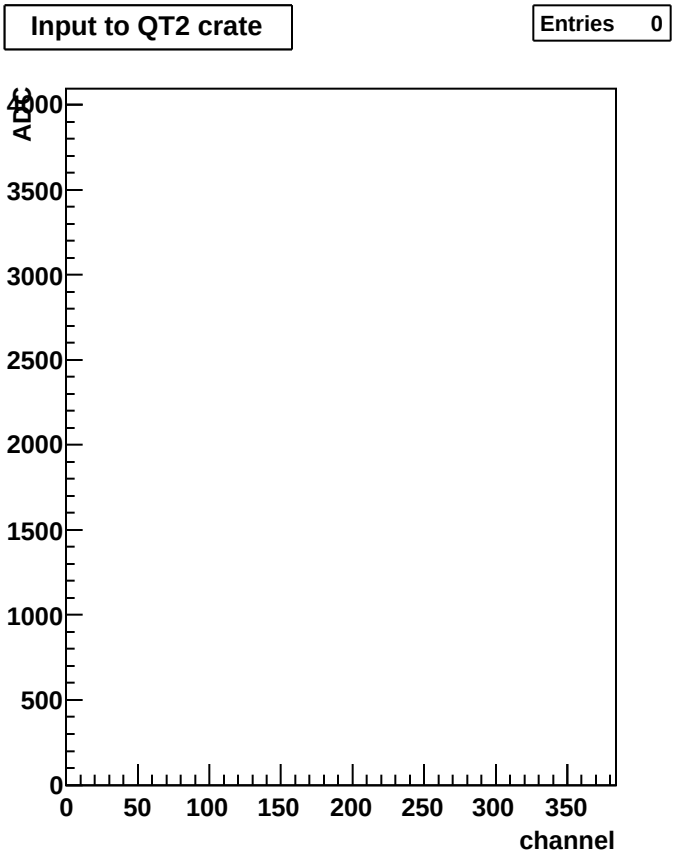

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 221812
Mean 4074
RMS 461.6

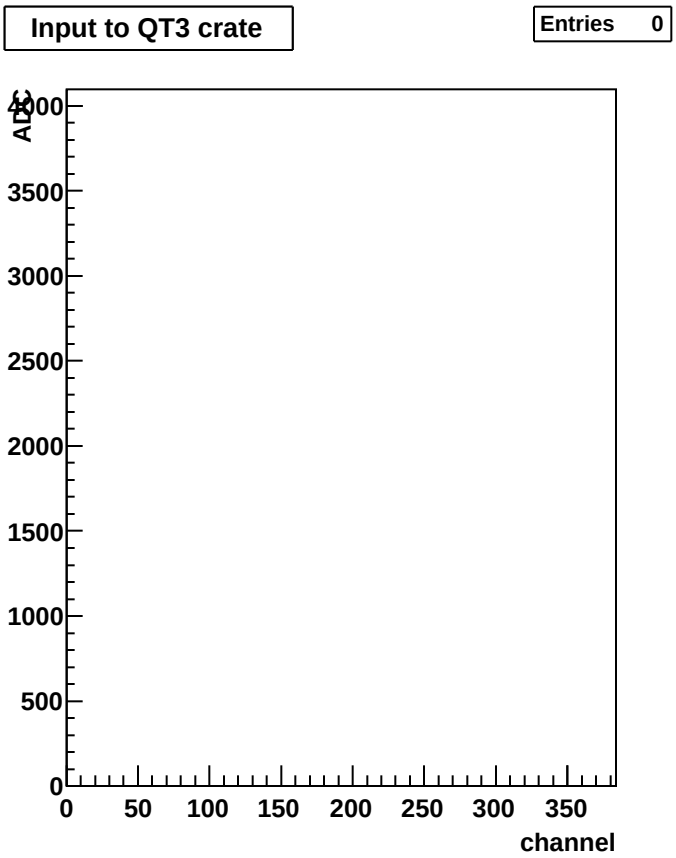

VPD-TAC-E
Entries 201369
Mean 1314
RMS 132.1

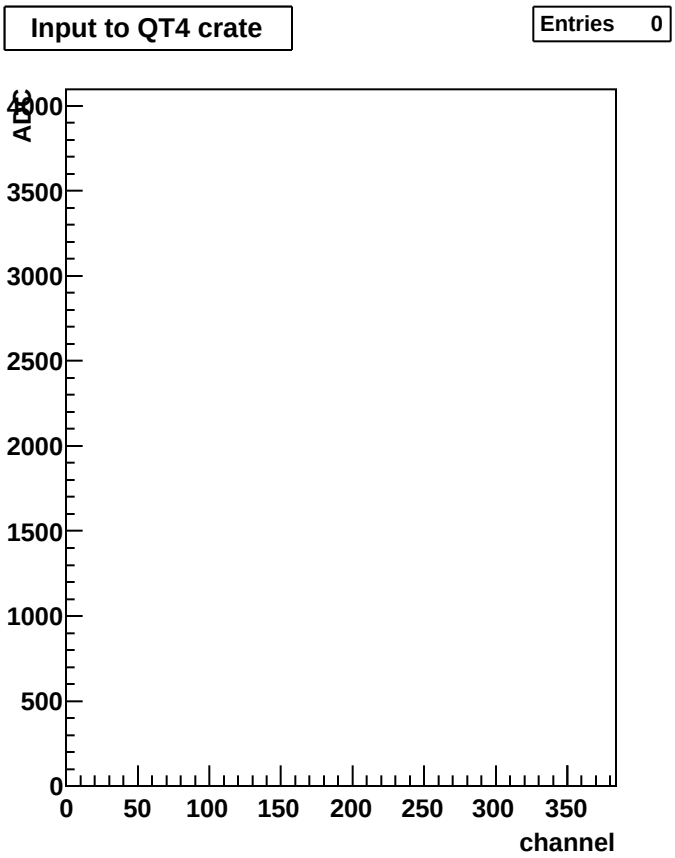

VPD-TAC-W
Entries 201443
Mean 1429
RMS 135.6

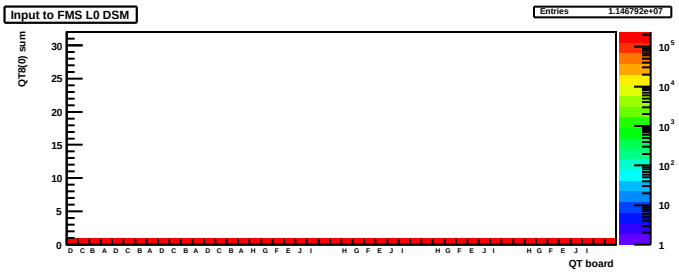

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 184366
Mean 3981
RMS 90.98

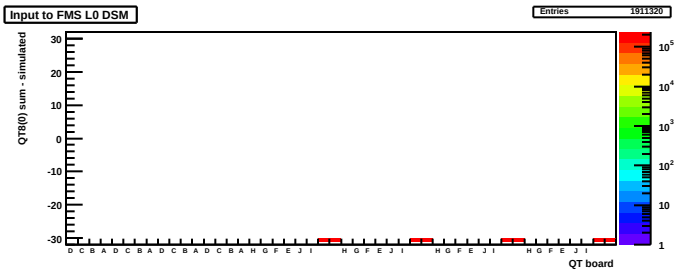

Input to QT1 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

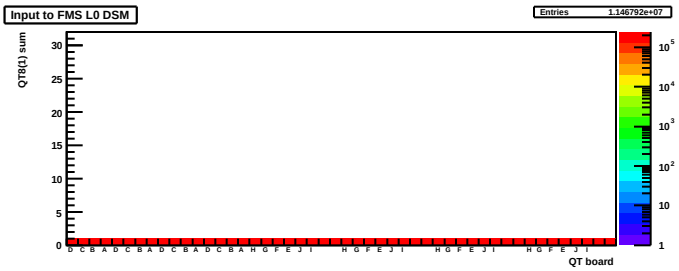

Input to QT2 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

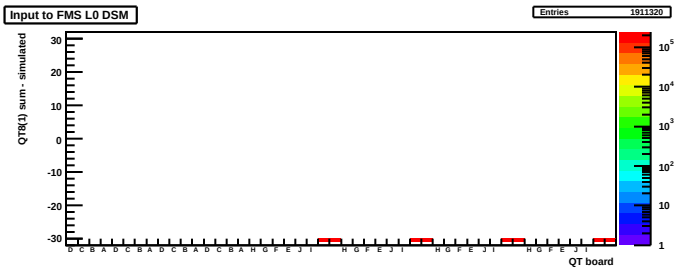

Input to QT3 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

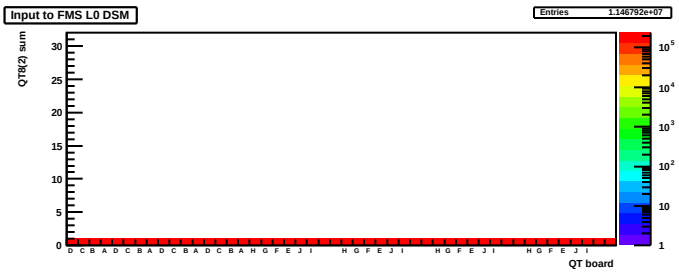

Input to QT4 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

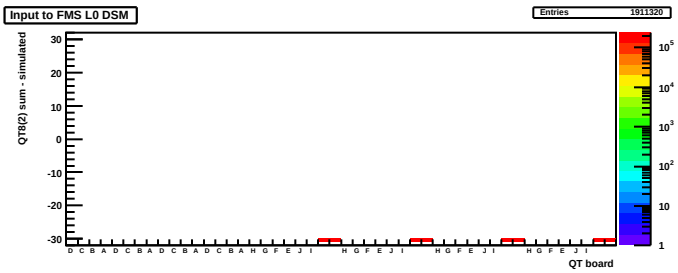

Input to FMS L0 DSM
Entries 1.146792e+07
QT8(0) sum
QT board

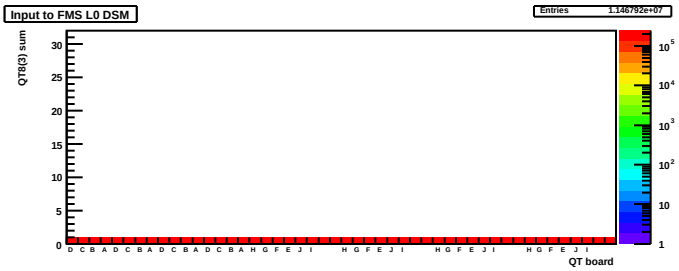

Input to FMS L0 DSM
Entries 1911320
QT8(0) sum - simulated
QT board

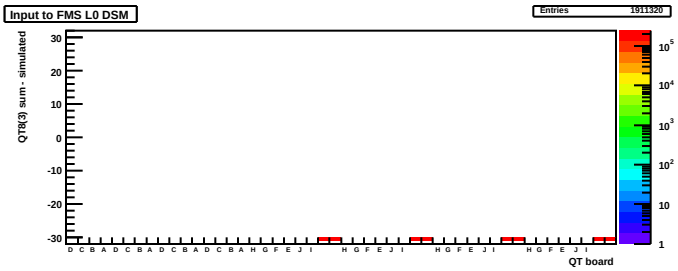

Input to FMS L0 DSM
Entries 1.146792e+07
QT8(1) sum
QT board

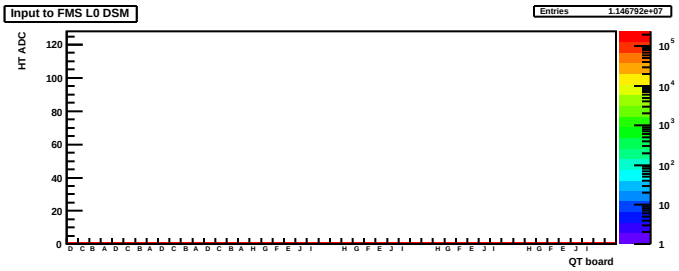

Input to FMS L0 DSM
Entries 1911320
QT8(1) sum - simulated
QT board

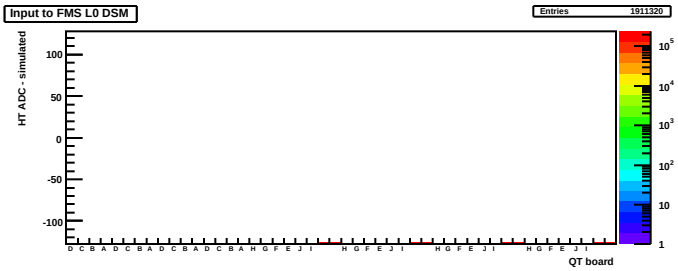

Input to FMS L0 DSM
Entries 1.146792e+07
QT8(2) sum
QT board

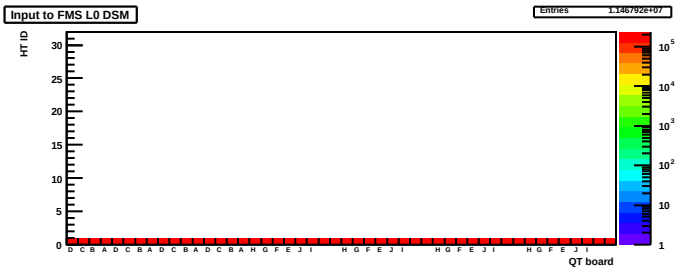

Input to FMS L0 DSM
Entries 1911320
QT8(2) sum - simulated
QT board

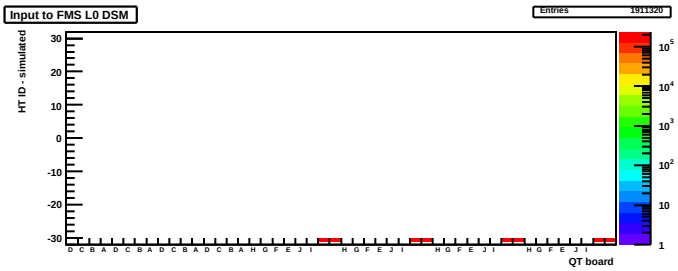

Input to FMS L0 DSM
Entries 1.146792e+07
QT8(3) sum
QT board

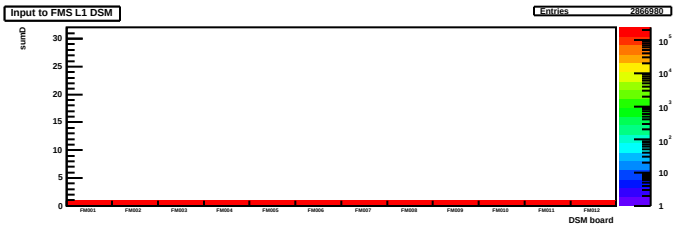

Input to FMS L0 DSM
Entries 1911320
QT8(3) sum - simulated
QT board

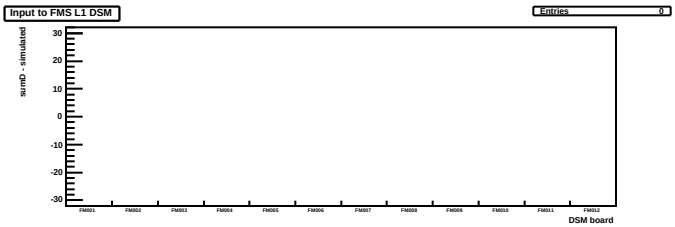

Input to FMS L0 DSM
Entries 1.146792e+07
HT ADC
QT board

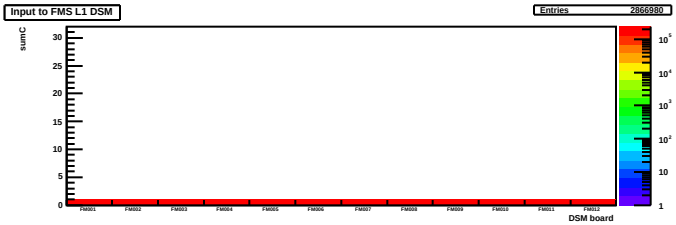

Input to FMS L0 DSM
Entries 1911320
HT ADC - simulated
QT board

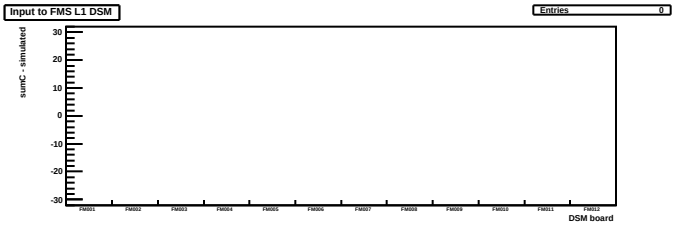

Input to FMS L0 DSM
Entries 1.146792e+07
HT ID
QT board

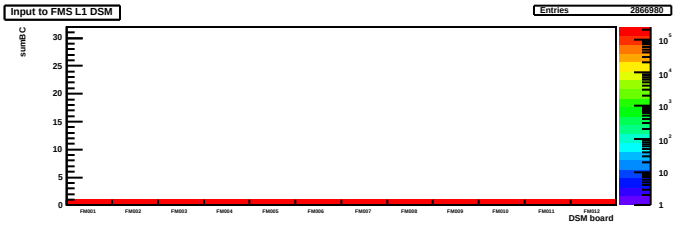

Input to FMS L0 DSM
Entries 1911320
HT ID - simulated
QT board

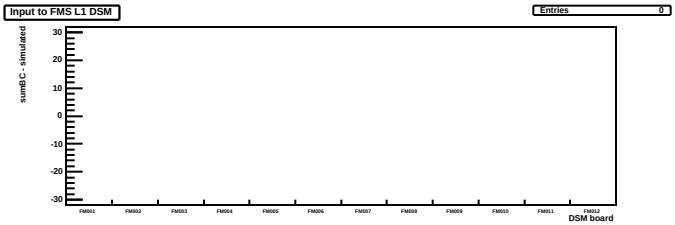

Input to FMS L1 DSM
Entries 2866980
sumD
DSM board

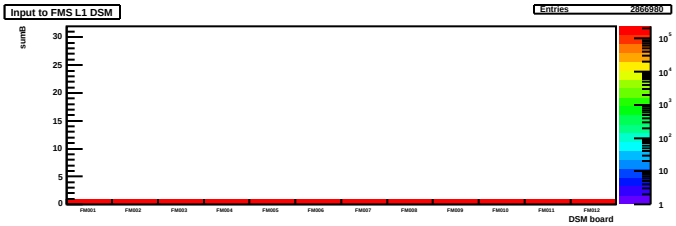

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

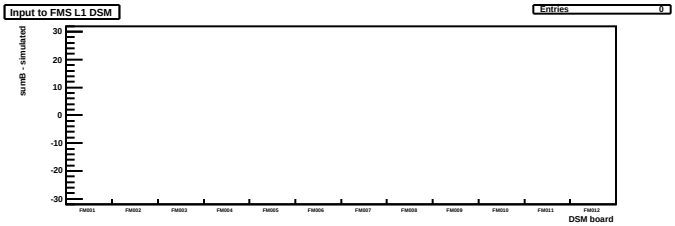

Input to FMS L1 DSM
Entries 2866980
sumC
DSM board

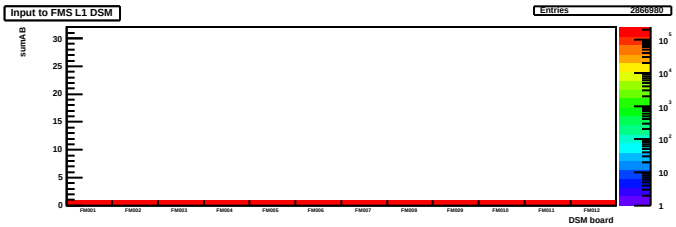

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

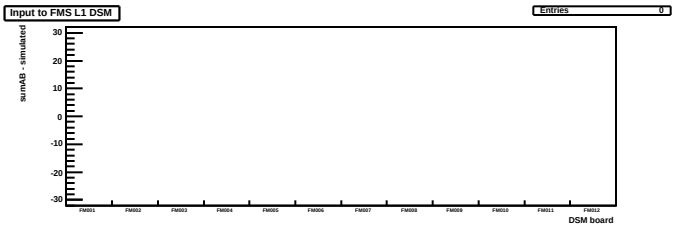

Input to FMS L1 DSM
Entries 2866980
sumBC
DSM board

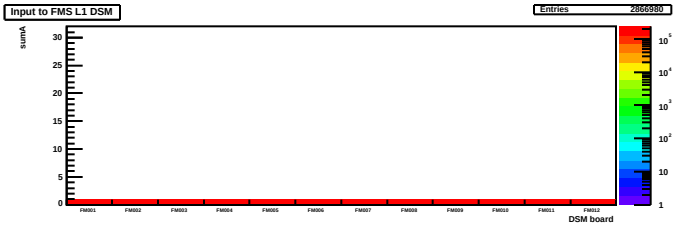

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

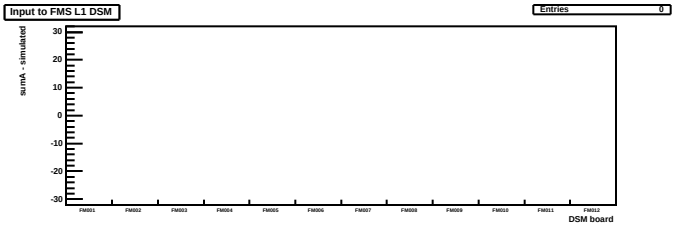

Input to FMS L1 DSM
Entries 2866980
sumB
DSM board

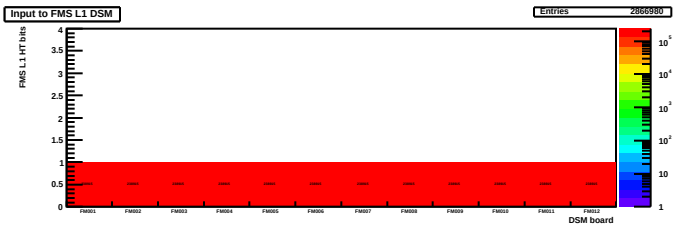

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

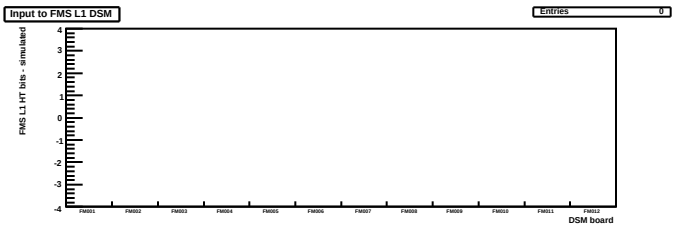

Input to FMS L1 DSM
Entries 2866980
sumAB
DSM board

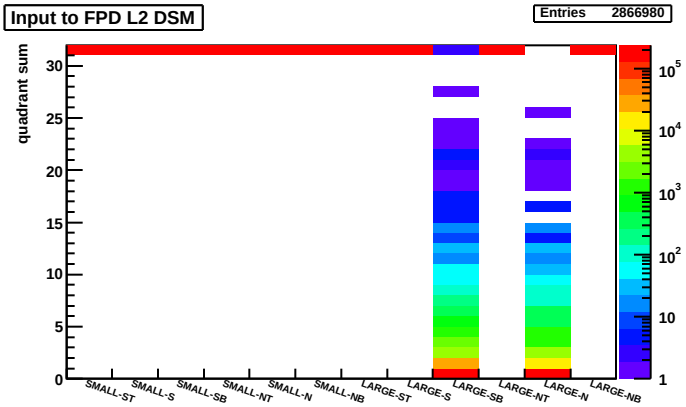

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

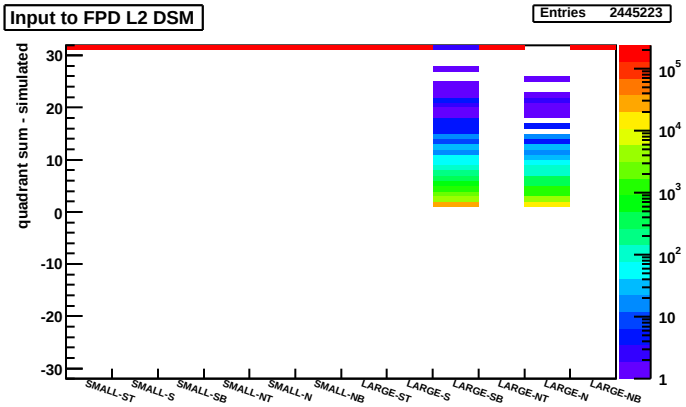

Input to FMS L1 DSM
Entries 2866980
sumA
DSM board

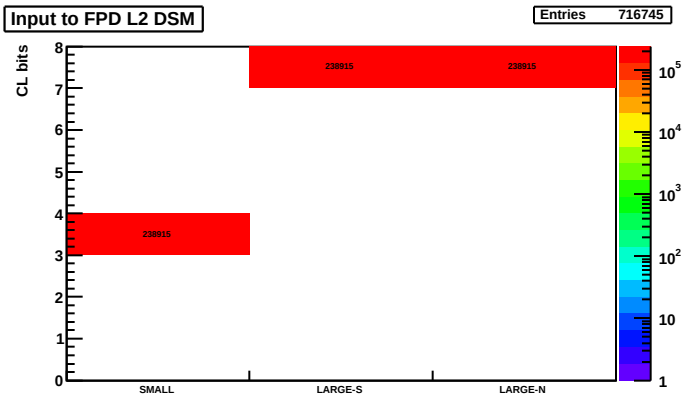

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

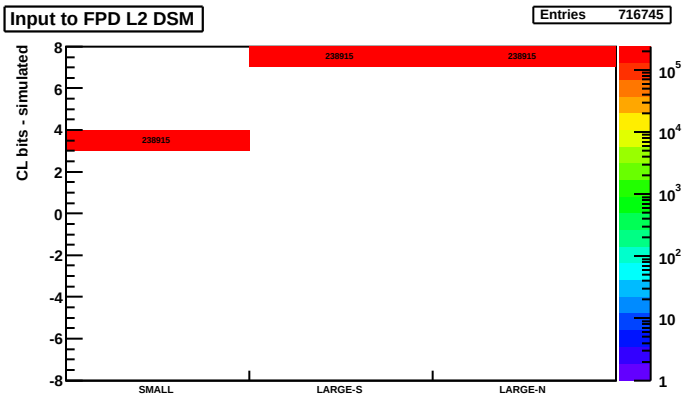

Input to FMS L1 DSM
Entries 2866980
FMS L1 HT bits
DSM board

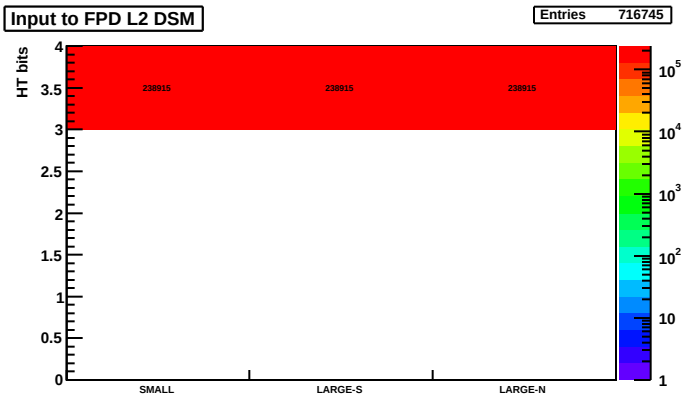

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

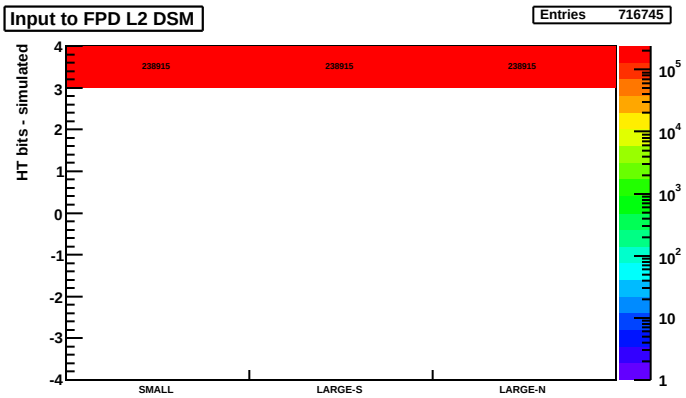

Input to FPD L2 DSM
Entries 2866980
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

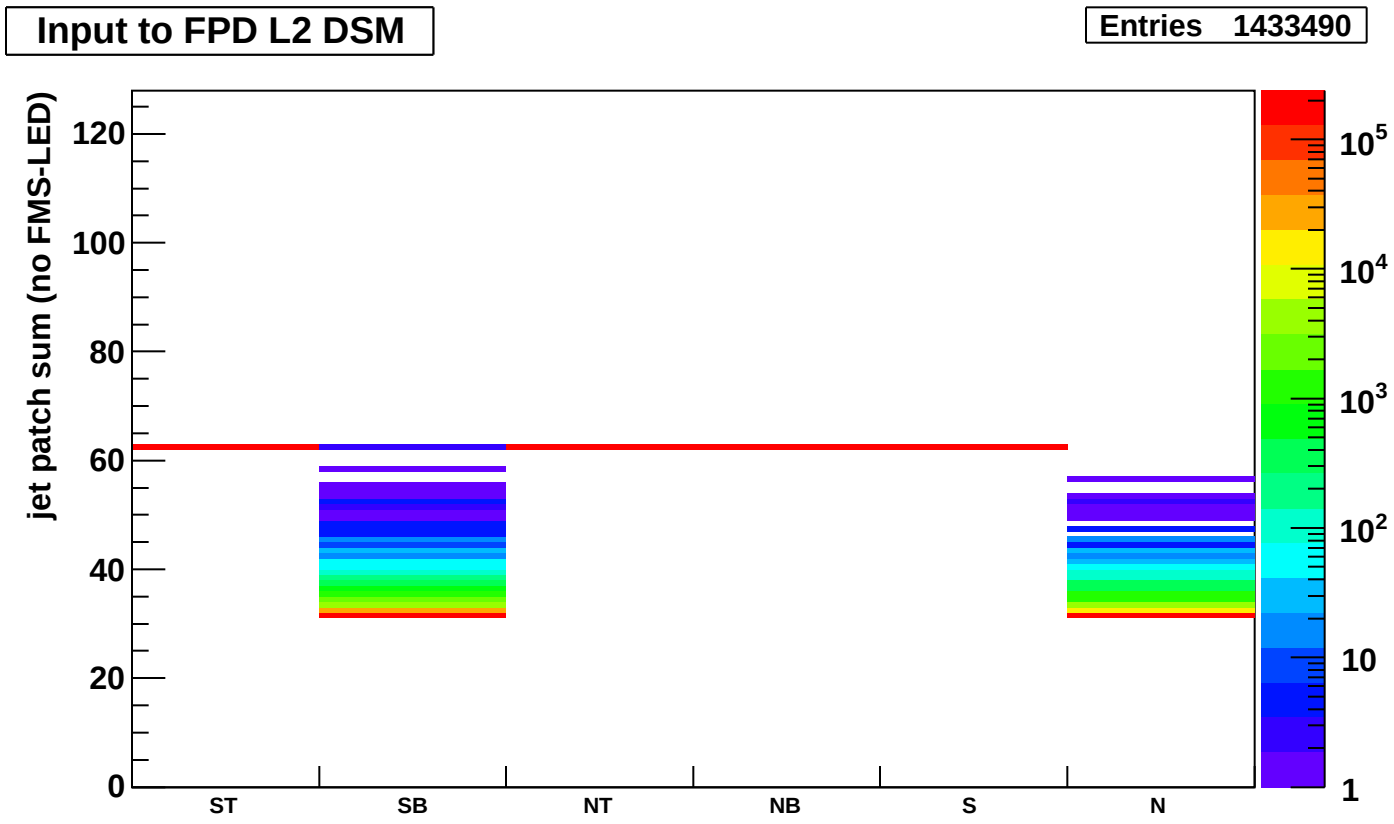

Input to FPD L2 DSM
Entries 2445223
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

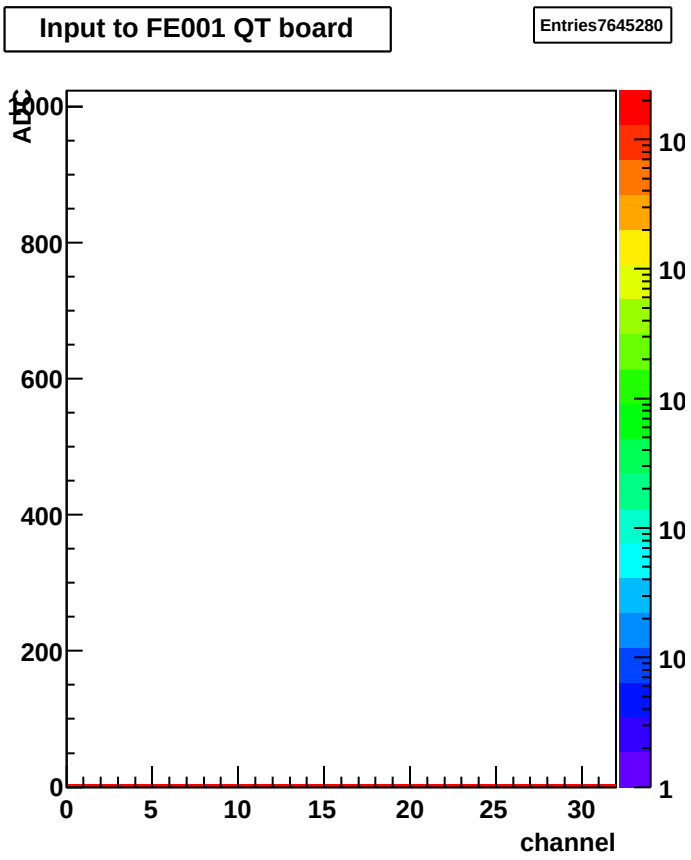

Input to FPD L2 DSM
Entries 716745
CL bits
238915
238915
238915
SMALL
LARGE-S
LARGE-N

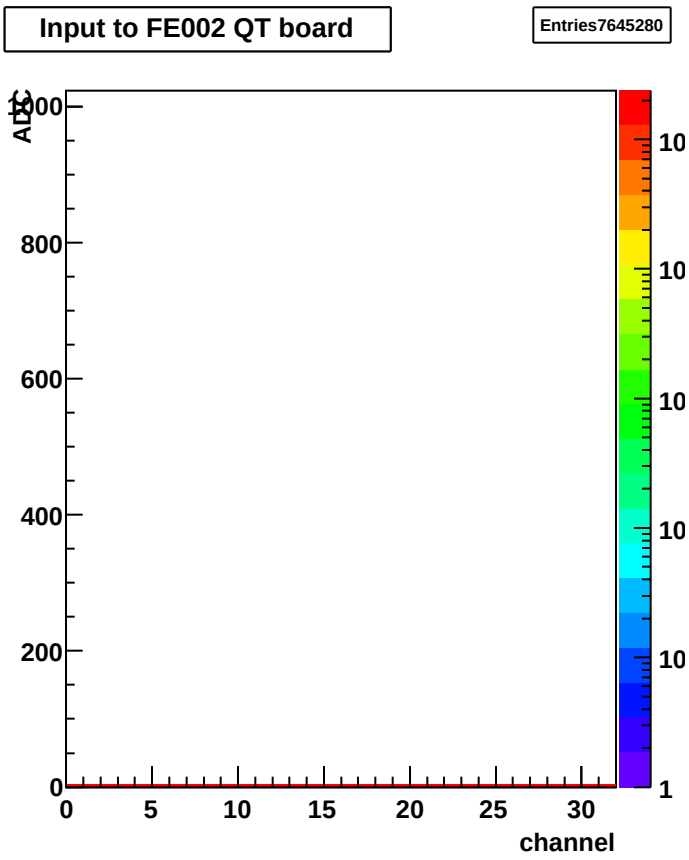

Input to FPD L2 DSM
Entries 716745
CL bits - simulated
238915
238915
238915
SMALL
LARGE-S
LARGE-N

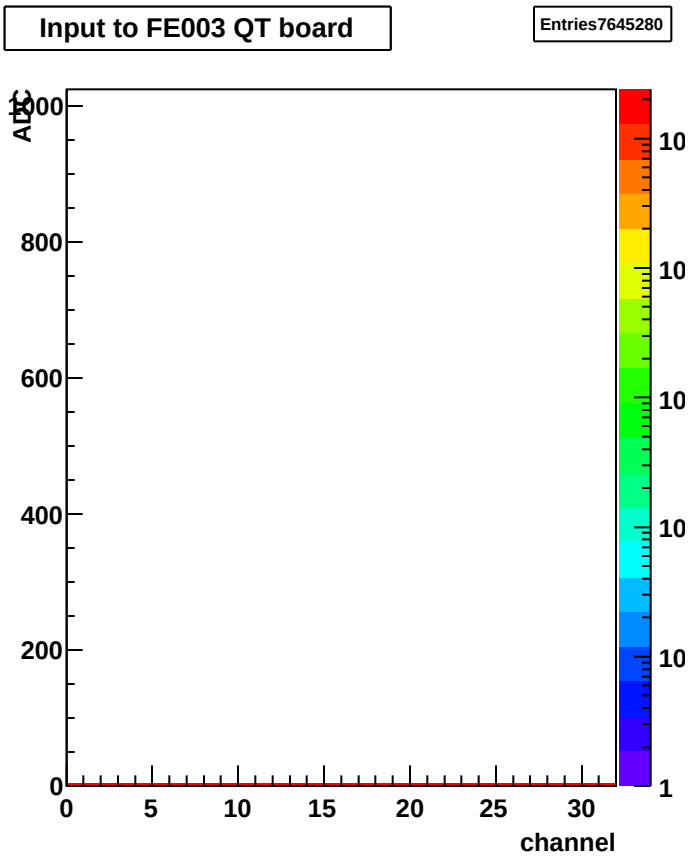

Input to FPD L2 DSM
Entries 716745
HT bits
238915
238915
238915
SMALL
LARGE-S
LARGE-N

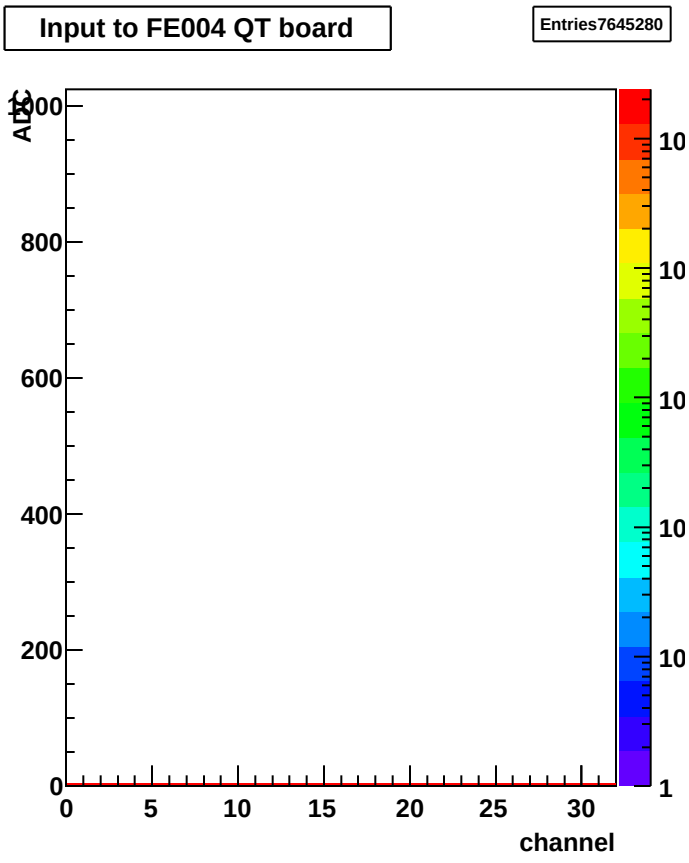

Input to FPD L2 DSM
Entries 716745
HT bits - simulated
238915
238915
238915
SMALL
LARGE-S
LARGE-N

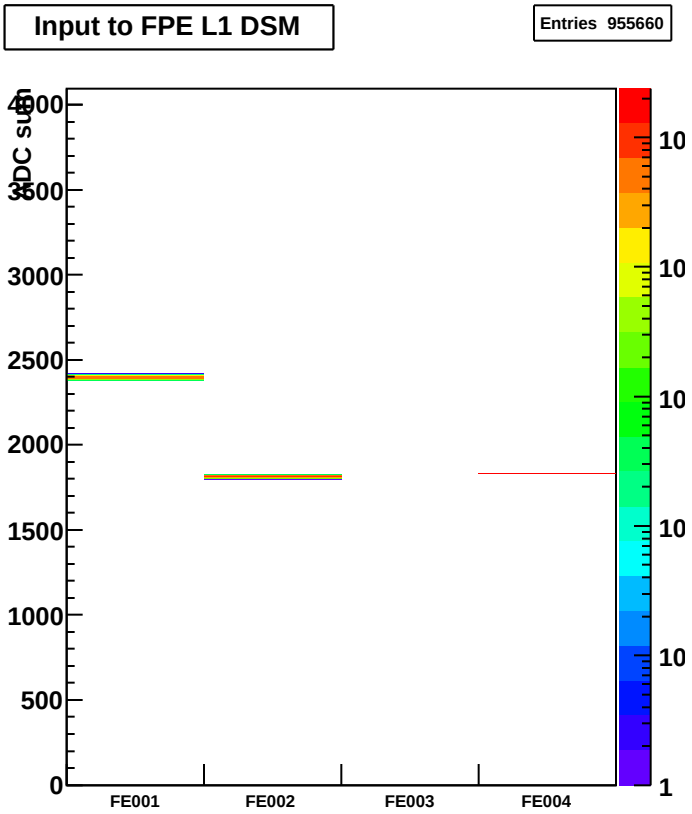

Input to FPD L2 DSM
Entries 1433490
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries7645280
ADC
channel

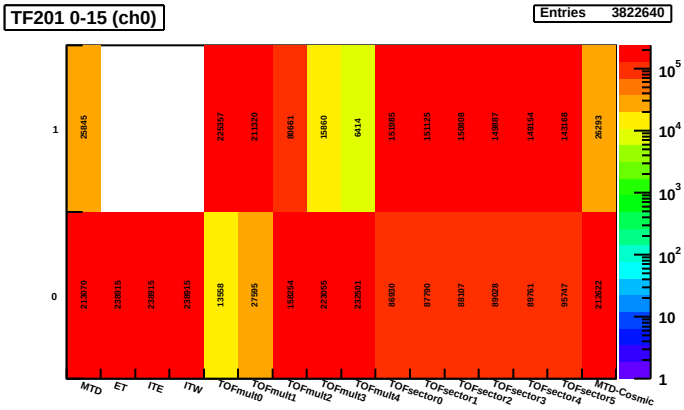

Input to FE002 QT board
Entries7645280
ADC
channel


Input to FE003 QT board
Entries7645280
ADC
channel

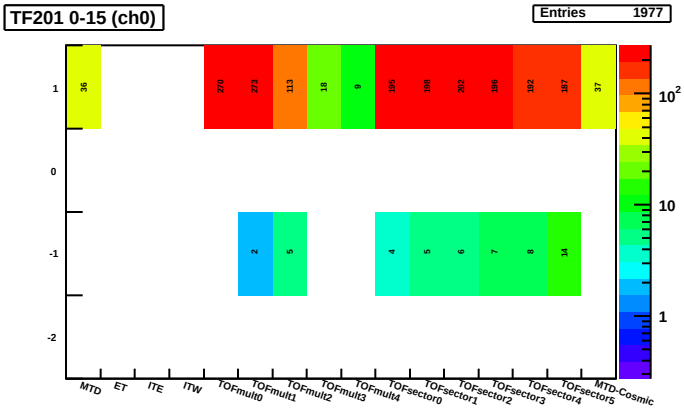

Input to FE004 QT board
Entries7645280
ADC
channel


Input to FPE L1 DSM
Entries 955660
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

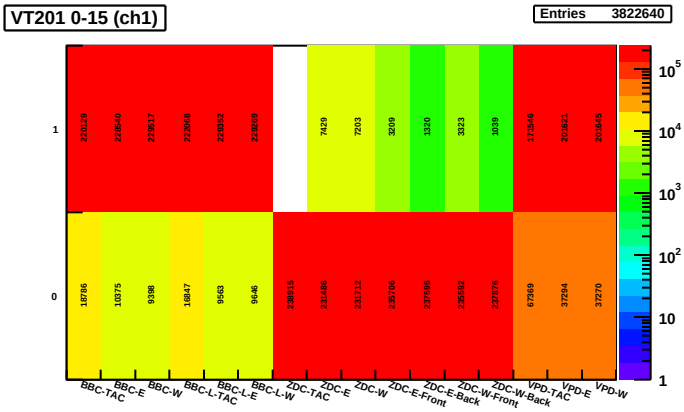
TF201 0-15 (ch0)



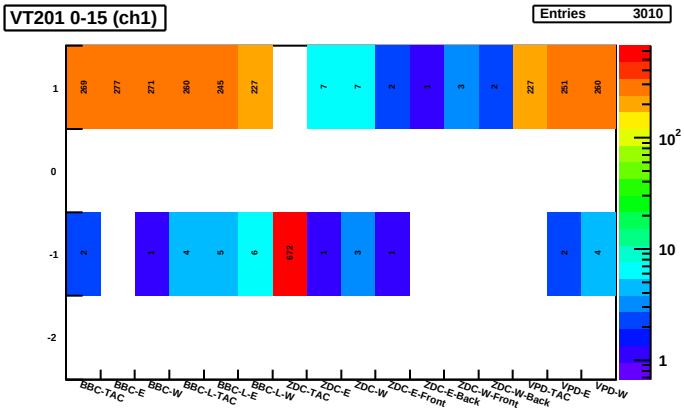
TF201 0-15 (ch0)



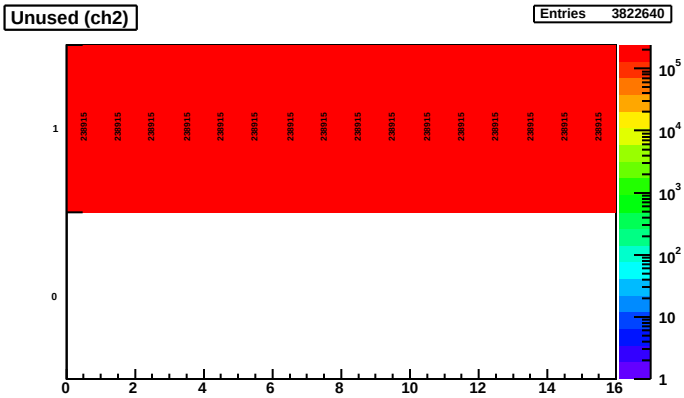
VT201 0-15 (ch1)

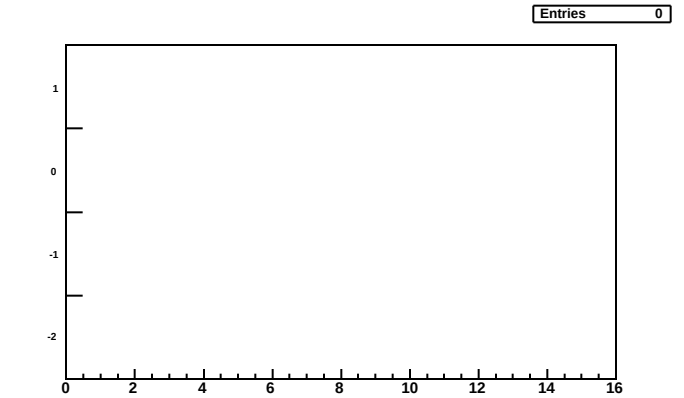


VT201 0-15 (ch1)

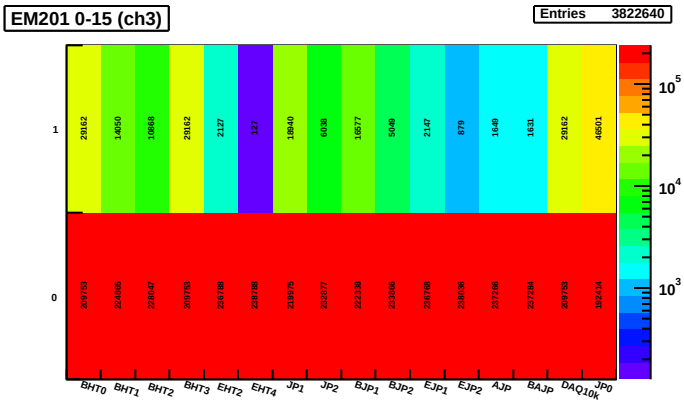


Unused (ch2)

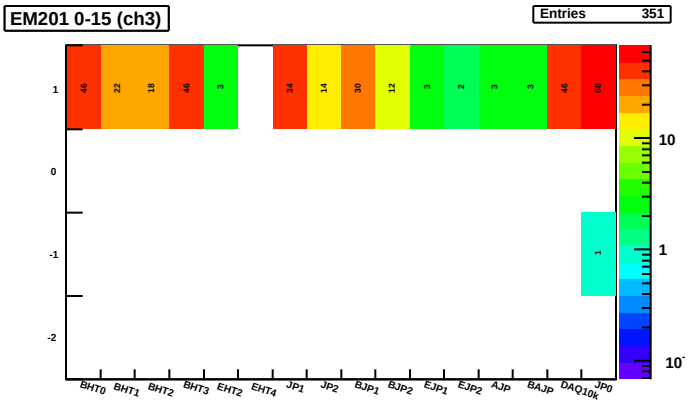




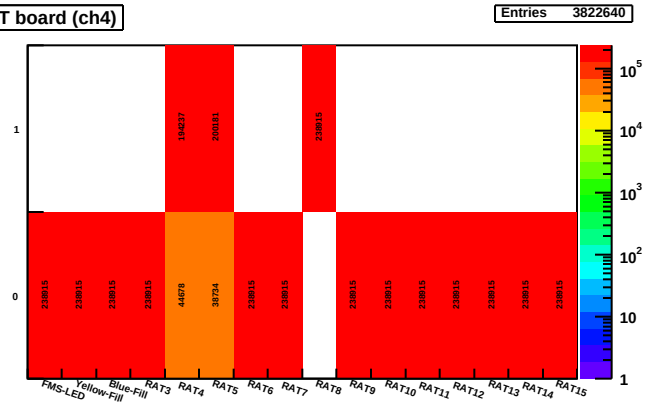
EM201 0-15 (ch3)

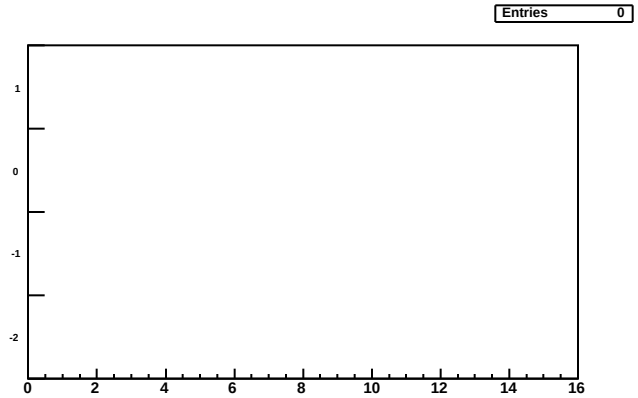


EM201 0-15 (ch3)

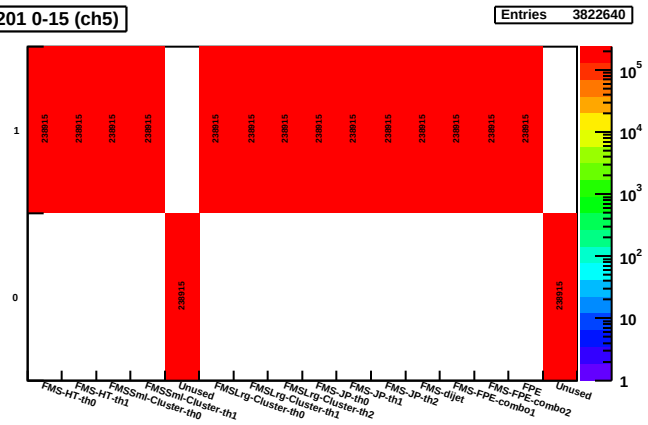


RAT board (ch4)

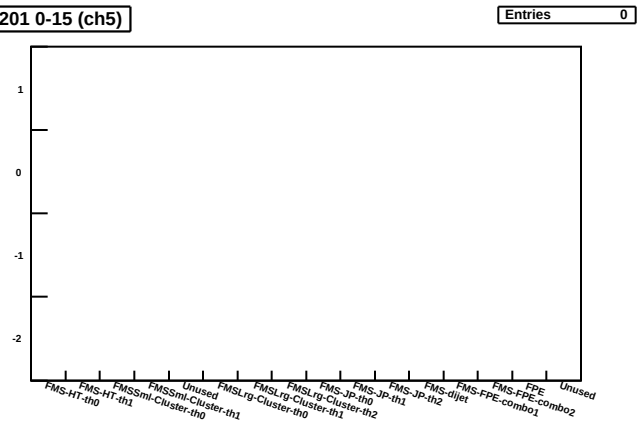




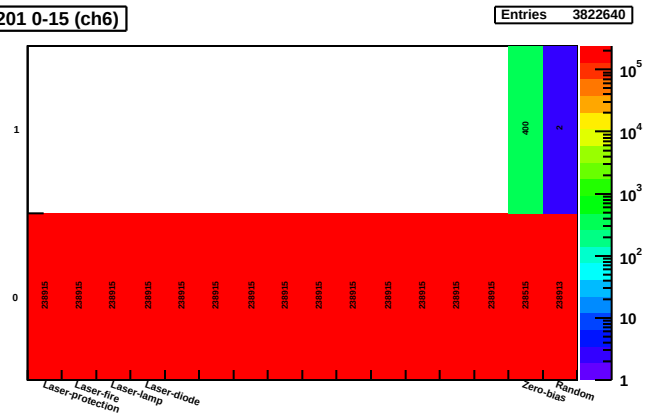
FP201 0-15 (ch5)

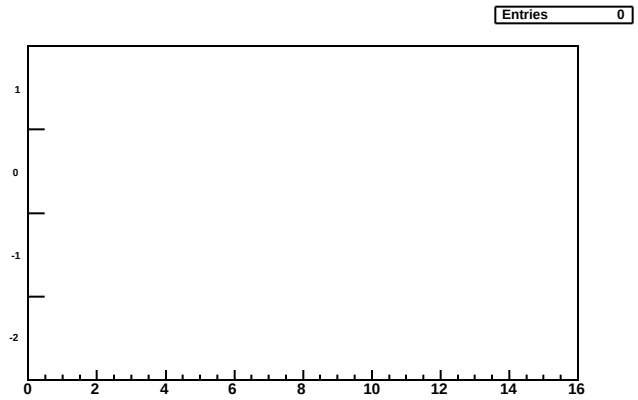
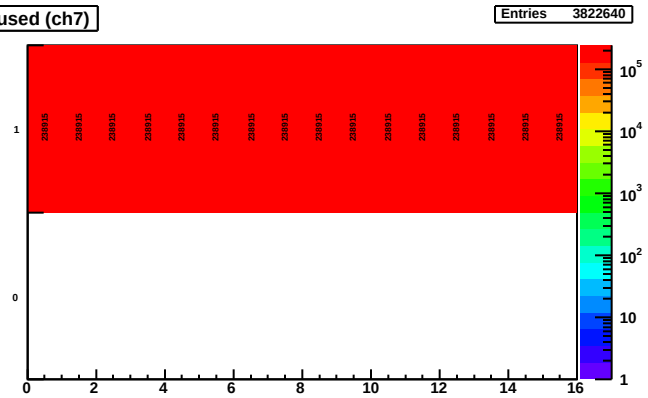
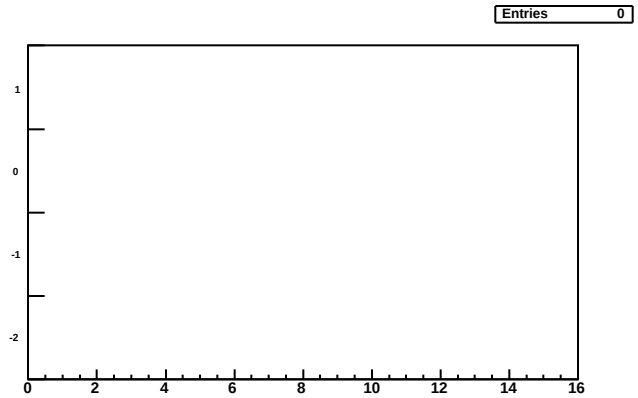


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)