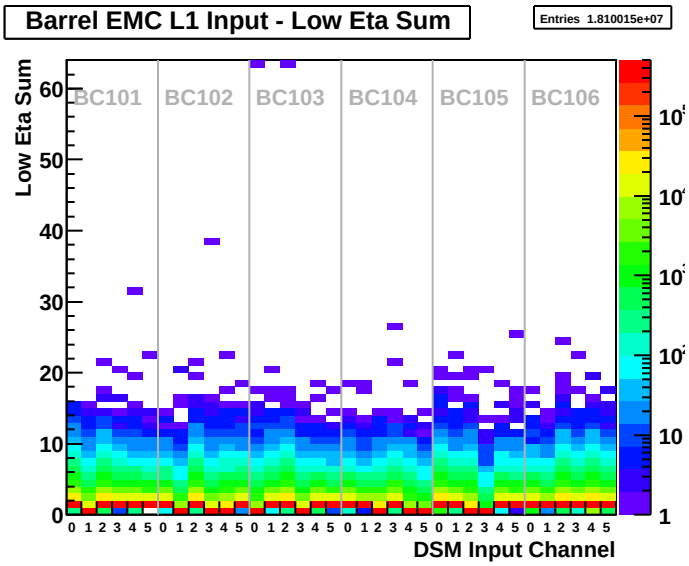
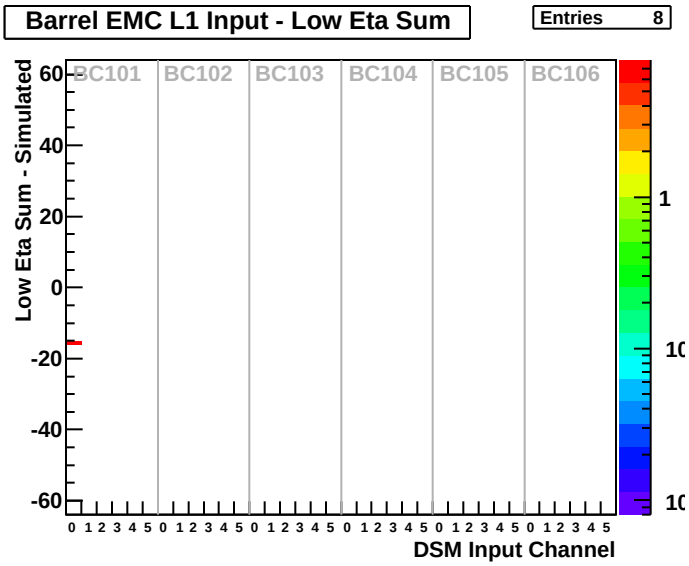
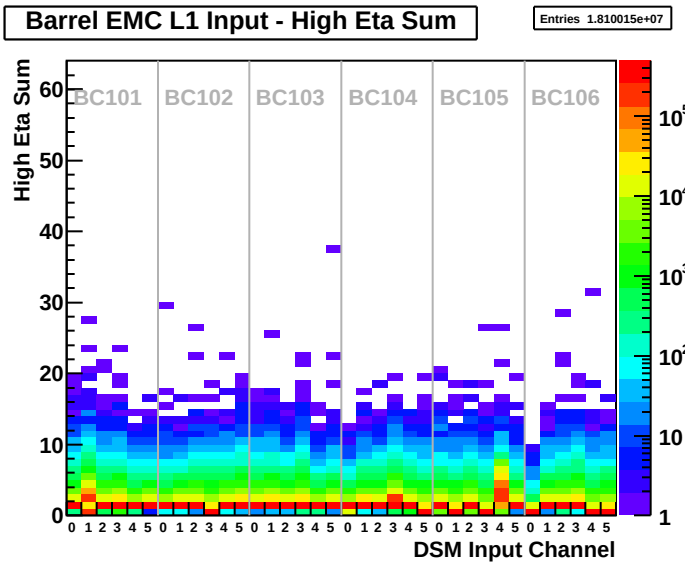
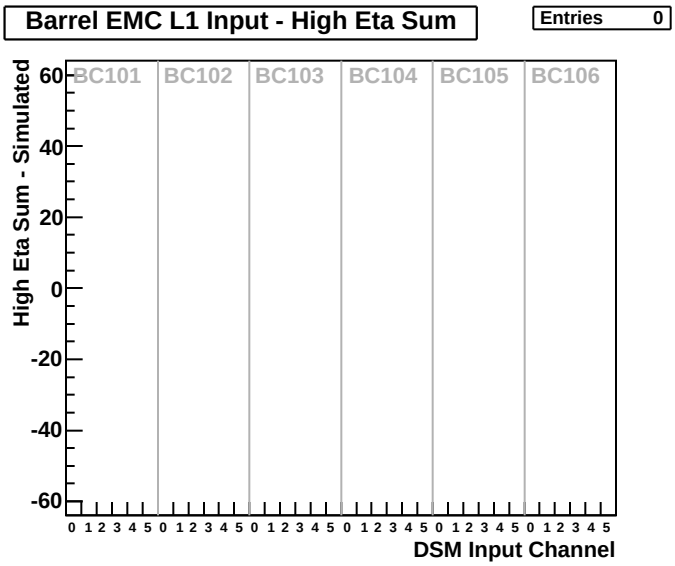
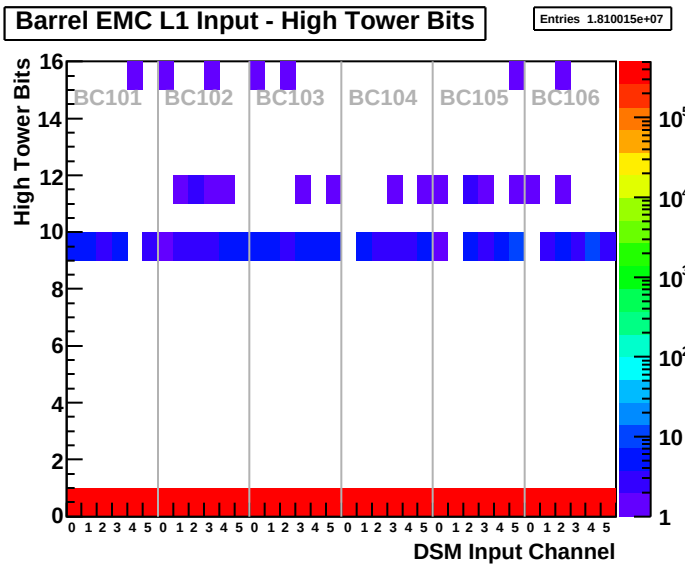
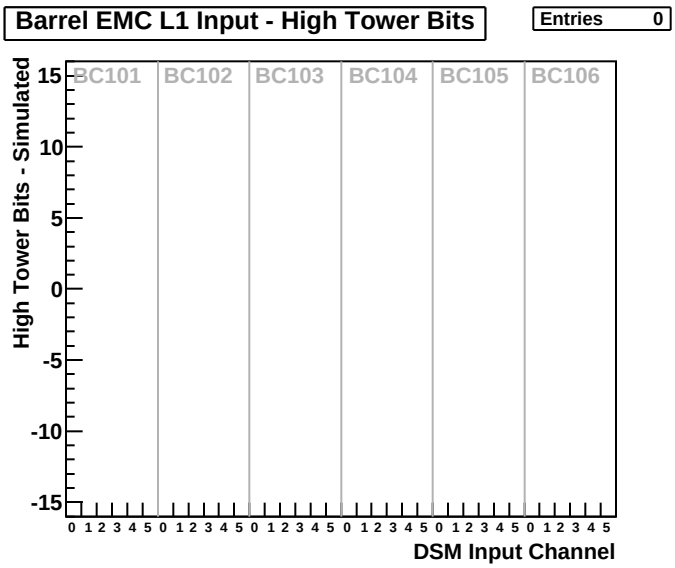

Barrel EMC L1 Input - Low Eta Sum
Entries 1.810015e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

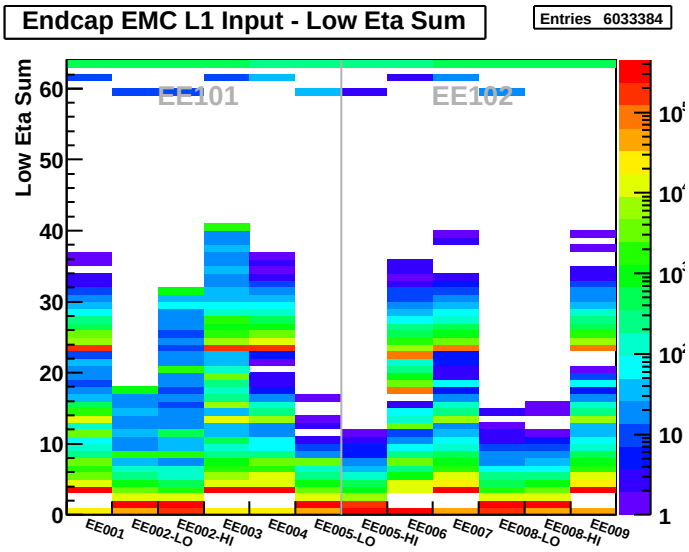

Barrel EMC L1 Input - Low Eta Sum
Entries 8
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

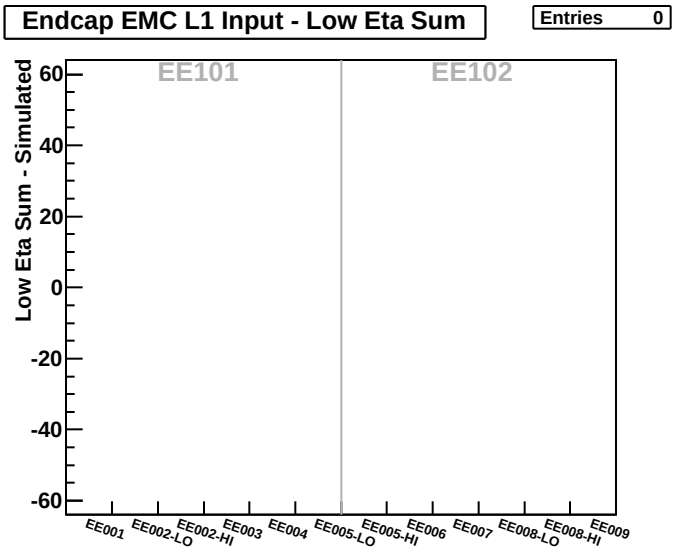

Barrel EMC L1 Input - High Eta Sum
Entries 1.810015e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

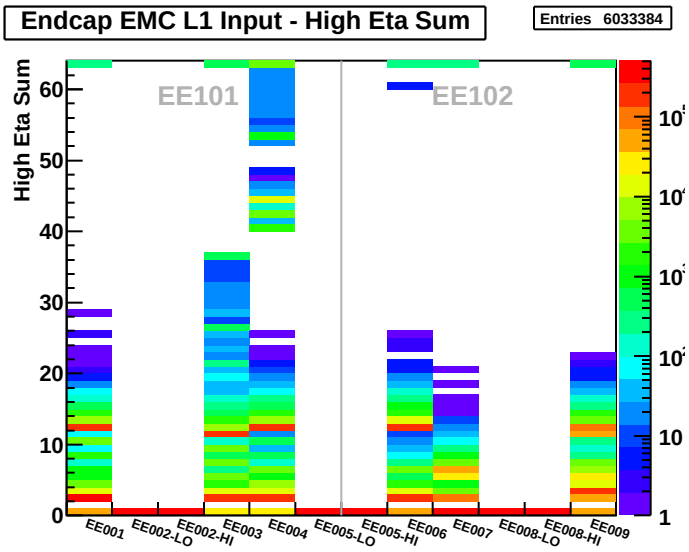

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

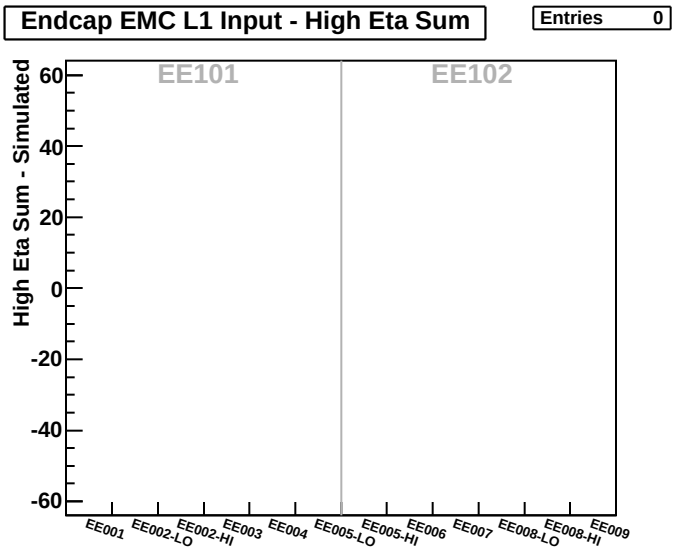

Barrel EMC L1 Input - High Tower Bits
Entries 1.810015e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

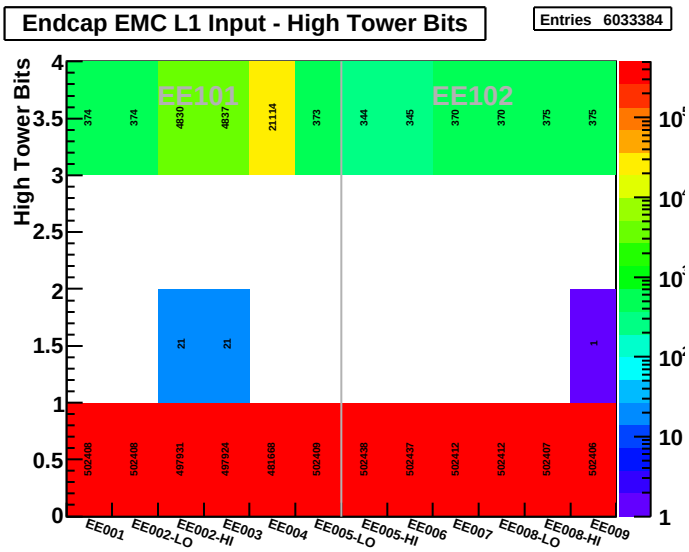

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

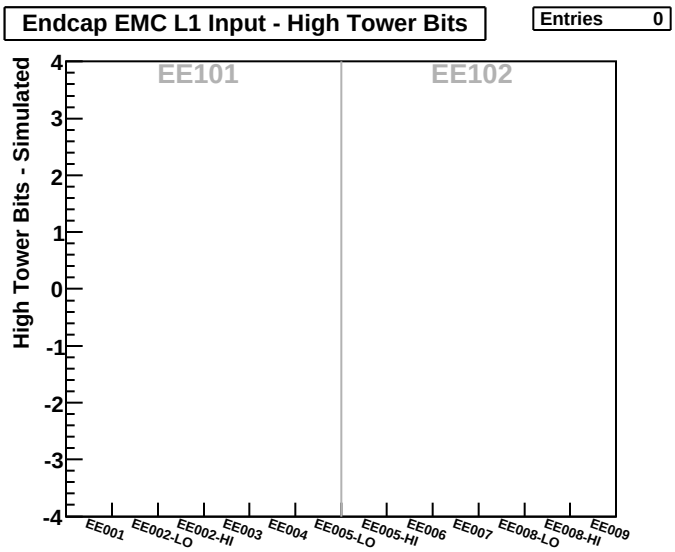

Endcap EMC L1 Input - Low Eta Sum
Entries 6033384
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

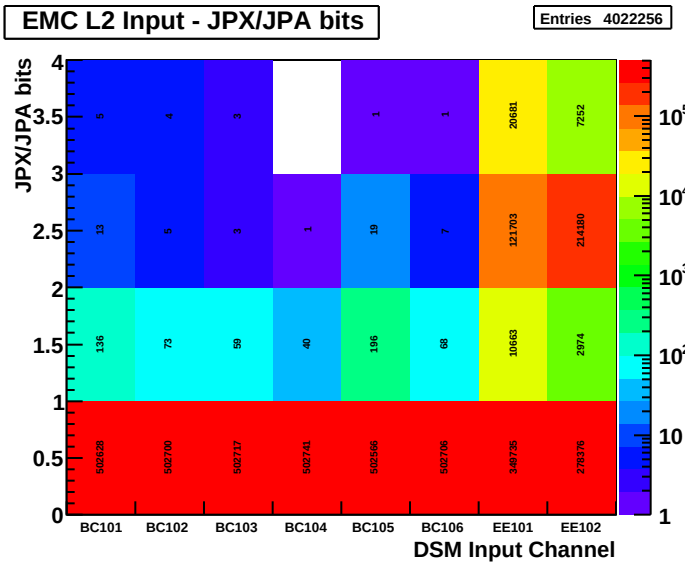

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

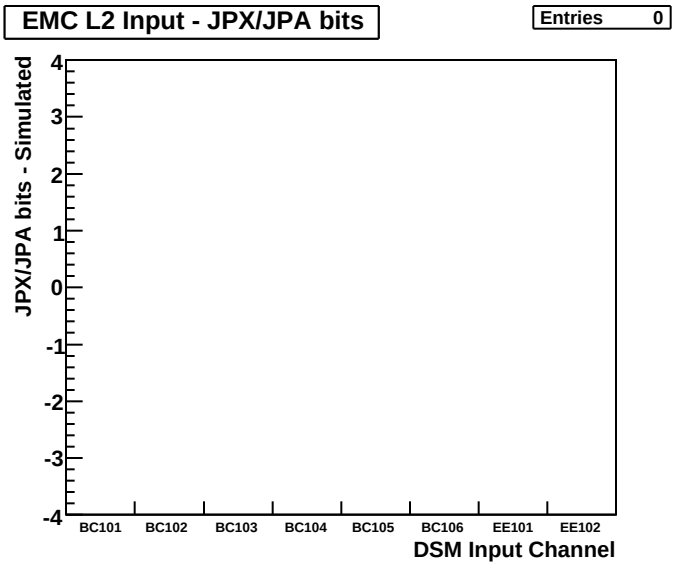

Endcap EMC L1 Input - High Eta Sum
Entries 6033384
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

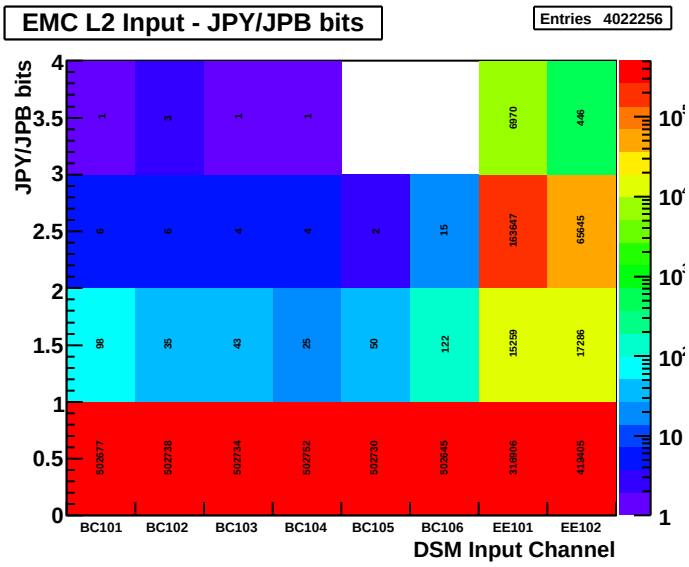

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

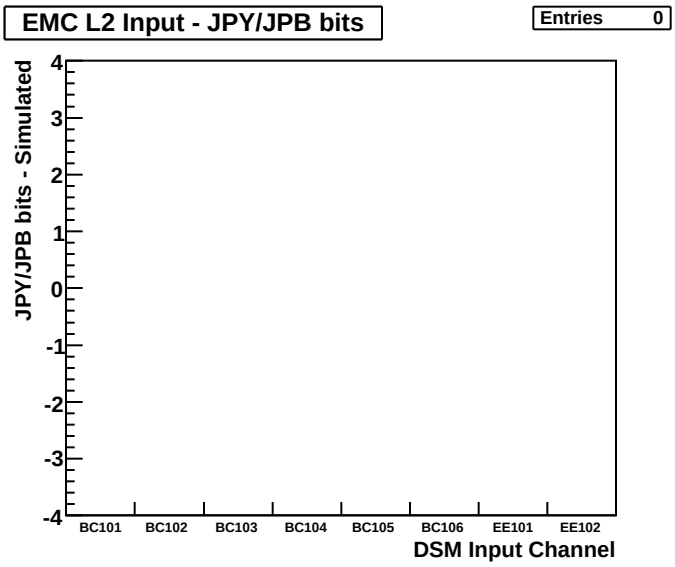

Endcap EMC L1 Input - High Tower Bits
Entries 6033384
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

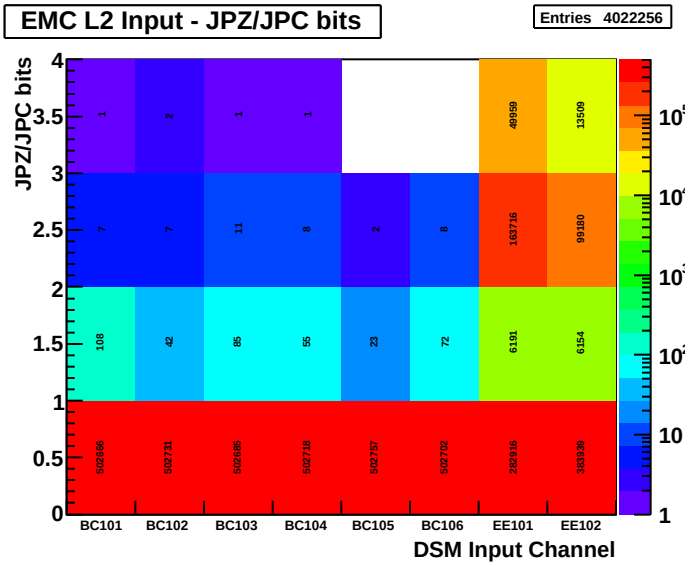

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

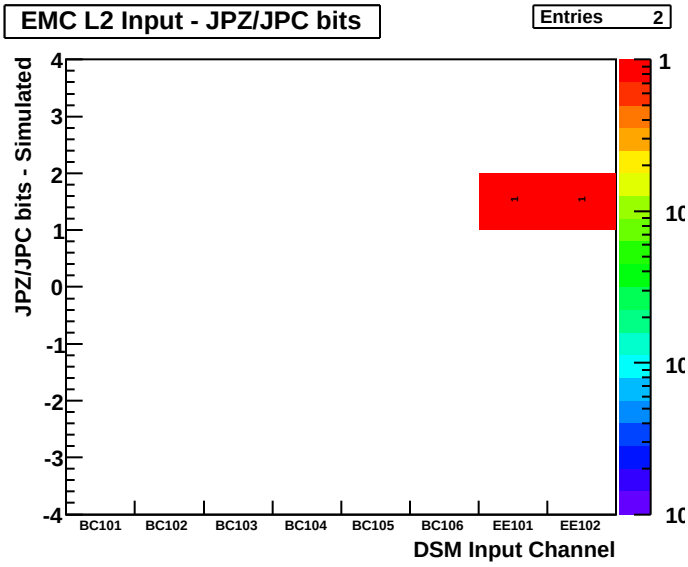

EMC L2 Input - JPX/JPA bits
Entries 4022256
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

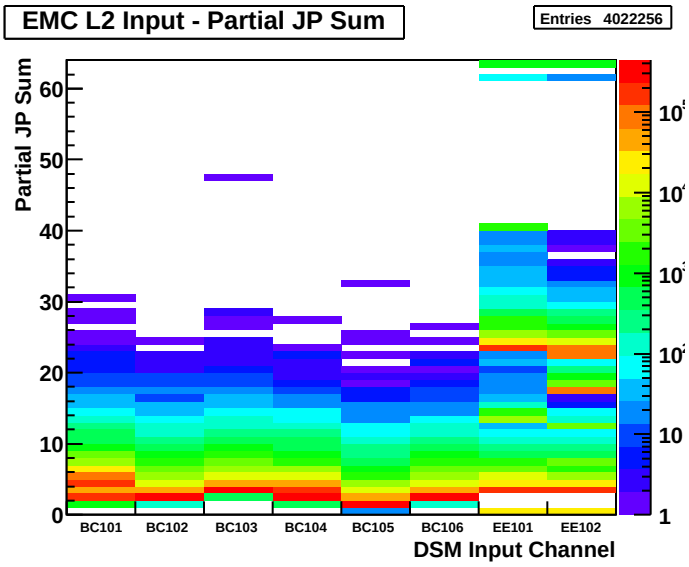

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

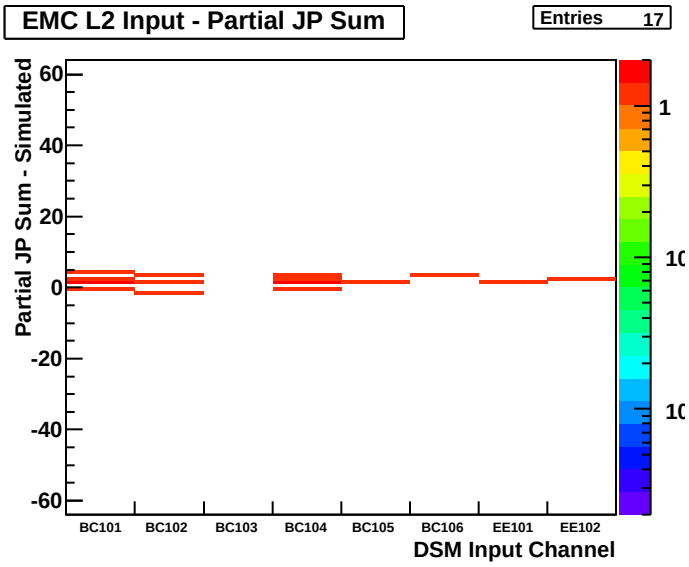

EMC L2 Input - JPY/JPB bits
Entries 4022256
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

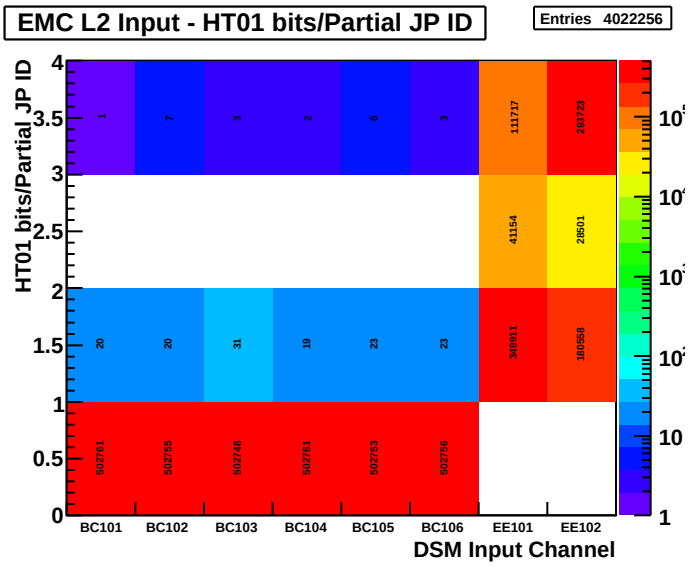

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

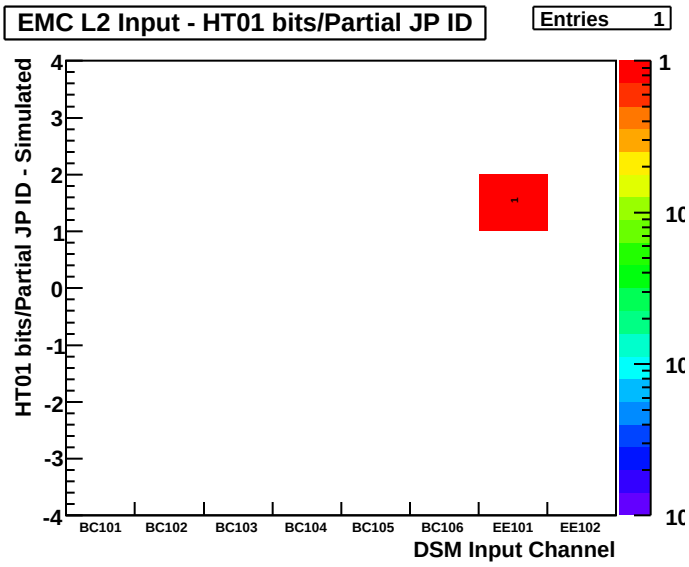

EMC L2 Input - JPZ/JPC bits
Entries 4022256
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

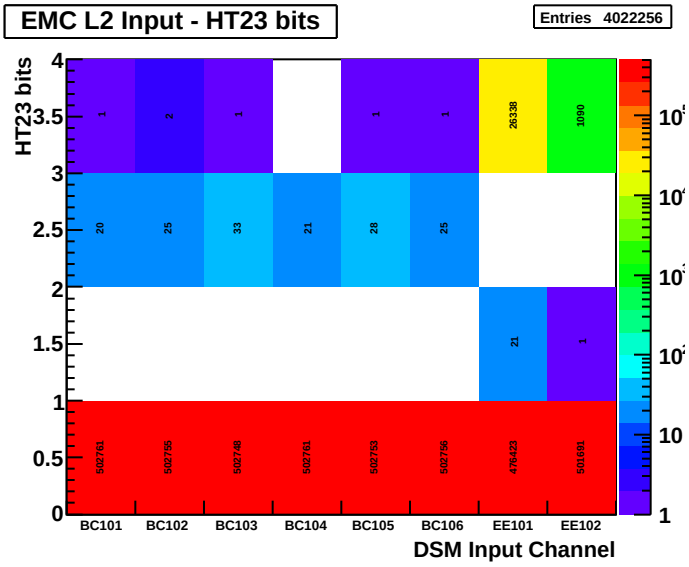

EMC L2 Input - JPZ/JPC bits
Entries 2
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

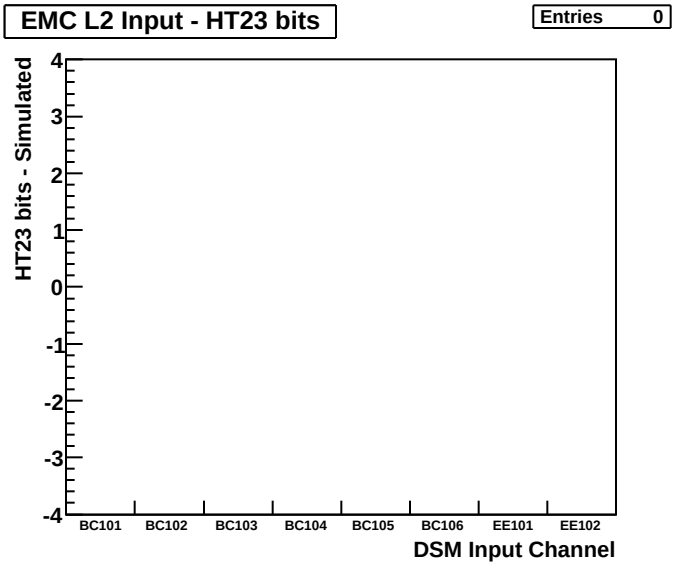

EMC L2 Input - Partial JP Sum
Entries 4022256
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

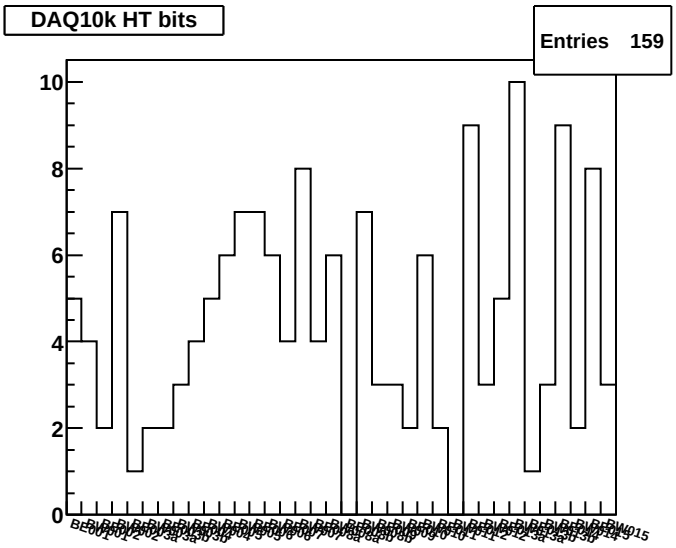

EMC L2 Input - Partial JP Sum
Entries 17
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

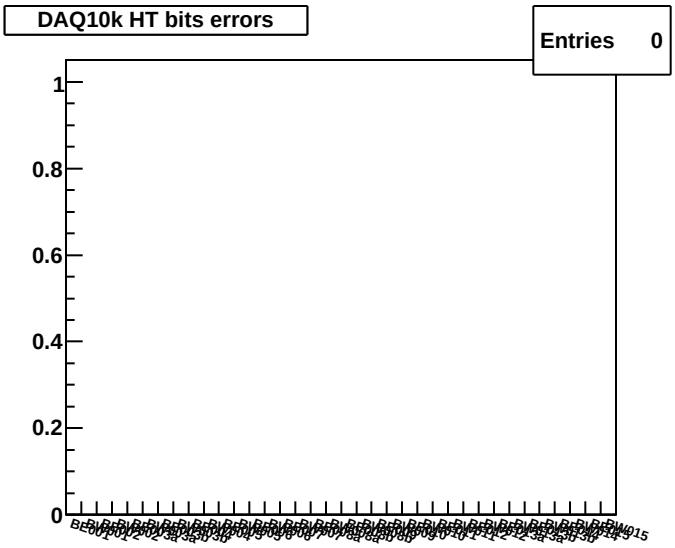

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4022256
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

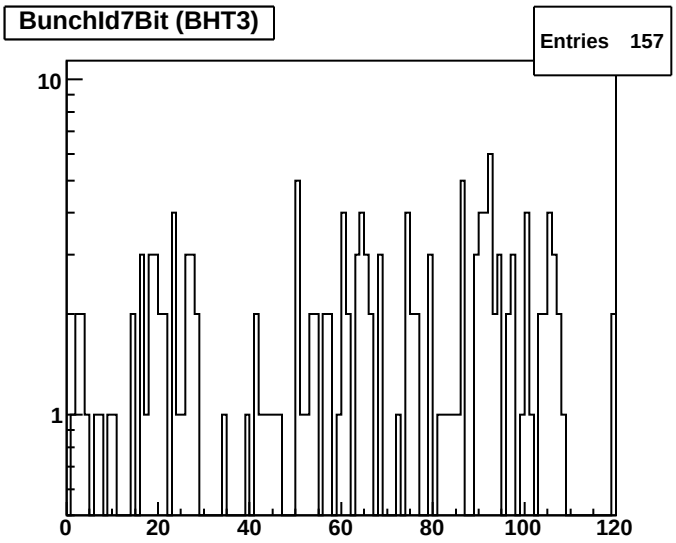

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

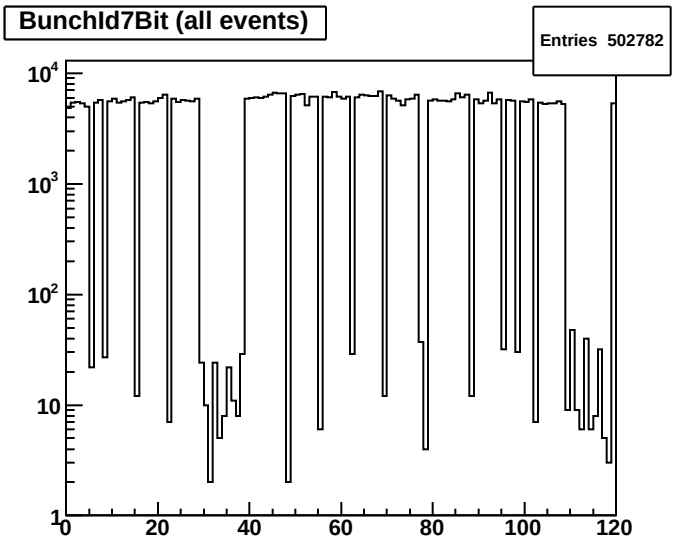

EMC L2 Input - HT23 bits
Entries 4022256
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

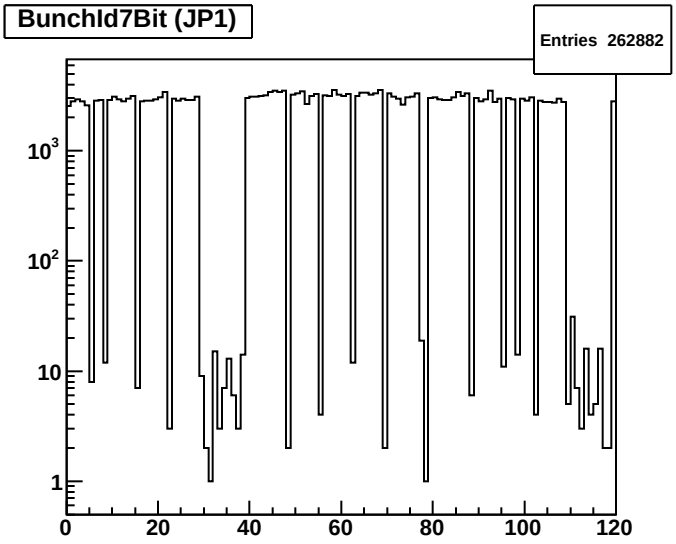

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

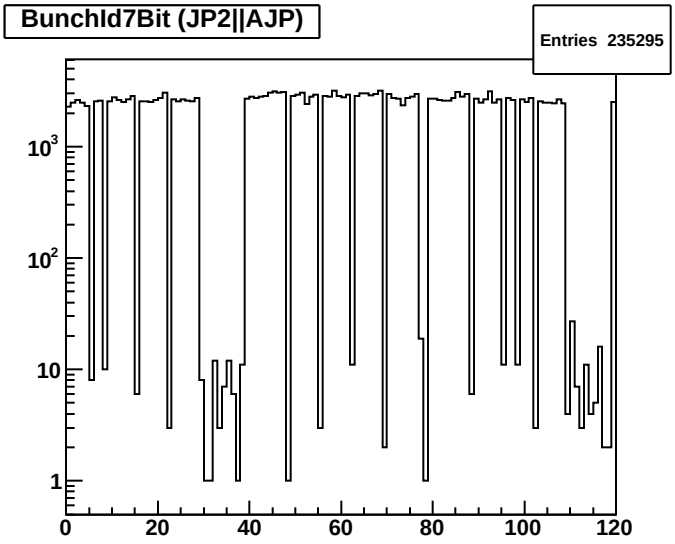

DAQ10k HT bits
Entries 159

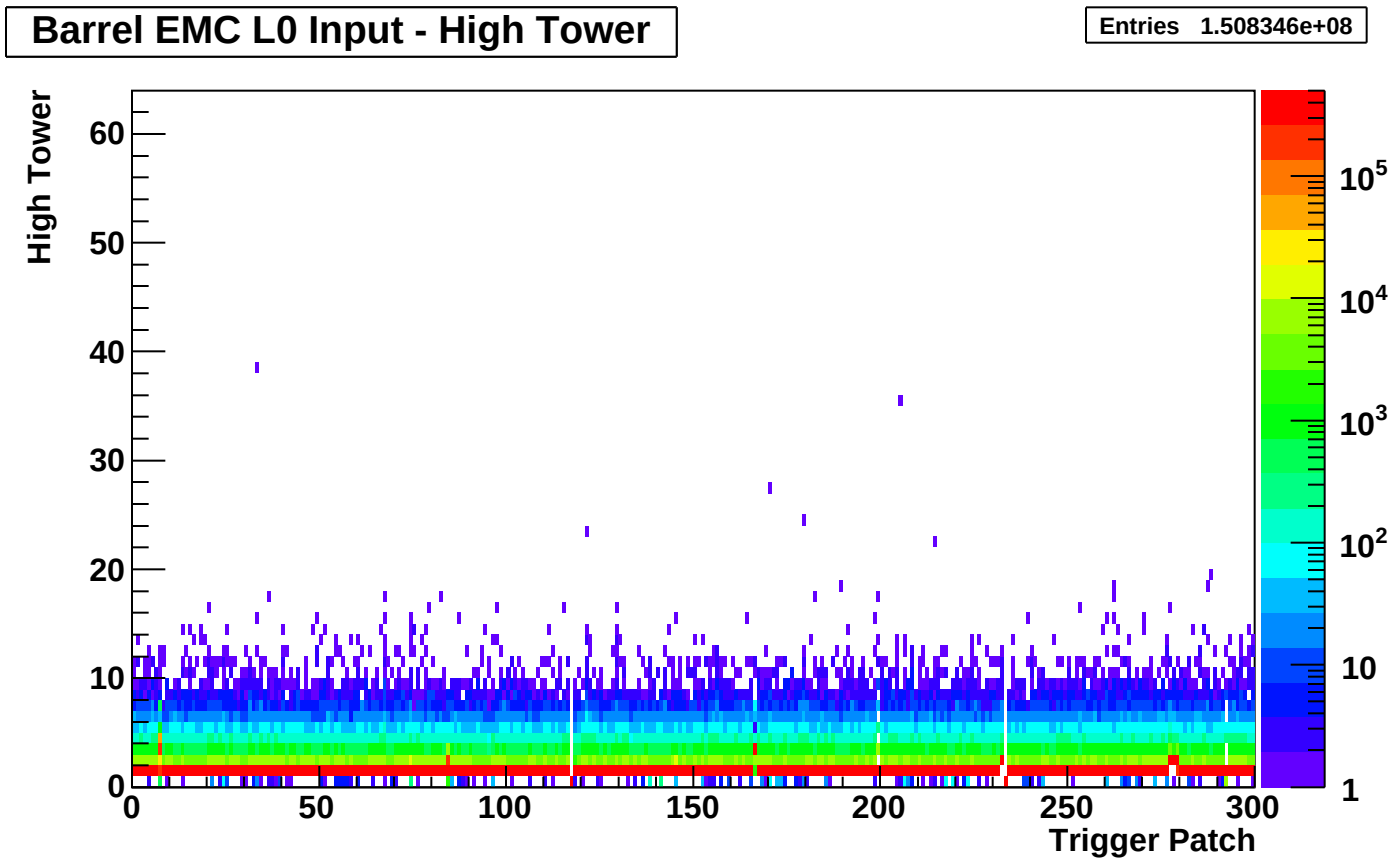

DAQ10k HT bits errors
Entries 0

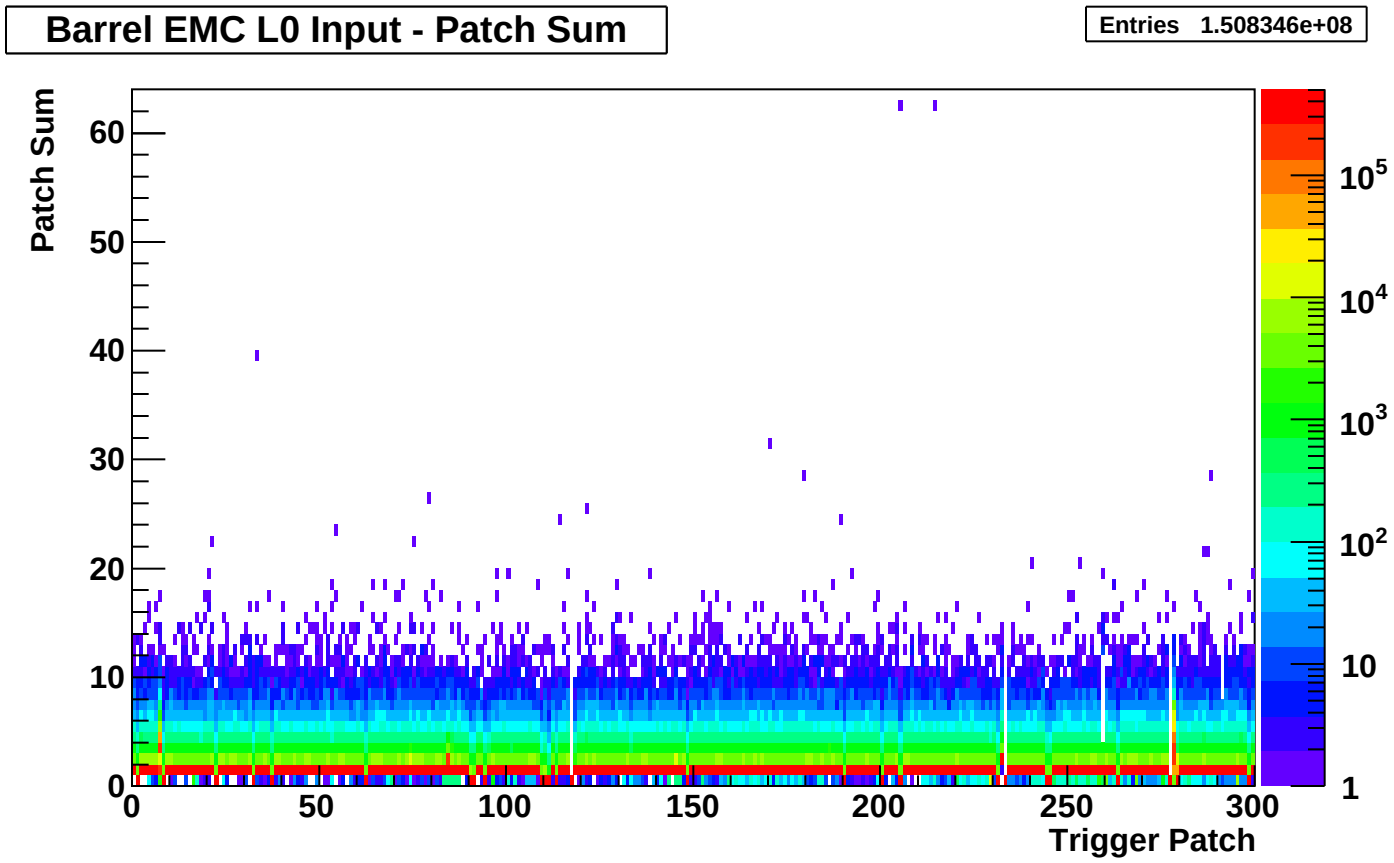

BunchId7Bit (BHT3)
Entries 157

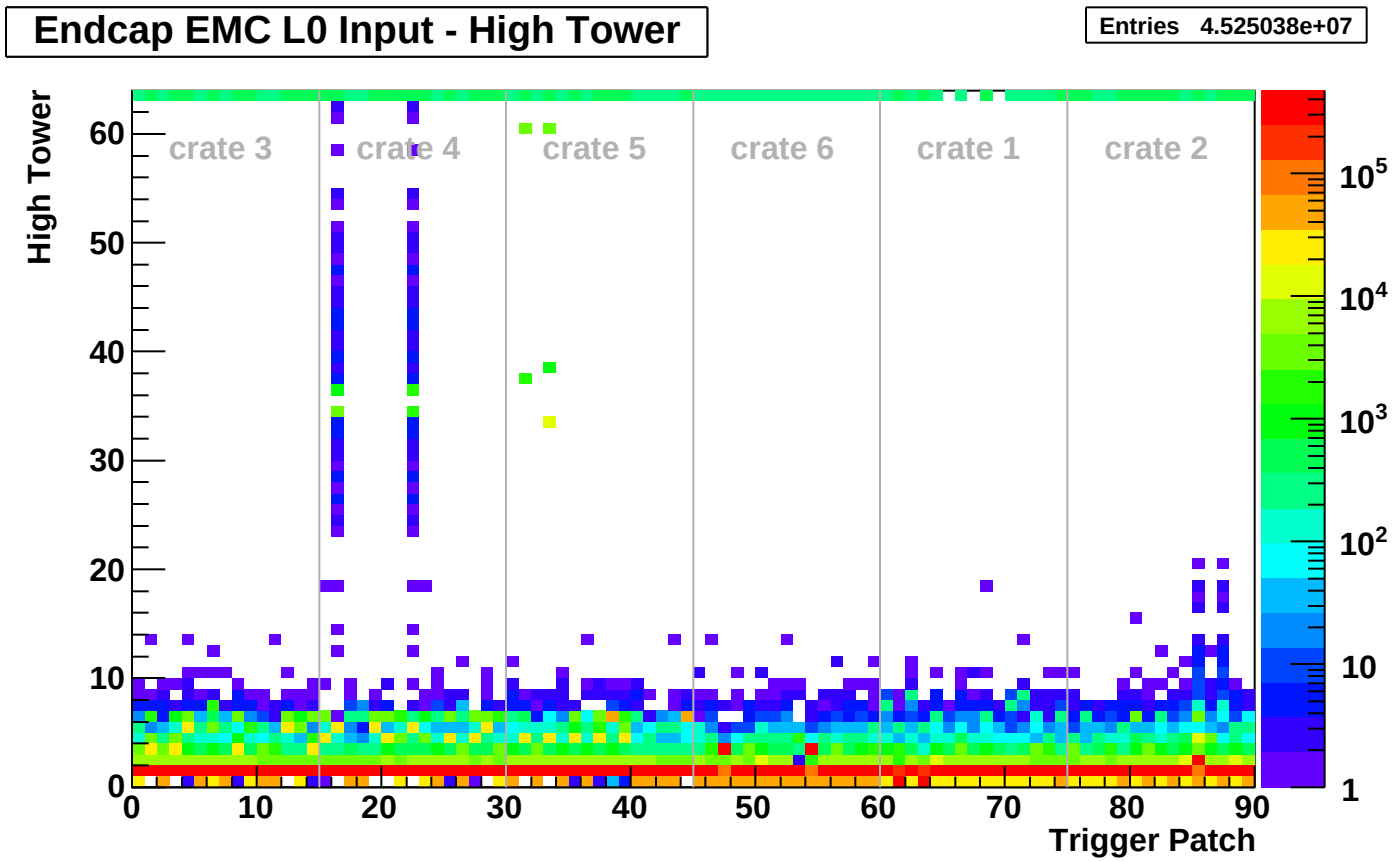

BunchId7Bit (all events)
Entries 502782

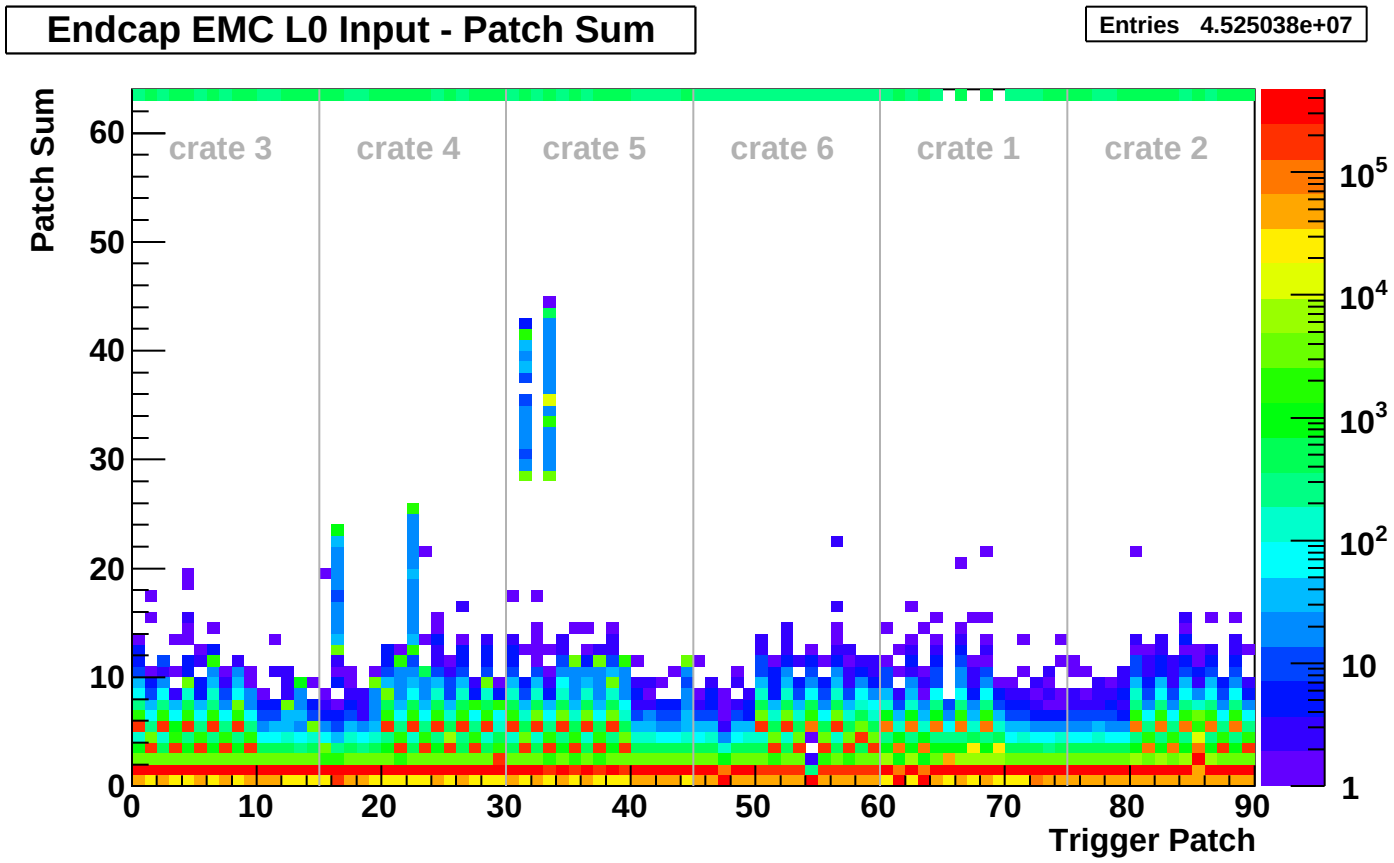

BunchId7Bit (JP1)
Entries 262882

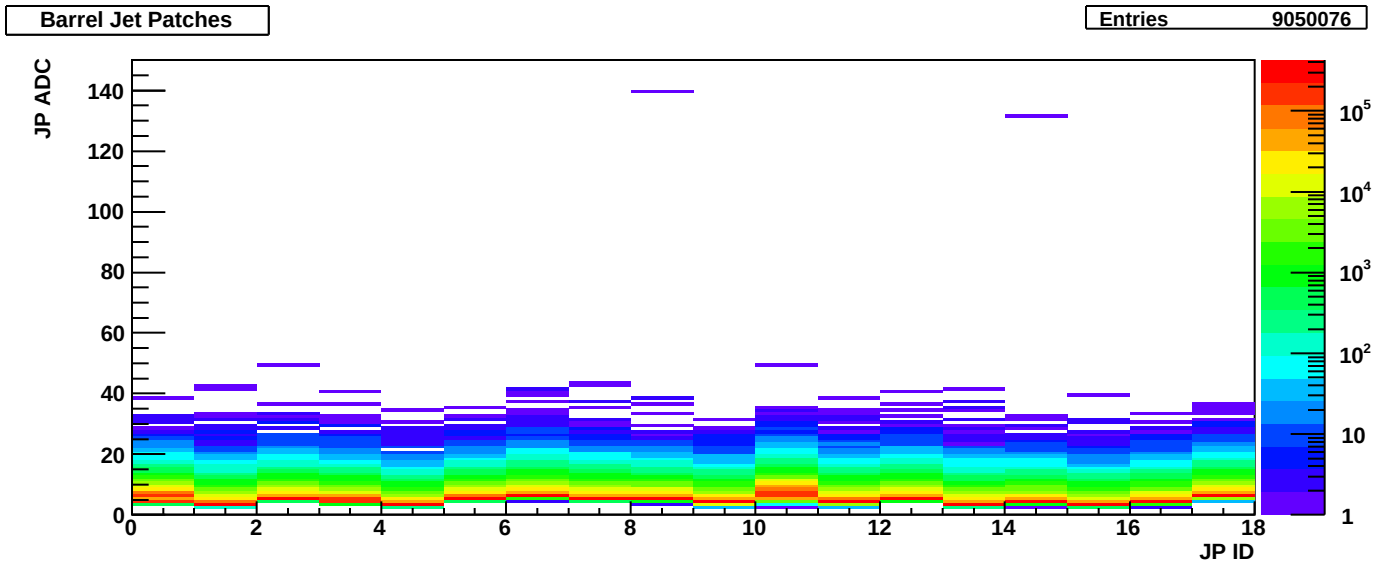

BunchId7Bit (JP2||AJP)
Entries 235295

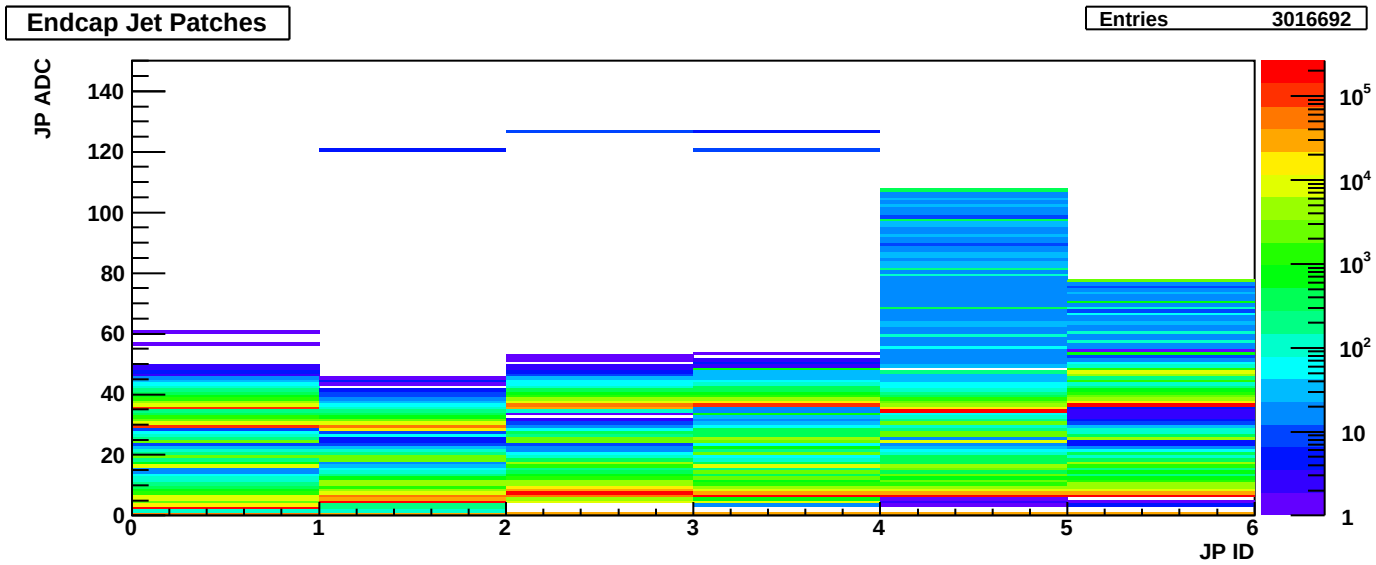

Barrel EMC L0 Input - High Tower
Entries 1.508346e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

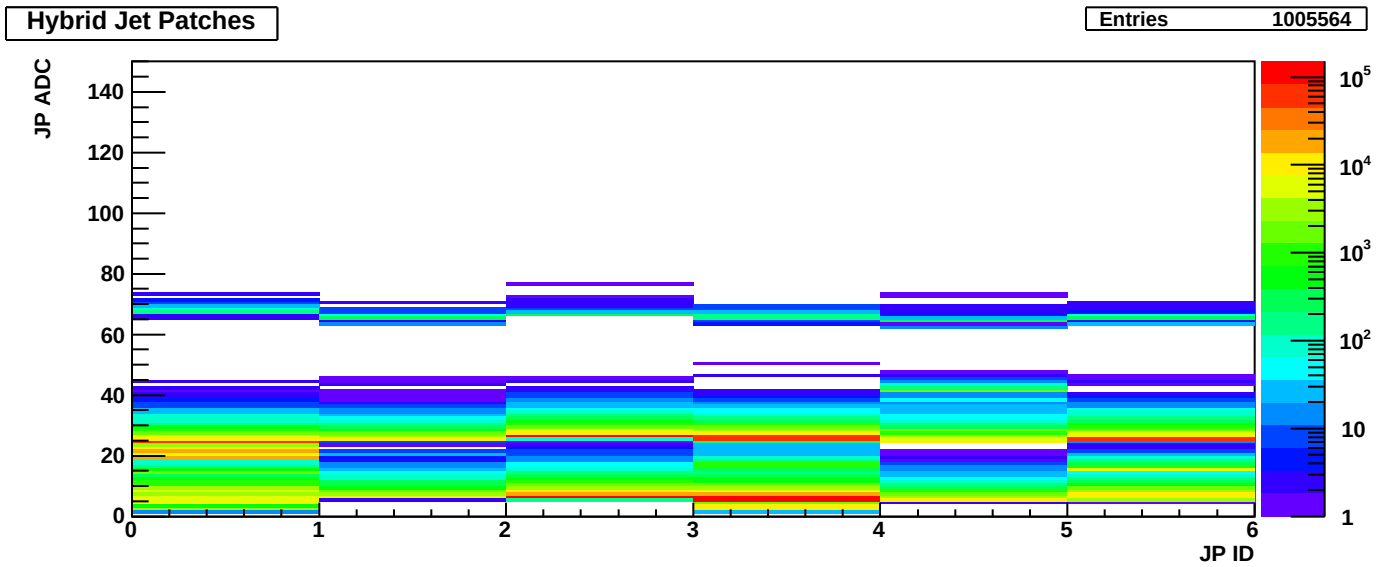

Barrel EMC L0 Input - Patch Sum
Entries 1.508346e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

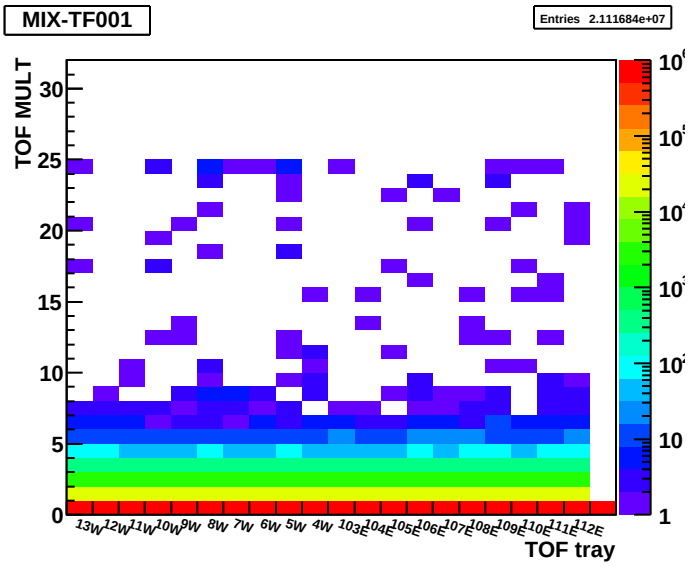

Endcap EMC L0 Input - High Tower
Entries 4.525038e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

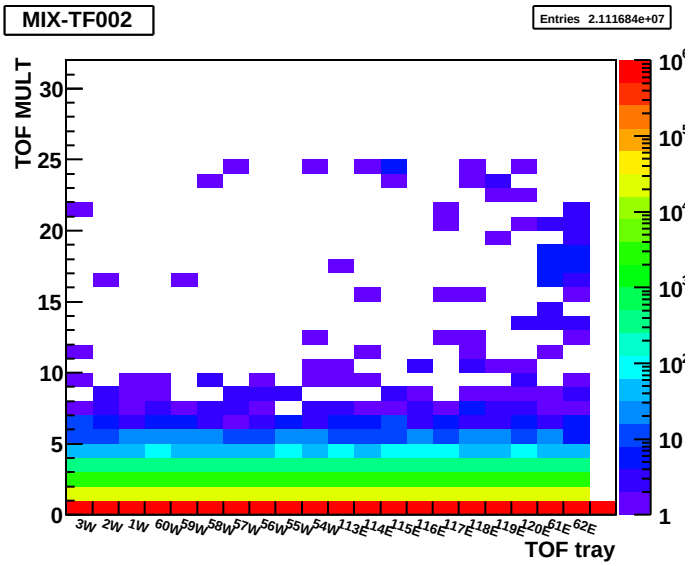

Endcap EMC L0 Input - Patch Sum
Entries 4.525038e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

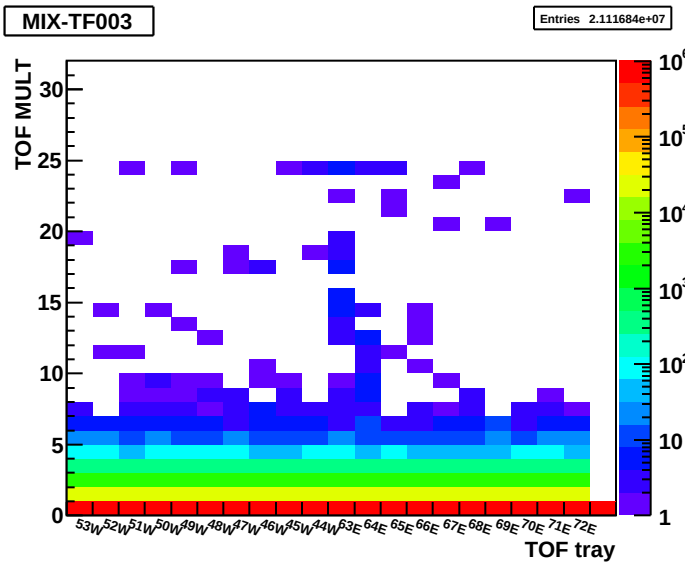

Barrel Jet Patches
Entries 9050076
JP ADC
JP ID

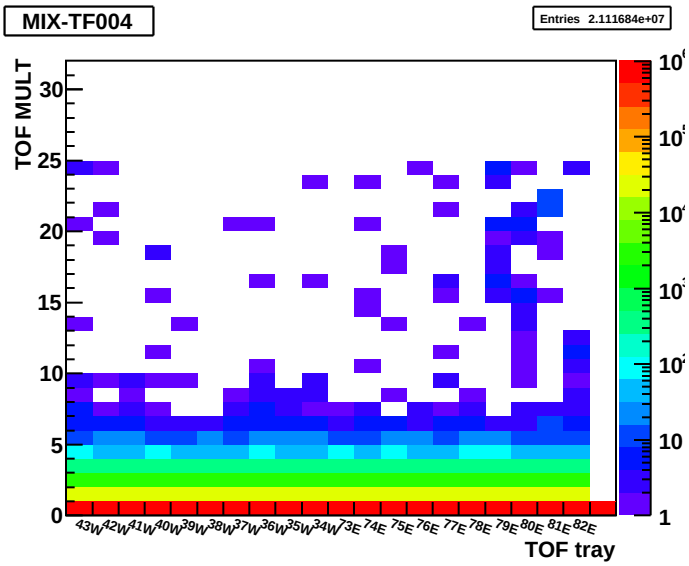

Endcap Jet Patches
Entries 3016692
JP ADC
JP ID

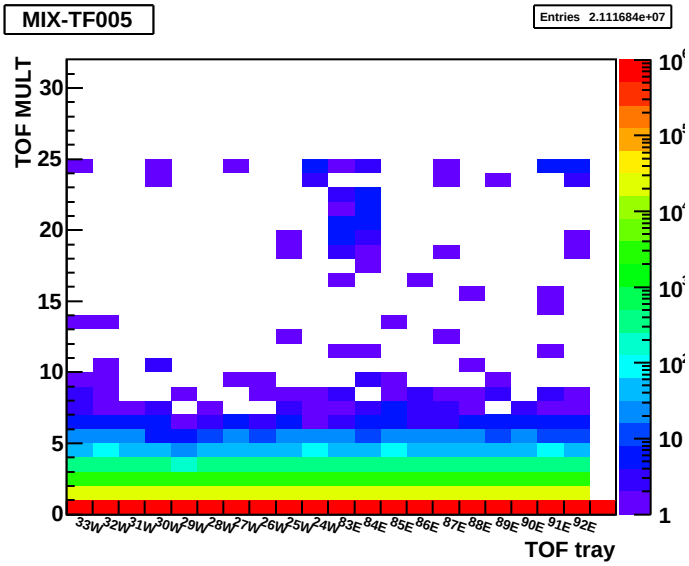

Hybrid Jet Patches
Entries 1005564
JP ADC
JP ID

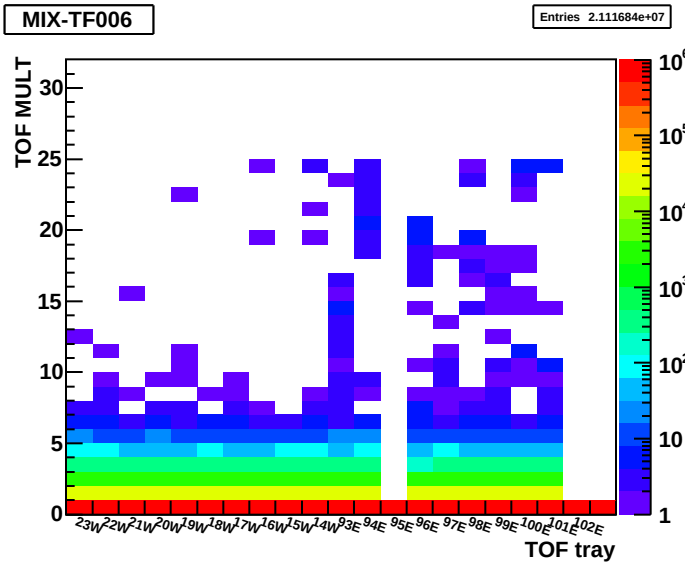

MIX-TF001
Entries 2.111684e+07
TOF MULT
TOF tray

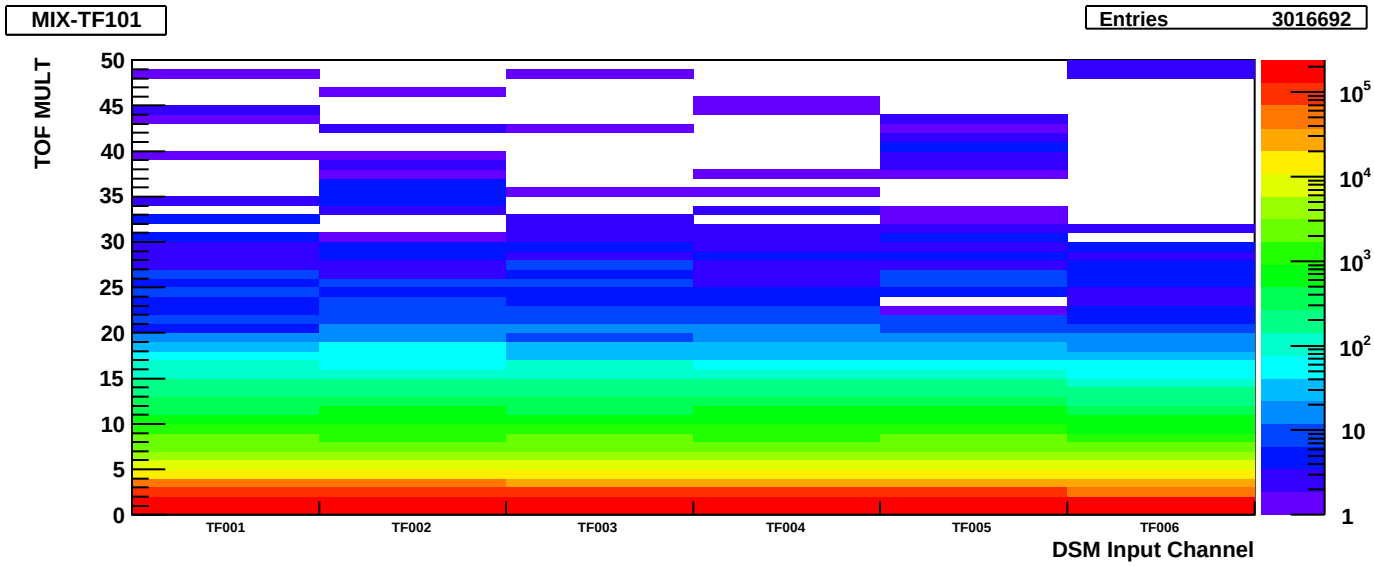

MIX-TF002
Entries 2.111684e+07
TOF MULT
TOF tray

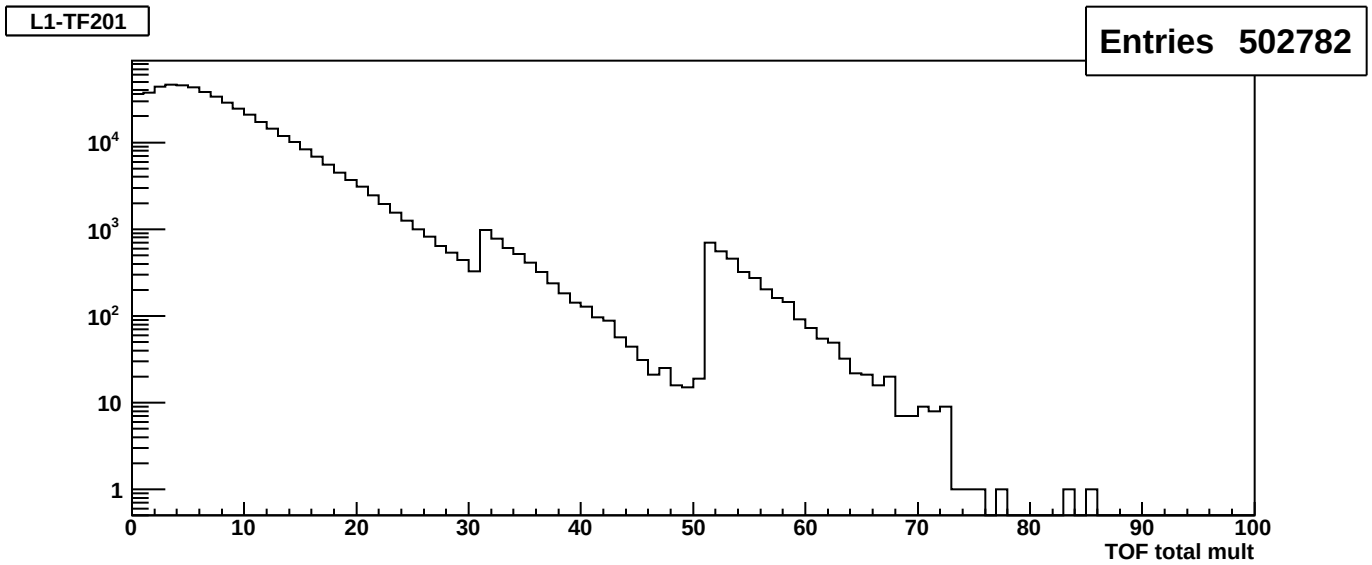

MIX-TF003
Entries 2.111684e+07
TOF MULT
TOF tray

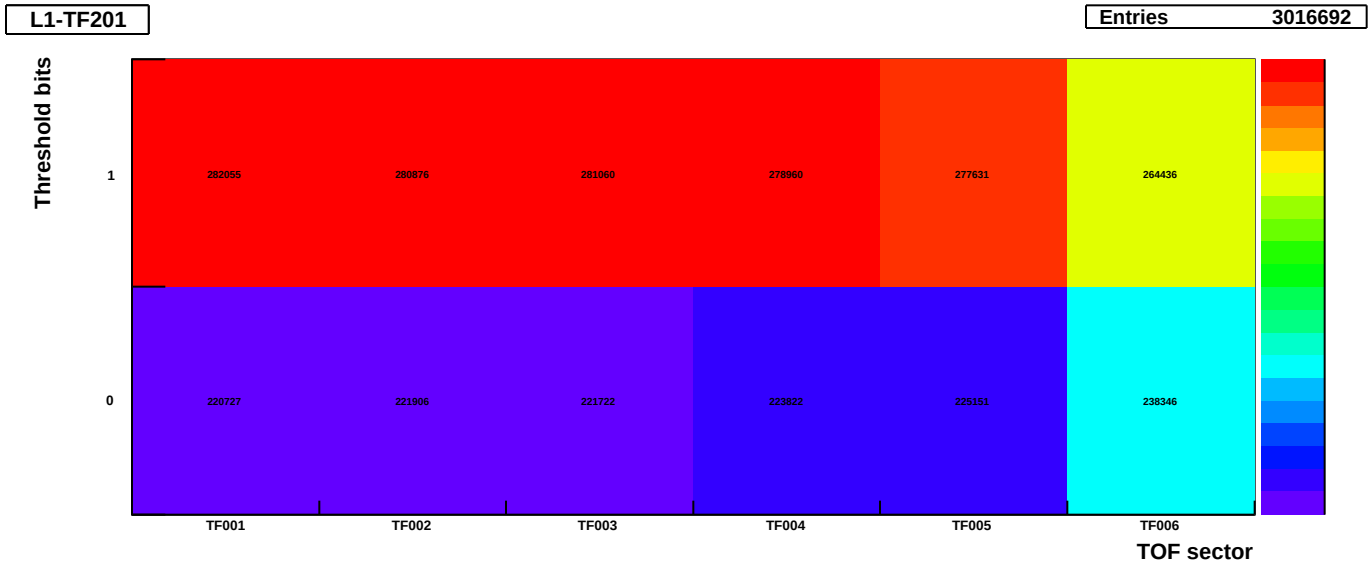

MIX-TF004
Entries 2.111684e+07
TOF MULT
TOF tray

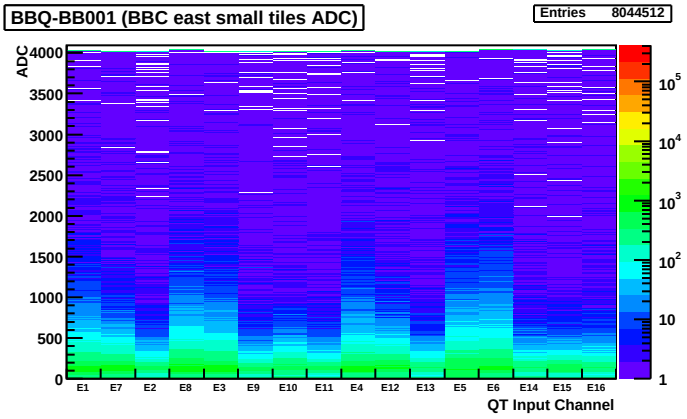

MIX-TF005
Entries 2.111684e+07
TOF MULT
TOF tray

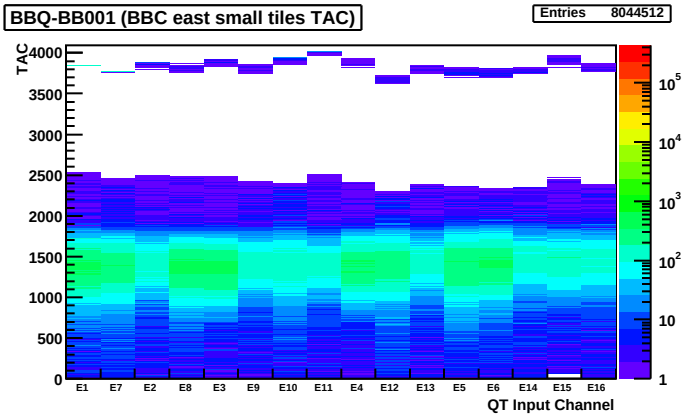

MIX-TF006
Entries 2.111684e+07
TOF MULT
TOF tray

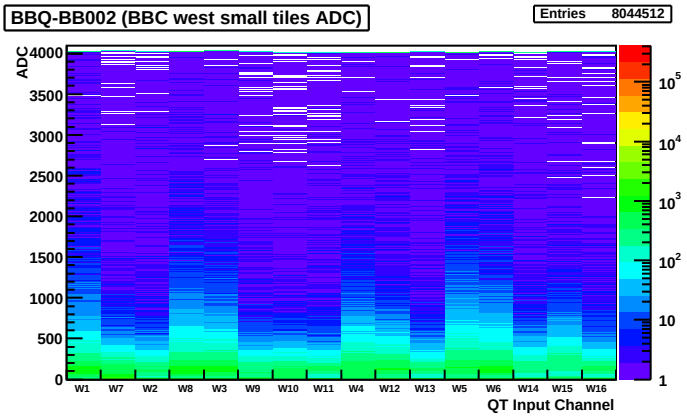

MIX-TF101
Entries 3016692
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

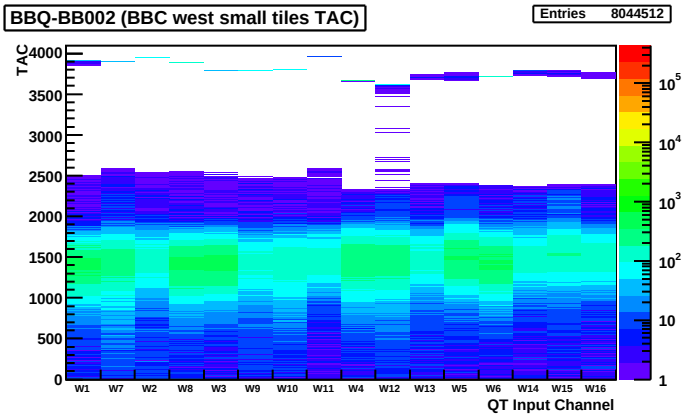

L1-TF201
Entries 502782
TOF total mult

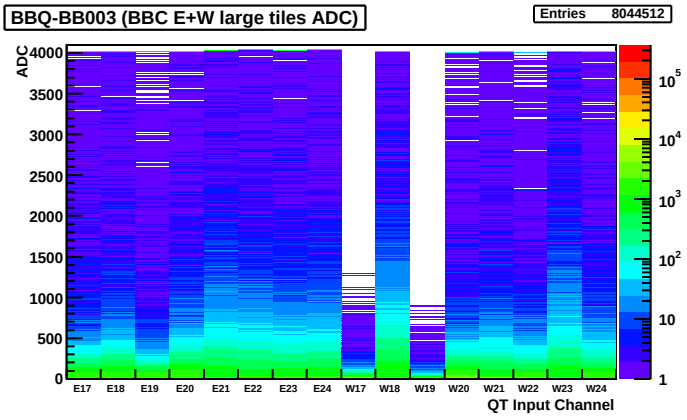

L1-TF201
Entries 3016692
Threshold bits
282055
280876
281060
278960
277631
264436
220727
221906
221722
223822
225151
238346
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

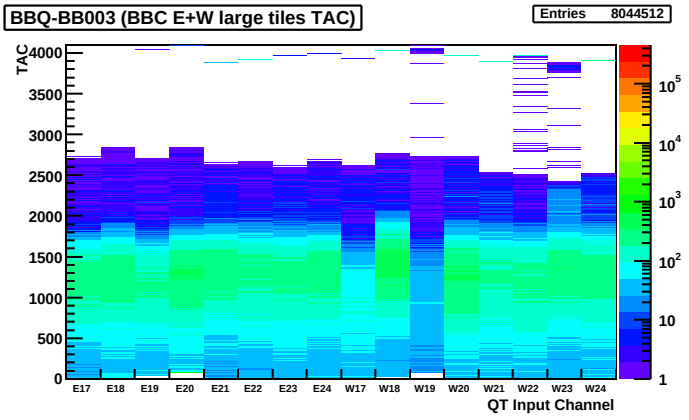

BBQ-BB001 (BBC east small tiles ADC)
Entries 8044512
ADC
QT Input Channel

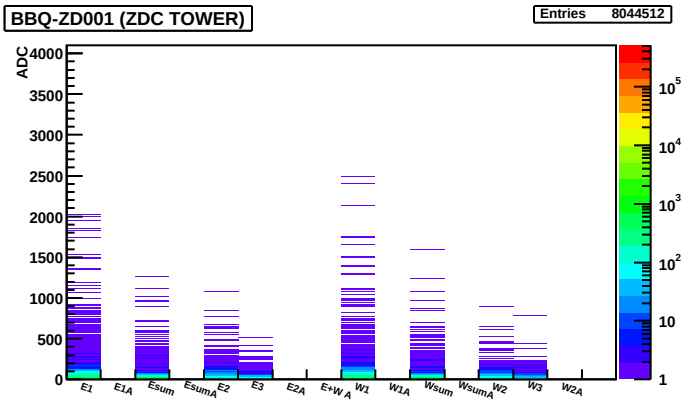

BBQ-BB001 (BBC east small tiles TAC)
Entries 8044512
TAC
QT Input Channel

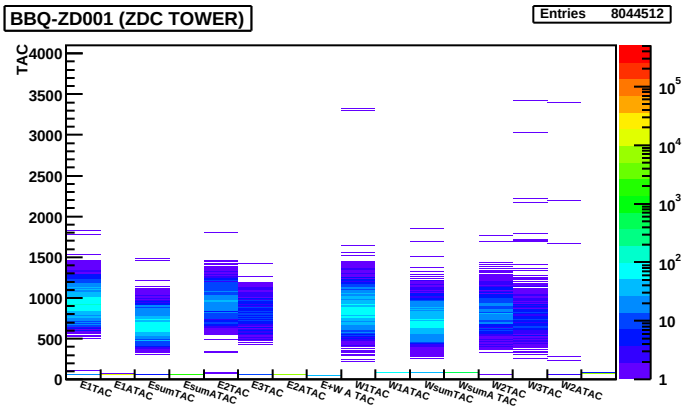

BBQ-BB002 (BBC west small tiles ADC)
Entries 8044512
ADC
QT Input Channel

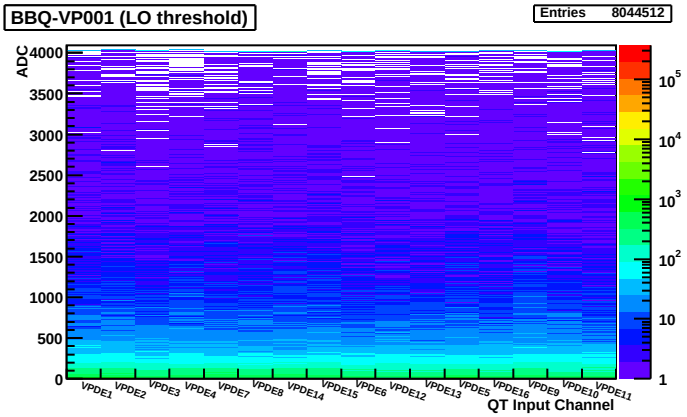

BBQ-BB002 (BBC west small tiles TAC)
Entries 8044512
TAC
QT Input Channel

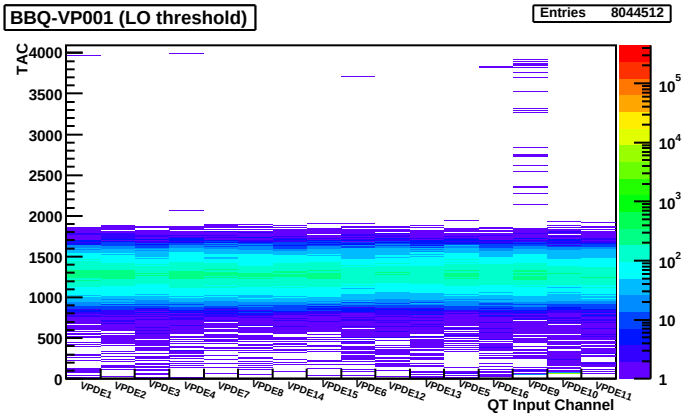

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8044512
ADC
QT Input Channel

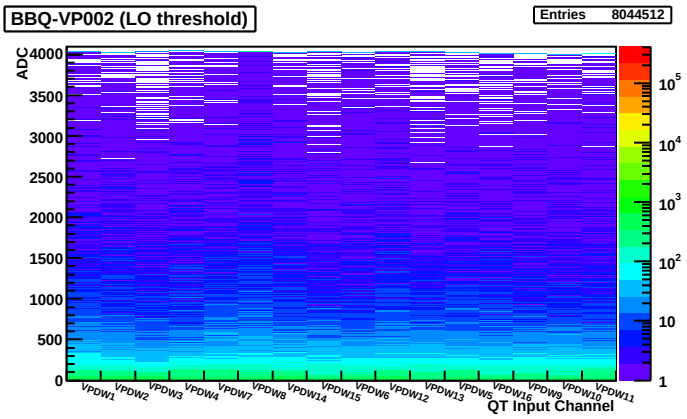

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8044512
TAC
QT Input Channel

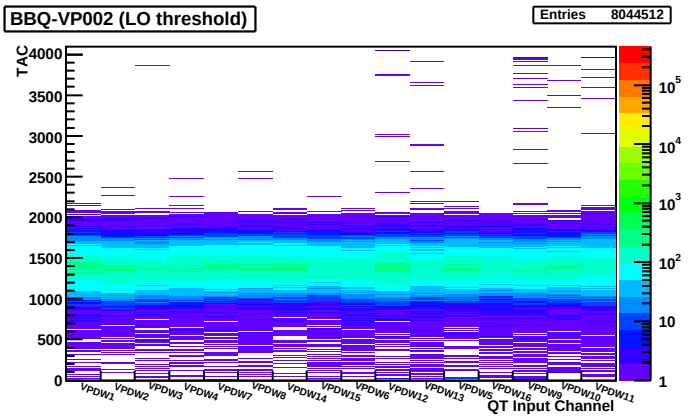

BBQ-ZD001 (ZDC TOWER)
Entries 8044512
ADC

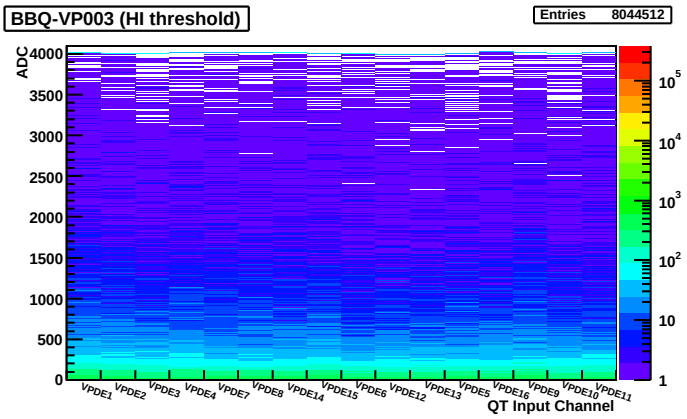

BBQ-ZD001 (ZDC TOWER)
Entries 8044512
TAC

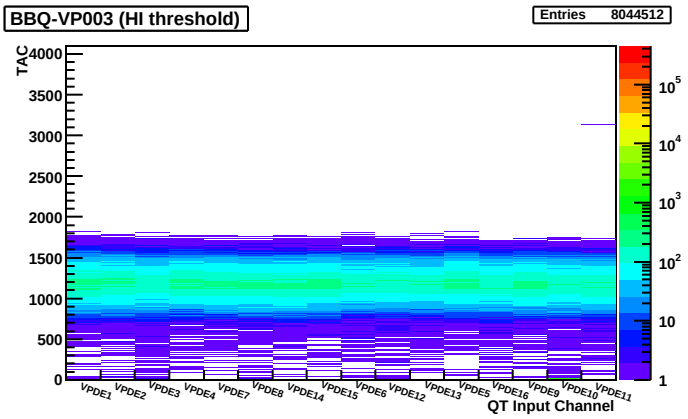

BBQ-VP001 (LO threshold)
Entries 8044512
ADC
QT Input Channel

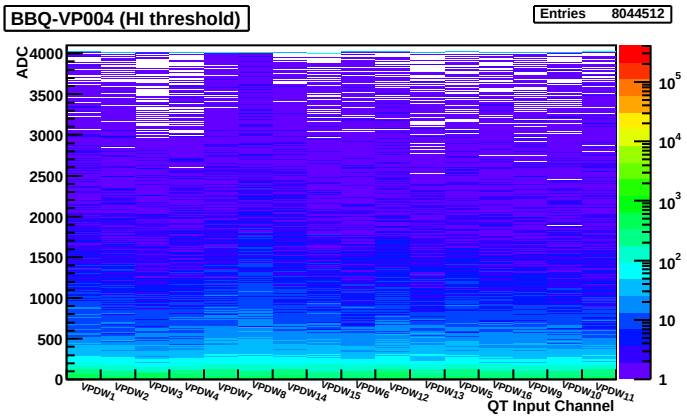

BBQ-VP001 (LO threshold)
Entries 8044512
TAC
QT Input Channel

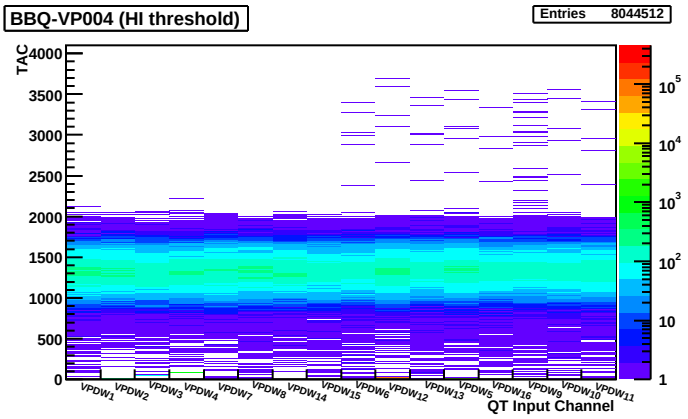

BBQ-VP002 (LO threshold)
Entries 8044512
ADC
QT Input Channel

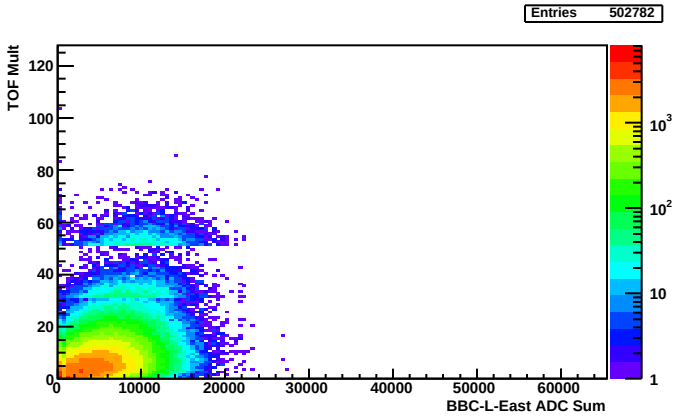

BBQ-VP002 (LO threshold)
Entries 8044512
TAC
QT Input Channel

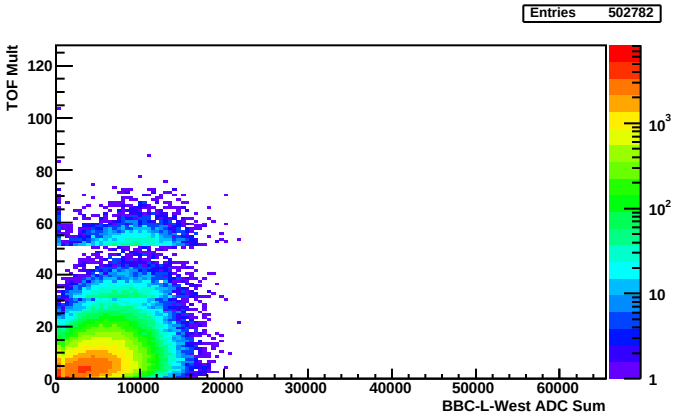

BBQ-VP003 (HI threshold)
Entries 8044512
ADC
QT Input Channel

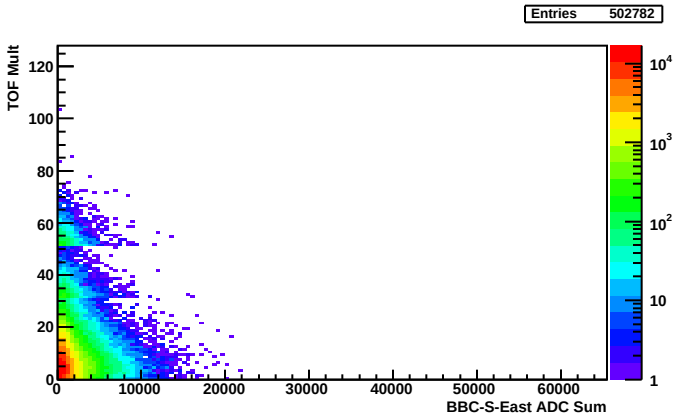

BBQ-VP003 (HI threshold)
Entries 8044512
TAC
QT Input Channel

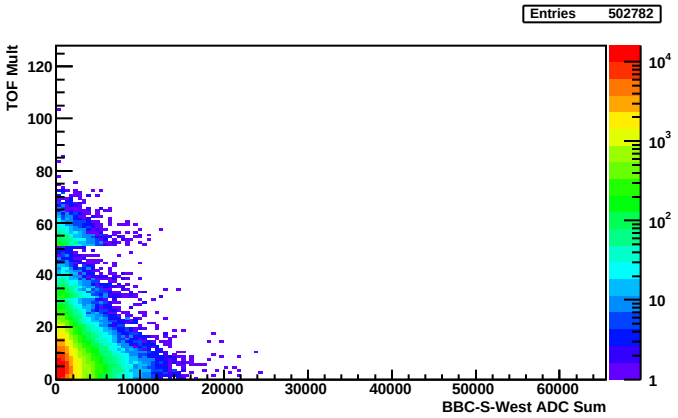

BBQ-VP004 (HI threshold)
Entries 8044512
ADC
QT Input Channel

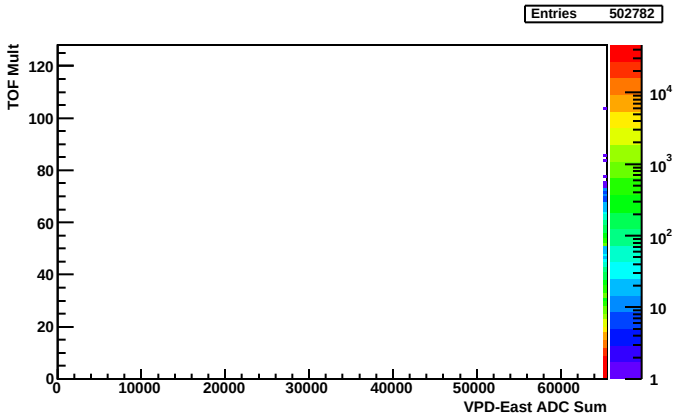

BBQ-VP004 (HI threshold)
Entries 8044512
TAC
QT Input Channel

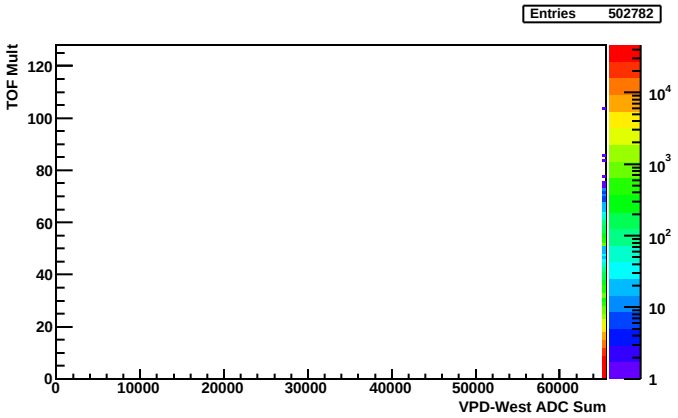

Entries 502782
TOF Mult
BBC-L-East ADC Sum

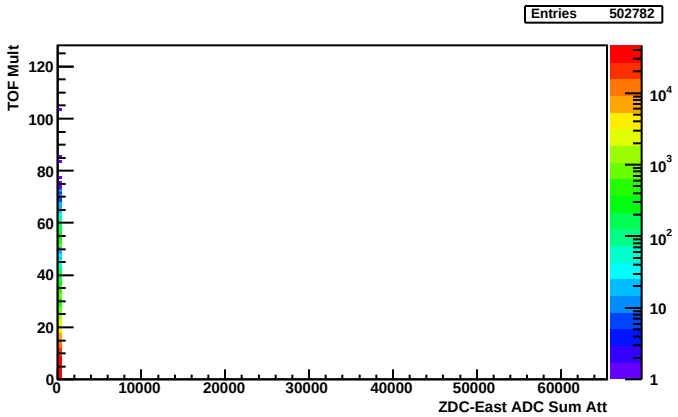

Entries 502782
TOF Mult
BBC-L-West ADC Sum

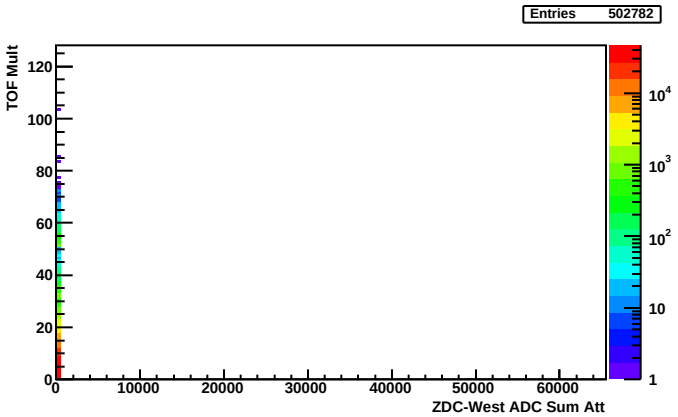

Entries 502782
TOF Mult
BBC-S-East ADC Sum

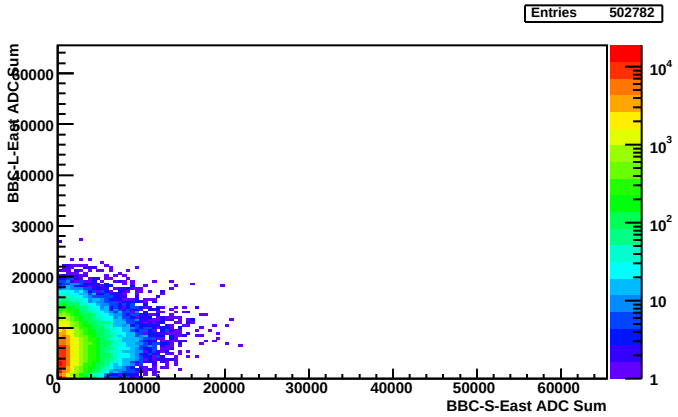

Entries 502782
TOF Mult
BBC-S-West ADC Sum

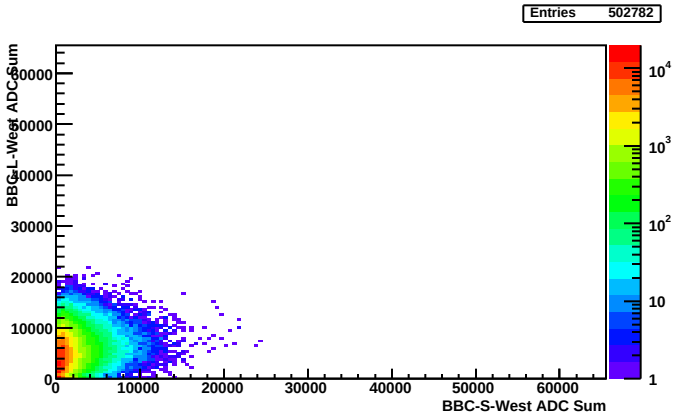

Entries 502782
TOF Mult
VPD-East ADC Sum

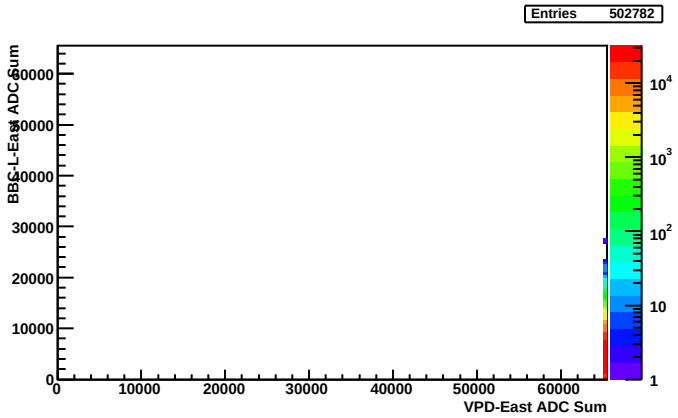

Entries 502782
TOF Mult
VPD-West ADC Sum

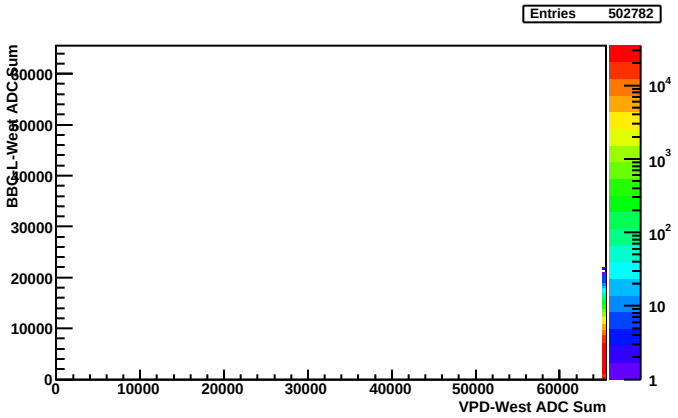

Entries 502782
TOF Mult
ZDC-East ADC Sum Att

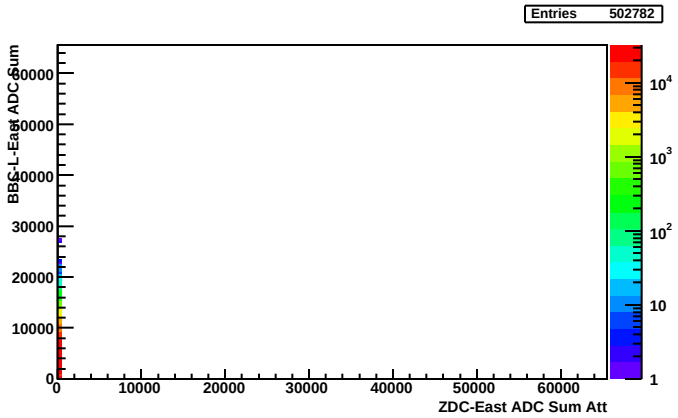

Entries 502782
TOF Mult
ZDC-West ADC Sum Att

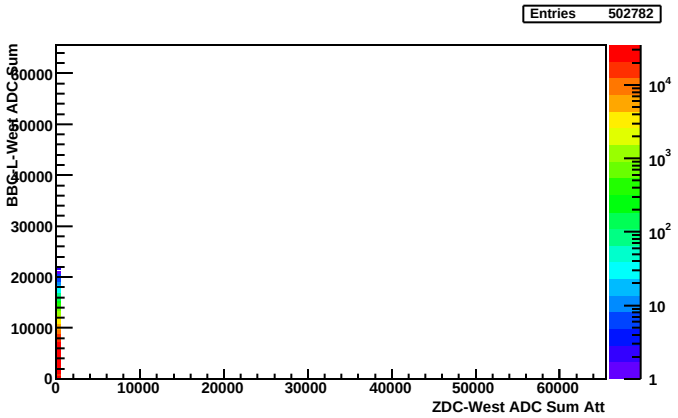

Entries 502782
BBC-L-East ADC Sum
BBC-S-East ADC Sum

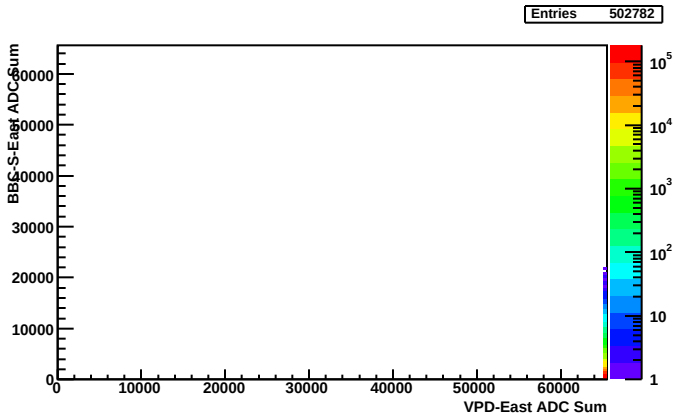

Entries 502782
BBC-L-West ADC Sum
BBC-S-West ADC Sum

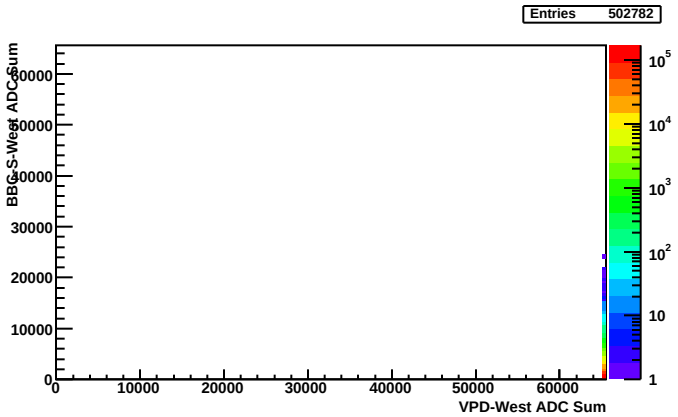

Entries 502782
BBC-L-East ADC Sum
VPD-East ADC Sum

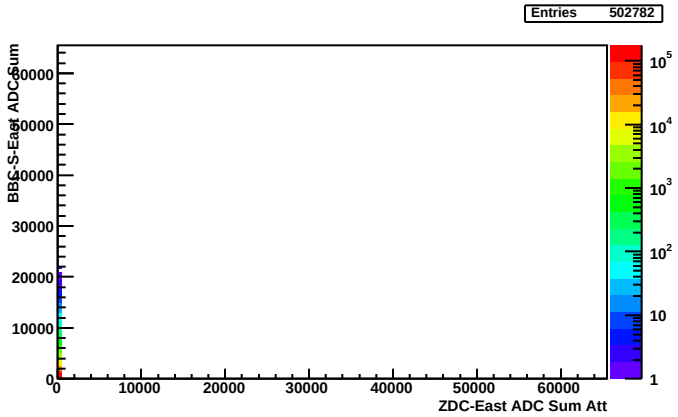

Entries 502782
BBC-L-West ADC Sum
VPD-West ADC Sum

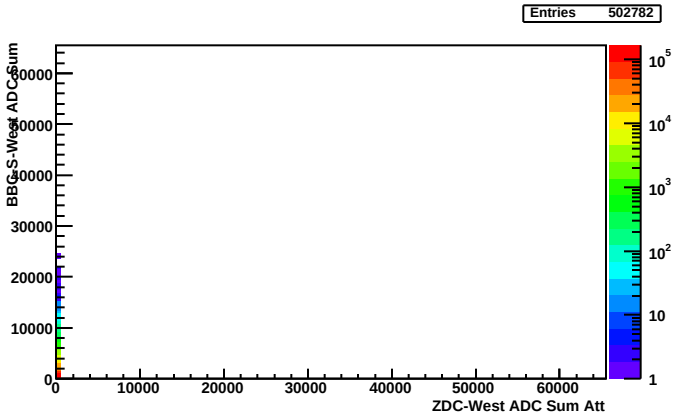

Entries 502782
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

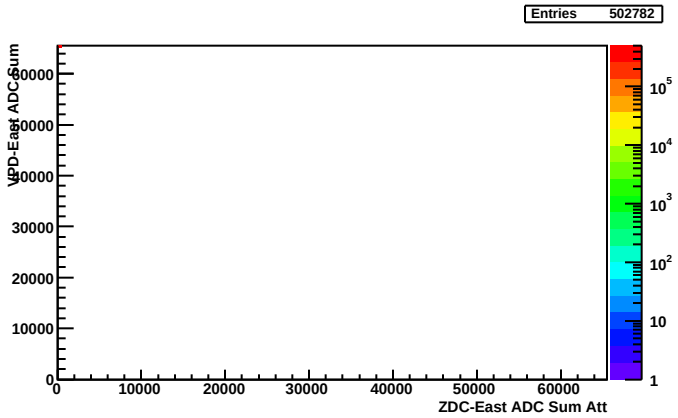

Entries 502782
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

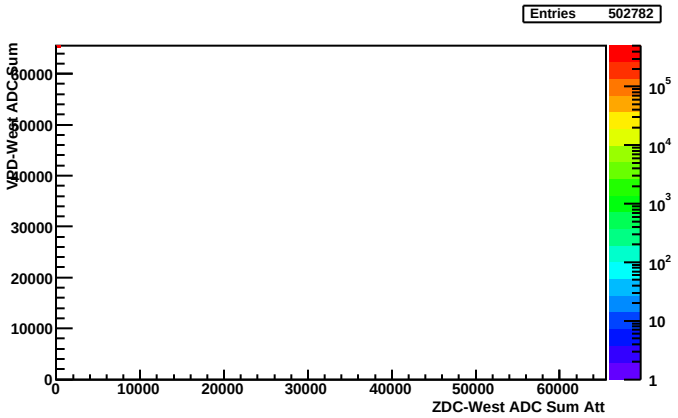

Entries 502782
BBC-S-East ADC Sum
VPD-East ADC Sum

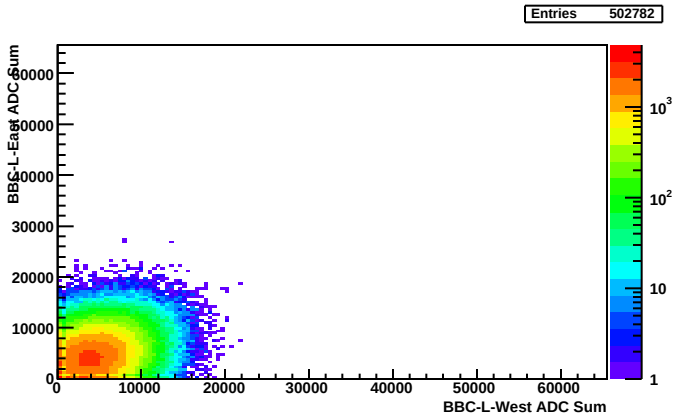

Entries 502782
BBC-S-West ADC Sum
VPD-West ADC Sum

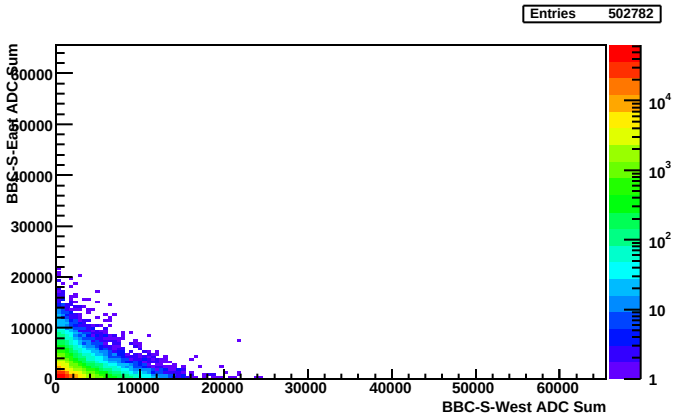

Entries 502782
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

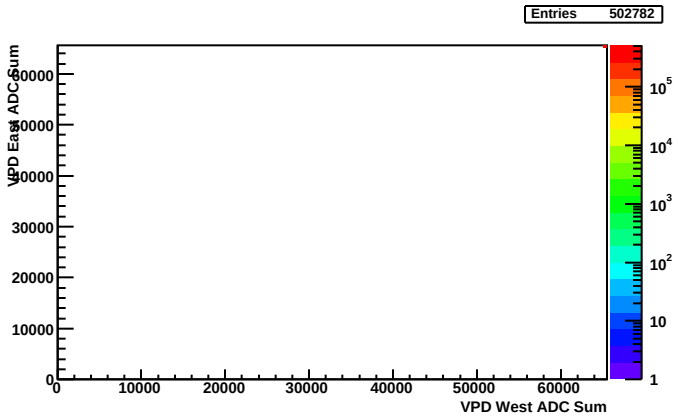

Entries 502782
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

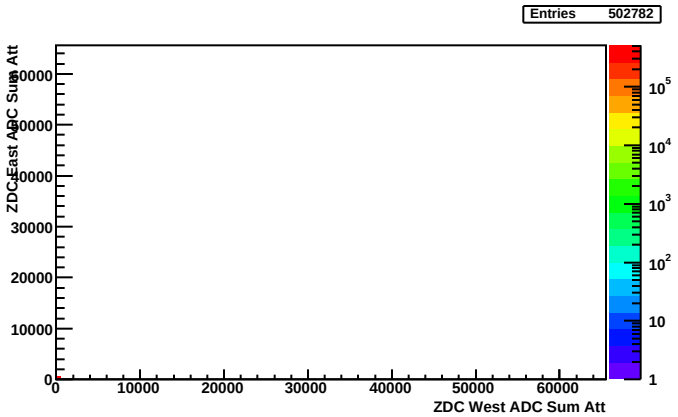

Entries 502782
VPD-East ADC Sum
ZDC-East ADC Sum Att

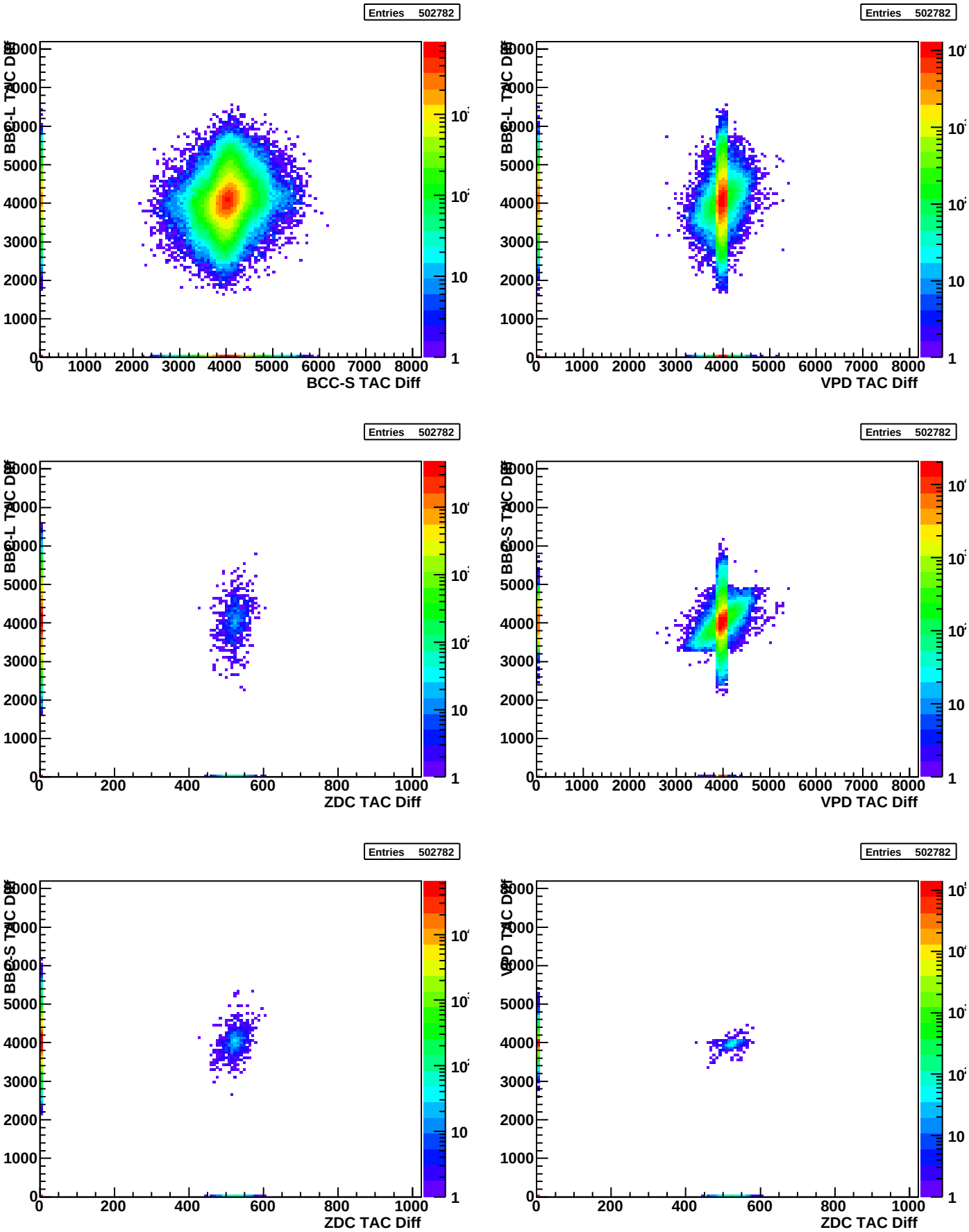

Entries 502782
VPD-West ADC Sum
ZDC-West ADC Sum Att

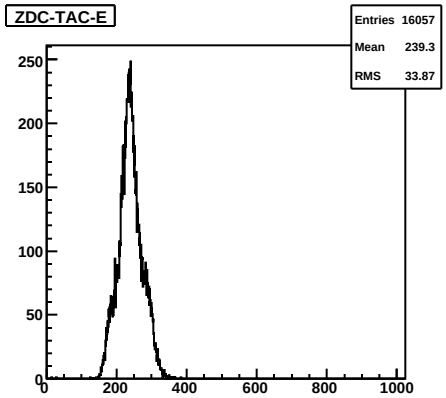

Entries 502782
BBC-L-East ADC Sum
BBC-L-West ADC Sum

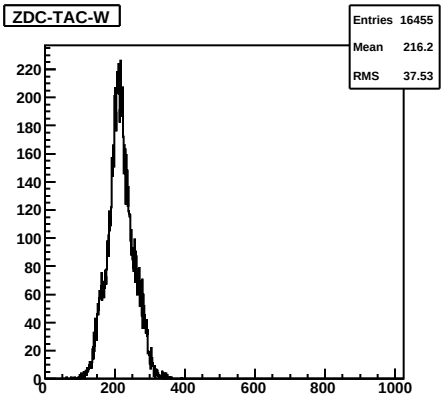

Entries 502782
BBC-S-East ADC Sum
BBC-S-West ADC Sum

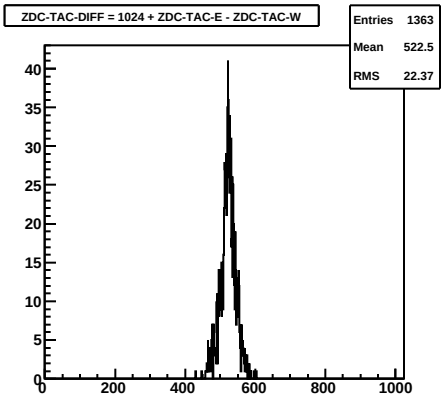

Entries 502782
VPD East ADC Sum
VPD West ADC Sum

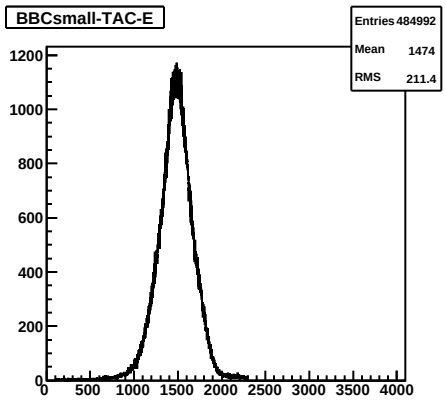

Entries 502782
ZDC East ADC Sum Att
ZDC West ADC Sum Att

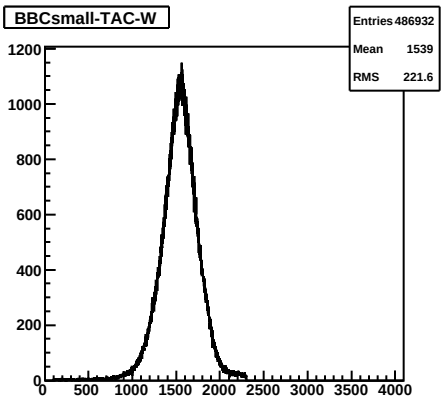

Entries 502782
BBC-L TAC Diff
BCC-S TAC Diff
Entries 502782
BBC-L TAC Diff
VPD TAC Diff
Entries 502782
BBC-L TAC Diff
ZDC TAC Diff
Entries 502782
BBC-S TAC Diff
VPD TAC Diff
Entries 502782
BBC-S TAC Diff
ZDC TAC Diff
Entries 502782
VPD TAC Diff
ZDC TAC Diff

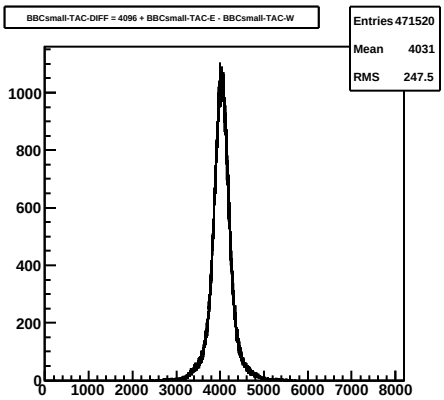

ZDC-TAC-E
Entries 16057
Mean 239.3
RMS 33.87

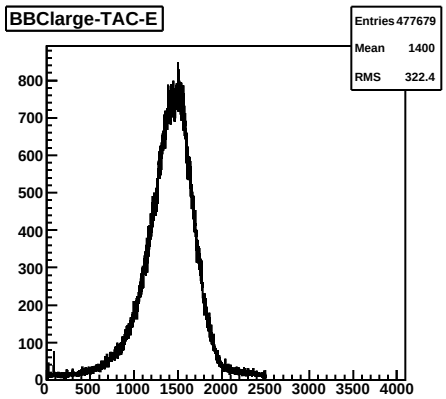

ZDC-TAC-W
Entries 16455
Mean 216.2
RMS 37.53

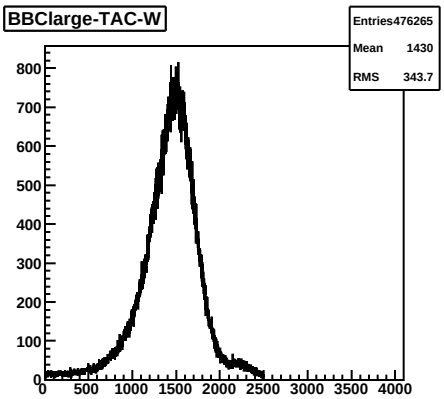

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1363
Mean 522.5
RMS 22.37

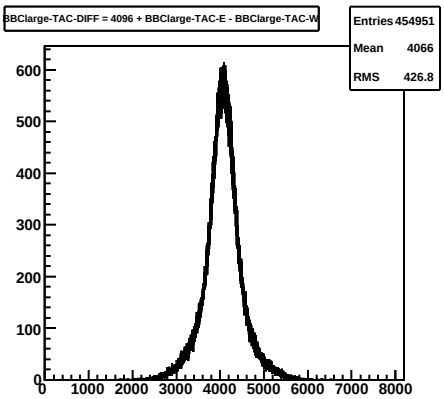

BBCsmall-TAC-E
Entries 484992
Mean 1474
RMS 211.4

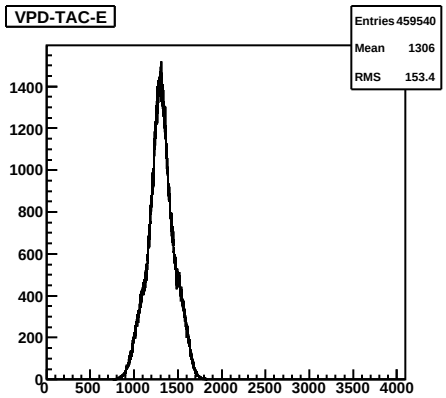

BBCsmall-TAC-W
Entries 486932
Mean 1539
RMS 221.6

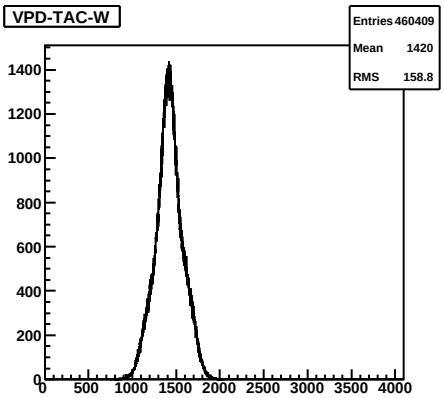

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 471520
Mean 4031
RMS 247.5

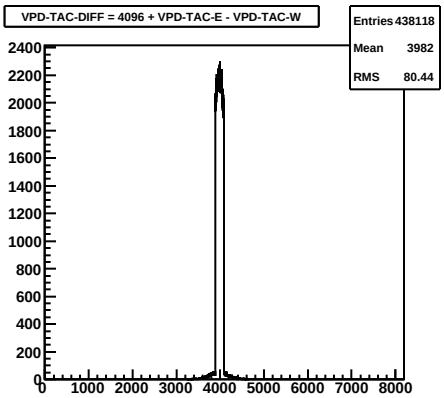

BBClarge-TAC-E
Entries 477679
Mean 1400
RMS 322.4

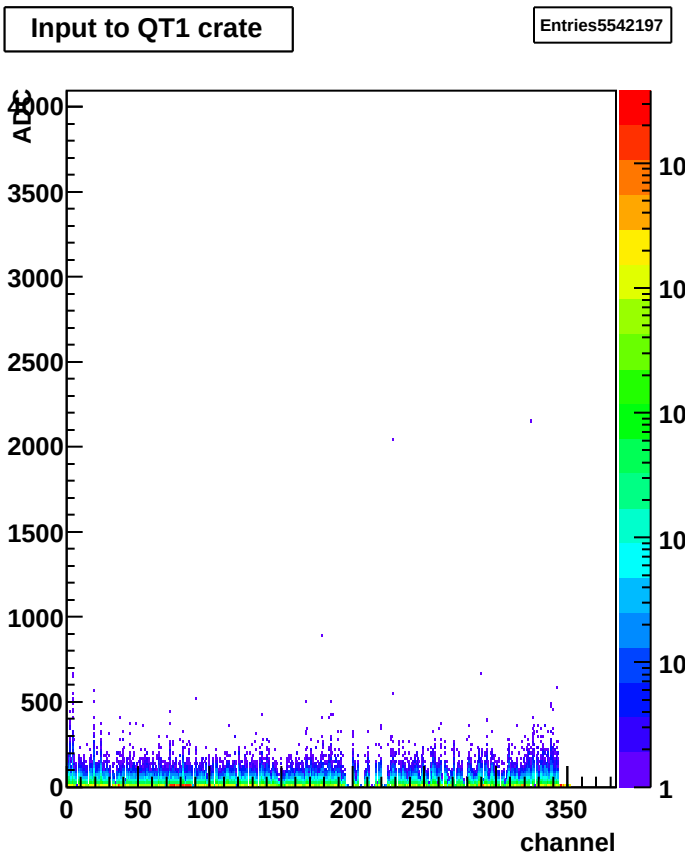

BBClarge-TAC-W
Entries 476265
Mean 1430
RMS 343.7

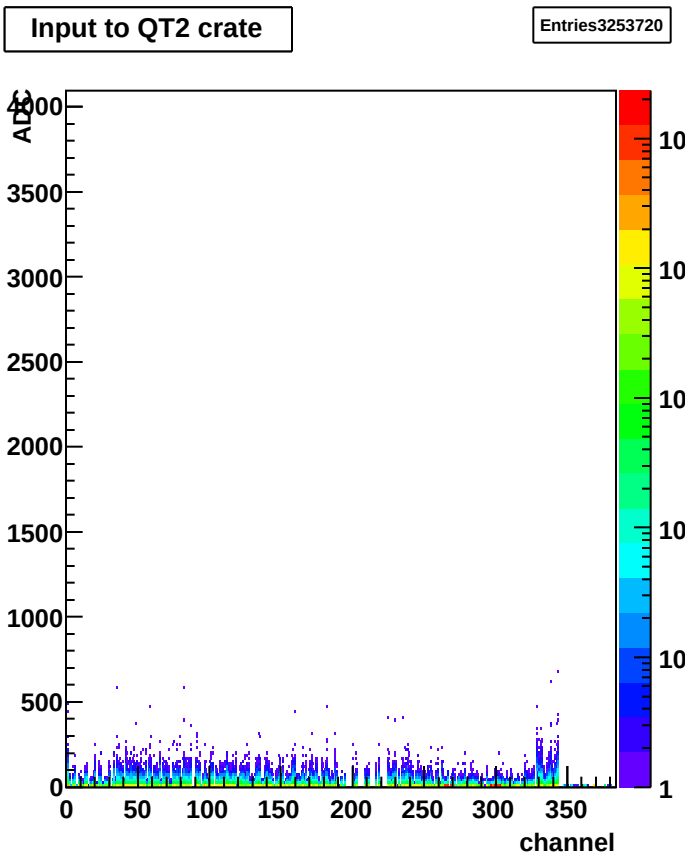

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 454951
Mean 4066
RMS 426.8

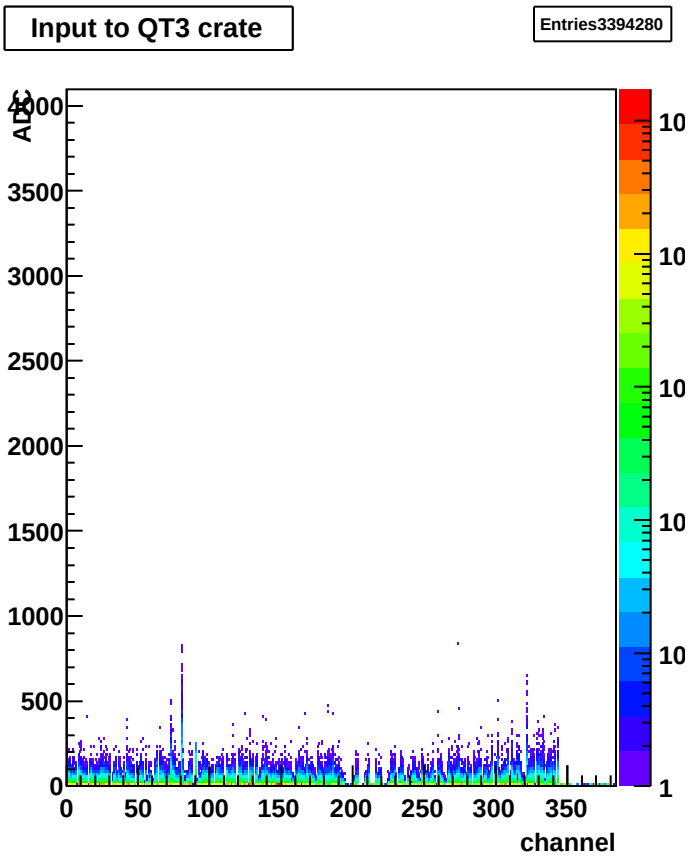

VPD-TAC-E
Entries 459540
Mean 1306
RMS 153.4

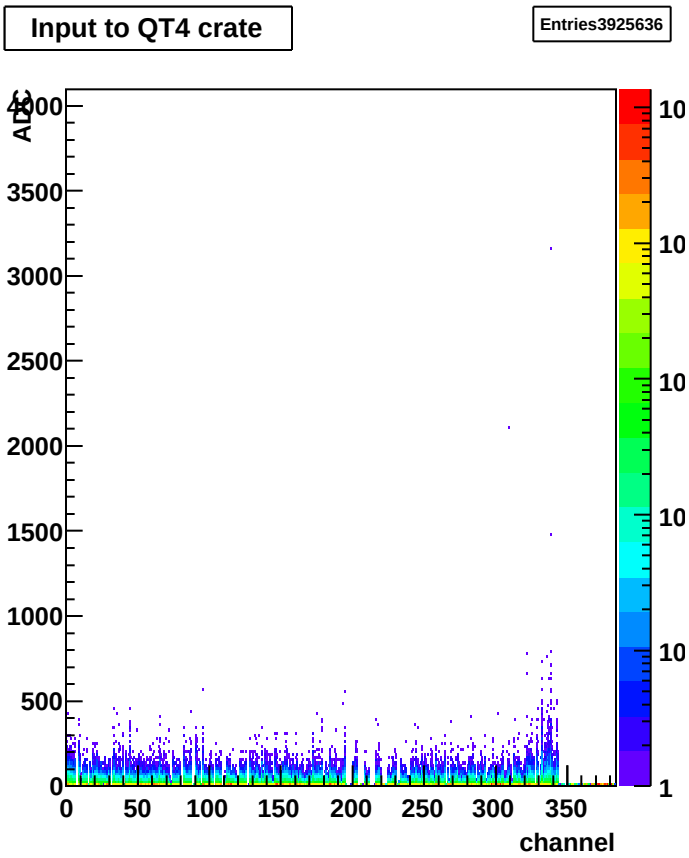

VPD-TAC-W
Entries 460409
Mean 1420
RMS 158.8

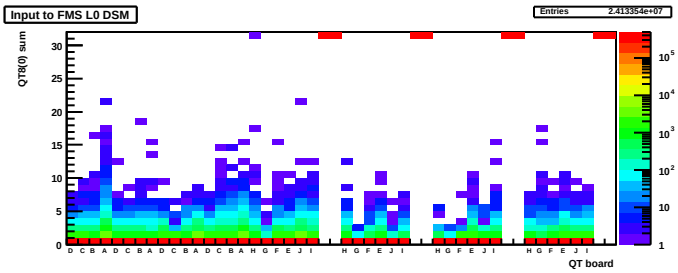

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 438118
Mean 3982
RMS 80.44

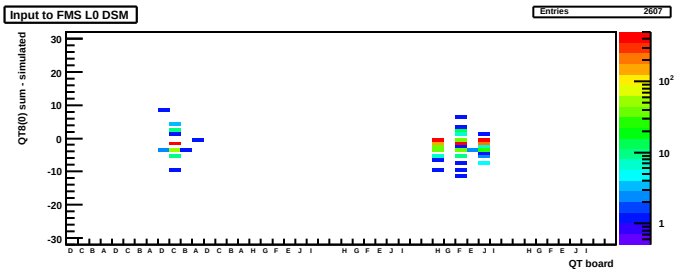

Input to QT1 crate
Entries5542197
ADC
channel

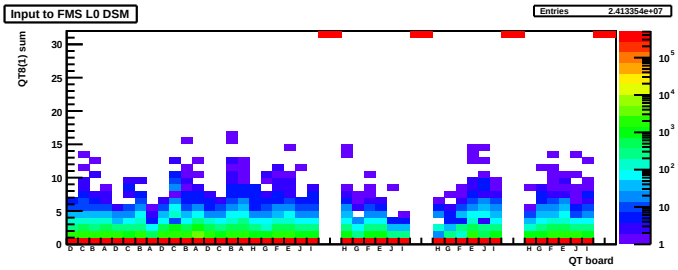

Input to QT2 crate
Entries3253720
ADC
channel

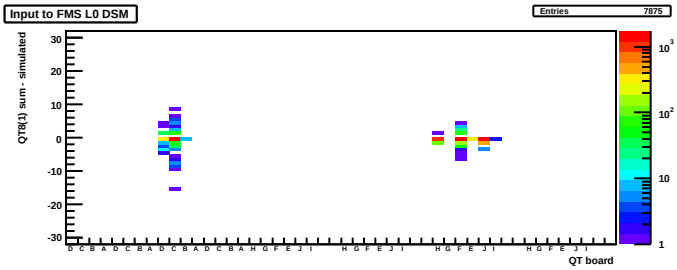

Input to QT3 crate
Entries3394280
ADC
channel

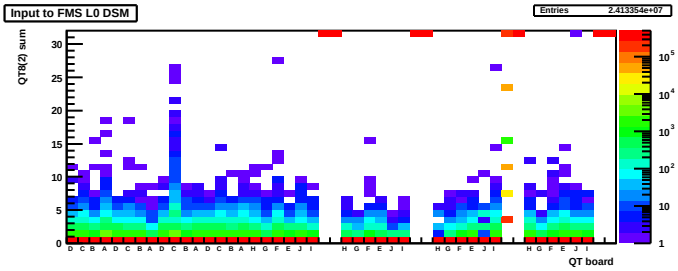

Input to QT4 crate
Entries3925636
ADC
channel

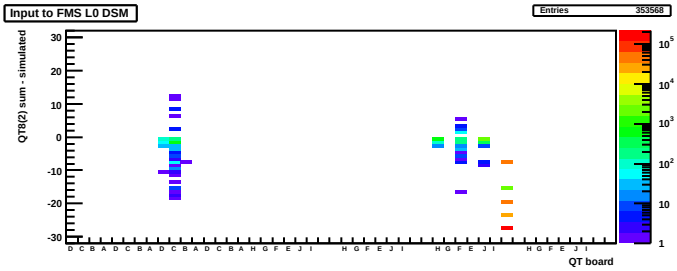

Input to FMS L0 DSM
Entries 2.413354e+07
QT8(0) sum
QT board

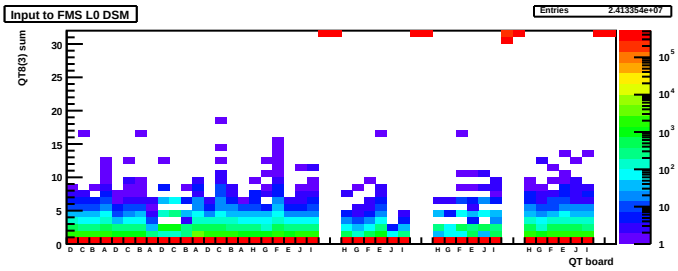

Input to FMS L0 DSM
Entries 2607
QT8(0) sum - simulated
QT board

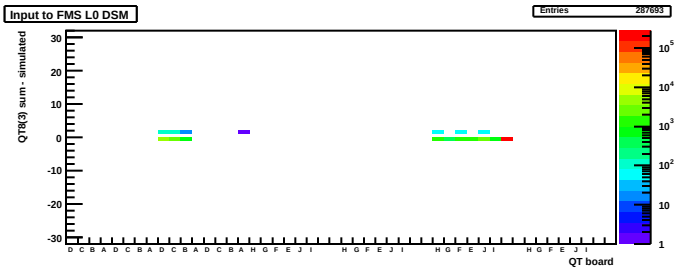

Input to FMS L0 DSM
Entries 2.413354e+07
QT8(1) sum
QT board

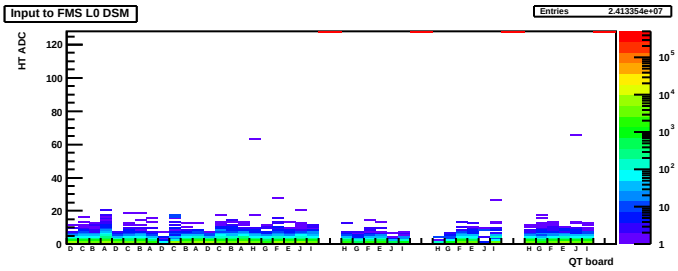

Input to FMS L0 DSM
Entries 7875
QT8(1) sum - simulated
QT board

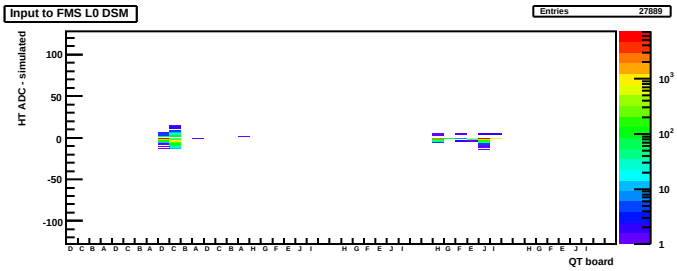

Input to FMS L0 DSM
Entries 2.413354e+07
QT8(2) sum
QT board

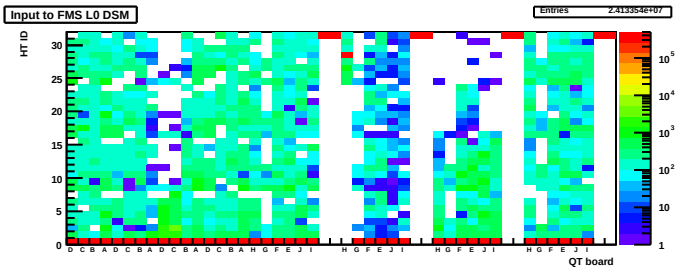

Input to FMS L0 DSM
Entries 353568
QT8(2) sum - simulated
QT board

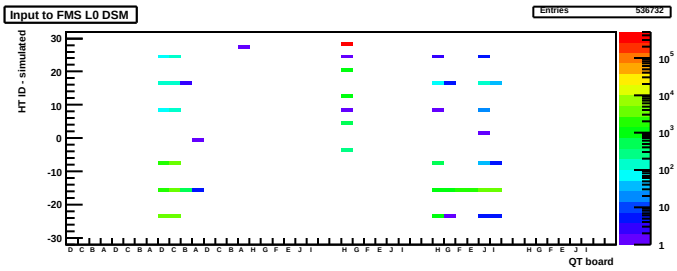

Input to FMS L0 DSM
Entries 2.413354e+07
QT8(3) sum
QT board

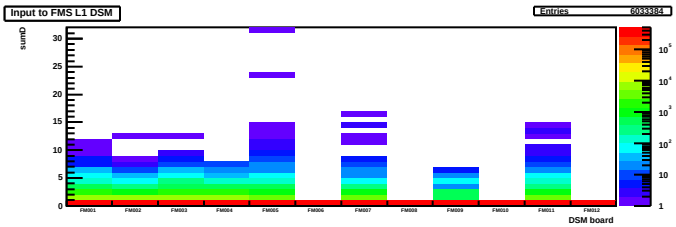

Input to FMS L0 DSM
Entries 287693
QT8(3) sum - simulated
QT board

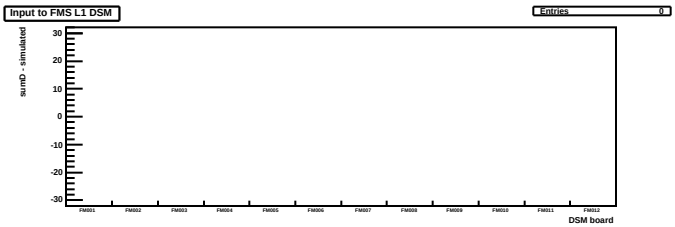

Input to FMS L0 DSM
Entries 2.413354e+07
HT ADC
QT board

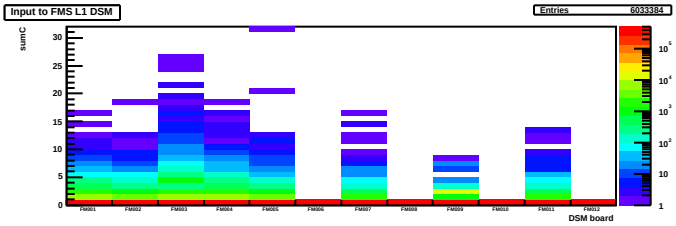

Input to FMS L0 DSM
Entries 27889
HT ADC - simulated
QT board

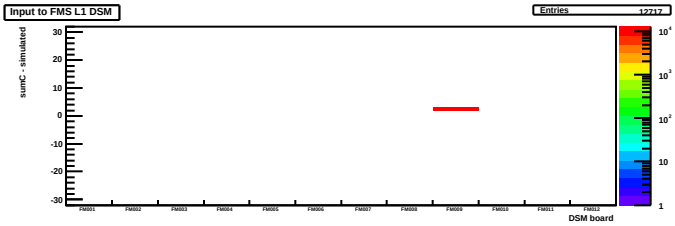

Input to FMS L0 DSM
Entries 2.413354e+07
HT ID
QT board

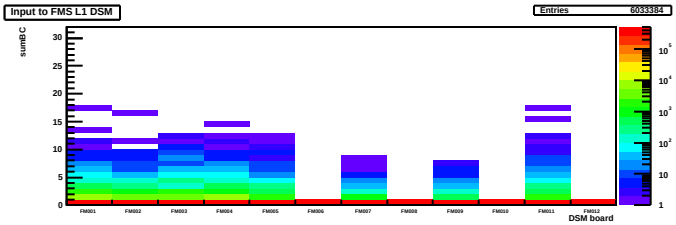

Input to FMS L0 DSM
Entries 536732
HT ID - simulated
QT board

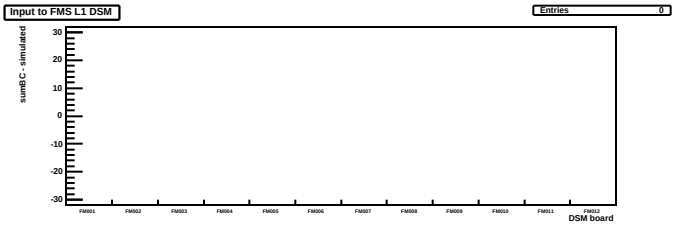

Input to FMS L1 DSM
Entries 6033384
sumD
DSM board

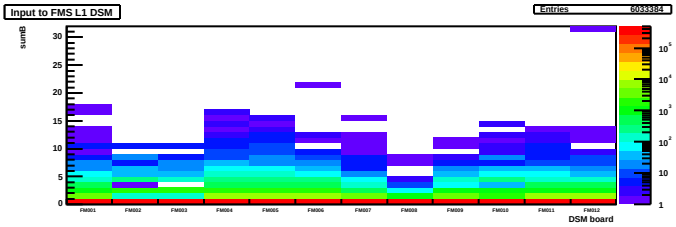

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

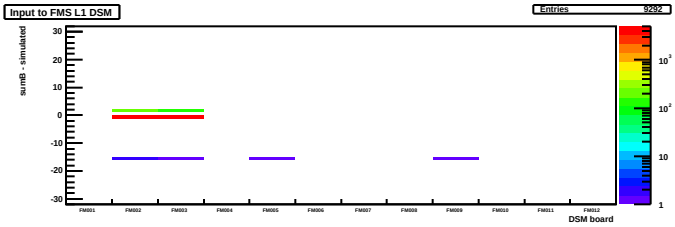

Input to FMS L1 DSM
Entries 6033384
sumC
DSM board

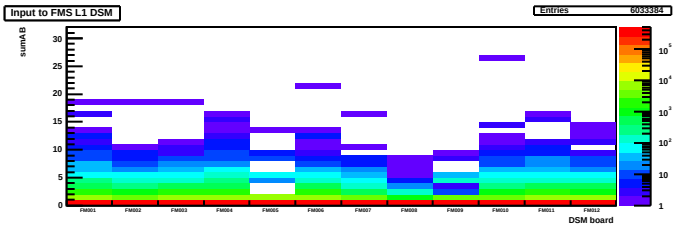

Input to FMS L1 DSM
Entries 12717
sumC - simulated
DSM board

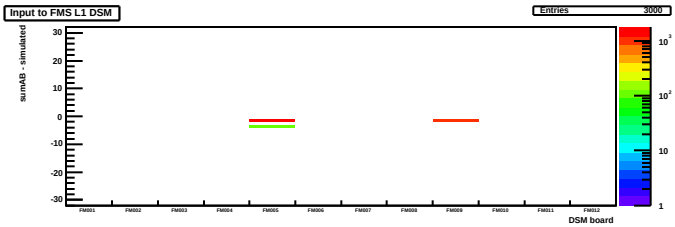

Input to FMS L1 DSM
Entries 6033384
sumBC
DSM board

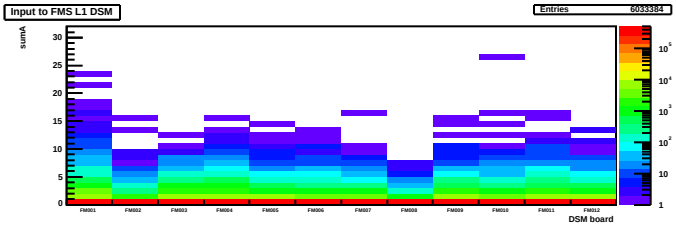

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

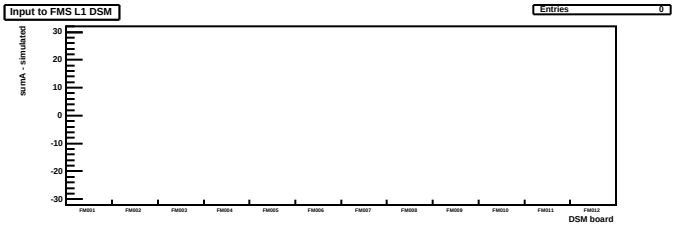

Input to FMS L1 DSM
Entries 6033384
sumB
DSM board

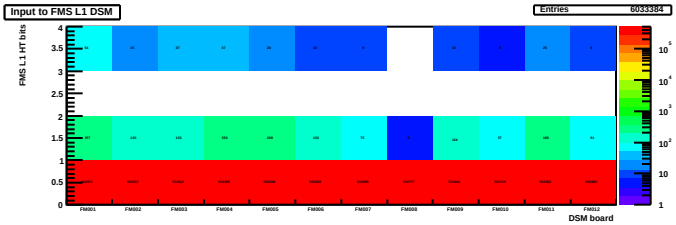

Input to FMS L1 DSM
Entries 9292
sumB - simulated
DSM board

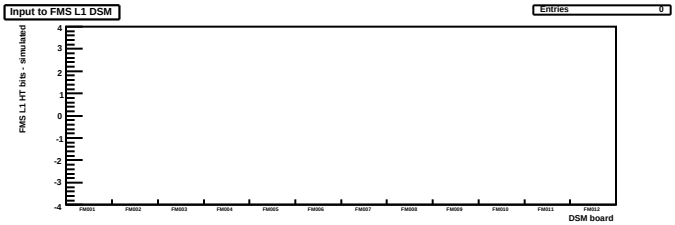

Input to FMS L1 DSM
Entries 6033384
sumAB
DSM board

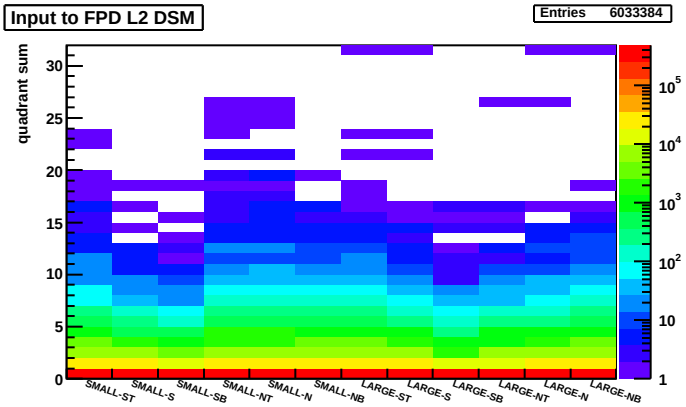

Input to FMS L1 DSM
Entries 3000
sumAB - simulated
DSM board

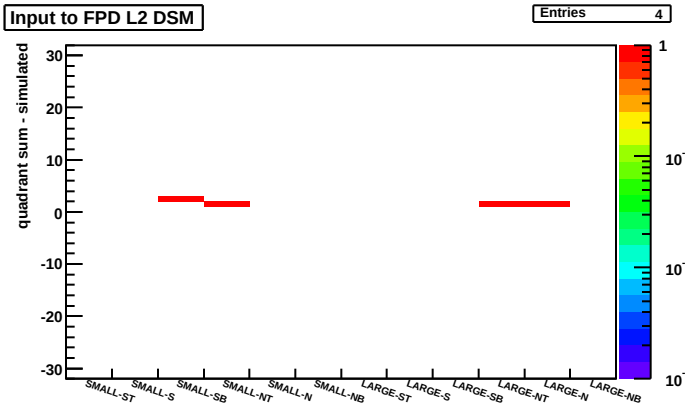

Input to FMS L1 DSM
Entries 6033384
sumA
DSM board

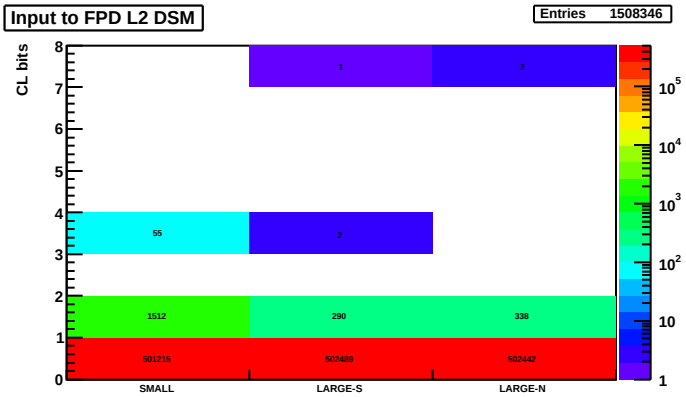

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

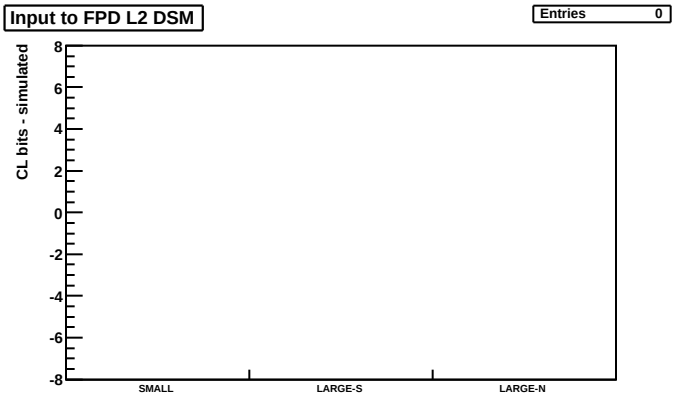

Input to FMS L1 DSM
Entries 6033384
FMS L1 HT bits
DSM board

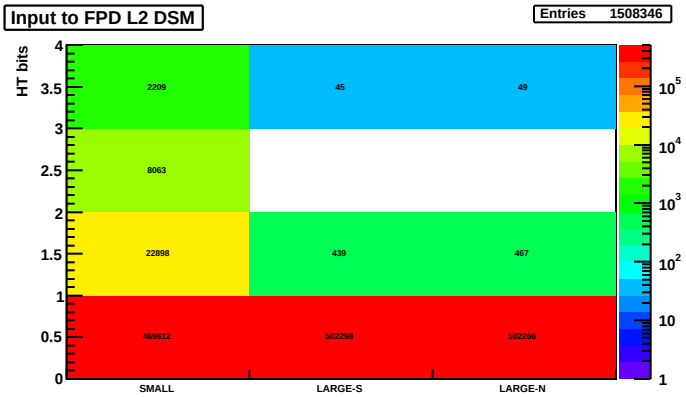

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

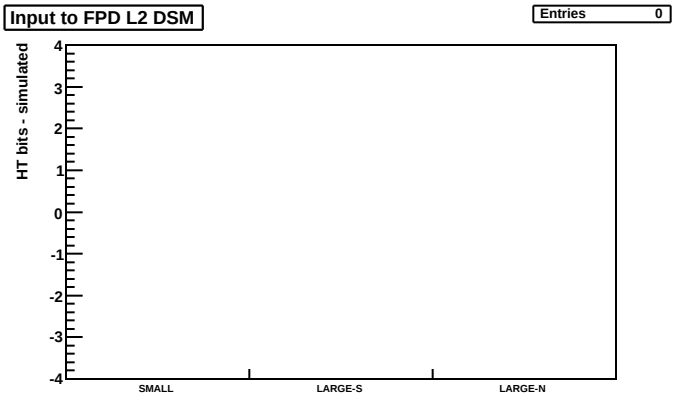

Input to FPD L2 DSM
Entries 6033384
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

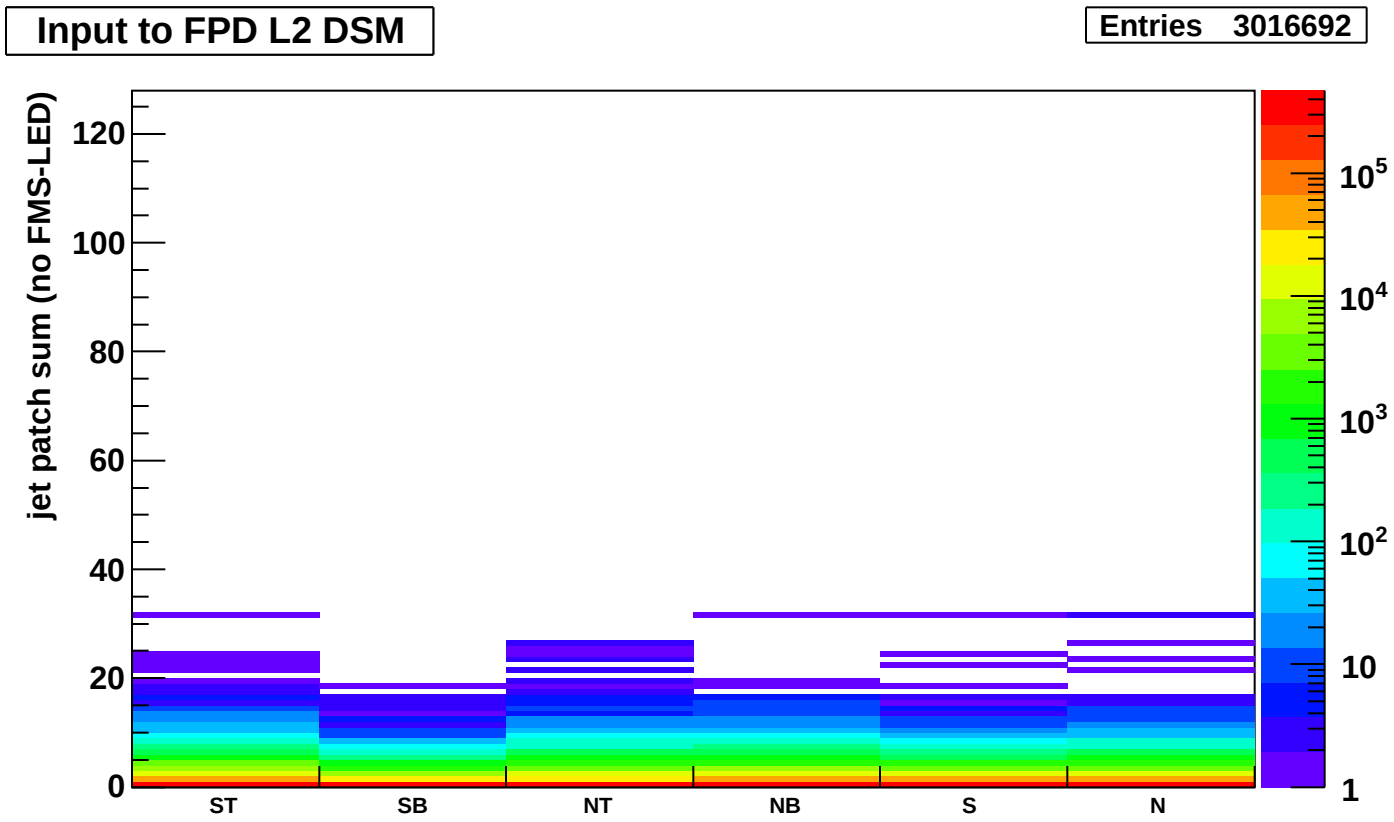

Input to FPD L2 DSM
Entries 4
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

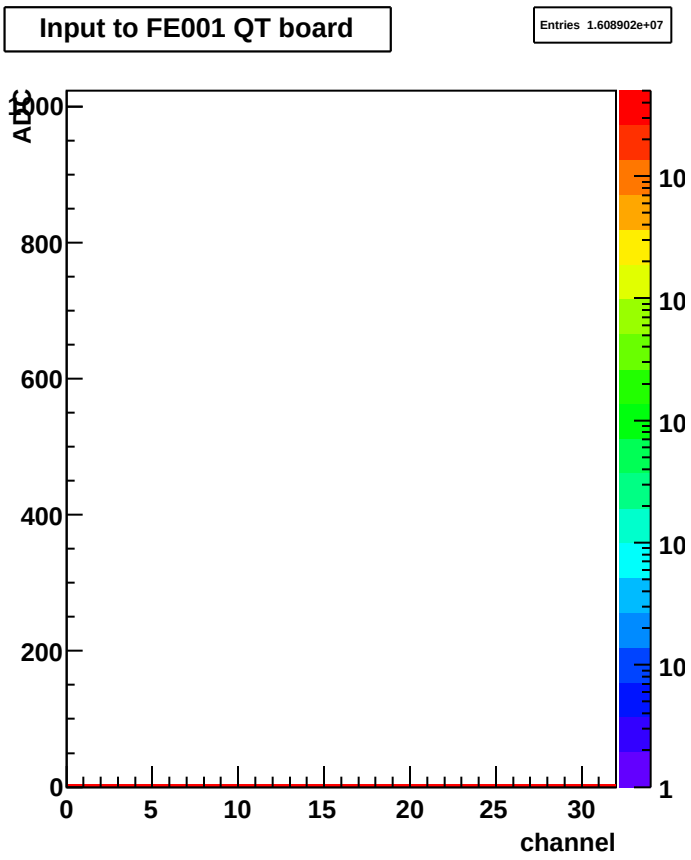

Input to FPD L2 DSM
Entries 1508346
CL bits
1
2
55
2
1512
290
338
501215
502489
502442
SMALL
LARGE-S
LARGE-N

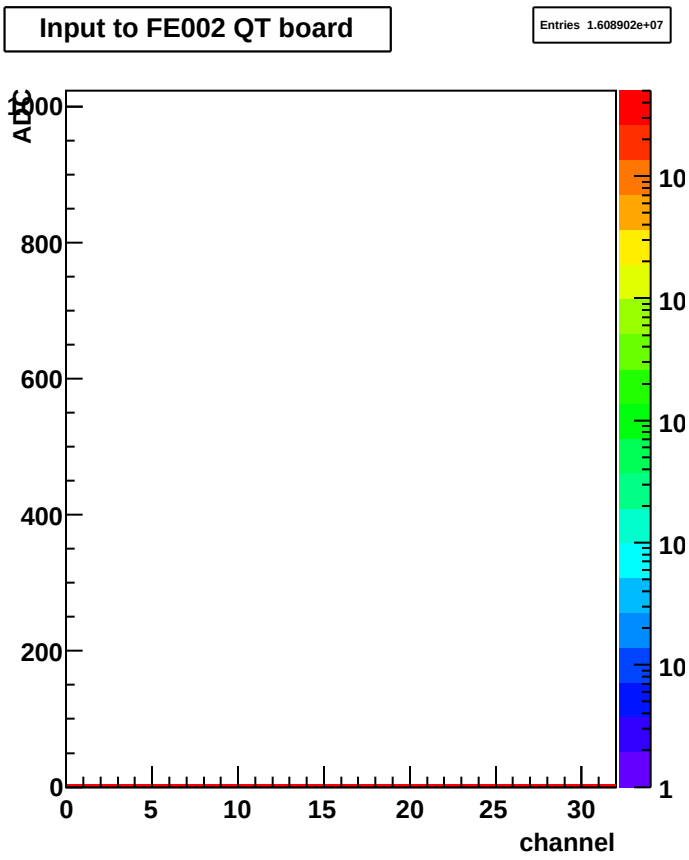

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

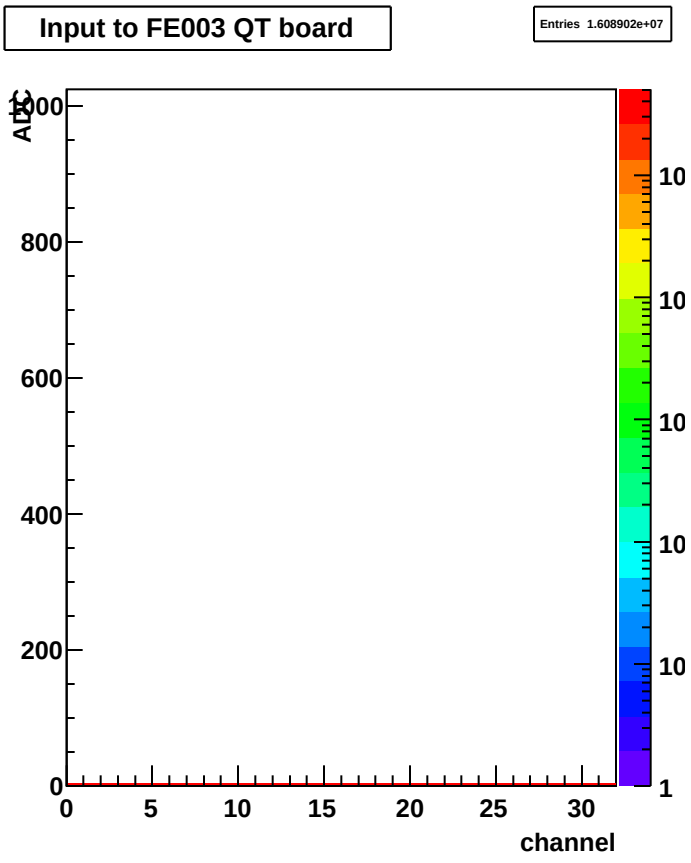

Input to FPD L2 DSM
Entries 1508346
HT bits
2209
45
49
8063
22898
439
467
469612
502298
502266
SMALL
LARGE-S
LARGE-N

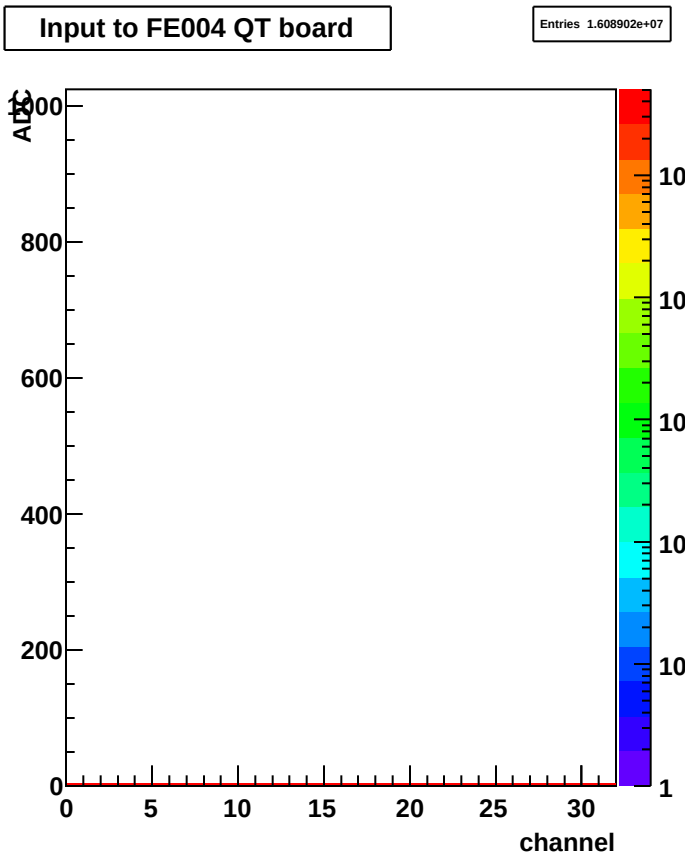

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

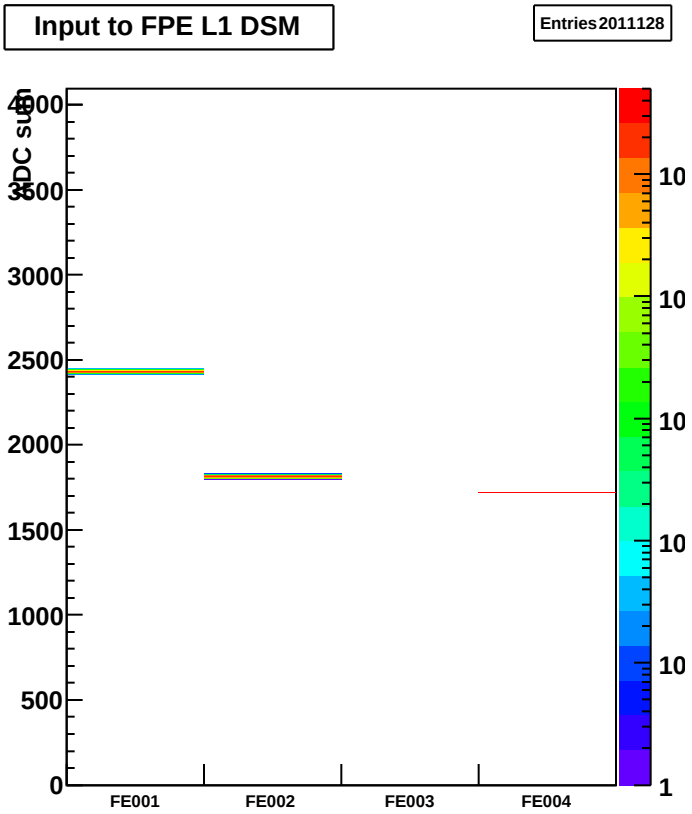

Input to FPD L2 DSM
Entries 3016692
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

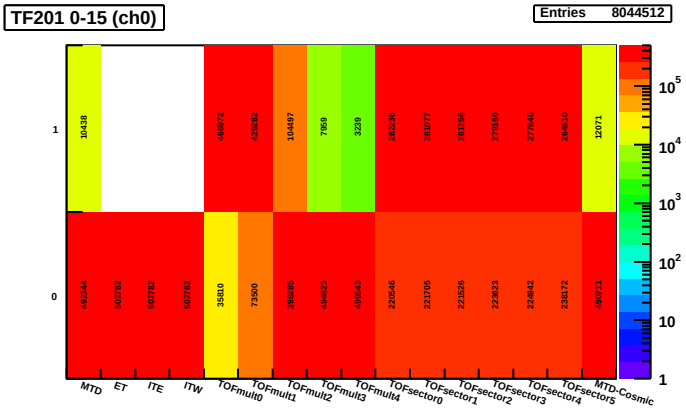

Input to FE001 QT board
Entries 1.608902e+07
ADC
channel

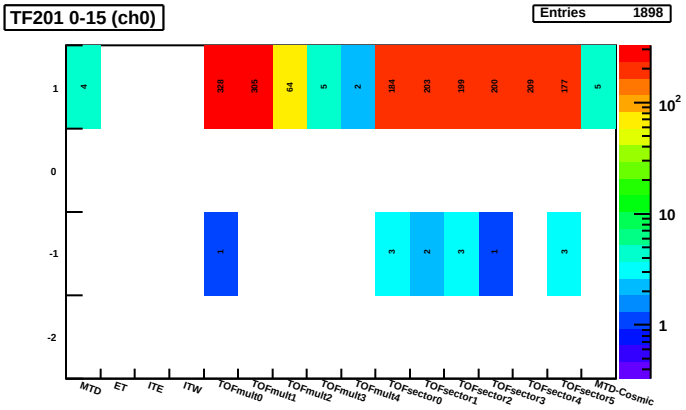

Input to FE002 QT board
Entries 1.608902e+07
ADC
channel

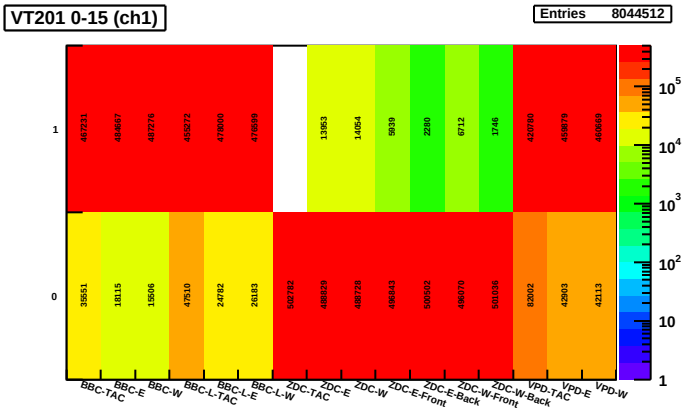

Input to FE003 QT board
Entries 1.608902e+07
ADC
channel

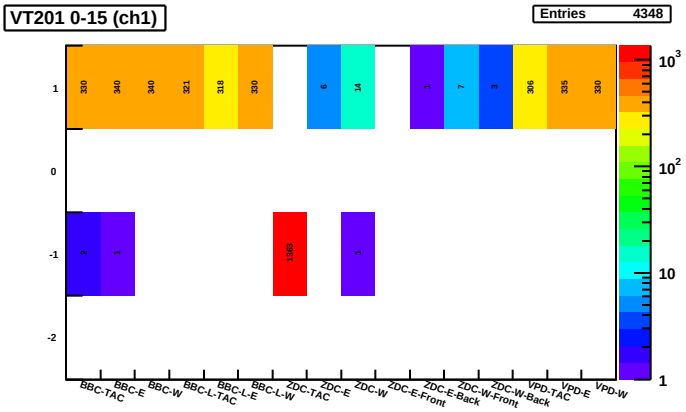

Input to FE004 QT board
Entries 1.608902e+07
ADC
channel

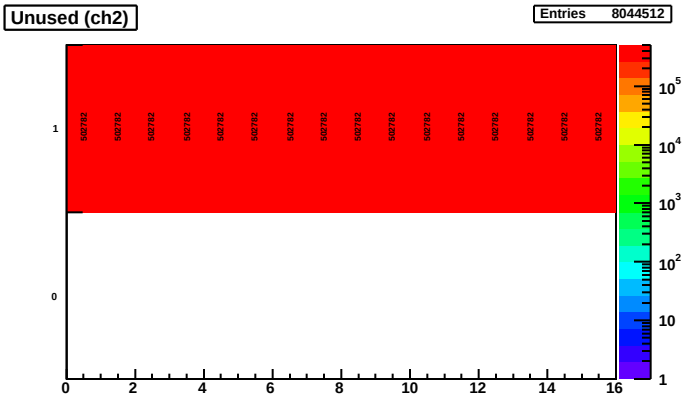

Input to FPE L1 DSM
Entries 2011128
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

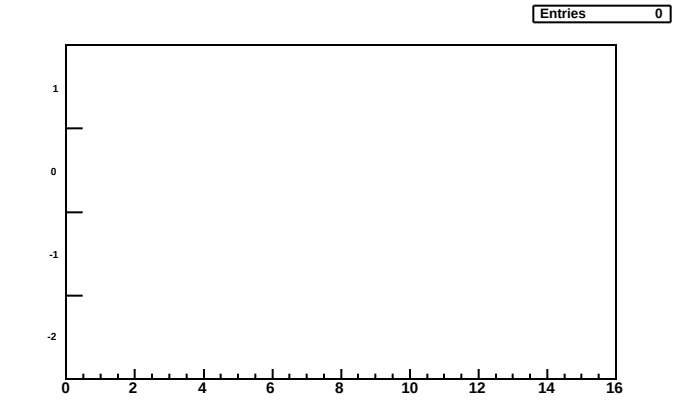

TF201 0-15 (ch0)
Entries 8044512
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

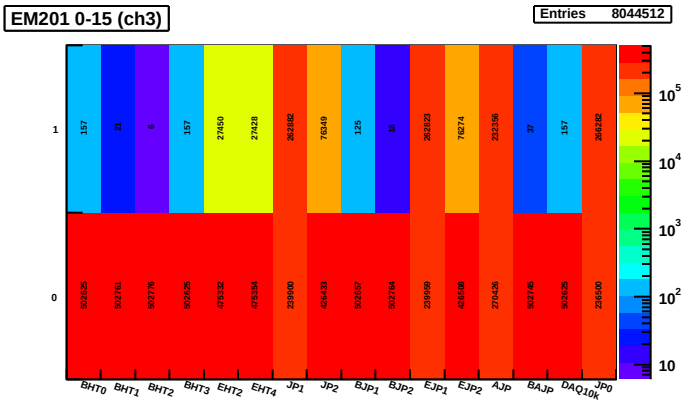

TF201 0-15 (ch0)
Entries 1898
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

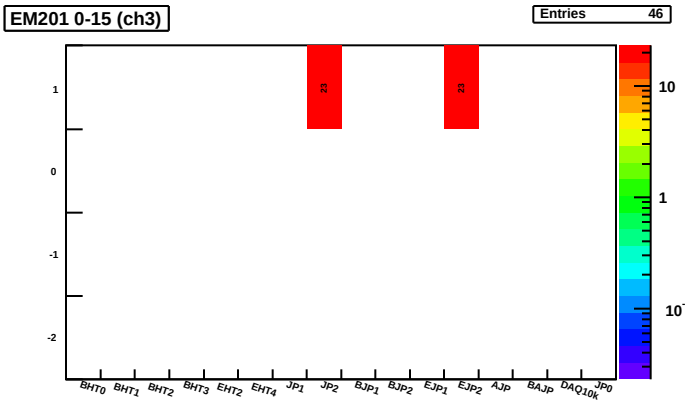

VT201 0-15 (ch1)
Entries 8044512
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4348
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

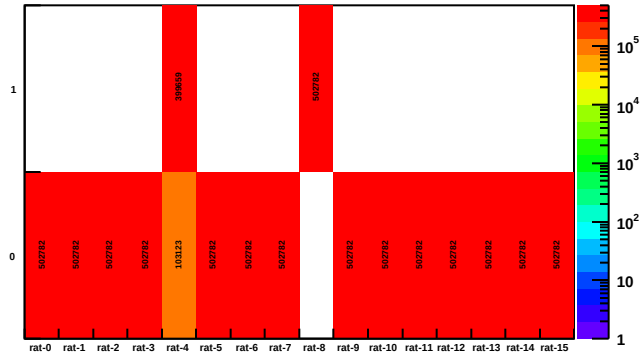

Unused (ch2)
Entries 8044512

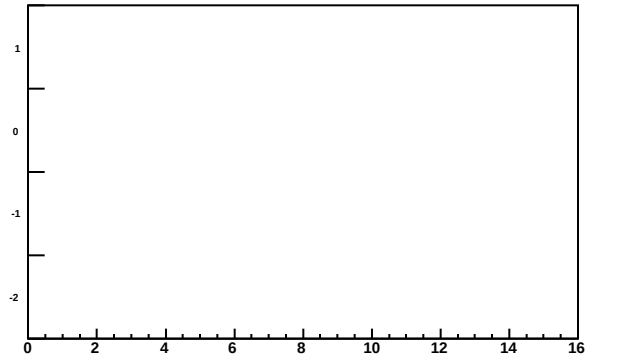

Entries 0


EM201 0-15 (ch3)
Entries 8044512
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

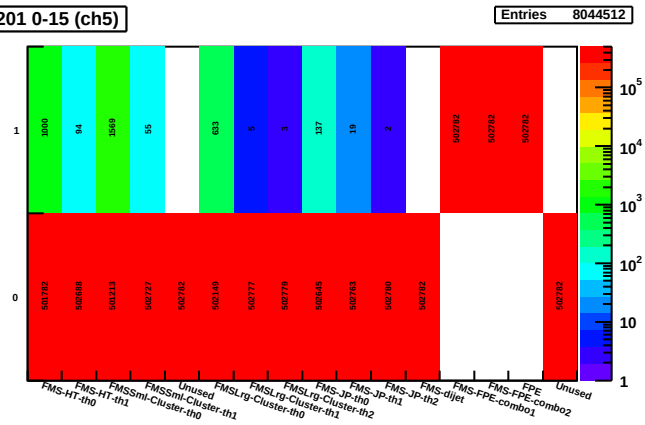

EM201 0-15 (ch3)
Entries 46
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

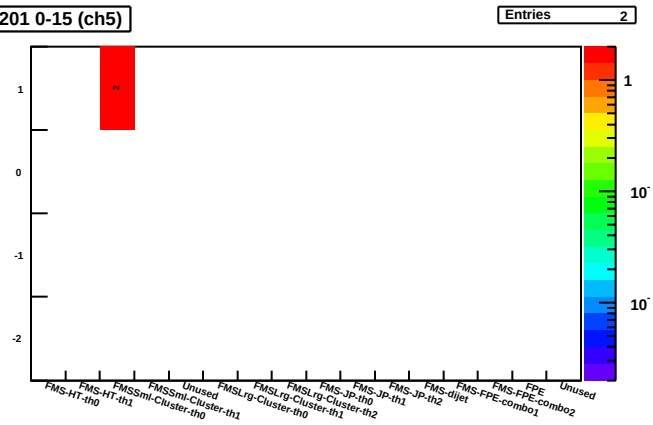




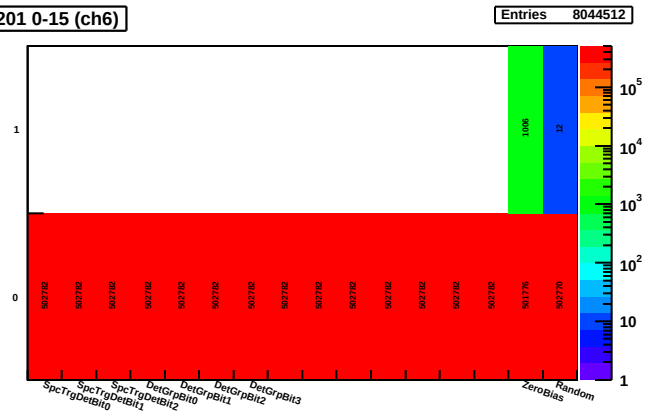
FP201 0-15 (ch5)

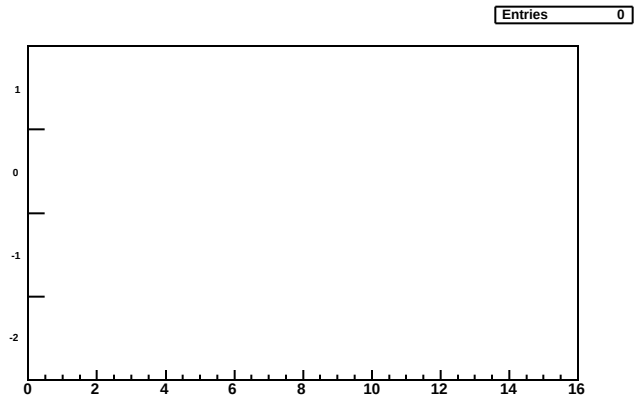
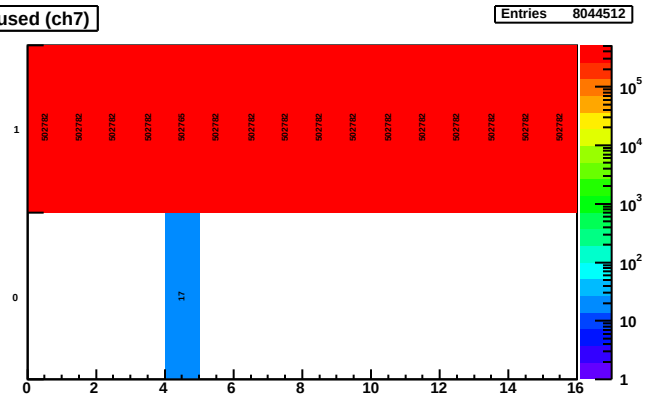
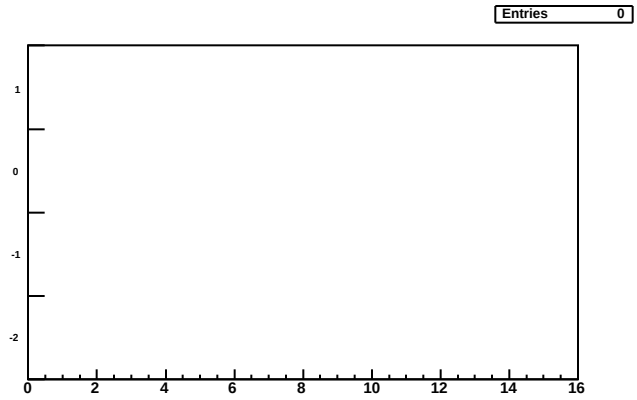


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)