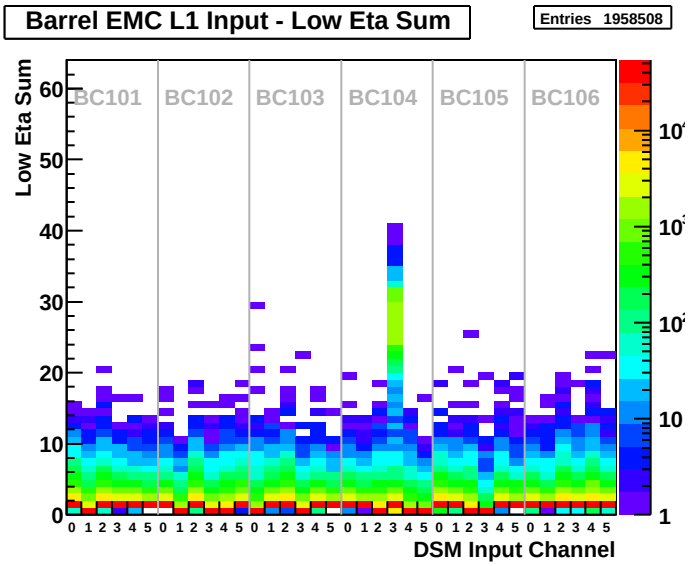
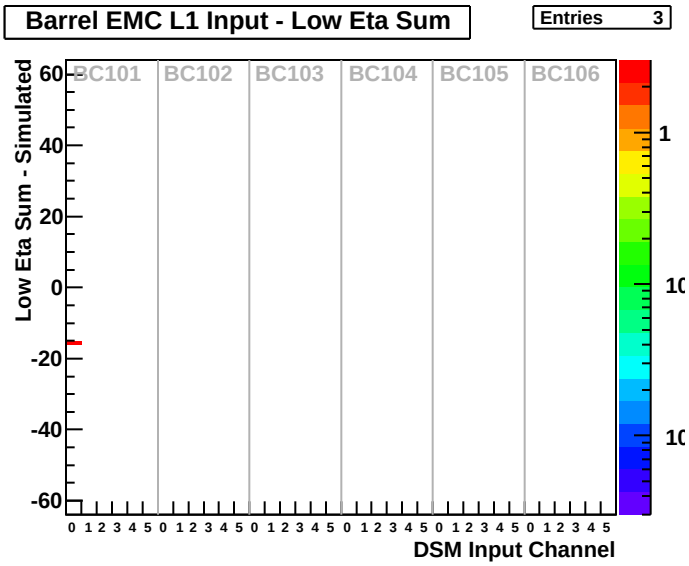
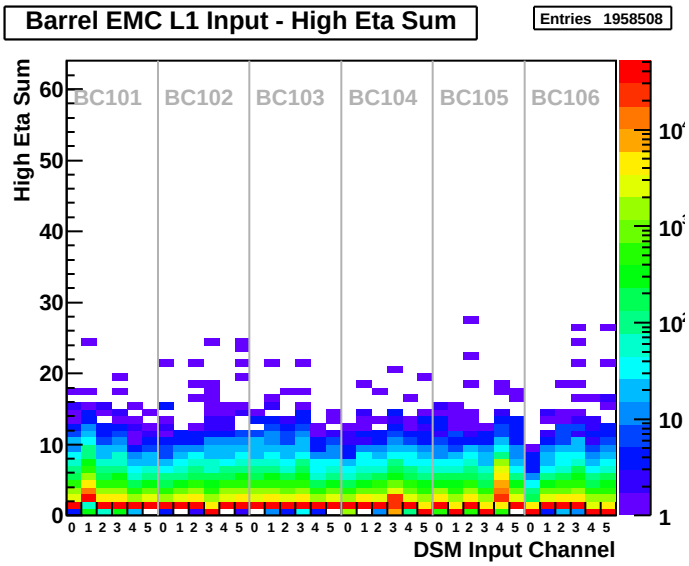
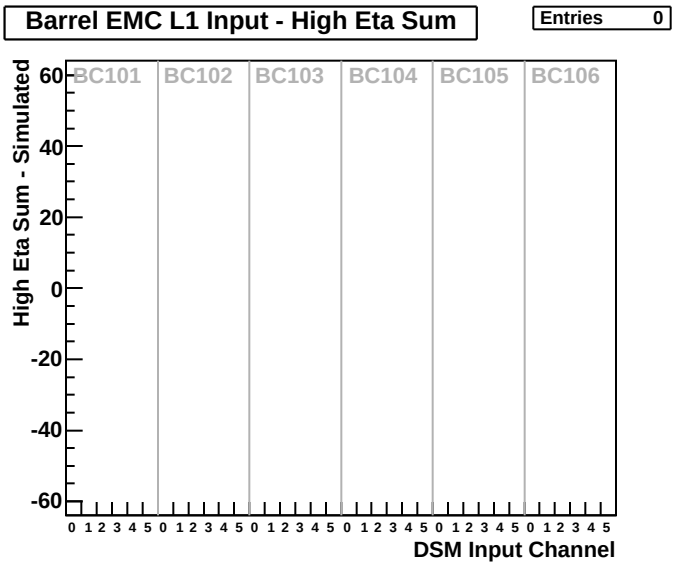
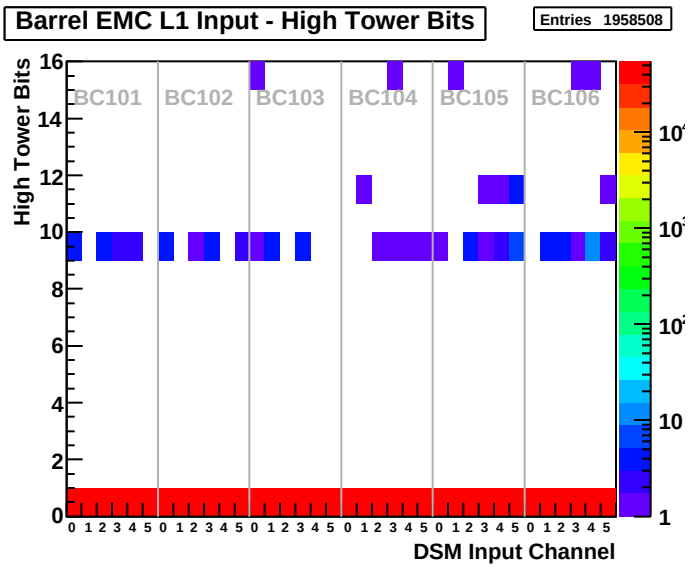
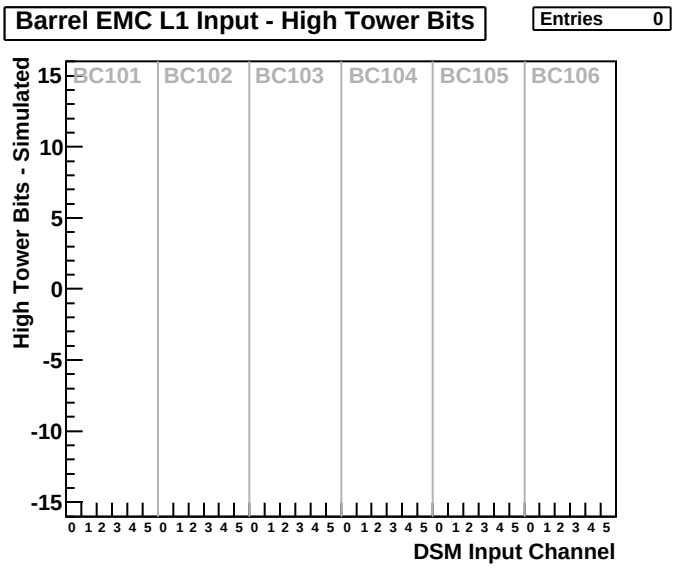

Barrel EMC L1 Input - Low Eta Sum
Entries 1958508
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

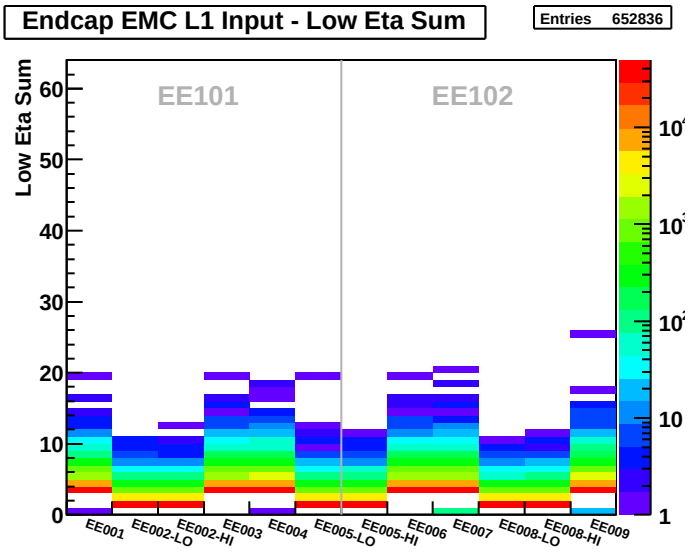

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

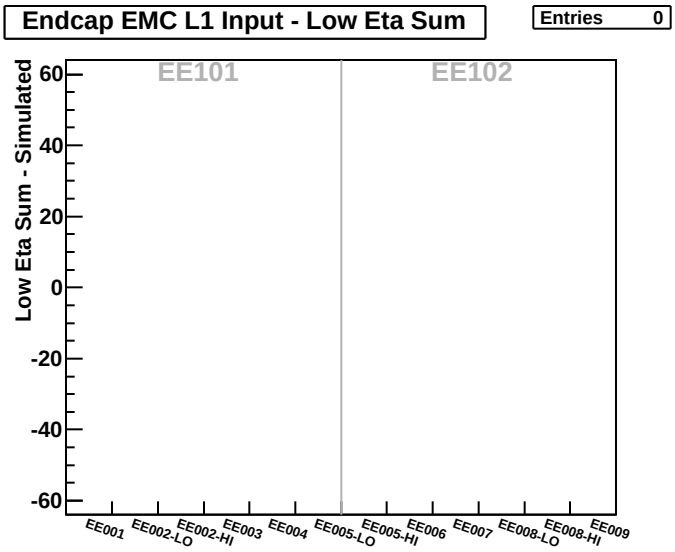

Barrel EMC L1 Input - High Eta Sum
Entries 1958508
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

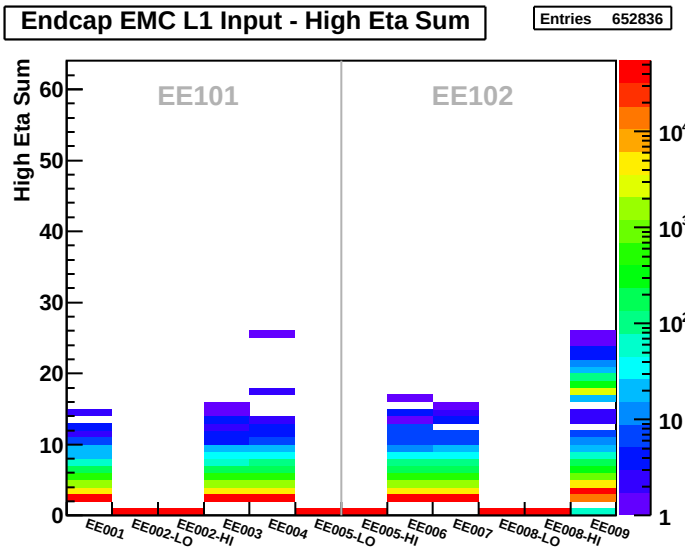

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

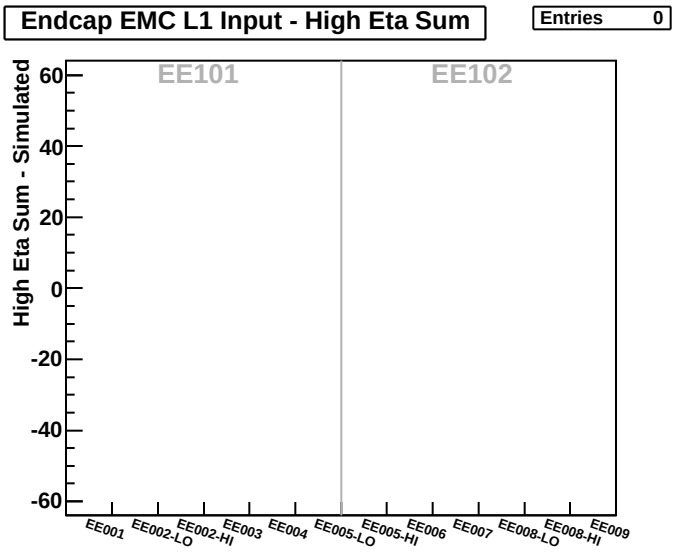

Barrel EMC L1 Input - High Tower Bits
Entries 1958508
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

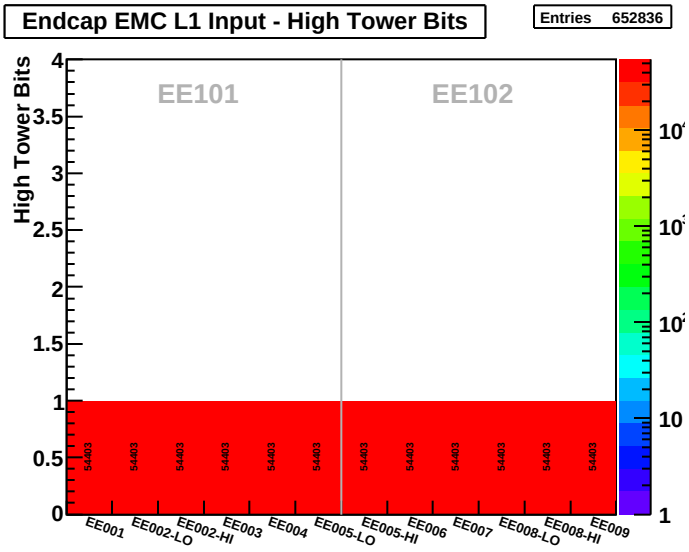

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

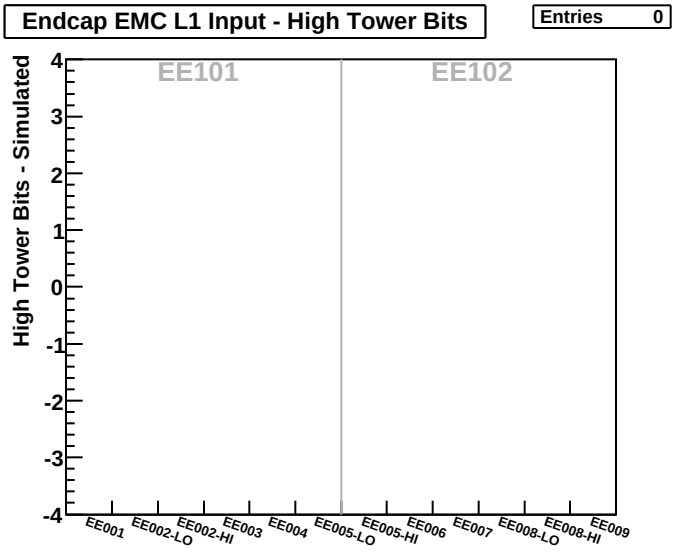

Endcap EMC L1 Input - Low Eta Sum
Entries 652836
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

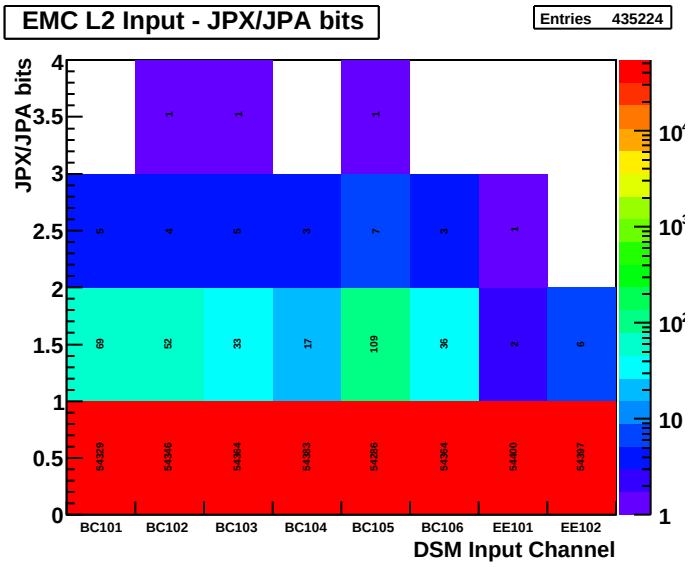

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

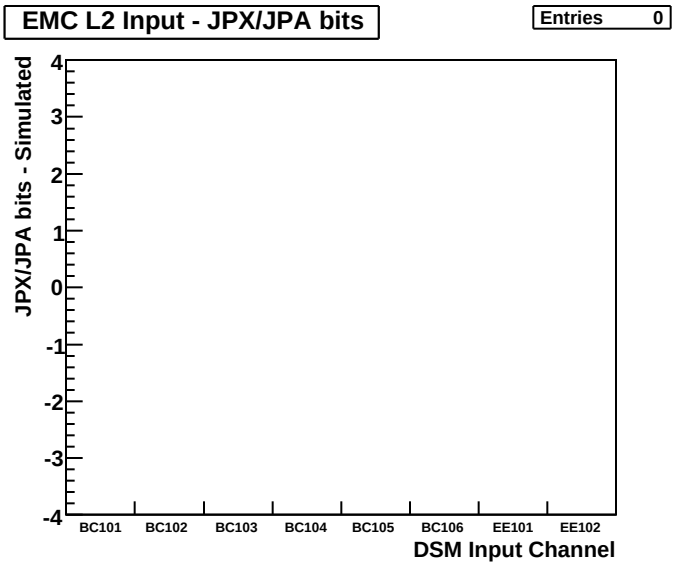

Endcap EMC L1 Input - High Eta Sum
Entries 652836
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

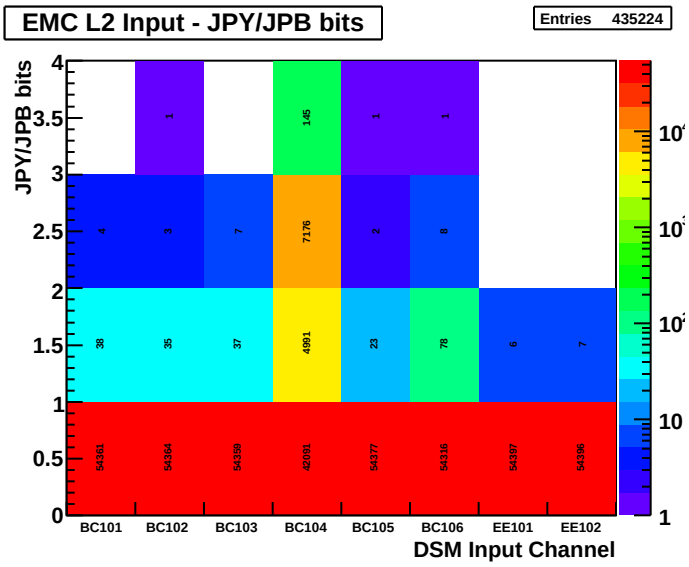

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

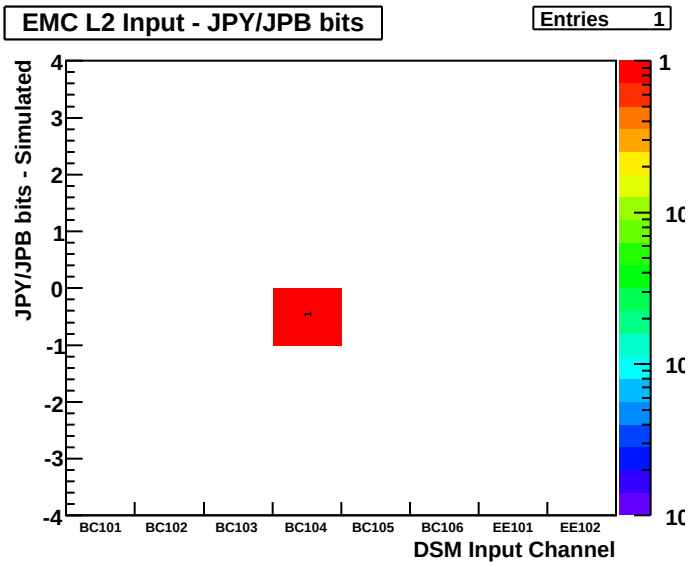

Endcap EMC L1 Input - High Tower Bits
Entries 652836
High Tower Bits
EE101
EE102
54403
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

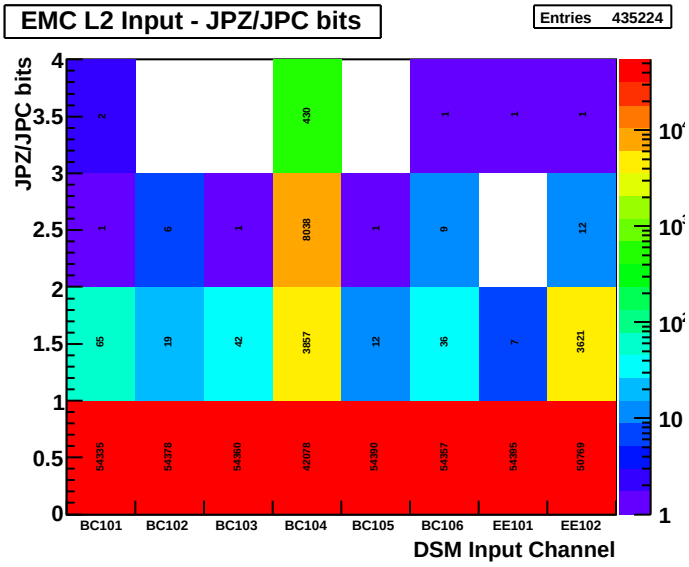

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

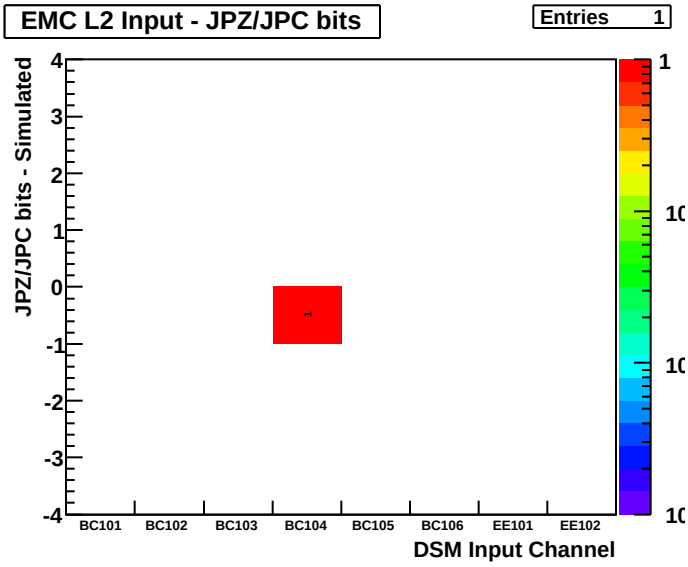

EMC L2 Input - JPX/JPA bits
Entries 435224
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

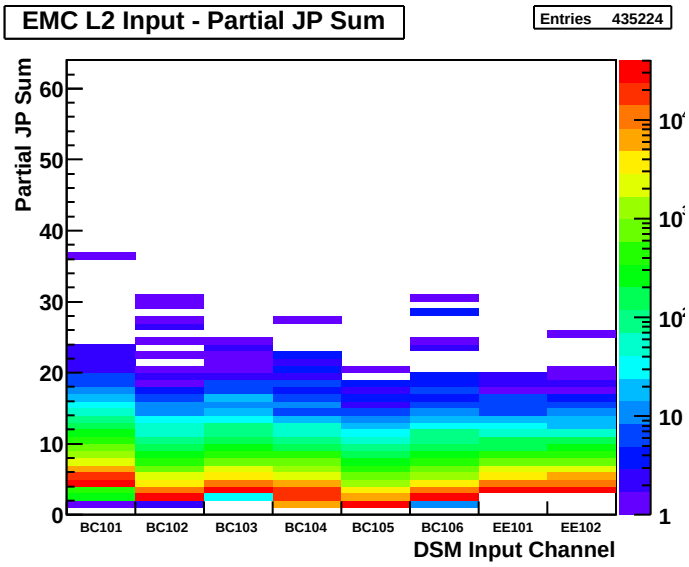

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

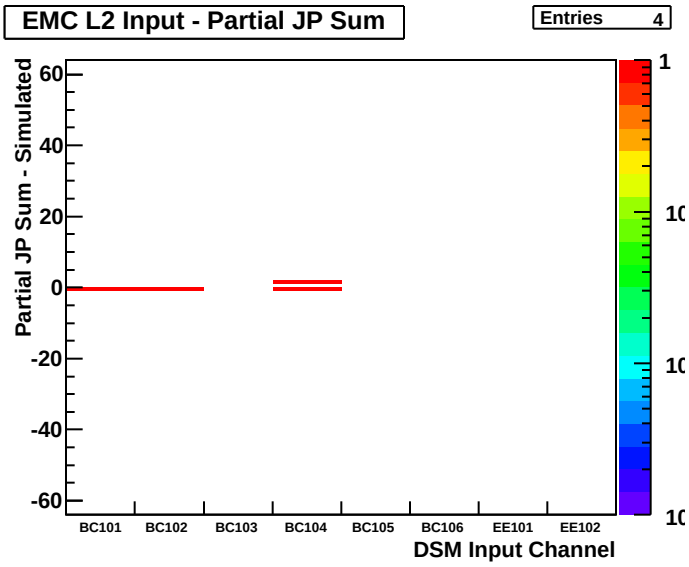

EMC L2 Input - JPY/JPB bits
Entries 435224
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

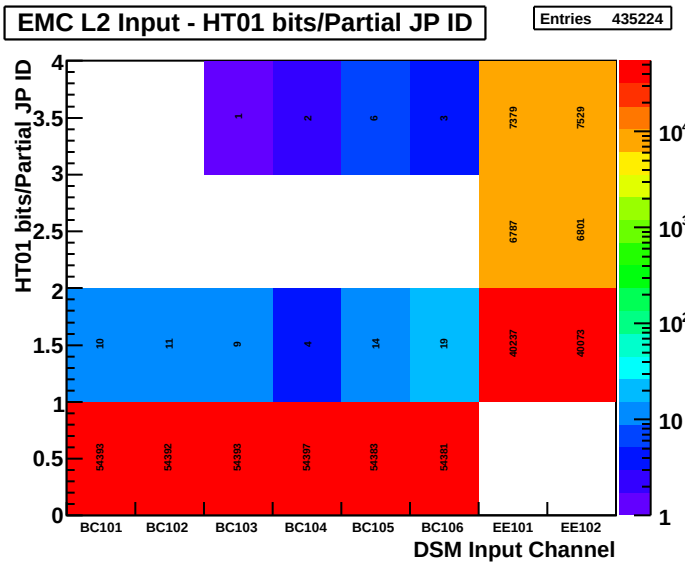

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

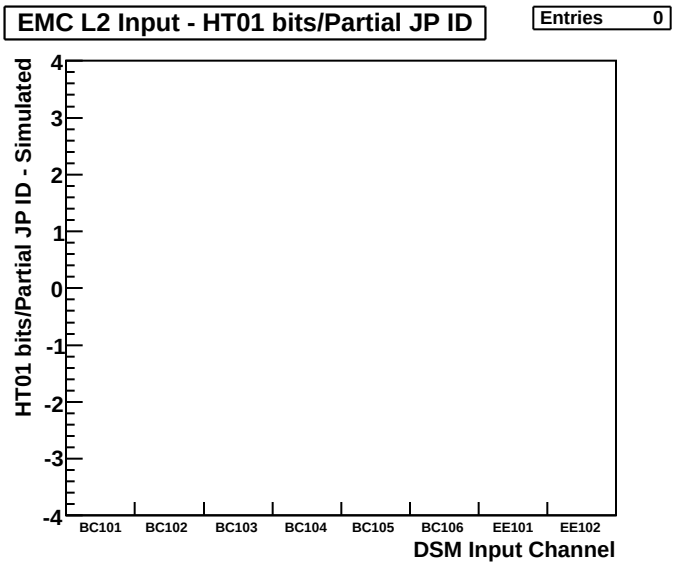

EMC L2 Input - JPZ/JPC bits
Entries 435224
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

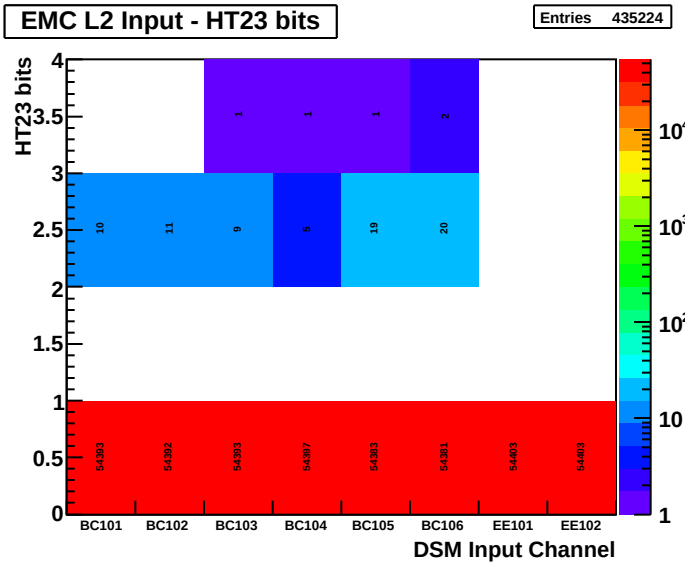

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

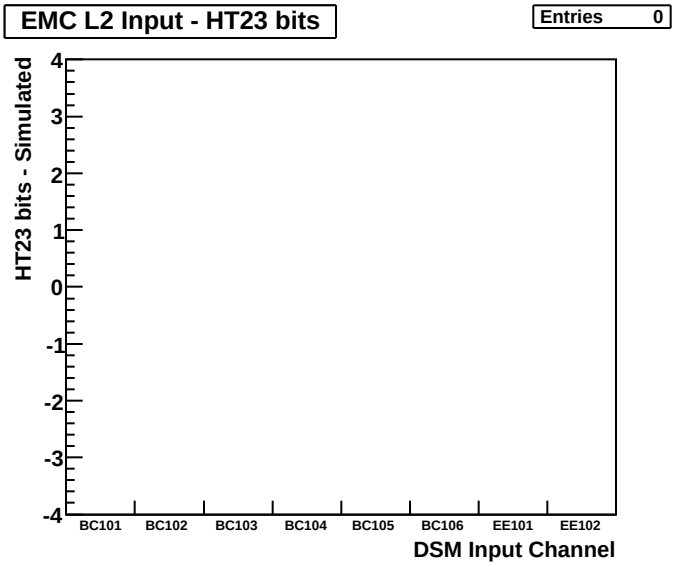

EMC L2 Input - Partial JP Sum
Entries 435224
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

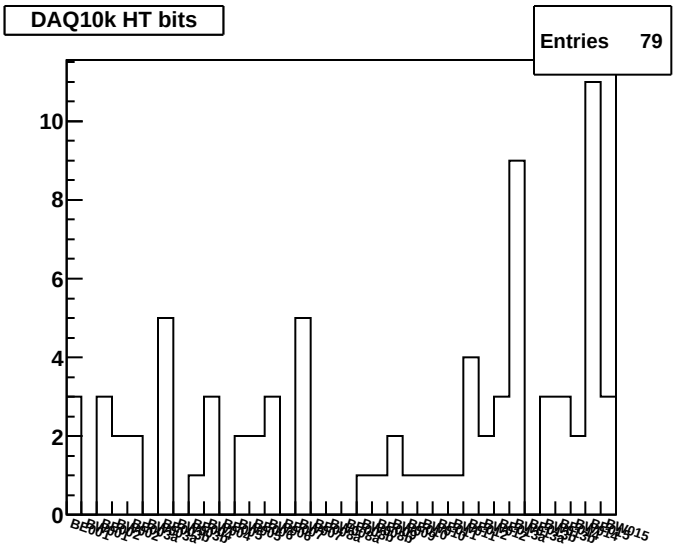

EMC L2 Input - Partial JP Sum
Entries 4
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

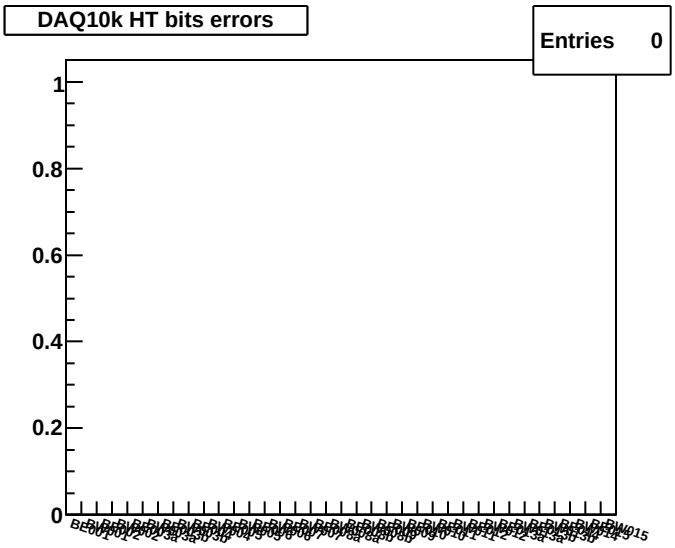

EMC L2 Input - HT01 bits/Partial JP ID
Entries 435224
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

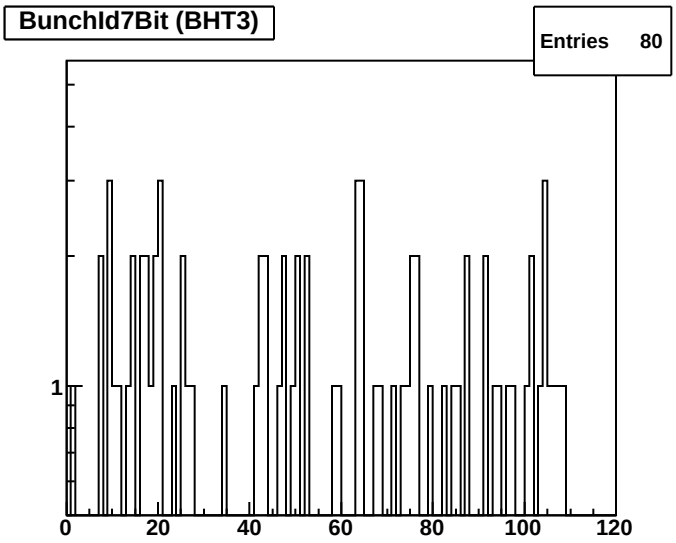

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

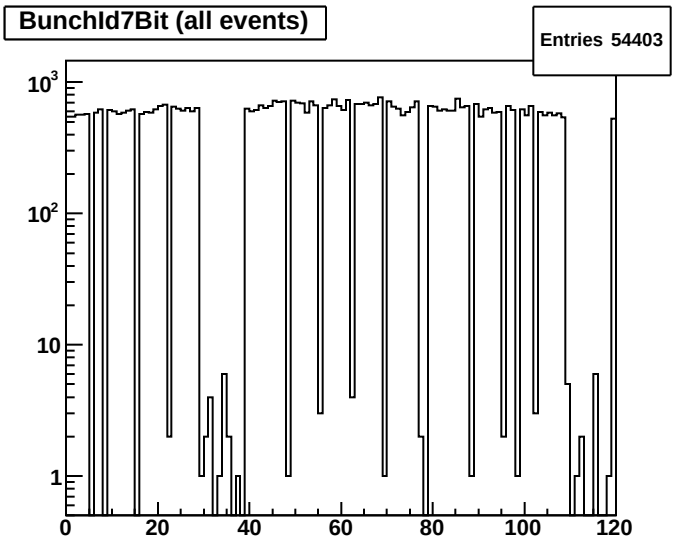

EMC L2 Input - HT23 bits
Entries 435224
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

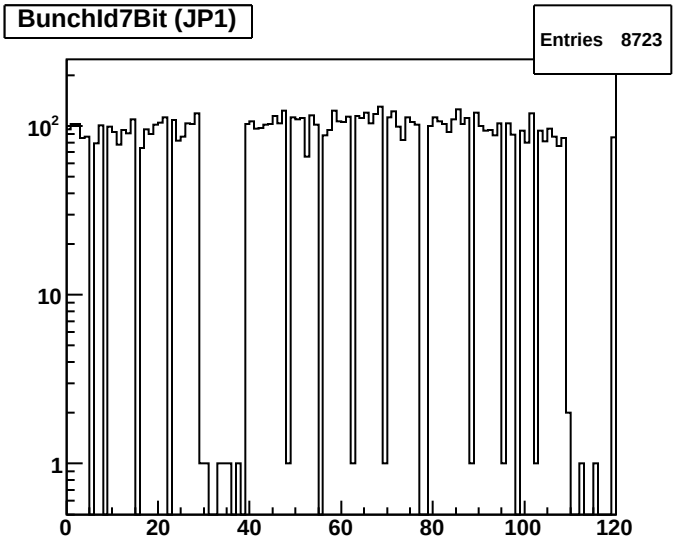

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

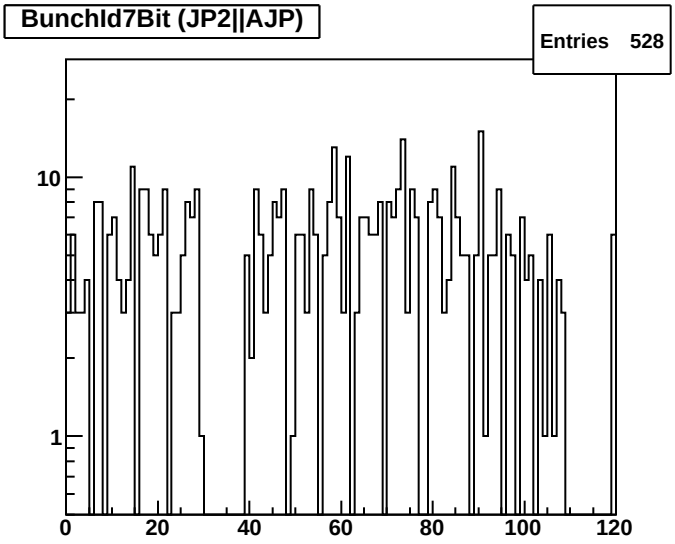

DAQ10k HT bits
Entries 79

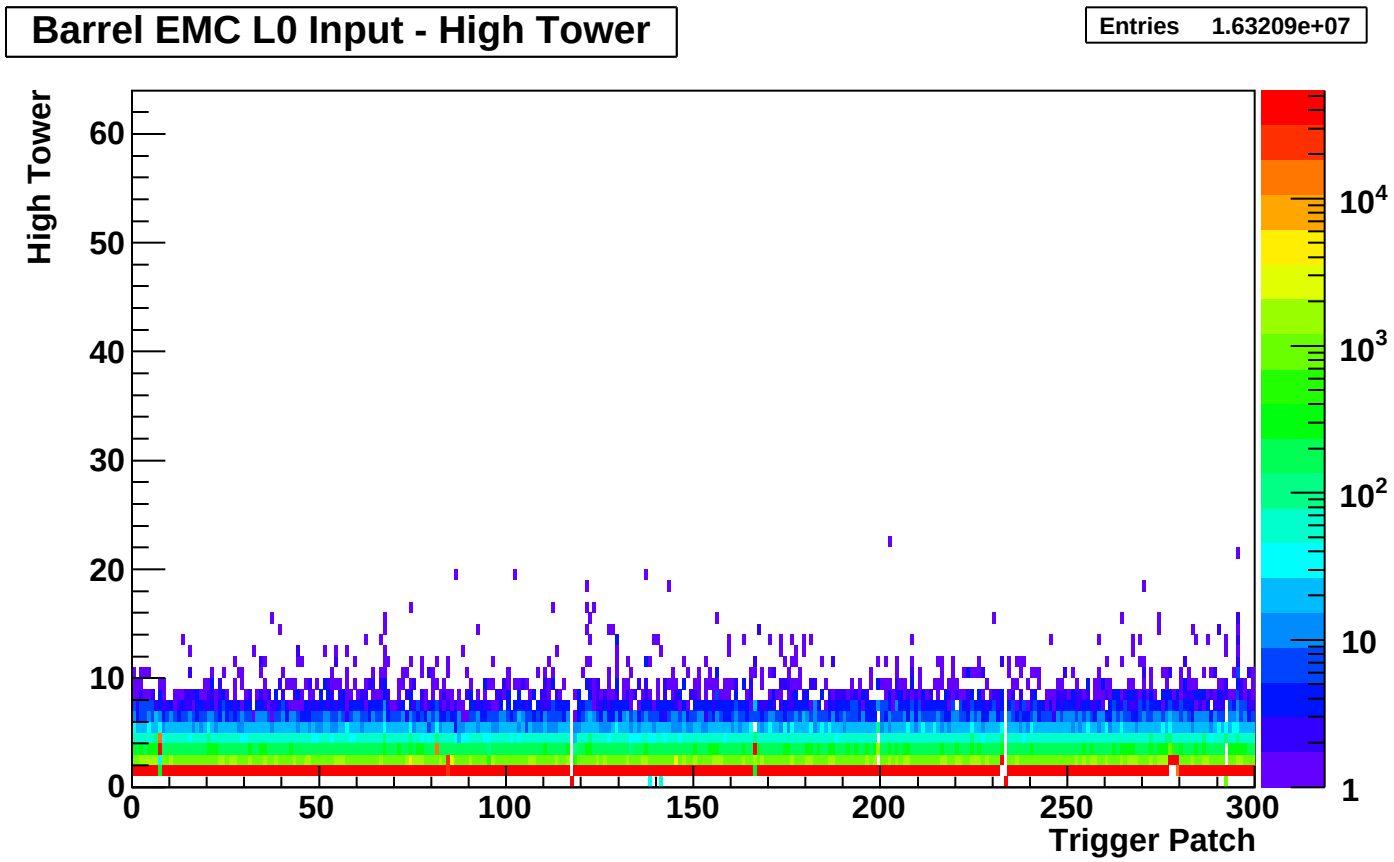

DAQ10k HT bits errors
Entries 0

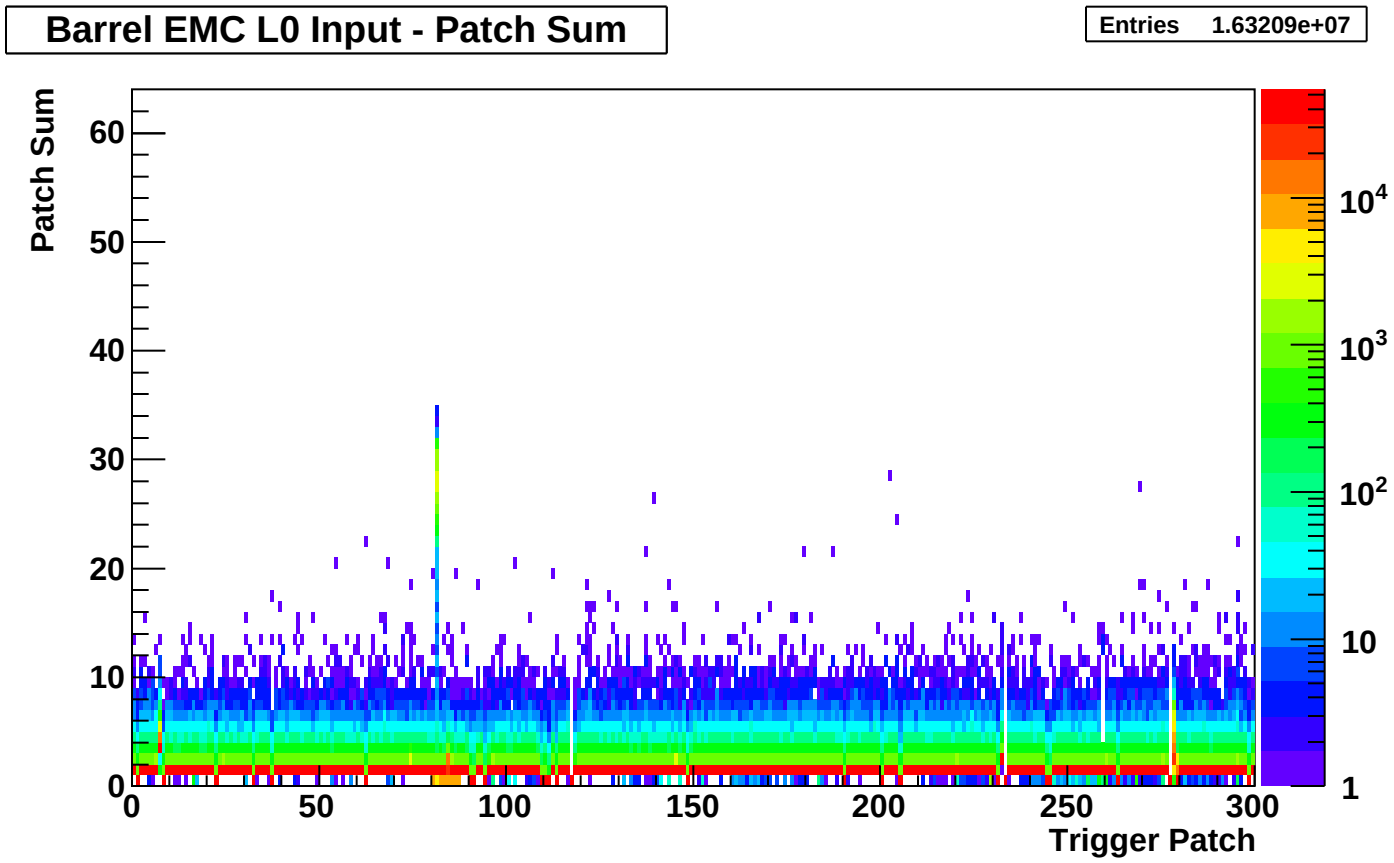

BunchId7Bit (BHT3)
Entries 80

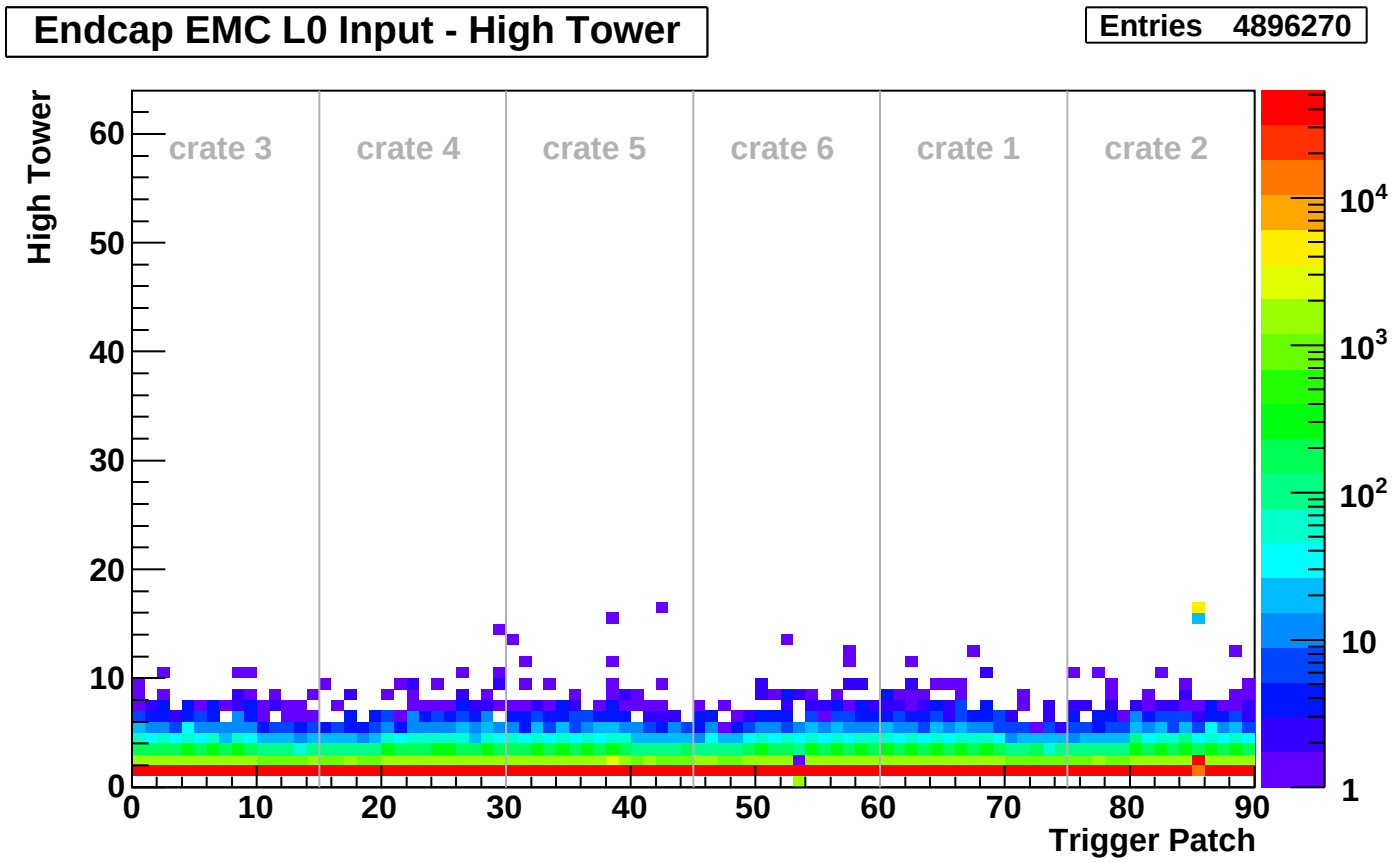

BunchId7Bit (all events)
Entries 54403

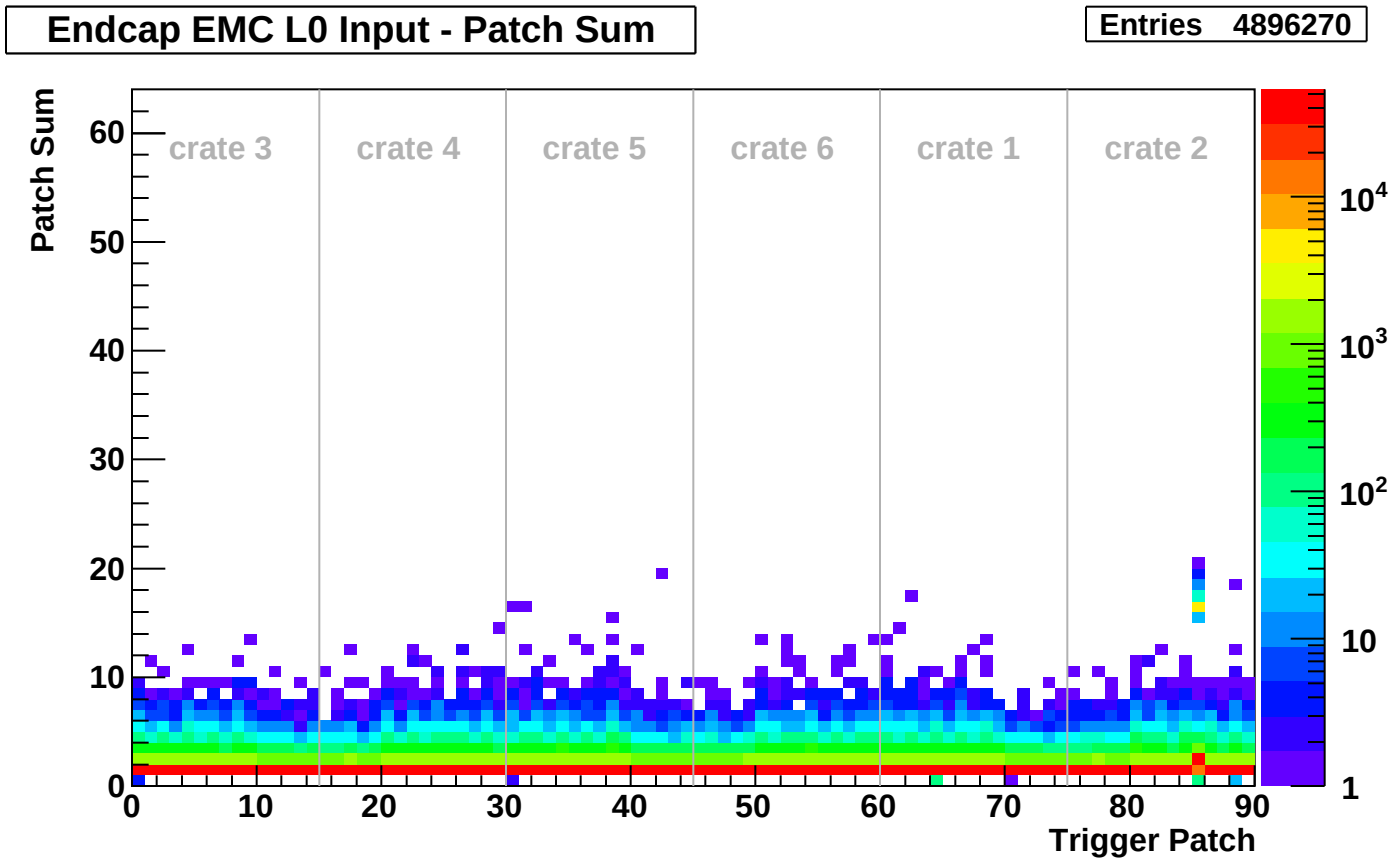

BunchId7Bit (JP1)
Entries 8723

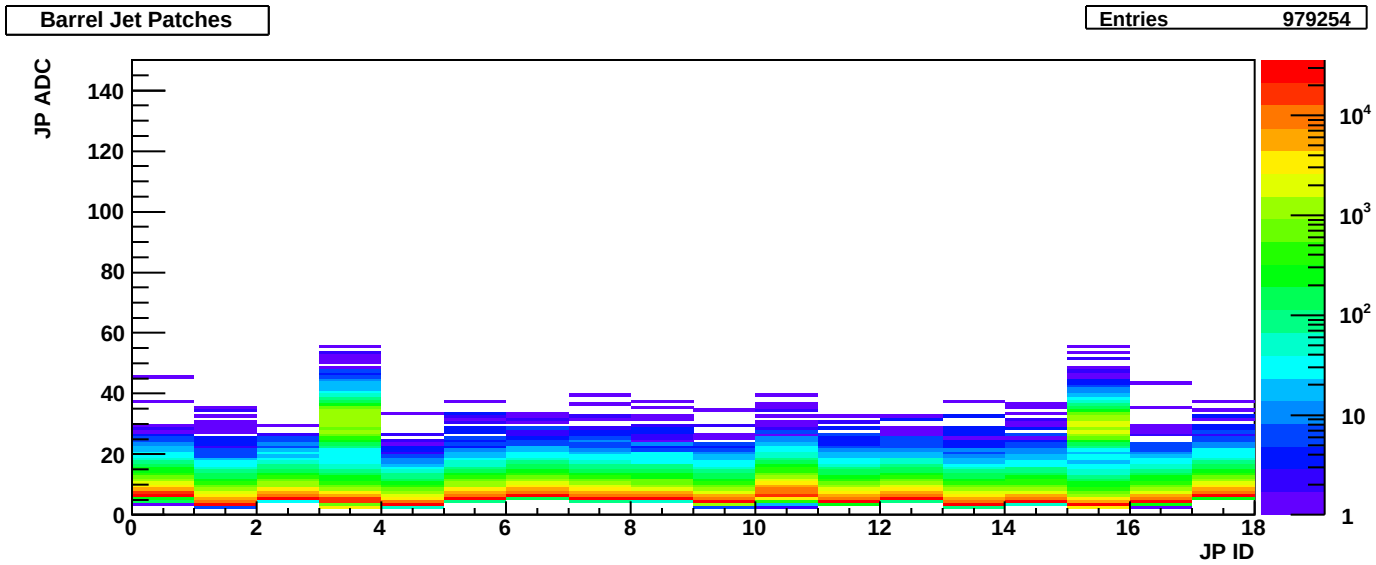

BunchId7Bit (JP2||AJP)
Entries 528

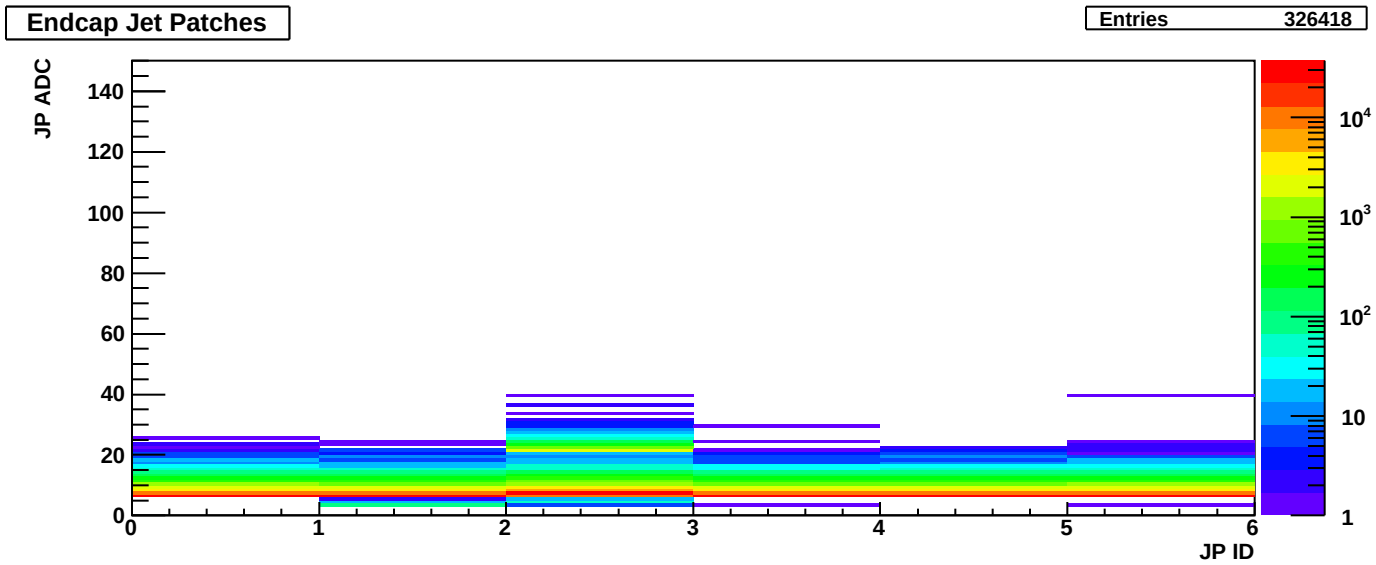

Barrel EMC L0 Input - High Tower
Entries 1.63209e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

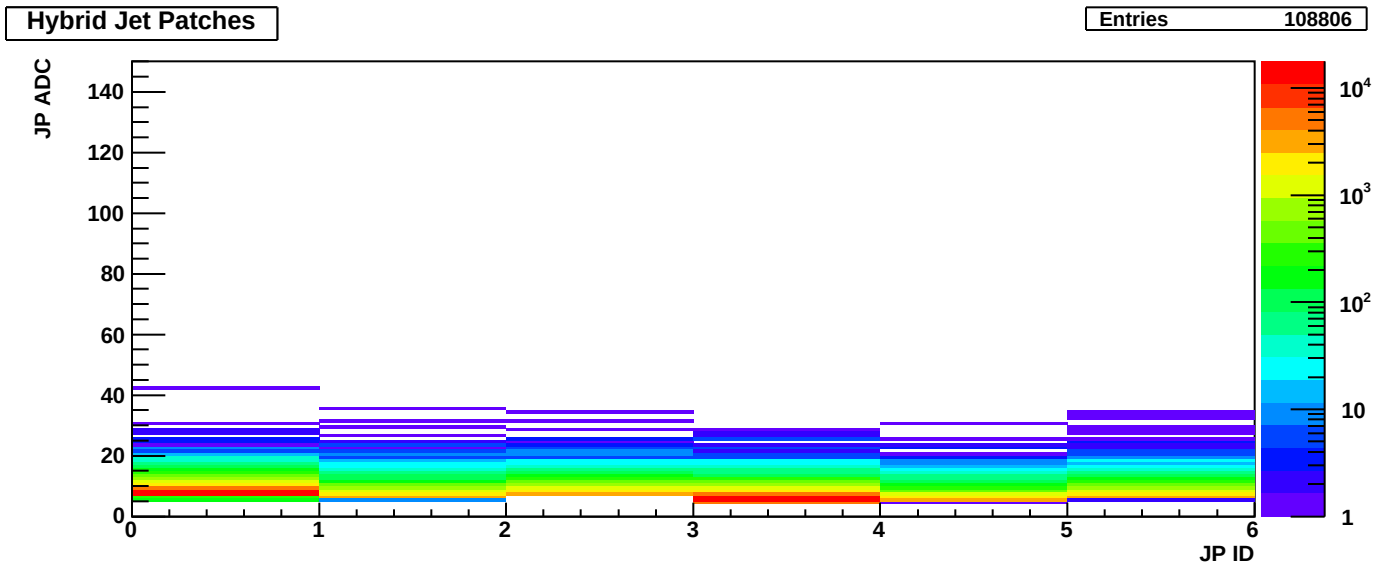

Barrel EMC L0 Input - Patch Sum
Entries 1.63209e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

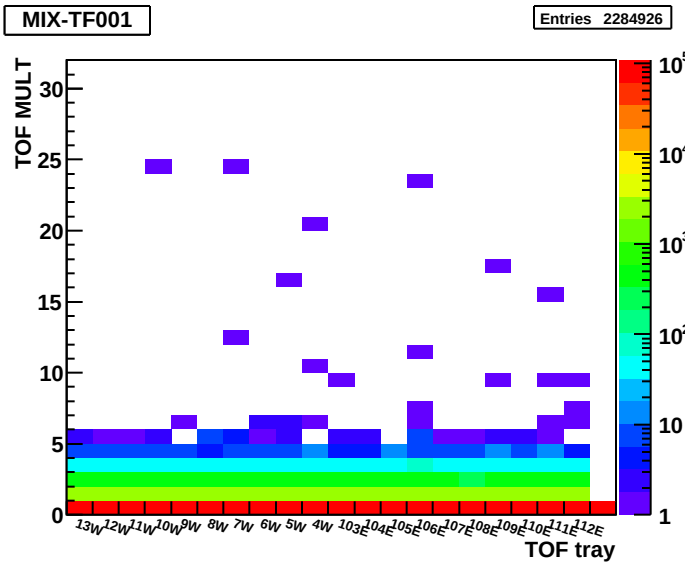

Endcap EMC L0 Input - High Tower
Entries 4896270
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

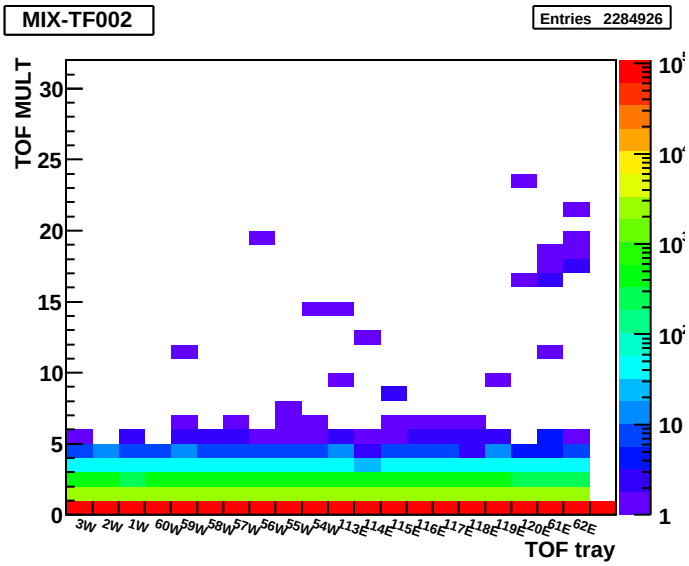

Endcap EMC L0 Input - Patch Sum
Entries 4896270
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

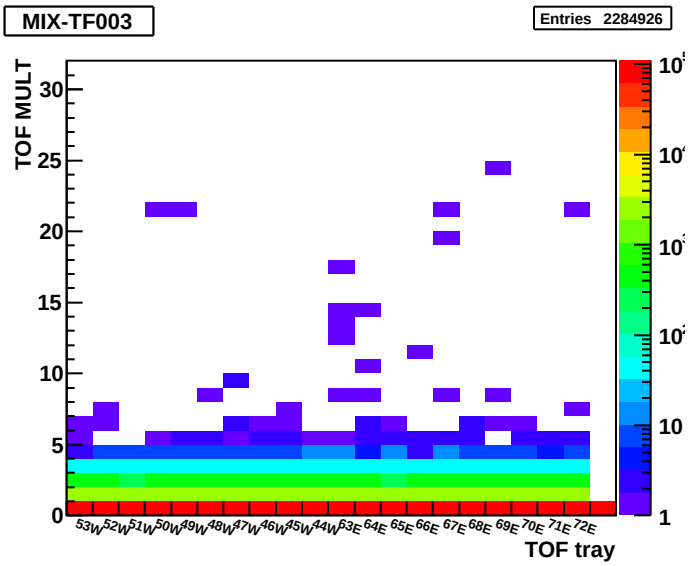

Barrel Jet Patches
Entries 979254
JP ADC
JP ID

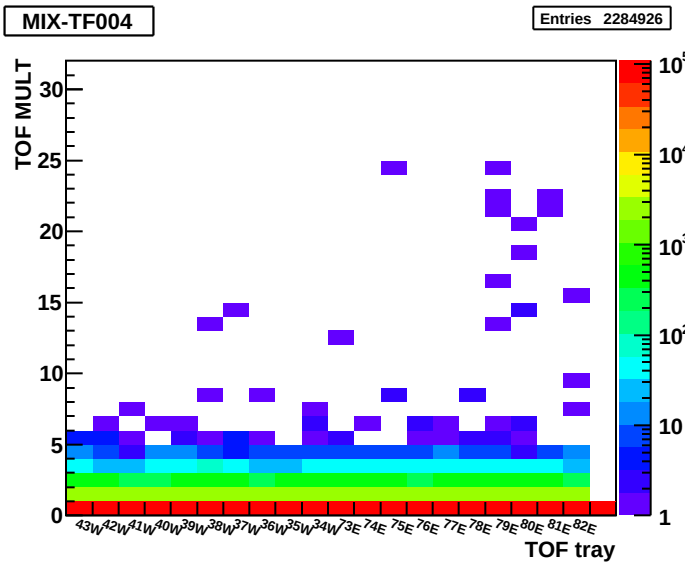

Endcap Jet Patches
Entries 326418
JP ADC
JP ID

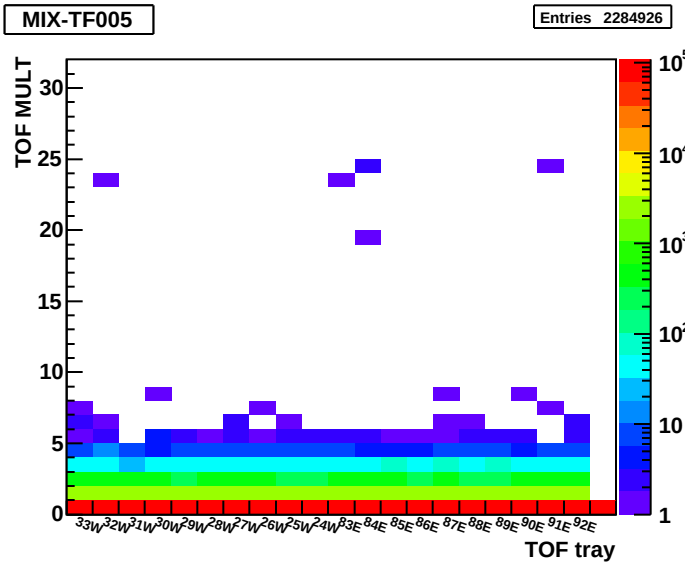

Hybrid Jet Patches
Entries 108806
JP ADC
JP ID

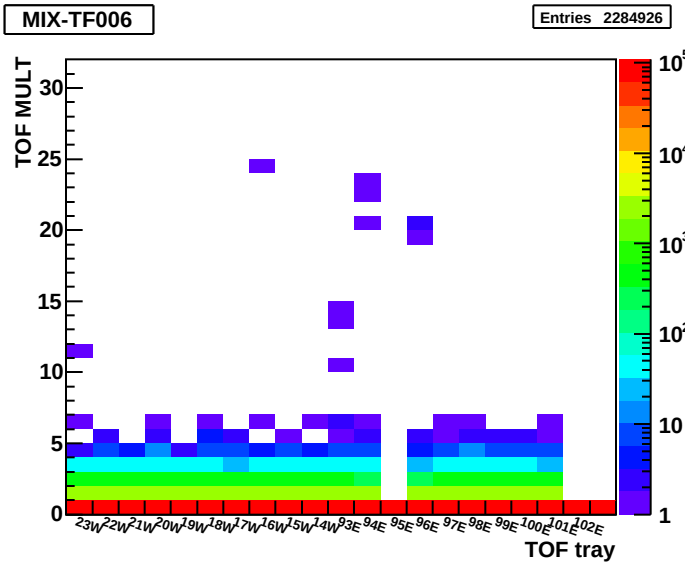

MIX-TF001
Entries 2284926
TOF MULT
TOF tray

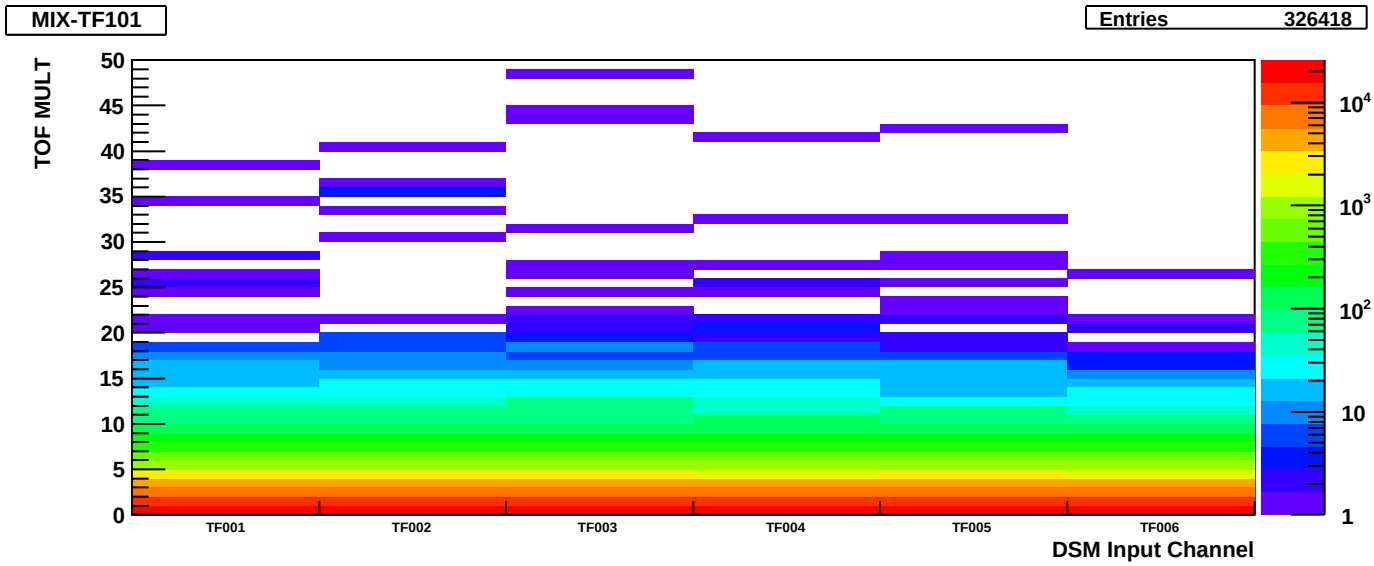

MIX-TF002
Entries 2284926
TOF MULT
TOF tray

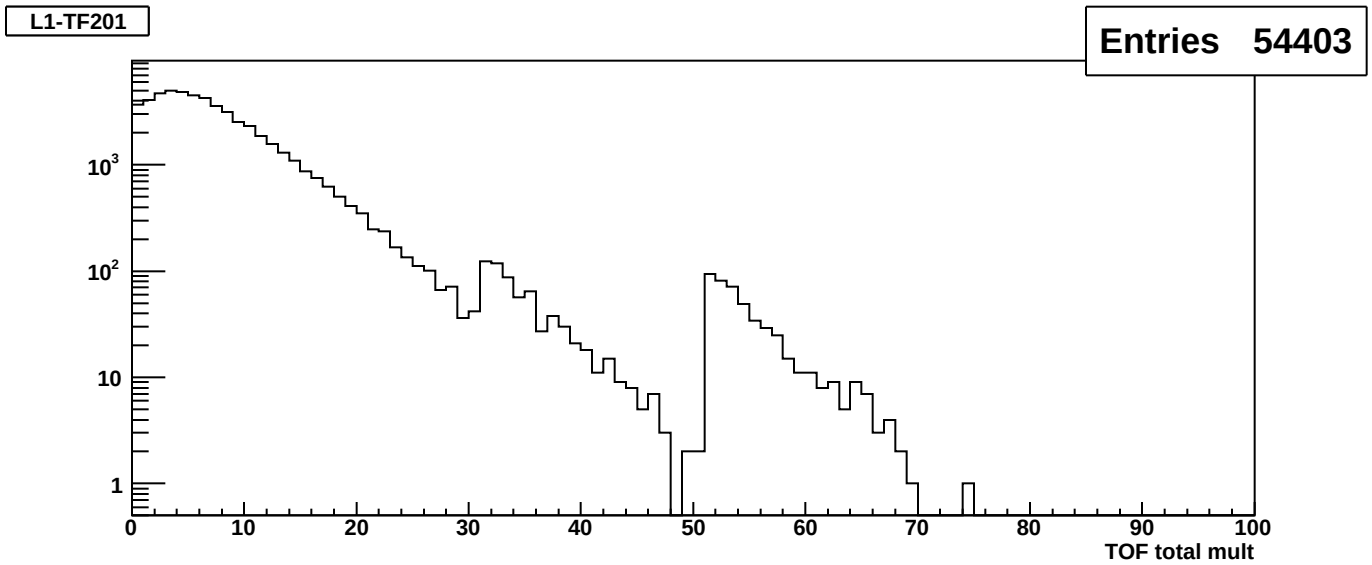

MIX-TF003
Entries 2284926
TOF MULT
TOF tray

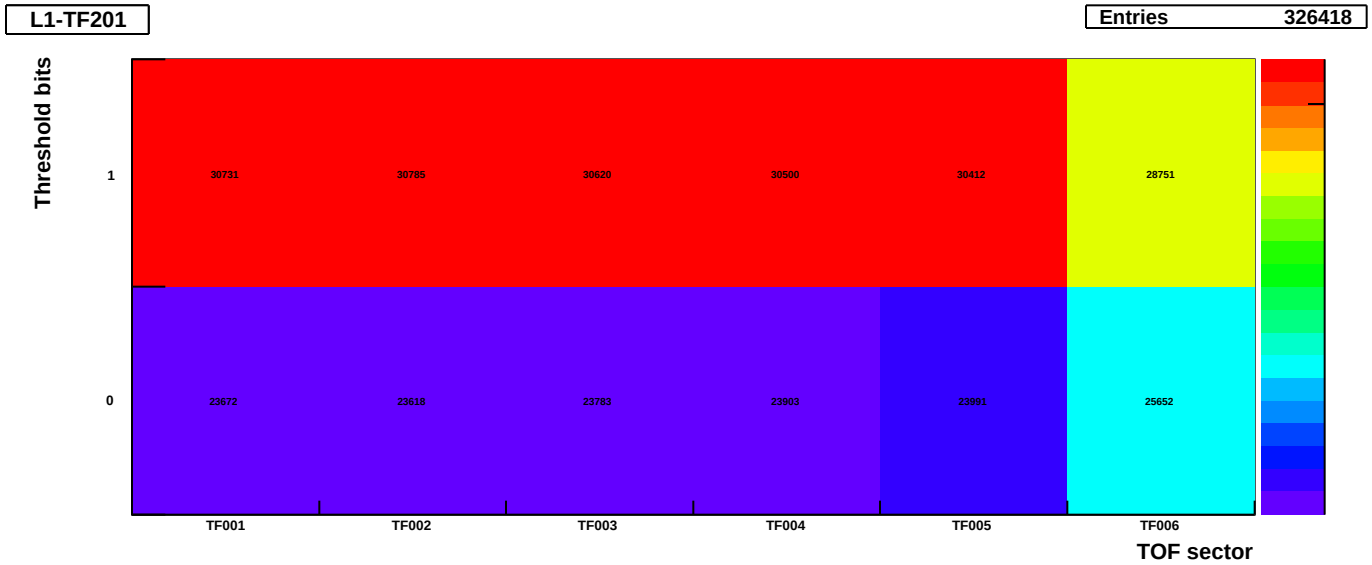

MIX-TF004
Entries 2284926
TOF MULT
TOF tray

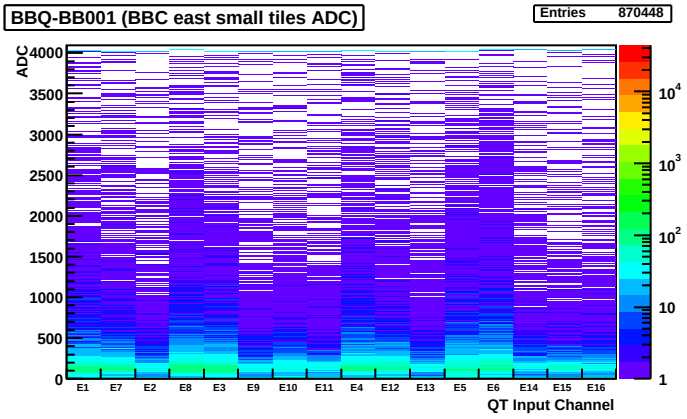

MIX-TF005
Entries 2284926
TOF MULT
TOF tray

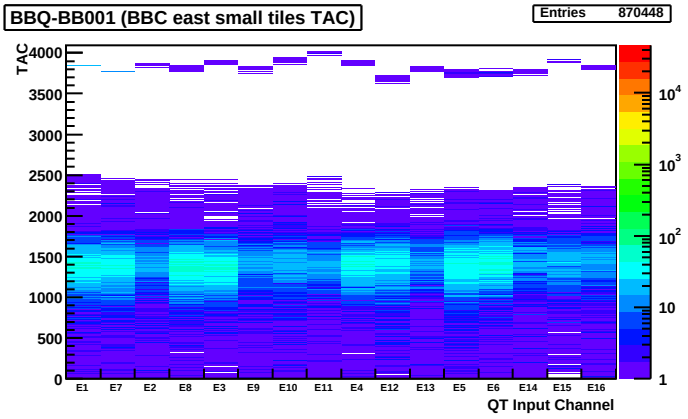

MIX-TF006
Entries 2284926
TOF MULT
TOF tray

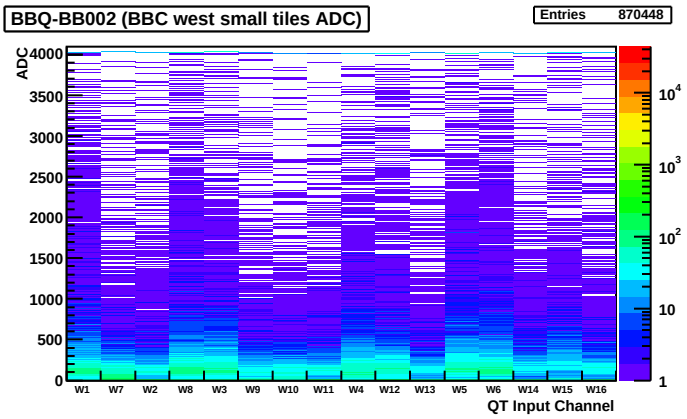

MIX-TF101
Entries 326418
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

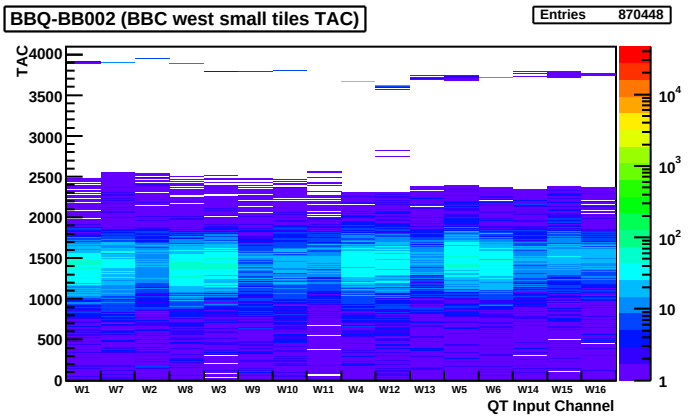

L1-TF201
Entries 54403
TOF total mult

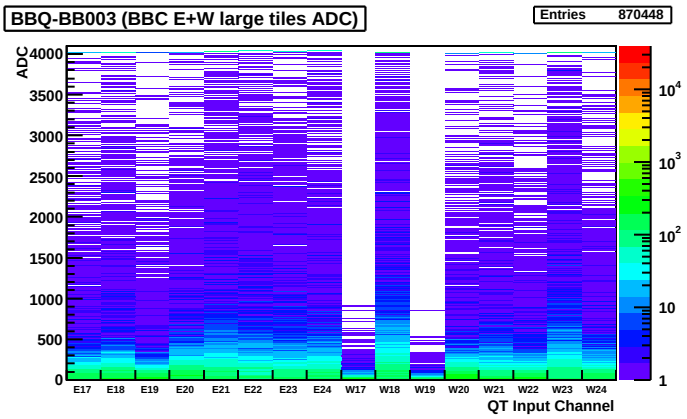

L1-TF201
Entries 326418
Threshold bits
30731
30785
30620
30500
30412
28751
23672
23618
23783
23903
23991
25652
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

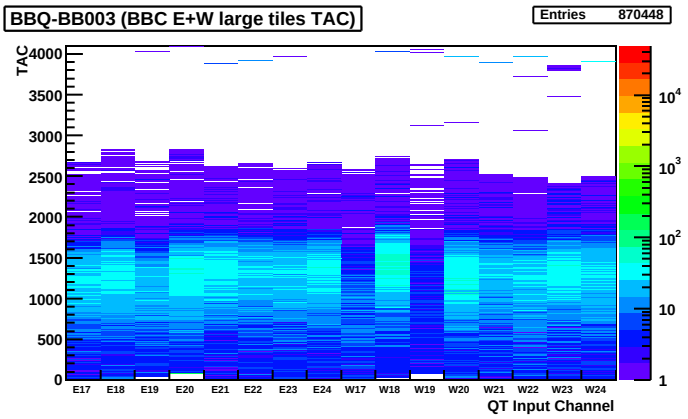

BBQ-BB001 (BBC east small tiles ADC)
Entries 870448
ADC
QT Input Channel

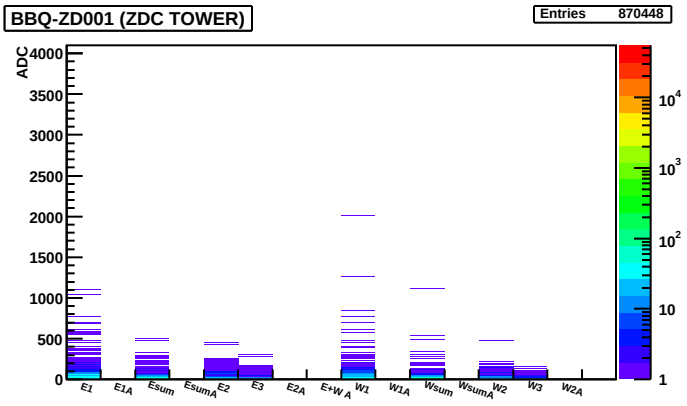

BBQ-BB001 (BBC east small tiles TAC)
Entries 870448
TAC
QT Input Channel

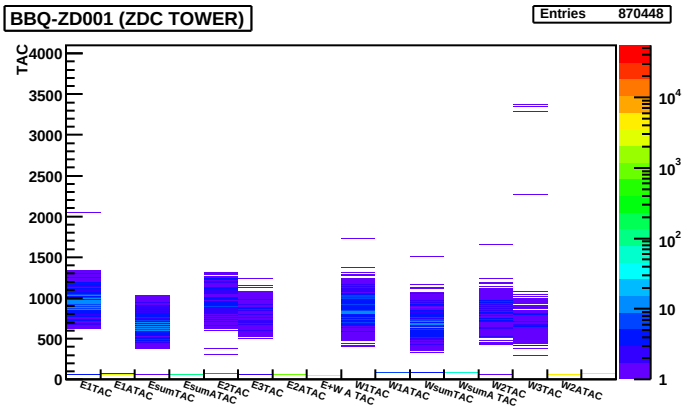

BBQ-BB002 (BBC west small tiles ADC)
Entries 870448
ADC
QT Input Channel

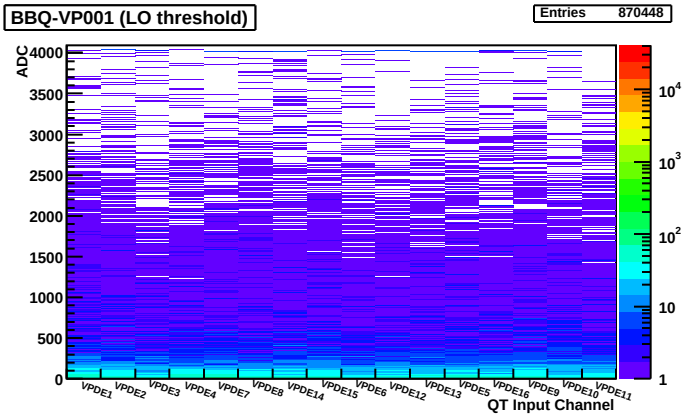

BBQ-BB002 (BBC west small tiles TAC)
Entries 870448
TAC
QT Input Channel

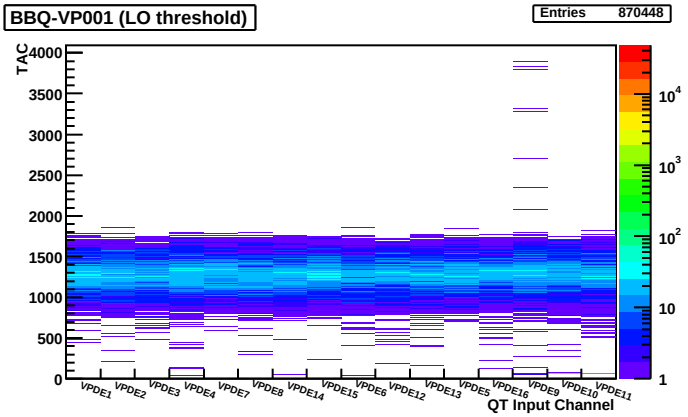

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 870448
ADC
QT Input Channel

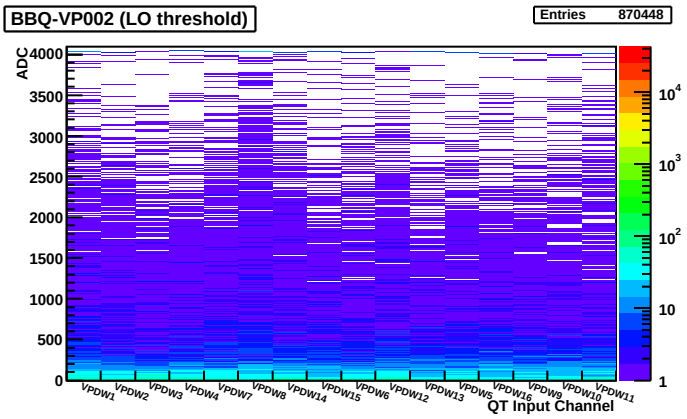

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 870448
TAC
QT Input Channel

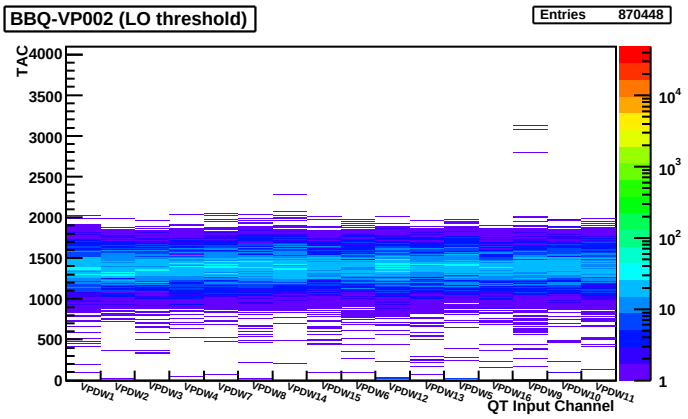

BBQ-ZD001 (ZDC TOWER)
Entries 870448
ADC

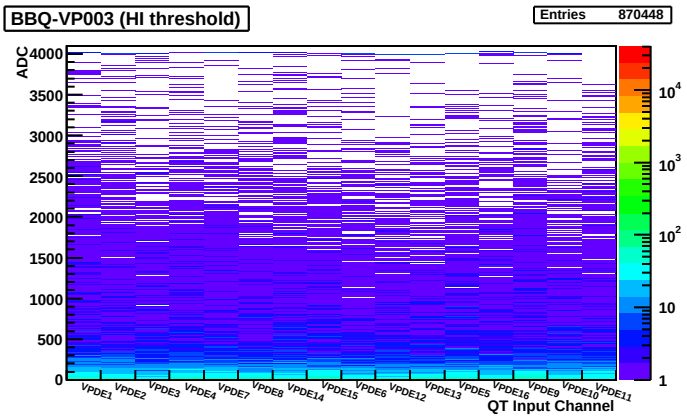

BBQ-ZD001 (ZDC TOWER)
Entries 870448
TAC

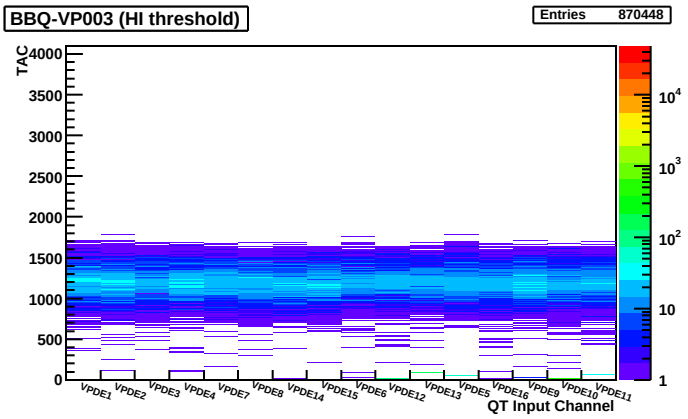

BBQ-VP001 (LO threshold)
Entries 870448
ADC
QT Input Channel

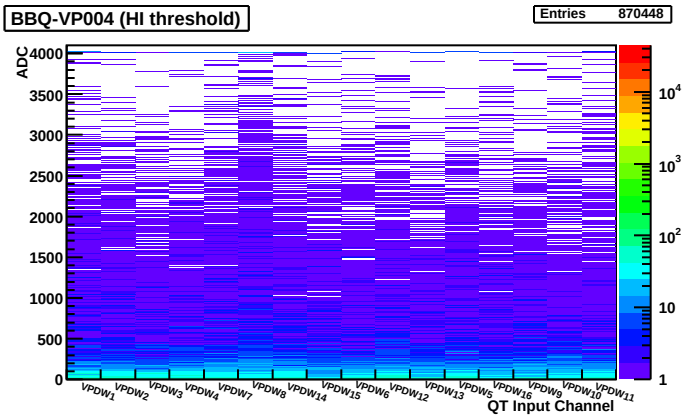

BBQ-VP001 (LO threshold)
Entries 870448
TAC
QT Input Channel

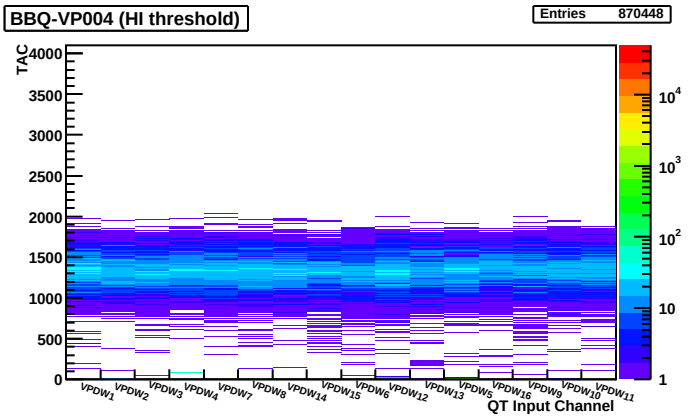

BBQ-VP002 (LO threshold)
Entries 870448
ADC
QT Input Channel

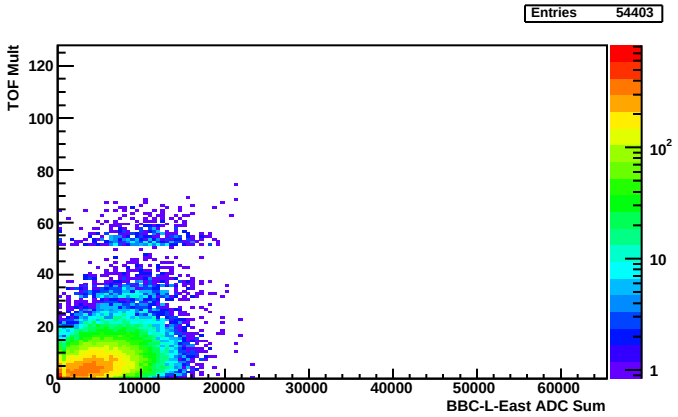

BBQ-VP002 (LO threshold)
Entries 870448
TAC
QT Input Channel

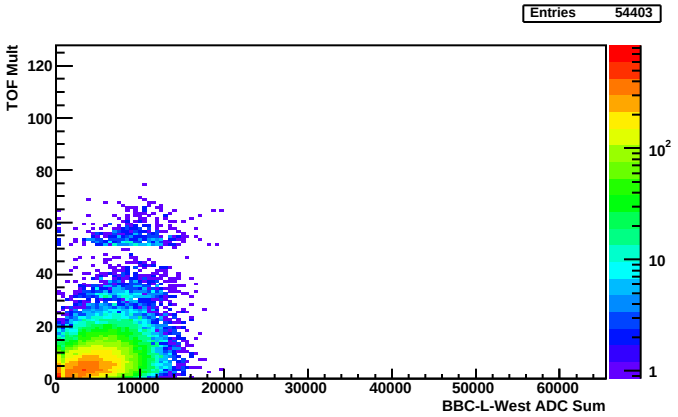

BBQ-VP003 (HI threshold)
Entries 870448
ADC
QT Input Channel

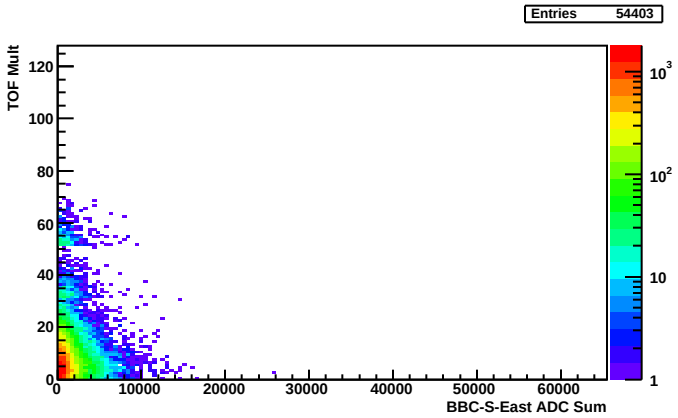

BBQ-VP003 (HI threshold)
Entries 870448
TAC
QT Input Channel

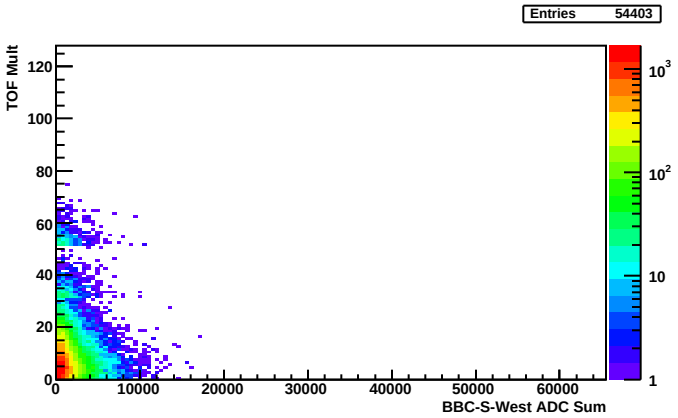

BBQ-VP004 (HI threshold)
Entries 870448
ADC
QT Input Channel

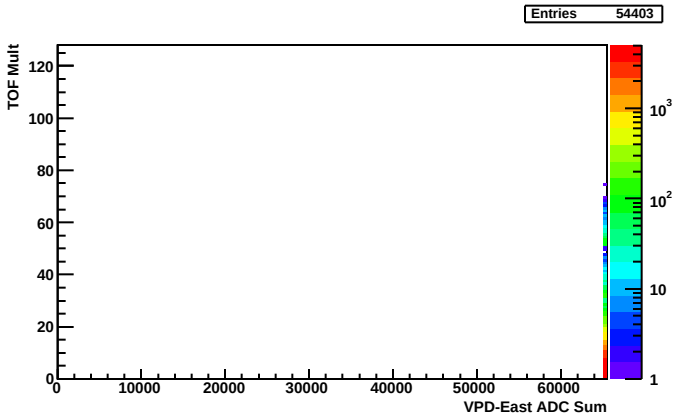

BBQ-VP004 (HI threshold)
Entries 870448
TAC
QT Input Channel

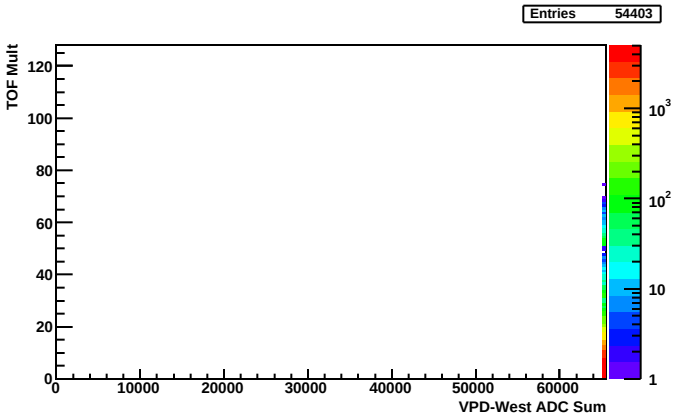

Entries 54403
TOF Mult
BBC-L-East ADC Sum

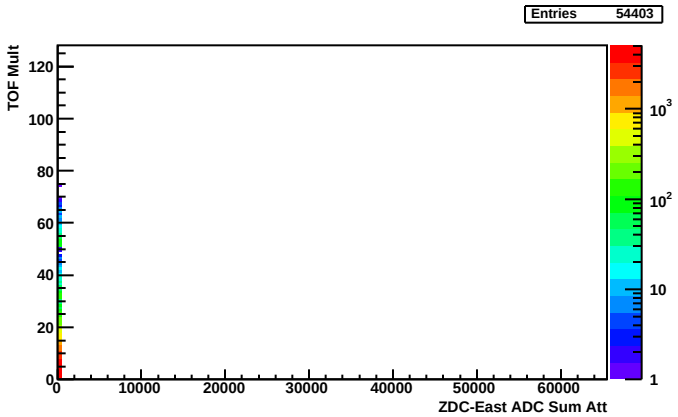

Entries 54403
TOF Mult
BBC-L-West ADC Sum

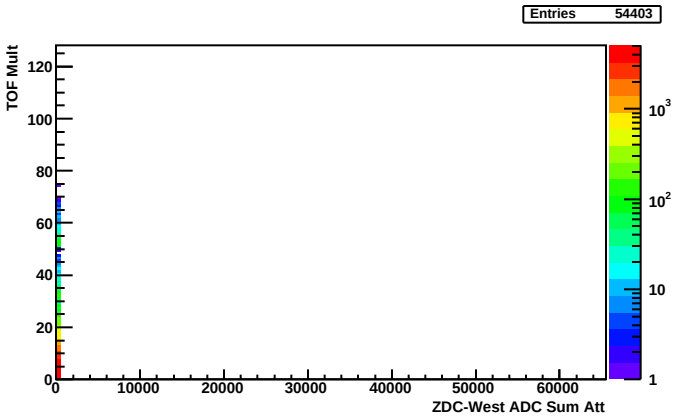

Entries 54403
TOF Mult
BBC-S-East ADC Sum

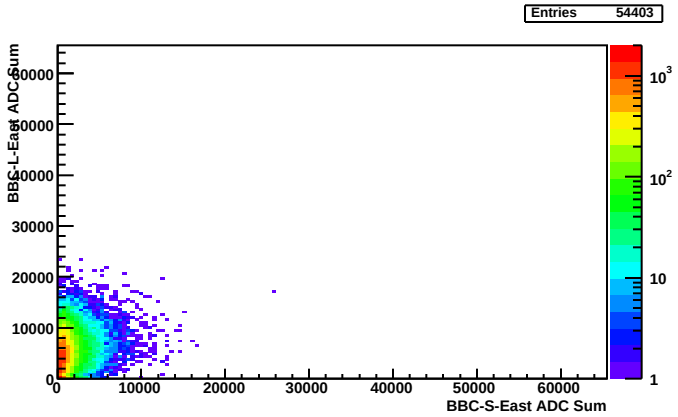

Entries 54403
TOF Mult
BBC-S-West ADC Sum

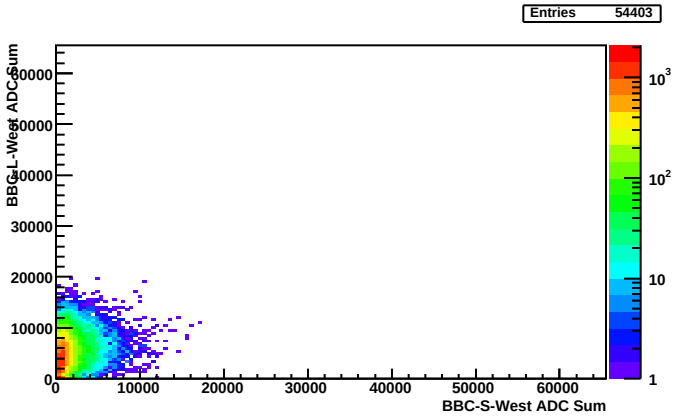

Entries 54403
TOF Mult
VPD-East ADC Sum

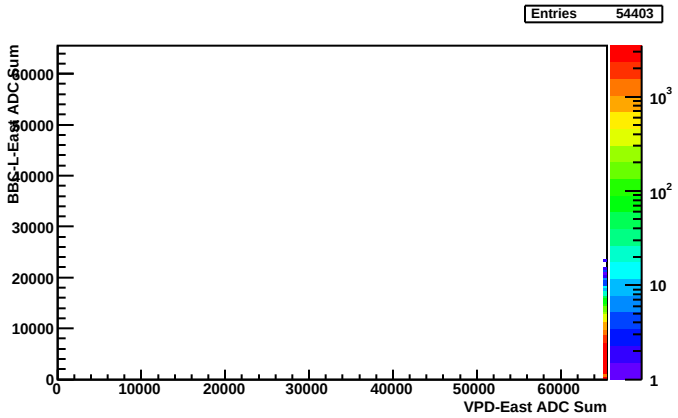

Entries 54403
TOF Mult
VPD-West ADC Sum

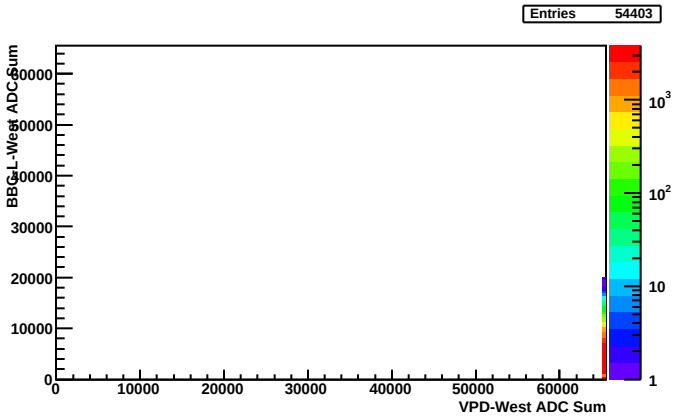

Entries 54403
TOF Mult
ZDC-East ADC Sum Att

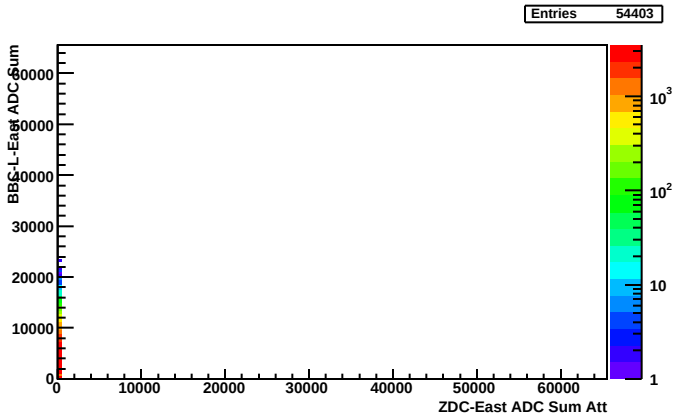

Entries 54403
TOF Mult
ZDC-West ADC Sum Att

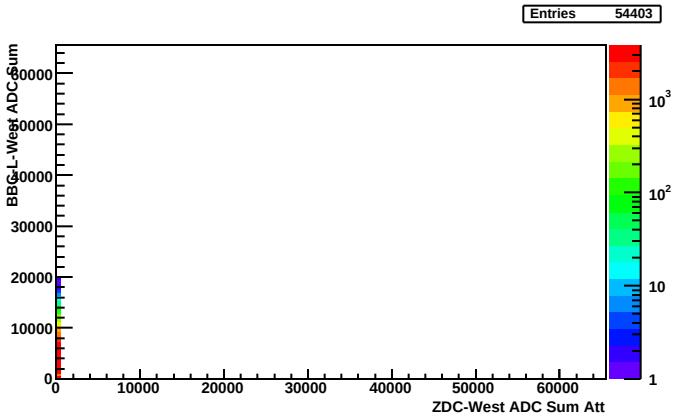

Entries 54403
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 54403
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 54403
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 54403
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 54403
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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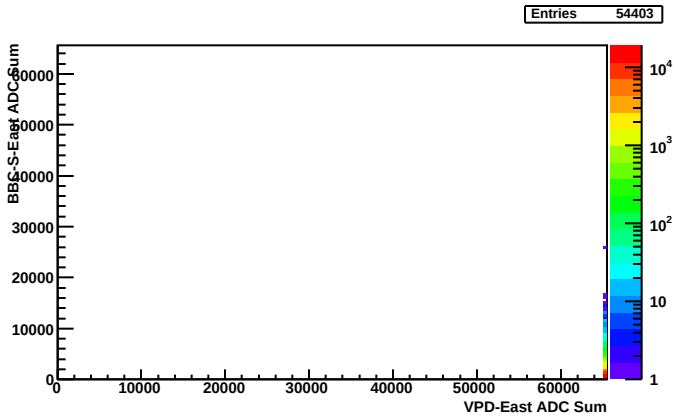
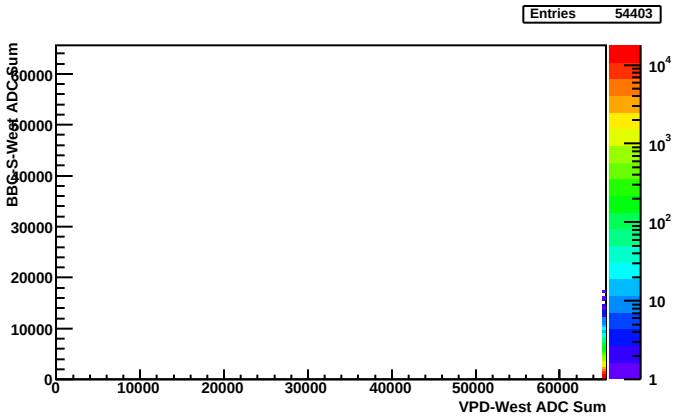
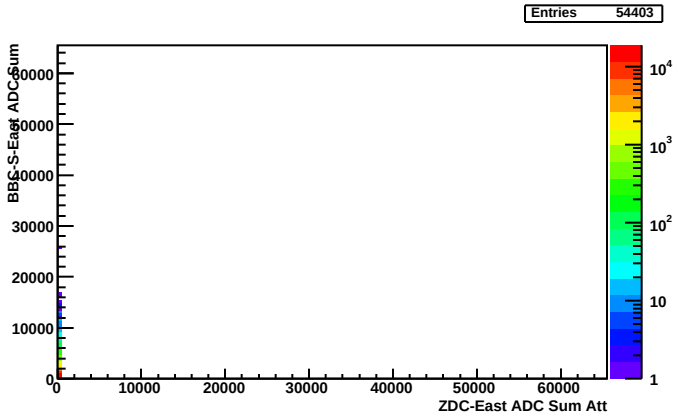
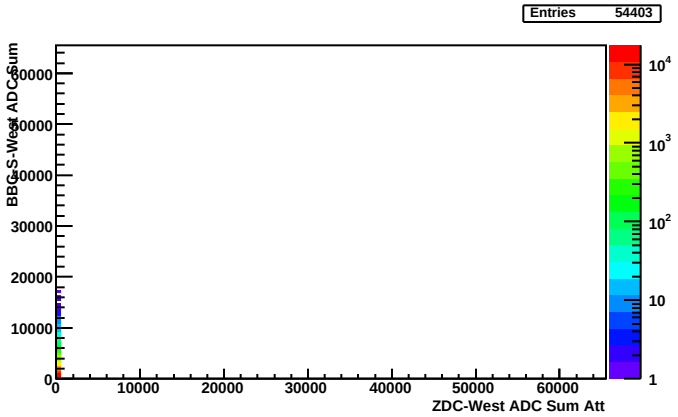
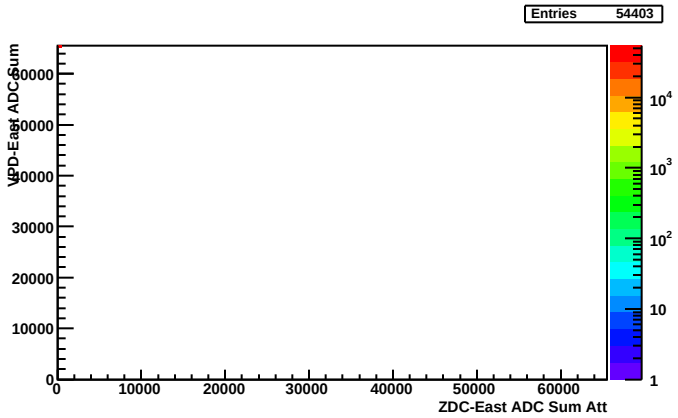
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

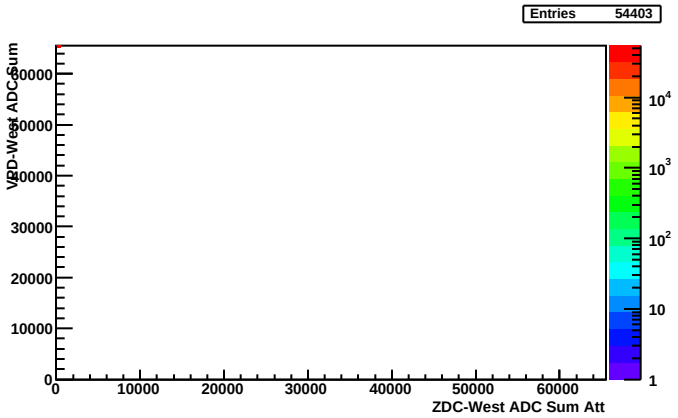

Entries 54403
BBC-S-East ADC Sum
VPD-East ADC Sum

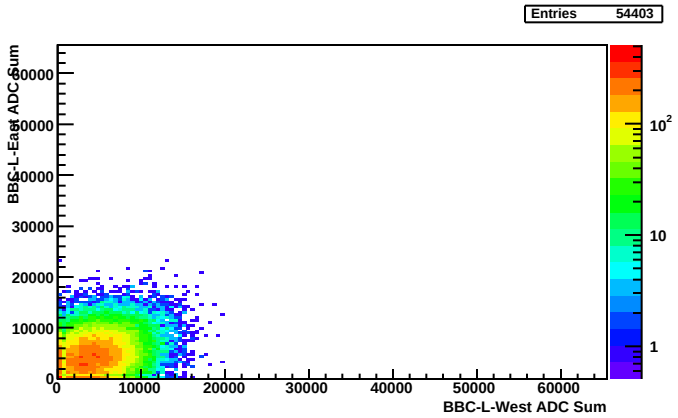

Entries 54403
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 54403
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 54403
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 54403
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 54403
VPD-West ADC Sum
ZDC-West ADC Sum Att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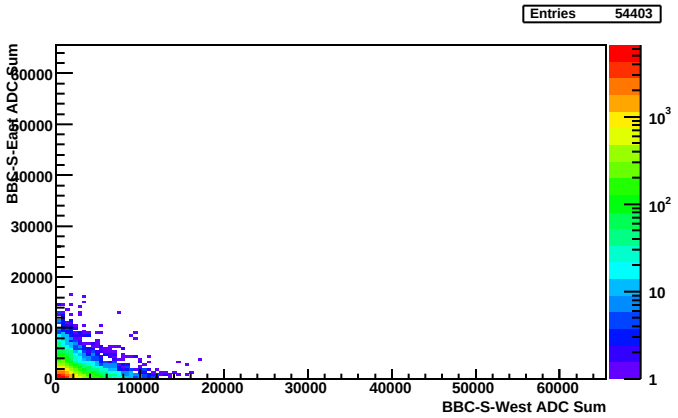
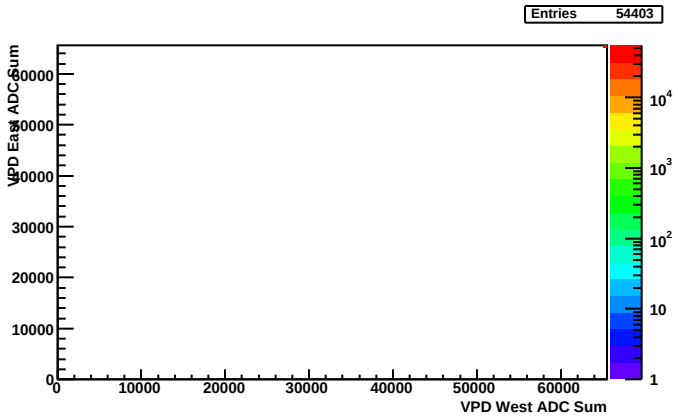
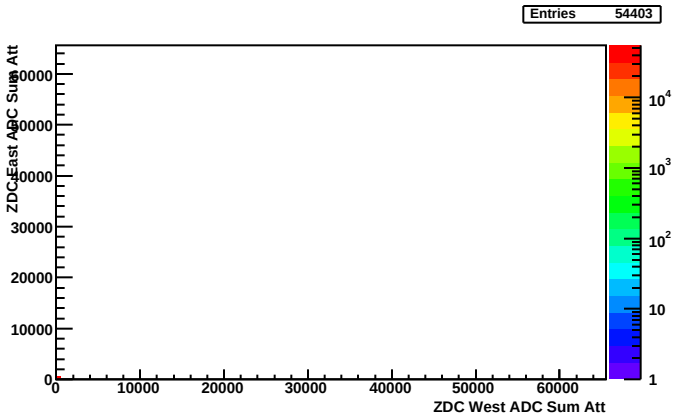
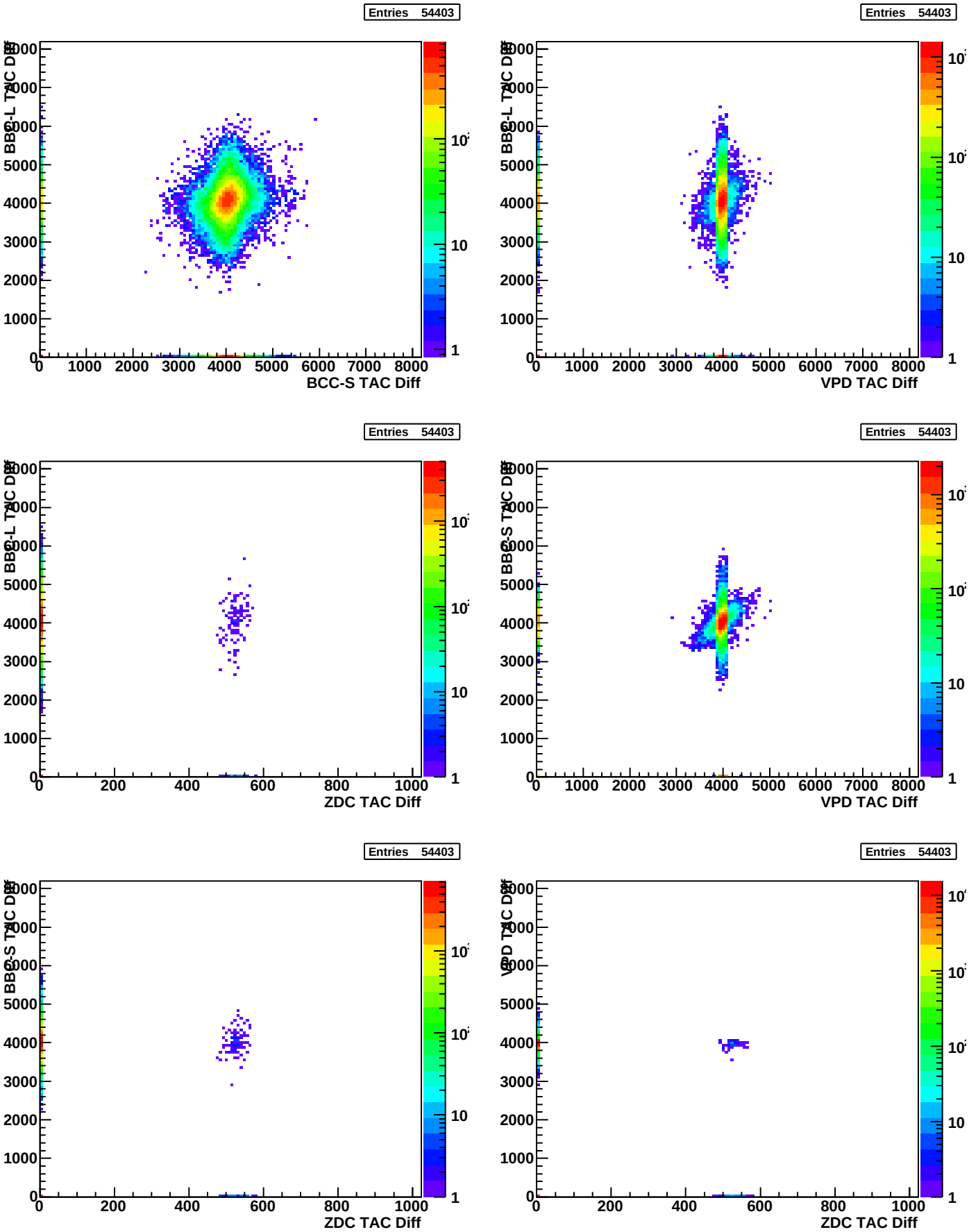
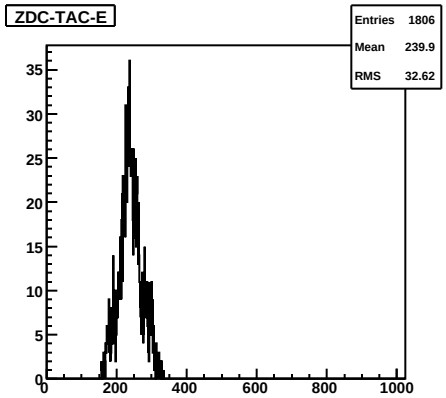
BBC-L-East ADC Sum
BBC-L-West ADC Sum

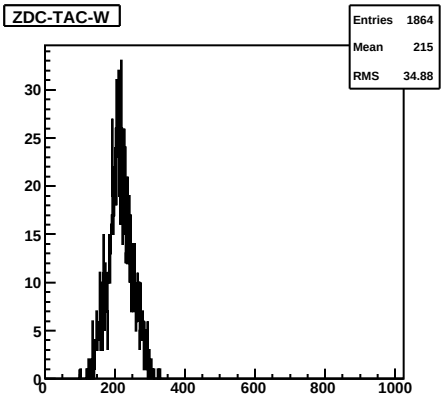

Entries 54403
BBC-S-East ADC Sum
BBC-S-West ADC Sum

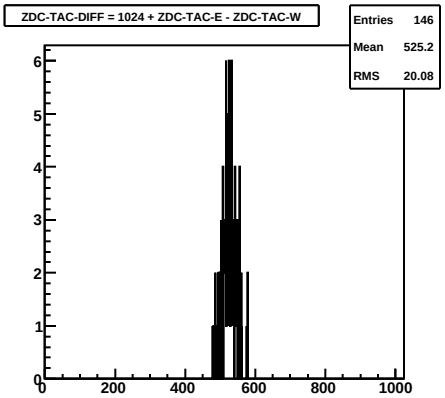

Entries 54403
VPD East ADC Sum
VPD West ADC Sum

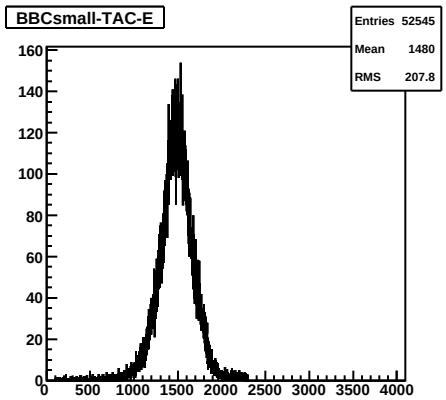

Entries 54403
ZDC East ADC Sum Att
ZDC West ADC Sum Att

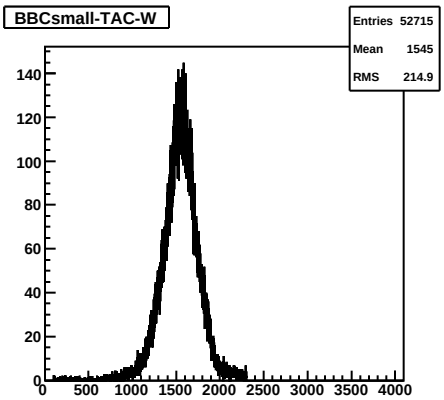

Entries 54403
BBC-L TAC Diff
BCC-S TAC Diff
Entries 54403
BBC-L TAC Diff
VPD TAC Diff
Entries 54403
BBC-L TAC Diff
ZDC TAC Diff
Entries 54403
BBC-S TAC Diff
VPD TAC Diff
Entries 54403
BBC-S TAC Diff
ZDC TAC Diff
Entries 54403
VPD TAC Diff
ZDC TAC Diff

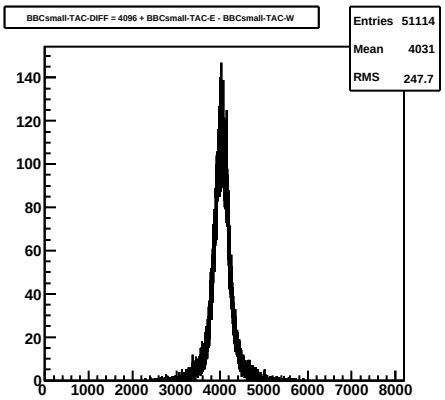

ZDC-TAC-E
Entries 1806
Mean 239.9
RMS 32.62

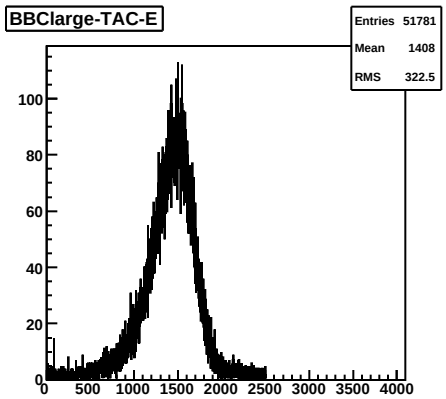

ZDC-TAC-W
Entries 1864
Mean 215
RMS 34.88

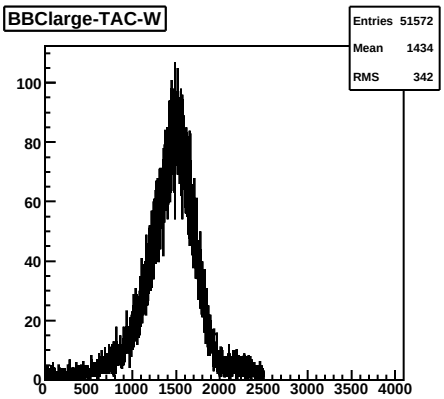

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 146
Mean 525.2
RMS 20.08

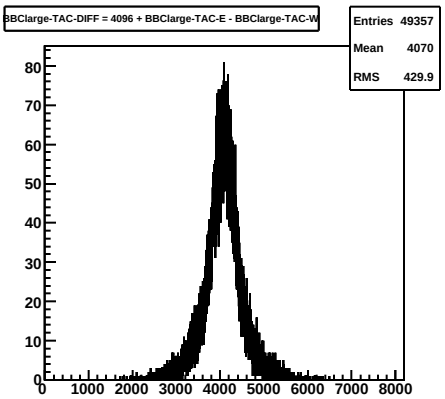

BBCsmall-TAC-E
Entries 52545
Mean 1480
RMS 207.8

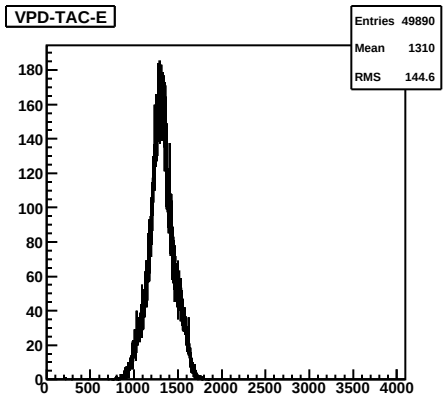

BBCsmall-TAC-W
Entries 52715
Mean 1545
RMS 214.9

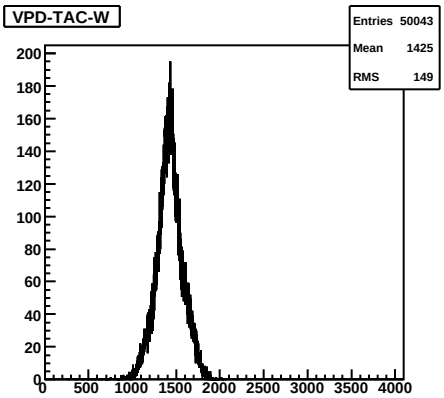

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 51114
Mean 4031
RMS 247.7

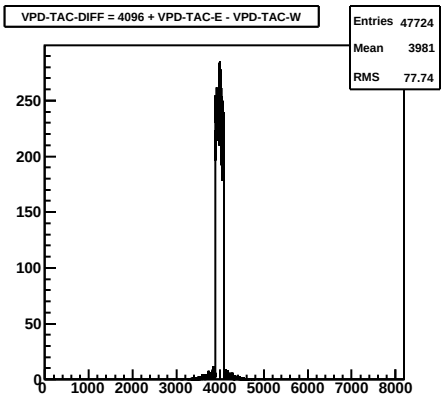

BBClarge-TAC-E
Entries 51781
Mean 1408
RMS 322.5

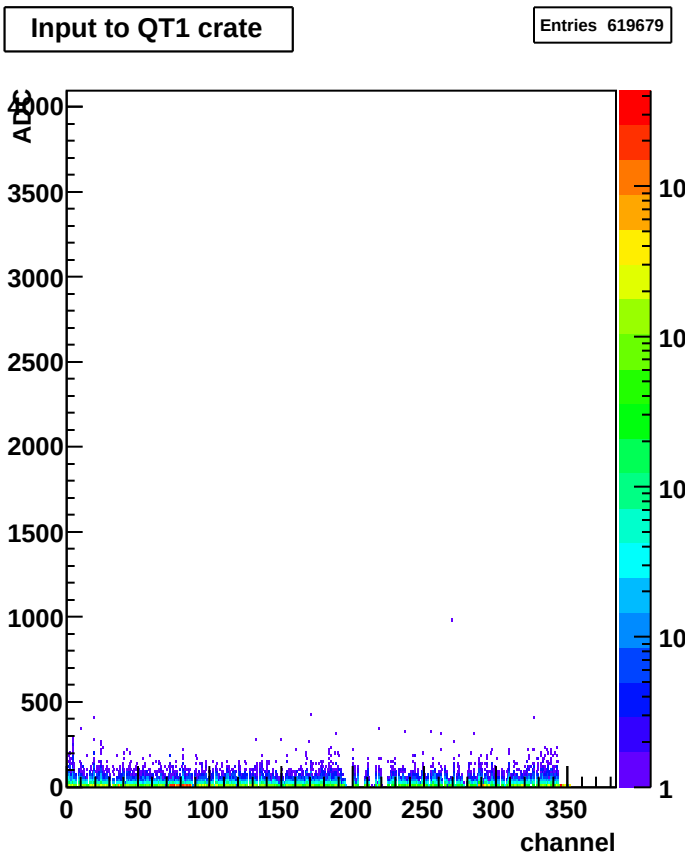

BBClarge-TAC-W
Entries 51572
Mean 1434
RMS 342

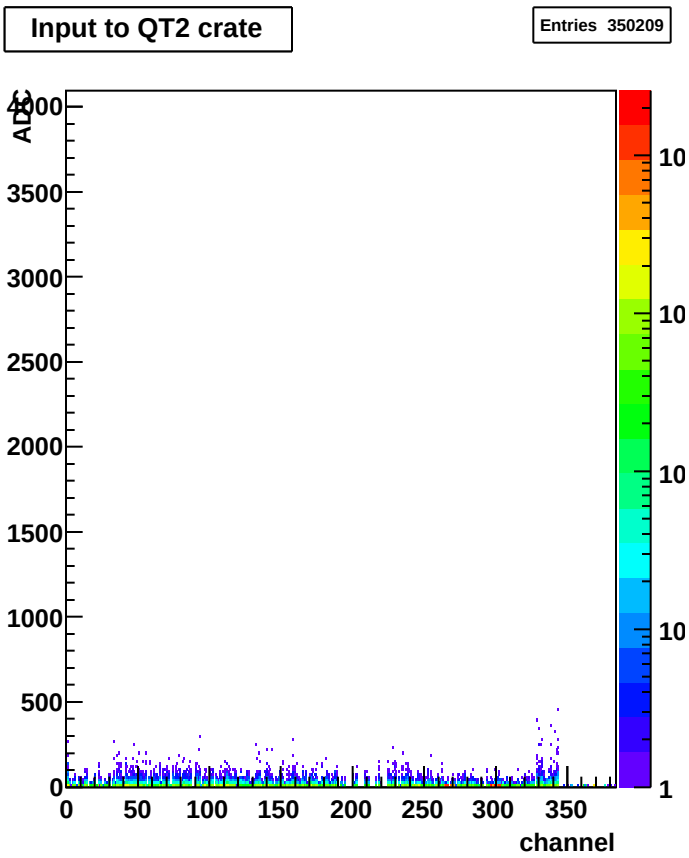

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 49357
Mean 4070
RMS 429.9

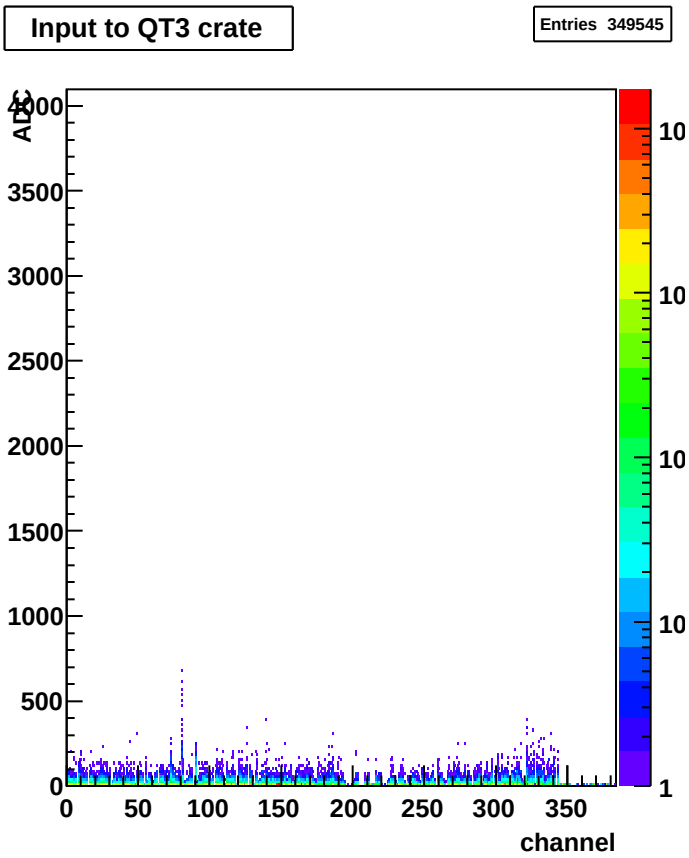

VPD-TAC-E
Entries 49890
Mean 1310
RMS 144.6

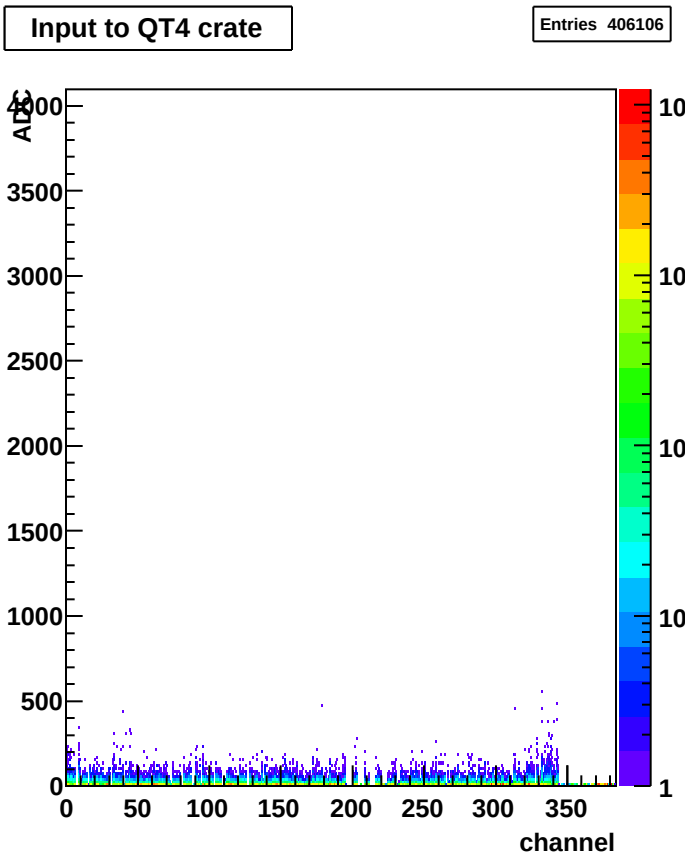

VPD-TAC-W
Entries 50043
Mean 1425
RMS 149

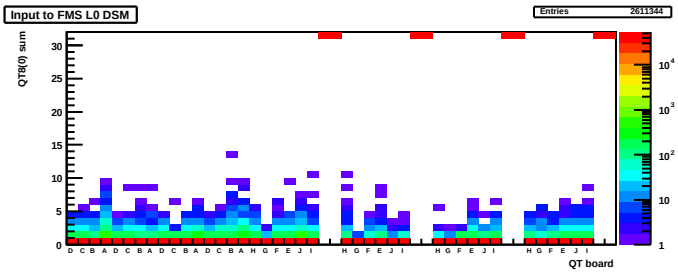

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 47724
Mean 3981
RMS 77.74

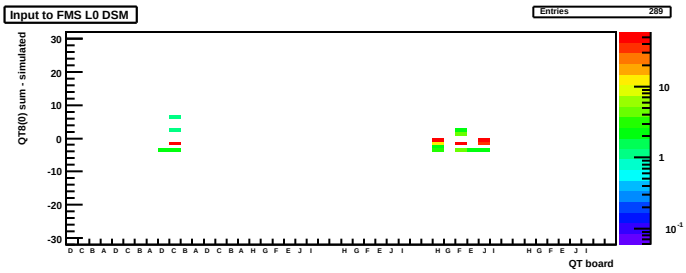

Input to QT1 crate
Entries 619679
ADC
channel

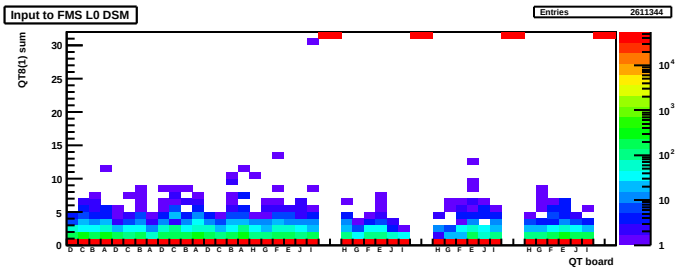

Input to QT2 crate
Entries 350209
ADC
channel

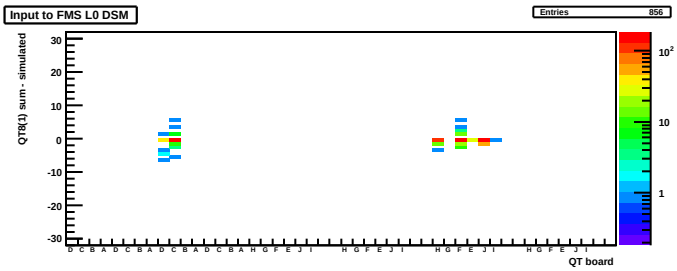

Input to QT3 crate
Entries 349545
ADC
channel

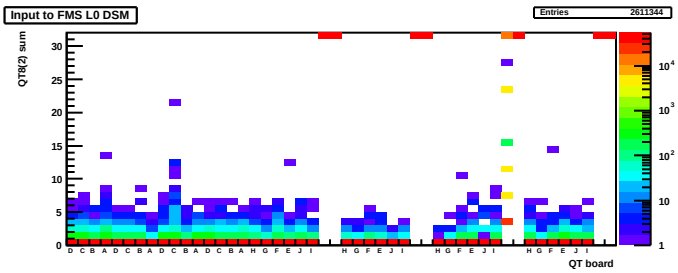

Input to QT4 crate
Entries 406106
ADC
channel

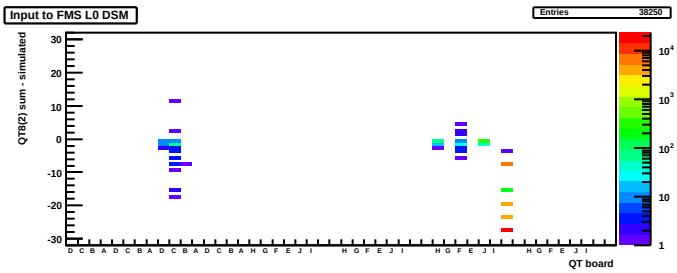

Input to FMS L0 DSM
Entries 2611344
QT8(0) sum
QT board

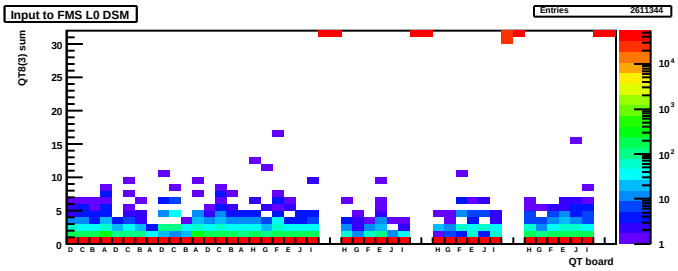

Input to FMS L0 DSM
Entries 289
QT8(0) sum - simulated
QT board

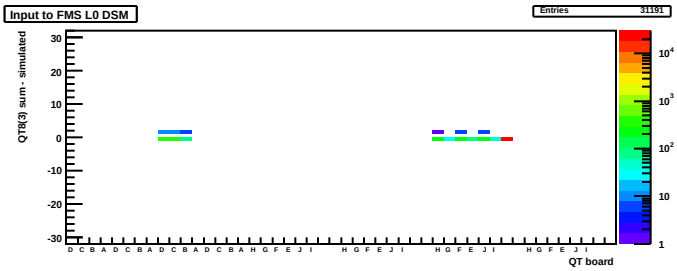

Input to FMS L0 DSM
Entries 2611344
QT8(1) sum
QT board

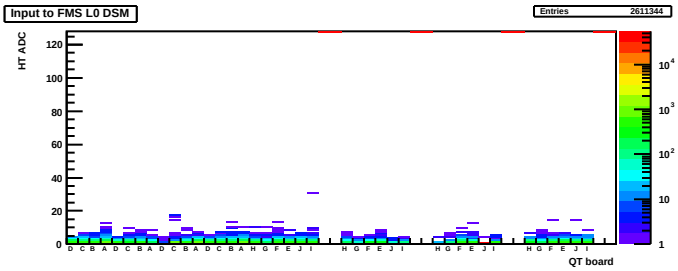

Input to FMS L0 DSM
Entries 856
QT8(1) sum - simulated
QT board

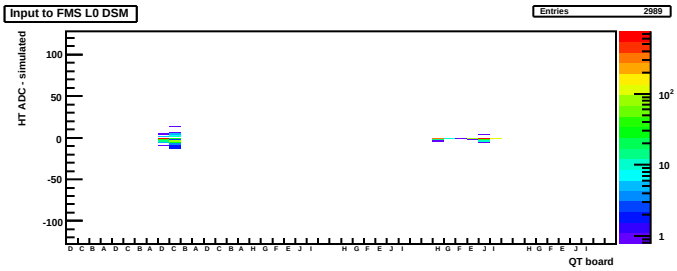

Input to FMS L0 DSM
Entries 2611344
QT8(2) sum
QT board

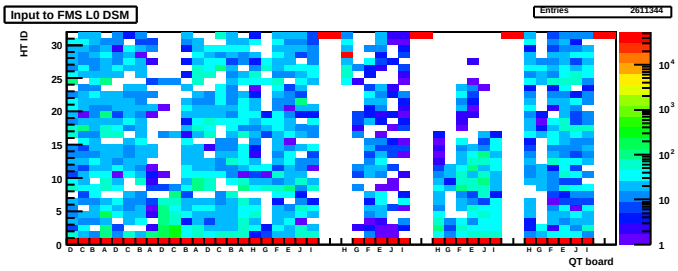

Input to FMS L0 DSM
Entries 38250
QT8(2) sum - simulated
QT board

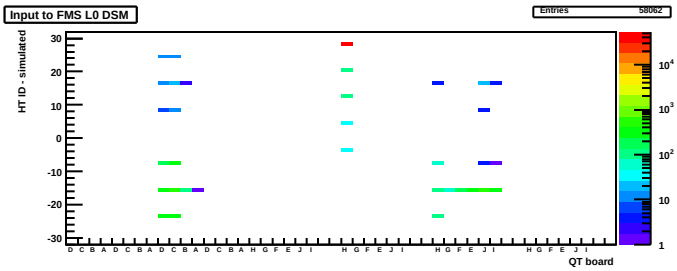

Input to FMS L0 DSM
Entries 2611344
QT8(3) sum
QT board

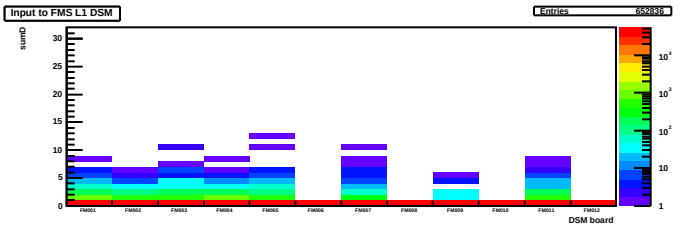

Input to FMS L0 DSM
Entries 31191
QT8(3) sum - simulated
QT board

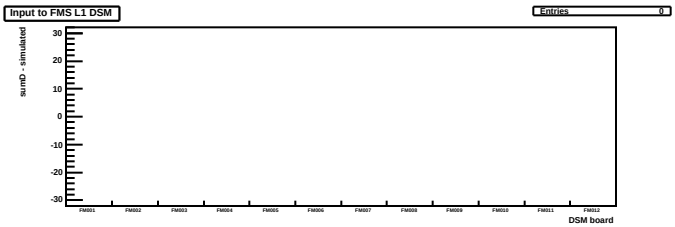

Input to FMS L0 DSM
Entries 2611344
HT ADC
QT board

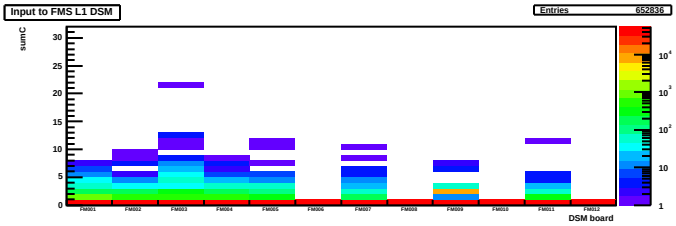

Input to FMS L0 DSM
Entries 2989
HT ADC - simulated
QT board

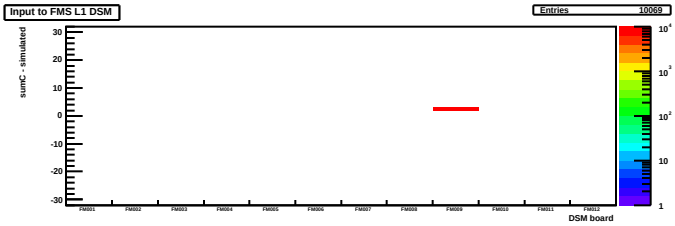

Input to FMS L0 DSM
Entries 2611344
HT ID
QT board

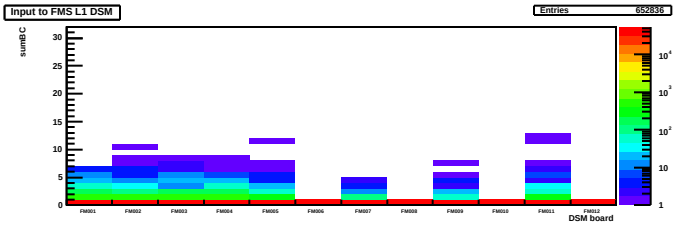

Input to FMS L0 DSM
Entries 58062
HT ID - simulated
QT board

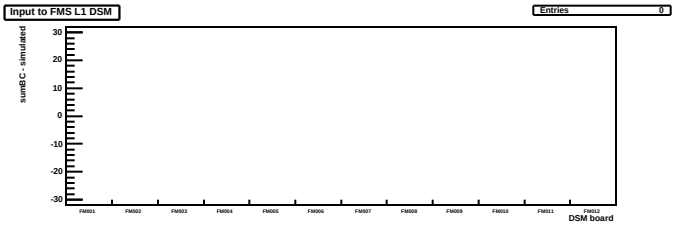

Input to FMS L1 DSM
Entries 652836
sumD
DSM board

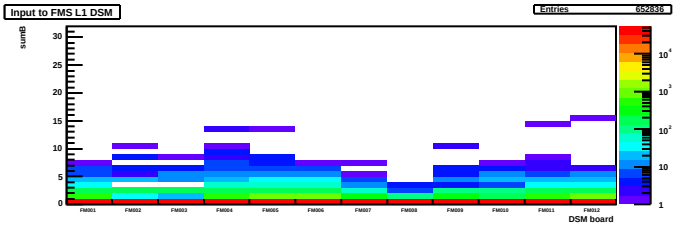

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

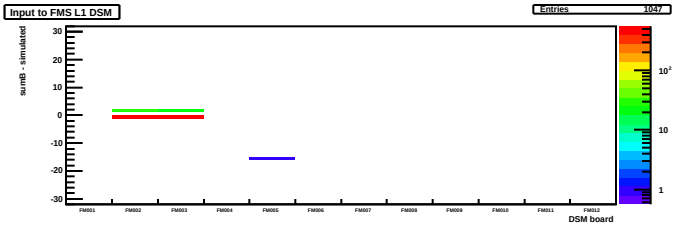

Input to FMS L1 DSM
Entries 652836
sumC
DSM board

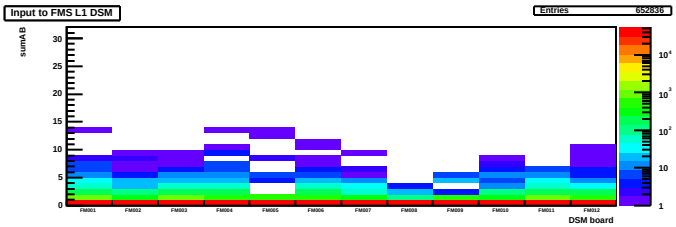

Input to FMS L1 DSM
Entries 10069
sumC - simulated
DSM board

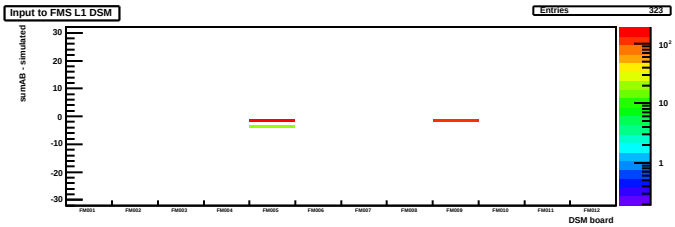

Input to FMS L1 DSM
Entries 652836
sumBC
DSM board

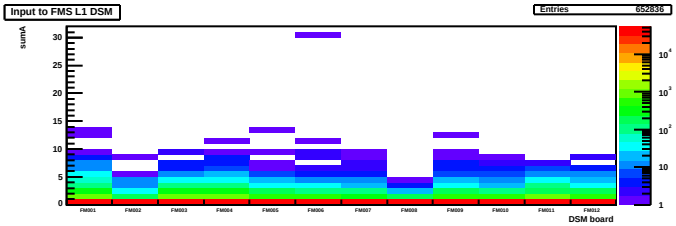

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

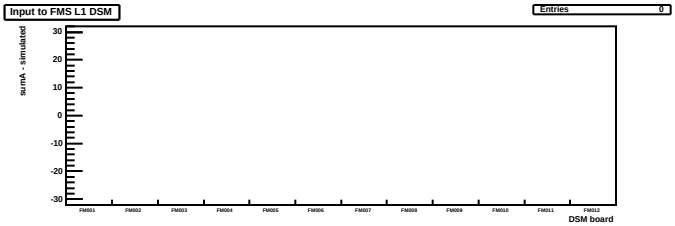

Input to FMS L1 DSM
Entries 652836
sumB
DSM board

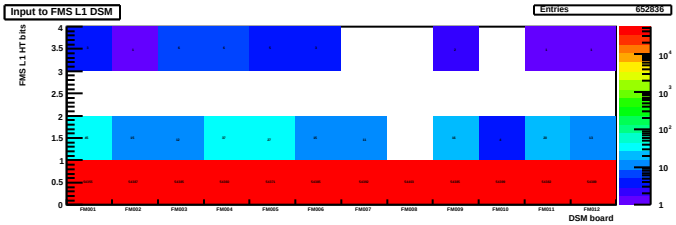

Input to FMS L1 DSM
Entries 1047
sumB - simulated
DSM board

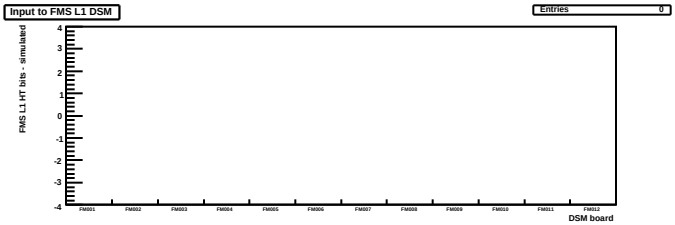

Input to FMS L1 DSM
Entries 652836
sumAB
DSM board

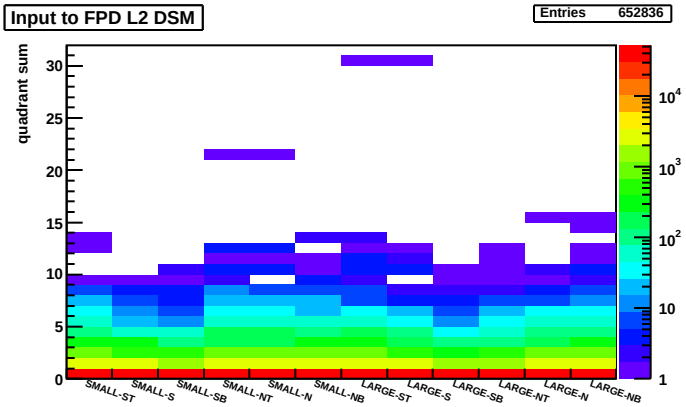

Input to FMS L1 DSM
Entries 323
sumAB - simulated
DSM board

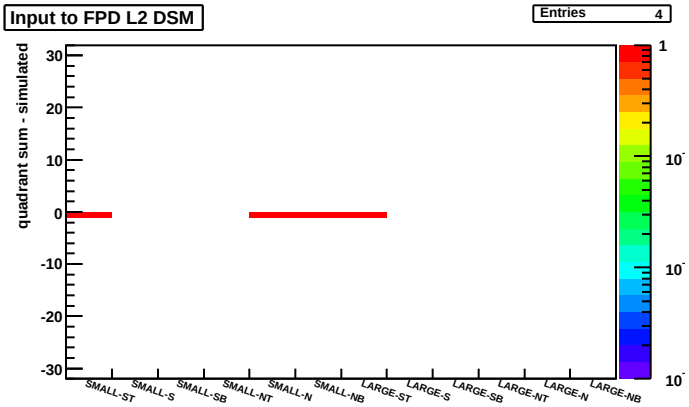

Input to FMS L1 DSM
Entries 652836
sumA
DSM board

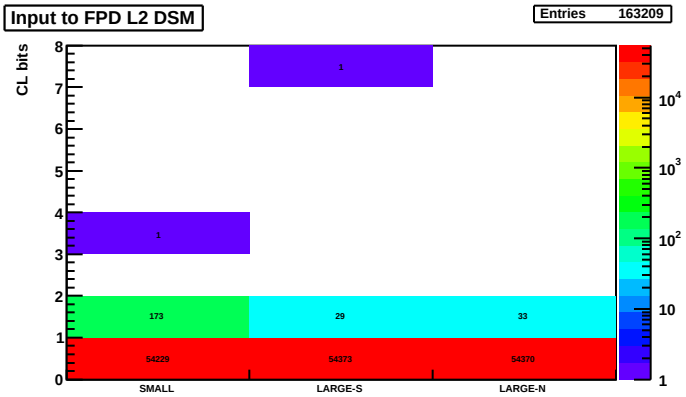

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

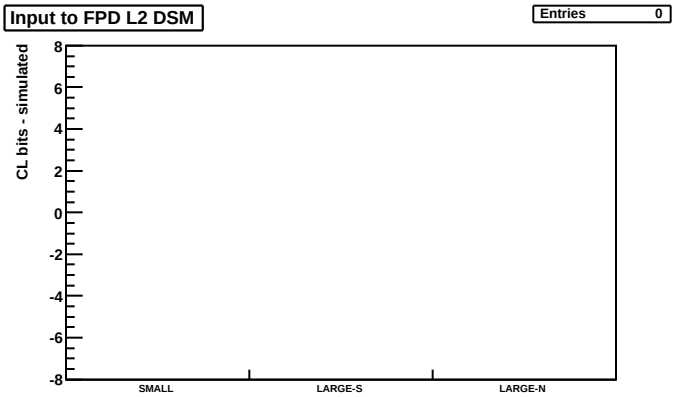

Input to FMS L1 DSM
Entries 652836
FMS L1 HT bits
DSM board

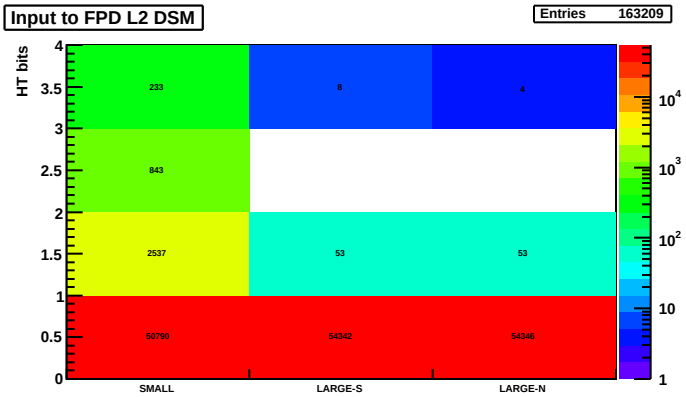

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

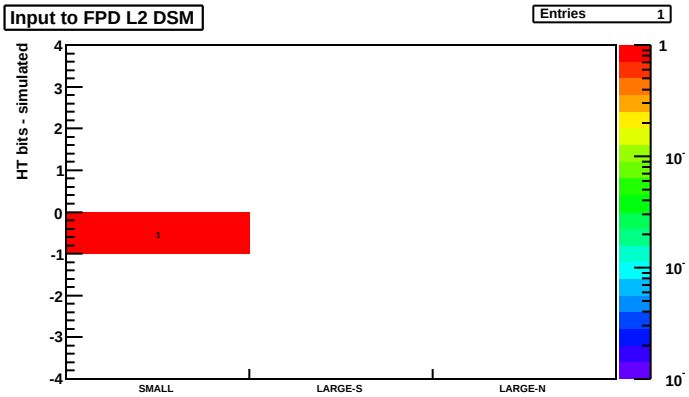

Input to FPD L2 DSM
Entries 652836
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

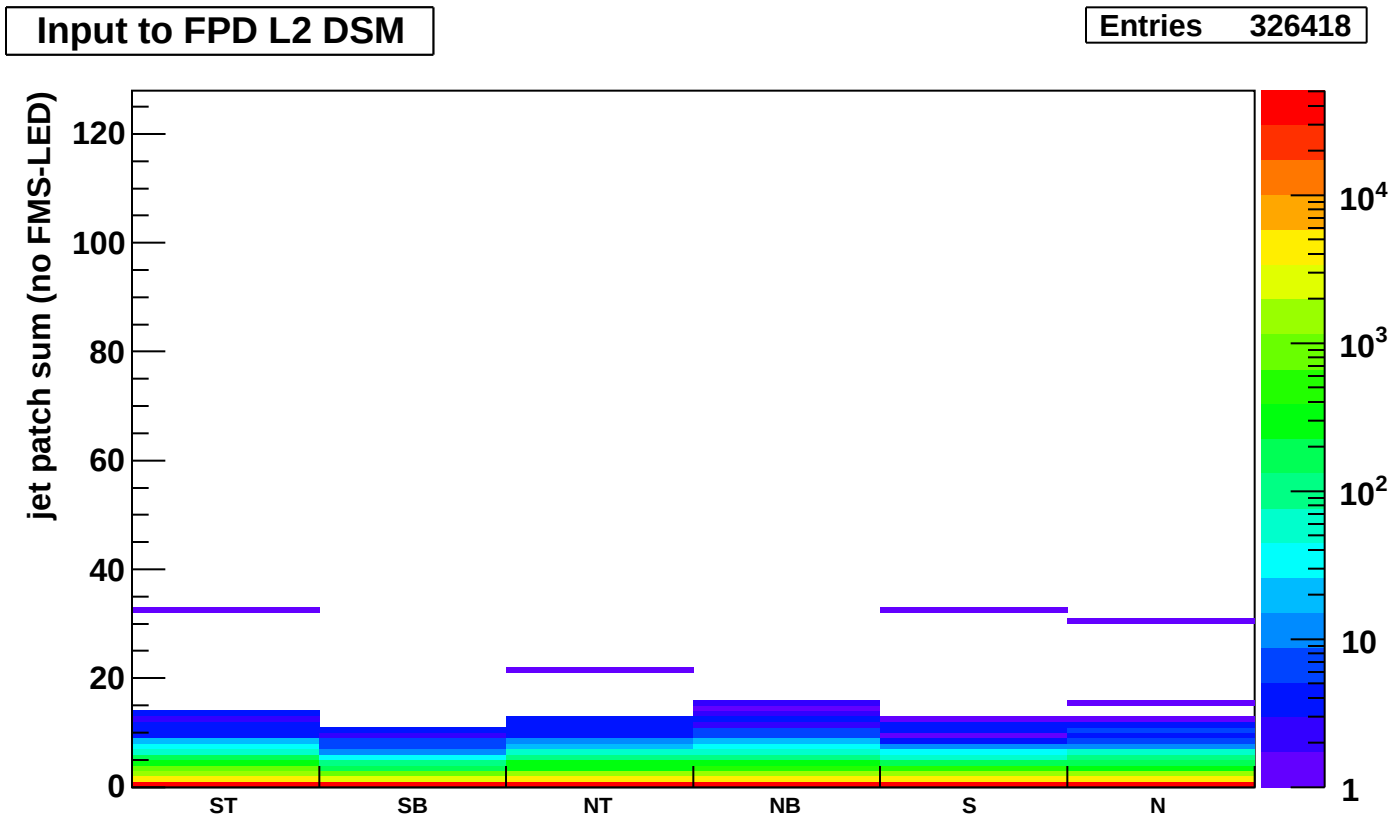

Input to FPD L2 DSM
Entries 4
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

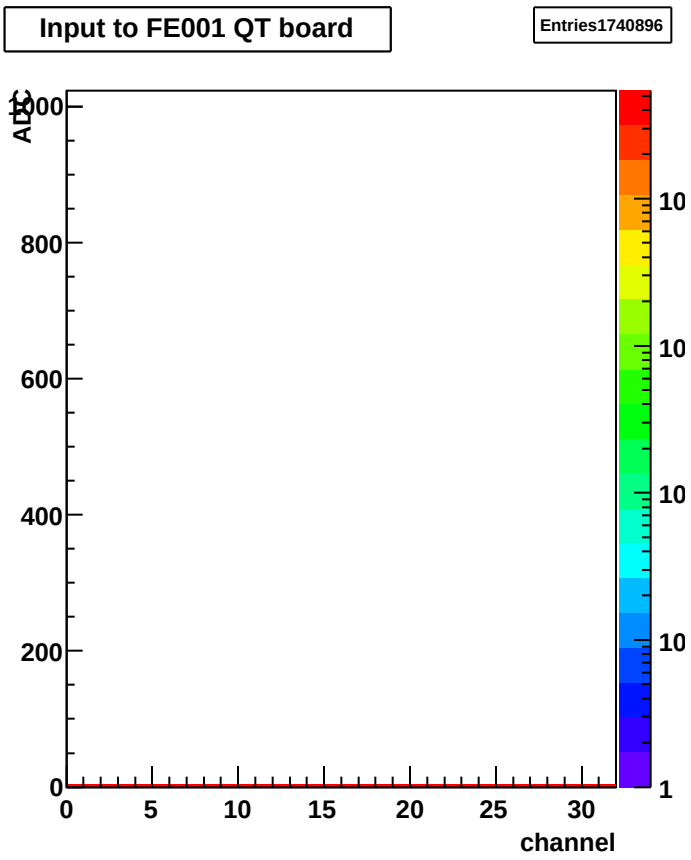

Input to FPD L2 DSM
Entries 163209
CL bits
1
1
173
29
33
54229
54373
54370
SMALL
LARGE-S
LARGE-N

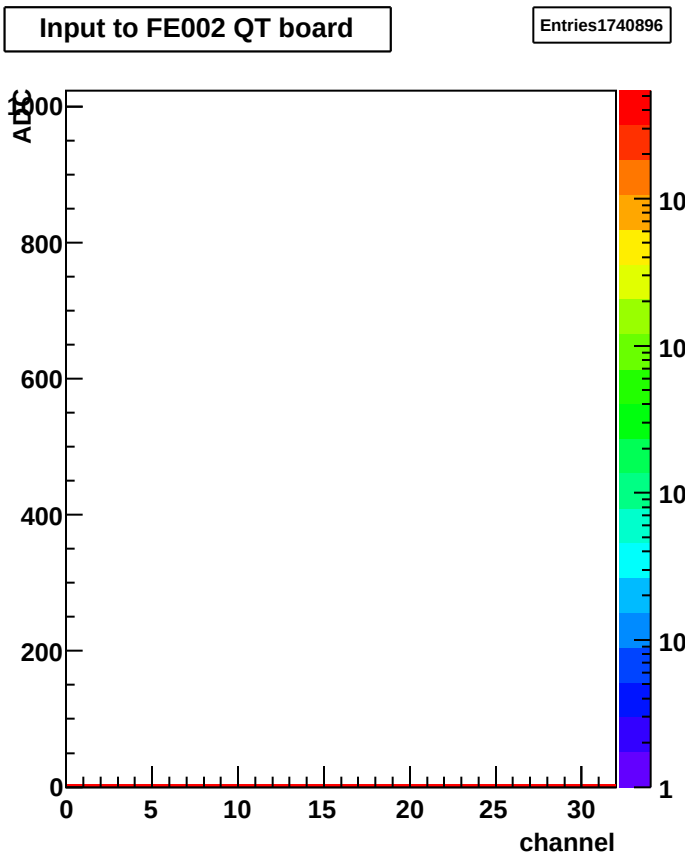

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

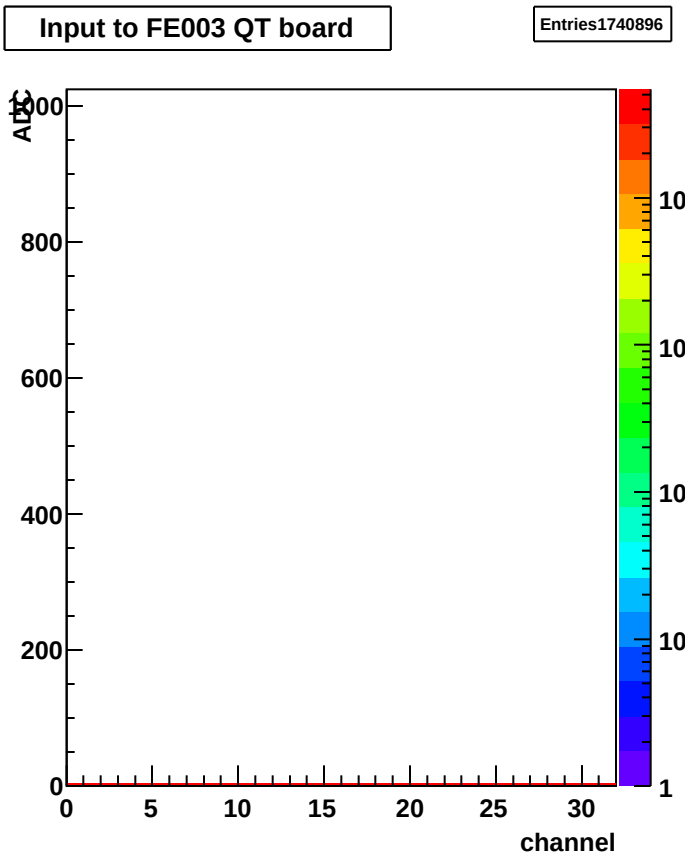

Input to FPD L2 DSM
Entries 163209
HT bits
233
8
4
843
2537
53
53
50790
54342
54346
SMALL
LARGE-S
LARGE-N

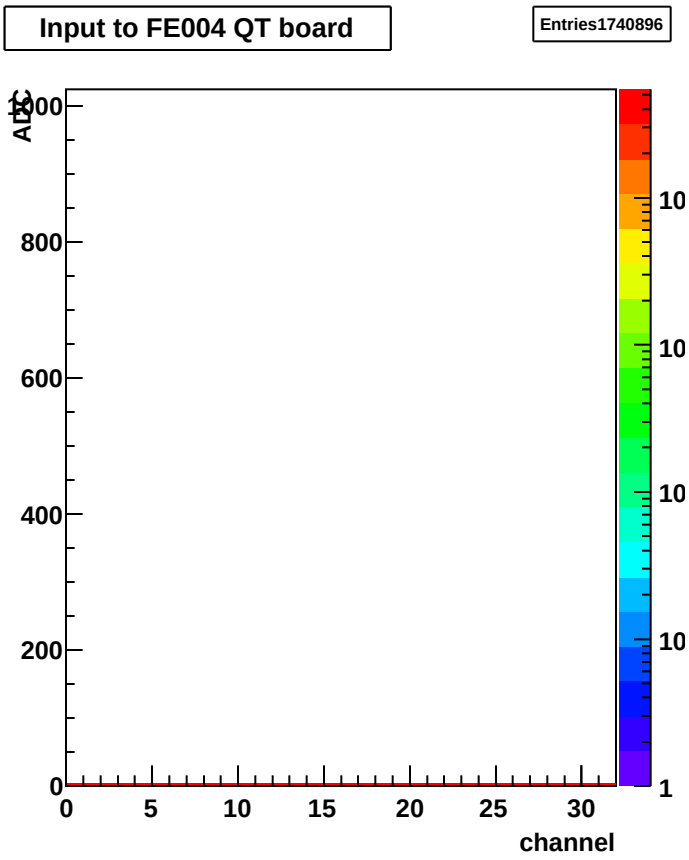

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

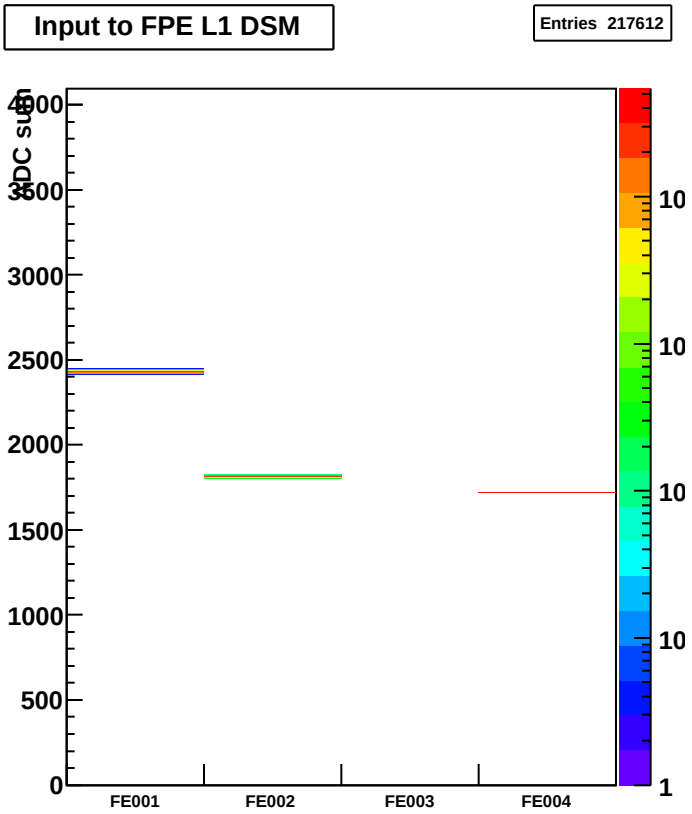

Input to FPD L2 DSM
Entries 326418
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

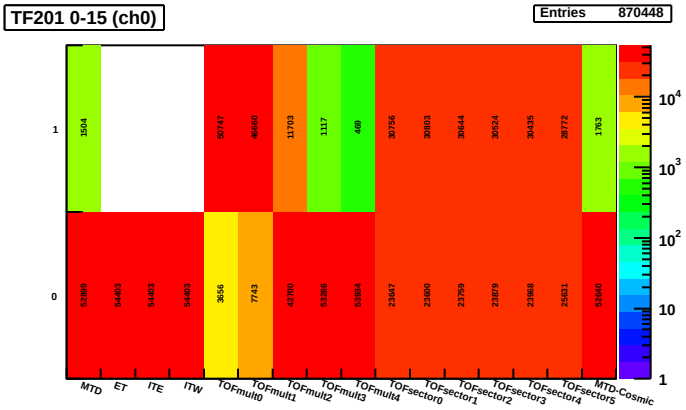

Input to FE001 QT board
Entries1740896
ADC
channel

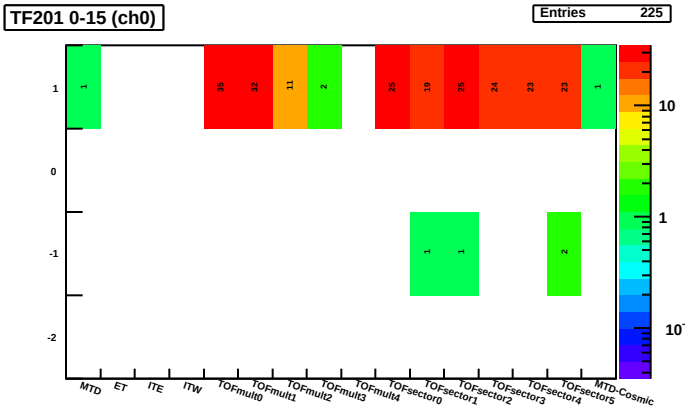

Input to FE002 QT board
Entries1740896
ADC
channel

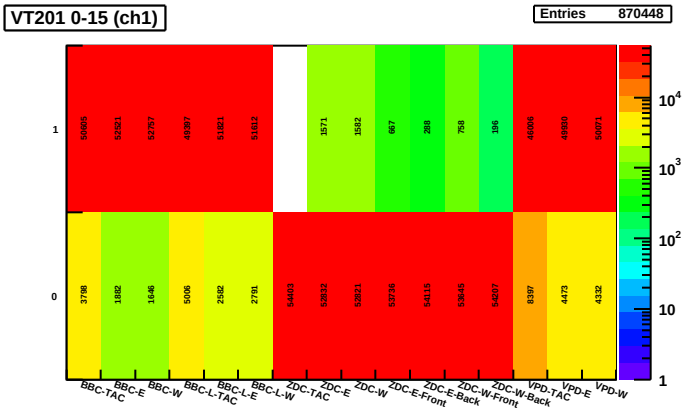

Input to FE003 QT board
Entries1740896
ADC
channel

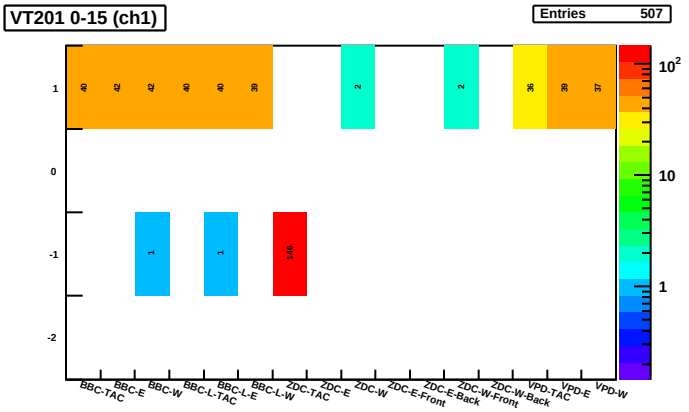

Input to FE004 QT board
Entries1740896
ADC
channel

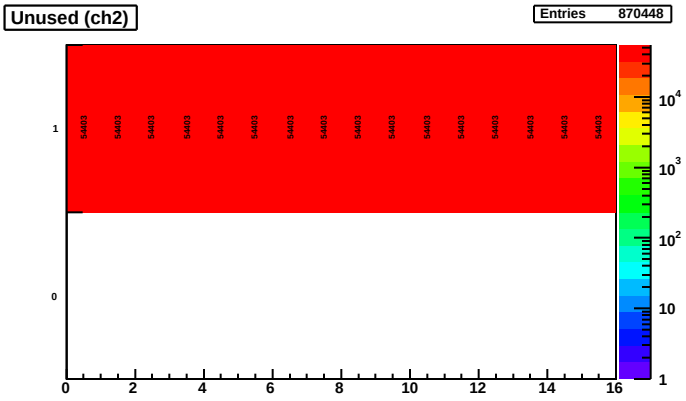

Input to FPE L1 DSM
Entries 217612
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

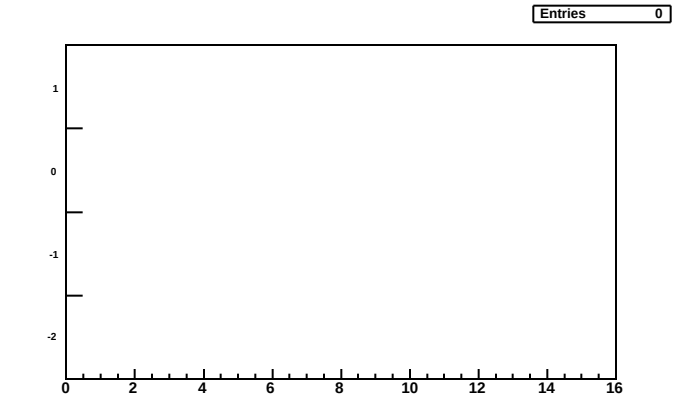

TF201 0-15 (ch0)
Entries 870448
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

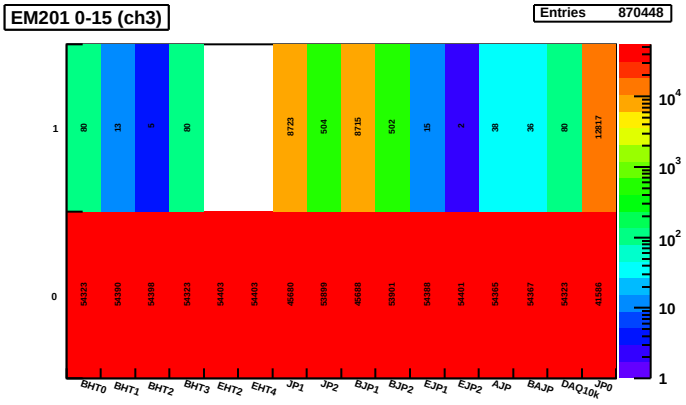

TF201 0-15 (ch0)
Entries 225
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

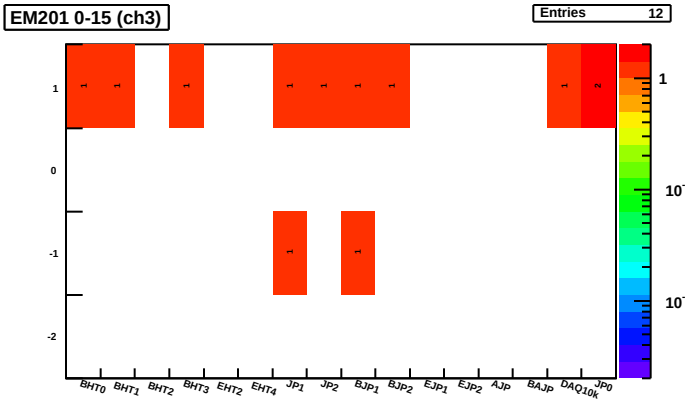

VT201 0-15 (ch1)
Entries 870448
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 507
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 870448


Entries 0


EM201 0-15 (ch3)
Entries 870448
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
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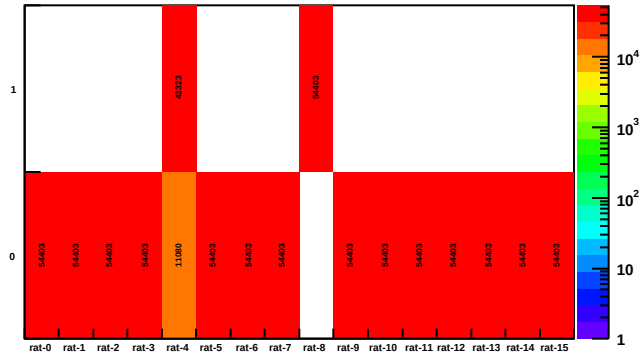

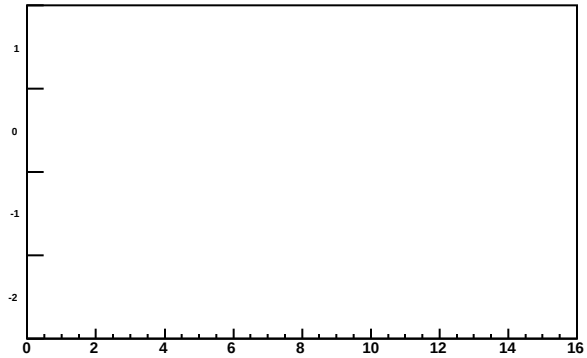

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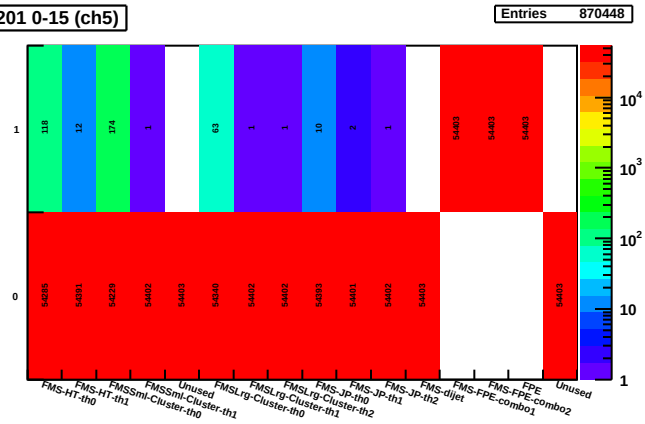

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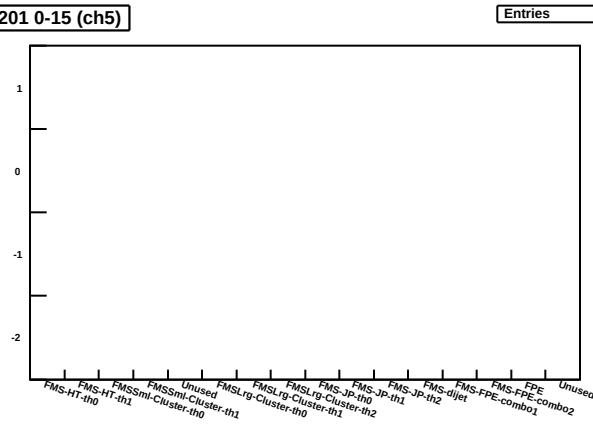

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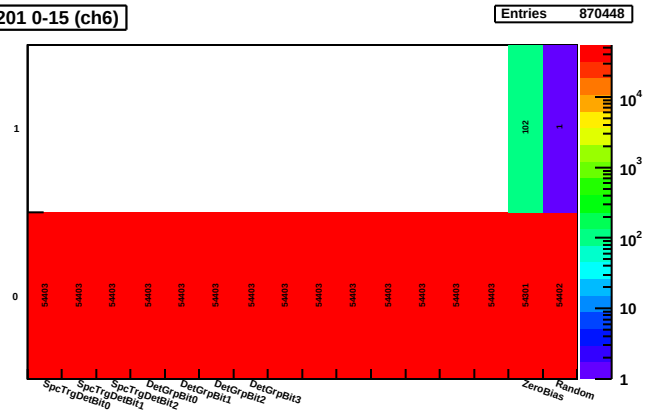

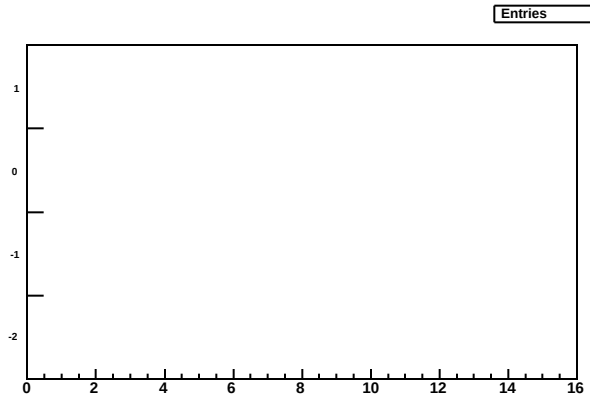
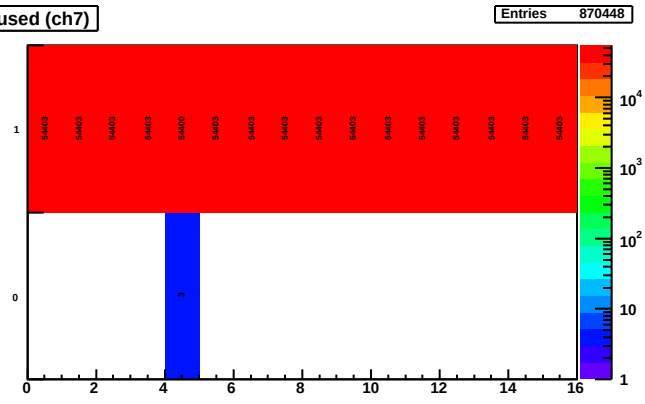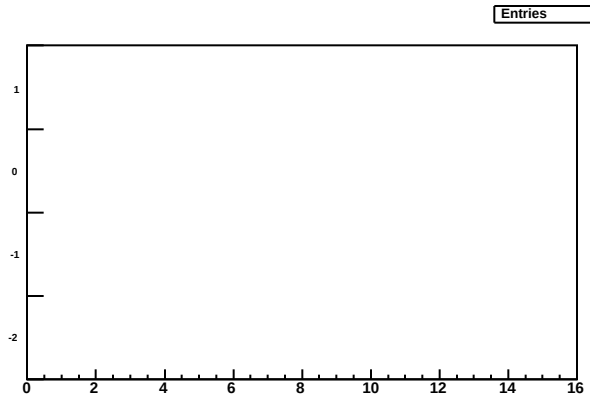
Unused (ch7)