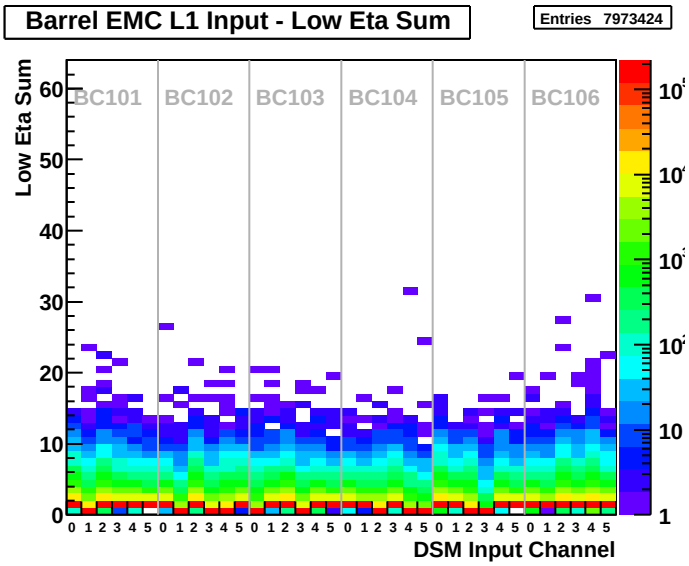
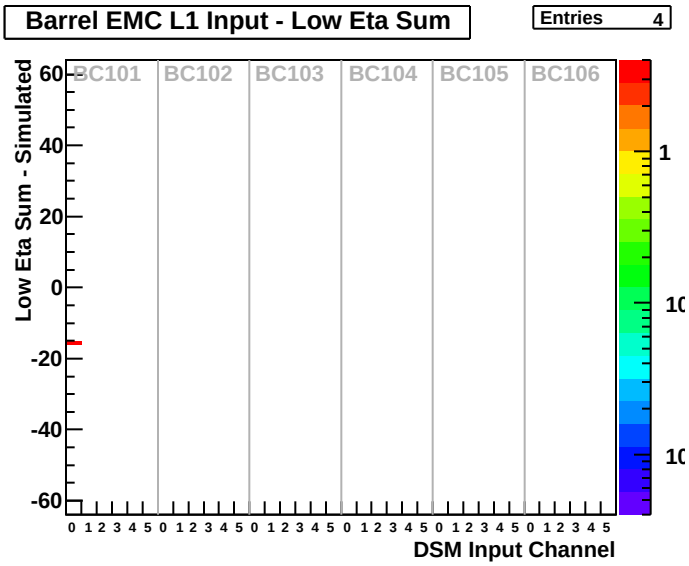
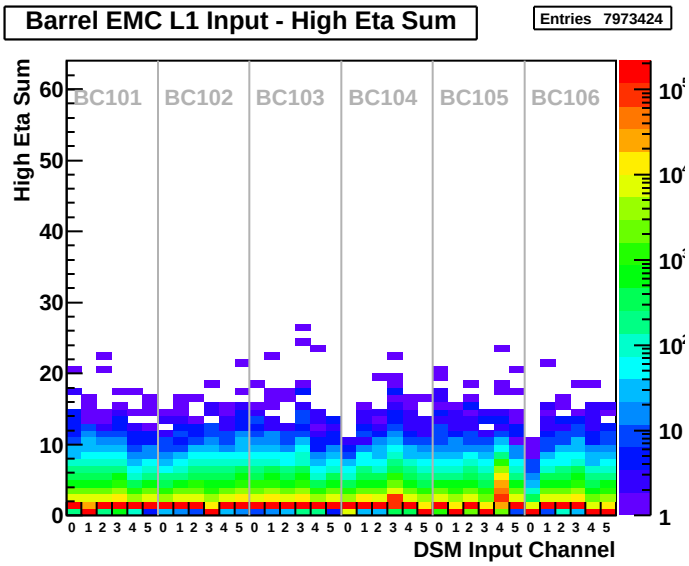
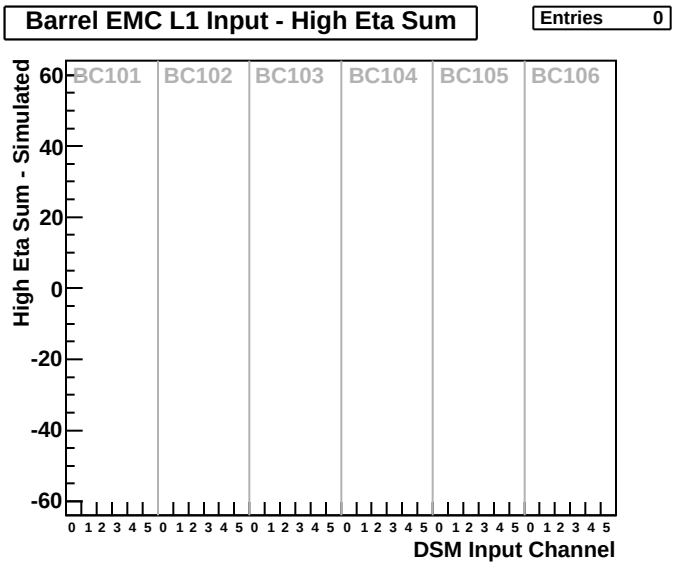
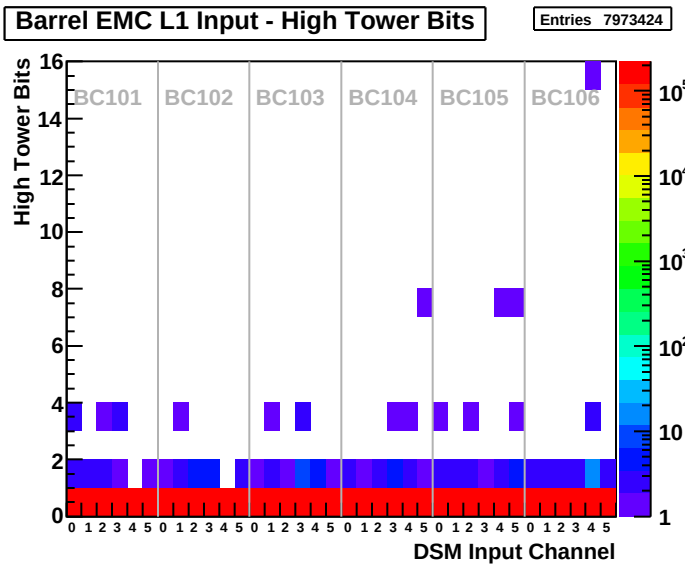
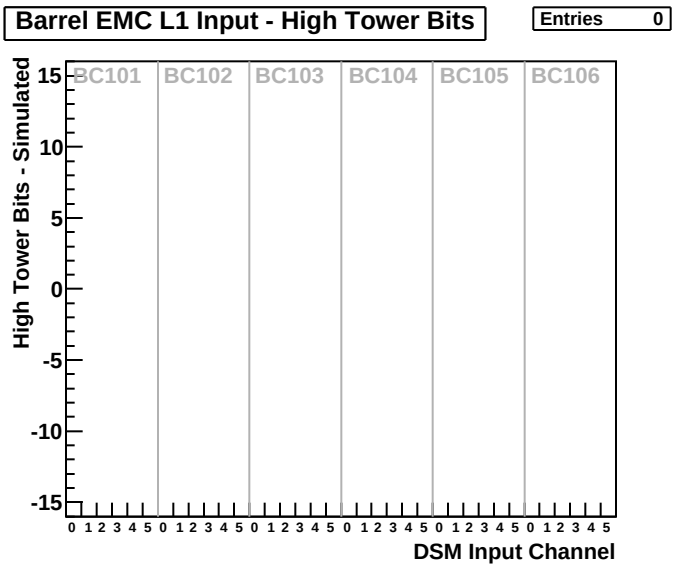

Barrel EMC L1 Input - Low Eta Sum
Entries 7973424
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

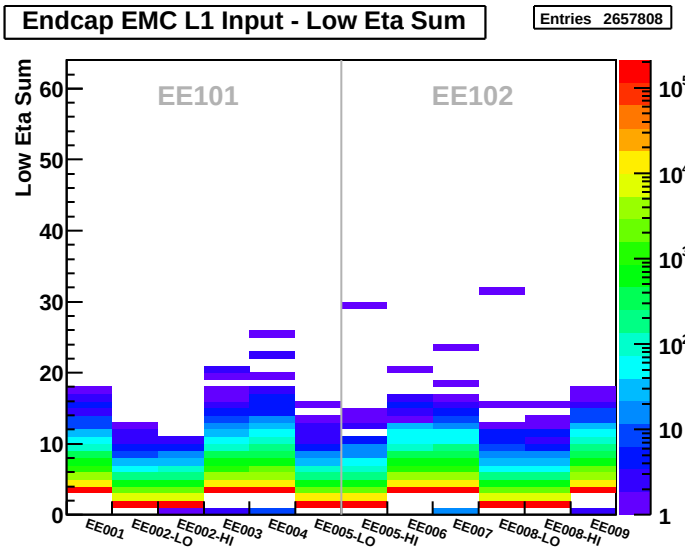

Barrel EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

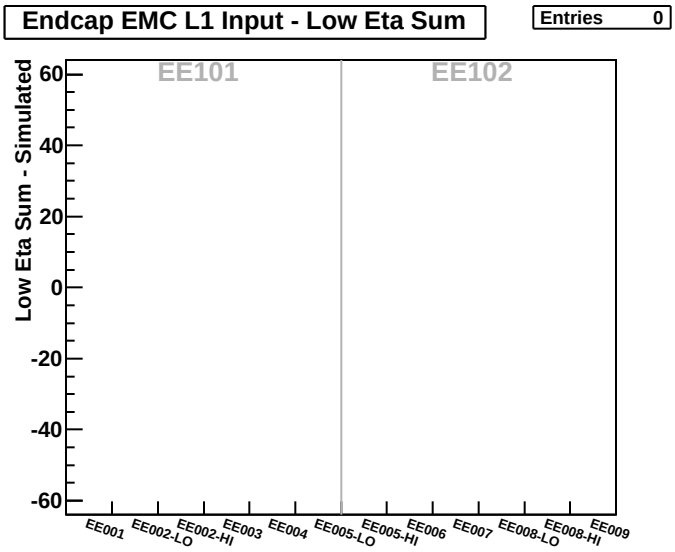

Barrel EMC L1 Input - High Eta Sum
Entries 7973424
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

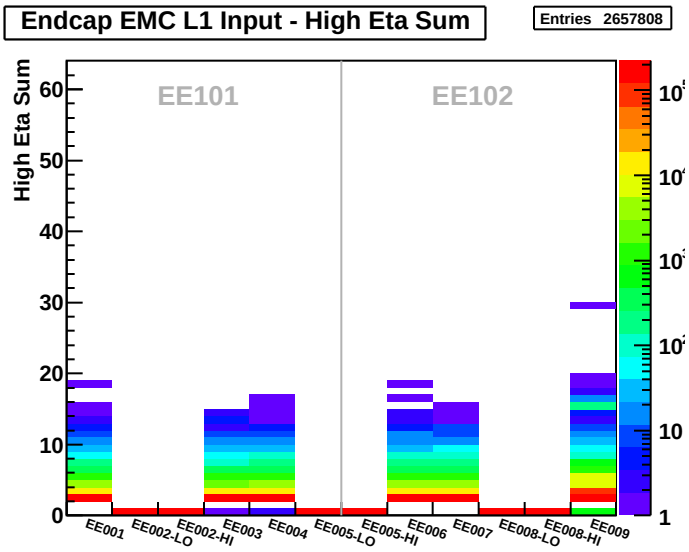

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

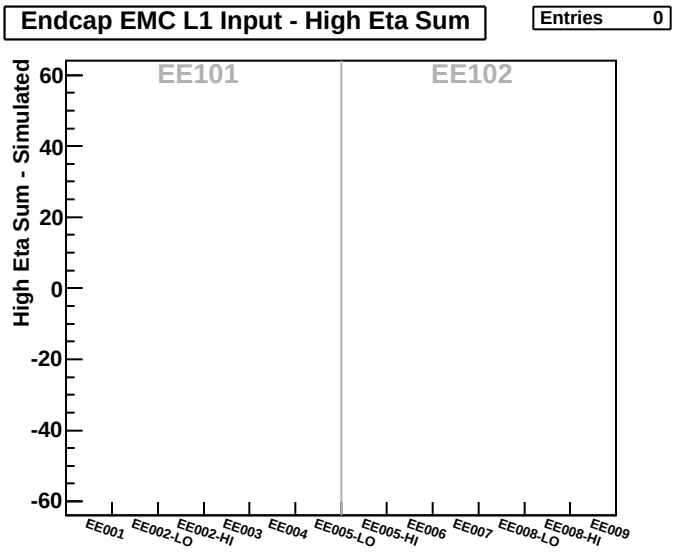

Barrel EMC L1 Input - High Tower Bits
Entries 7973424
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

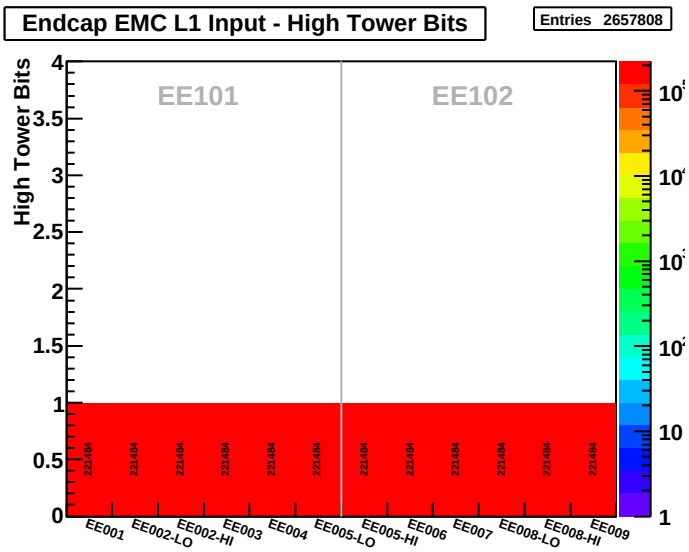

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

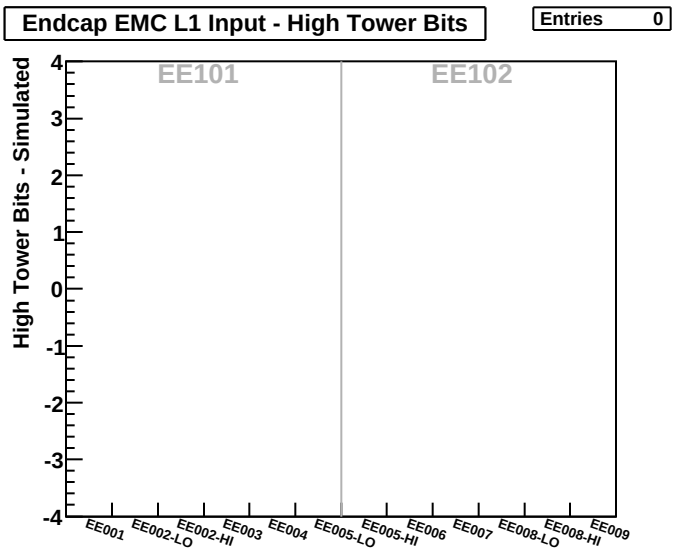

Endcap EMC L1 Input - Low Eta Sum
Entries 2657808
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

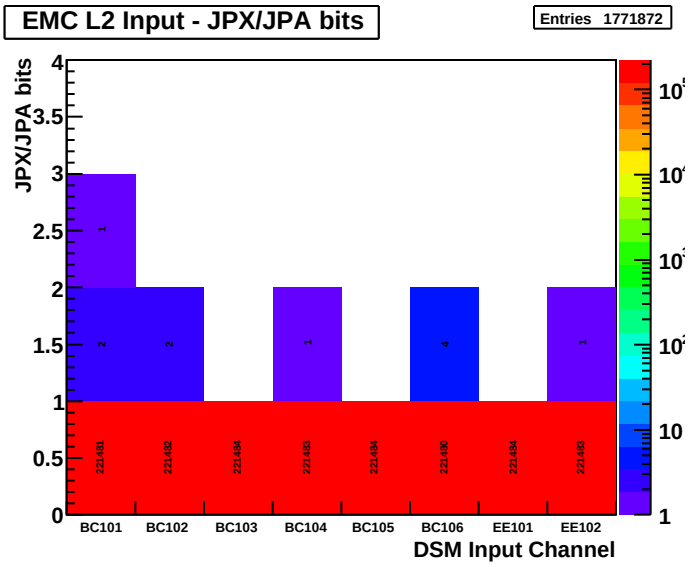

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

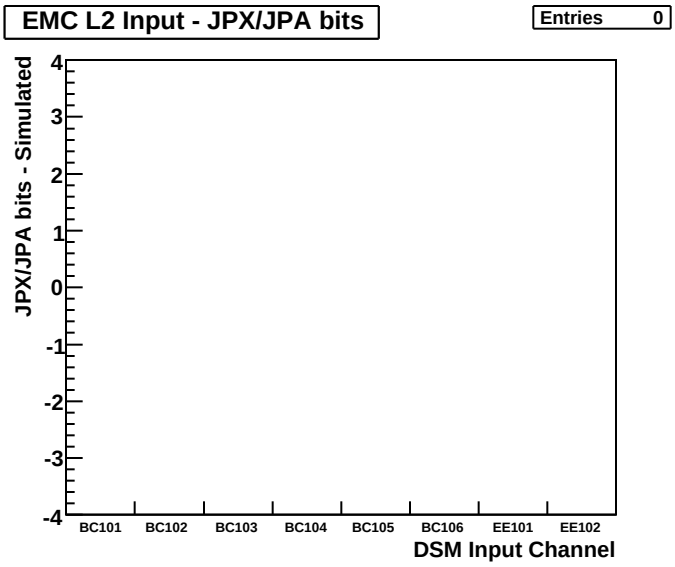

Endcap EMC L1 Input - High Eta Sum
Entries 2657808
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

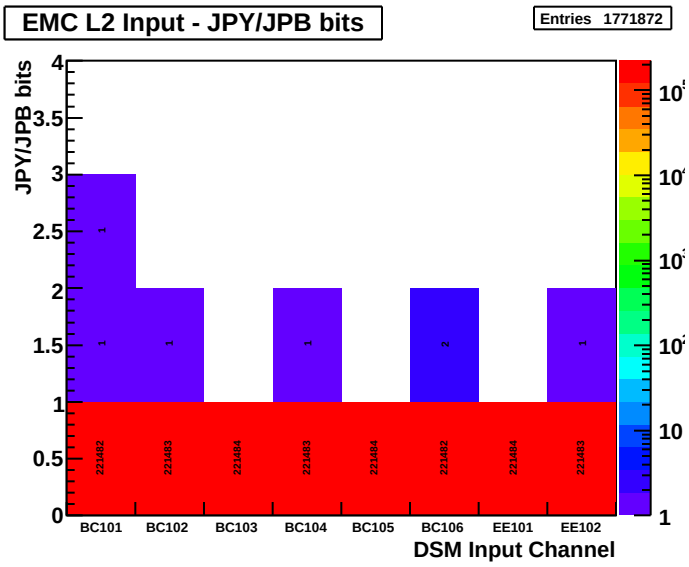

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

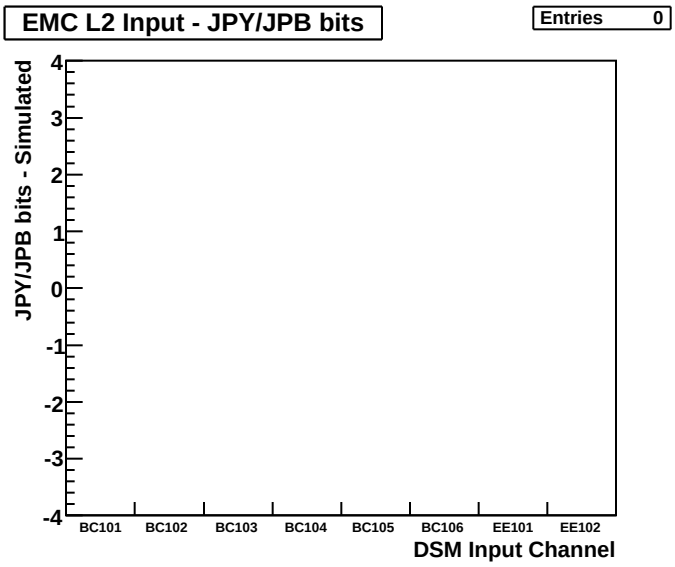

Endcap EMC L1 Input - High Tower Bits
Entries 2657808
High Tower Bits
EE101
EE102
221484
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

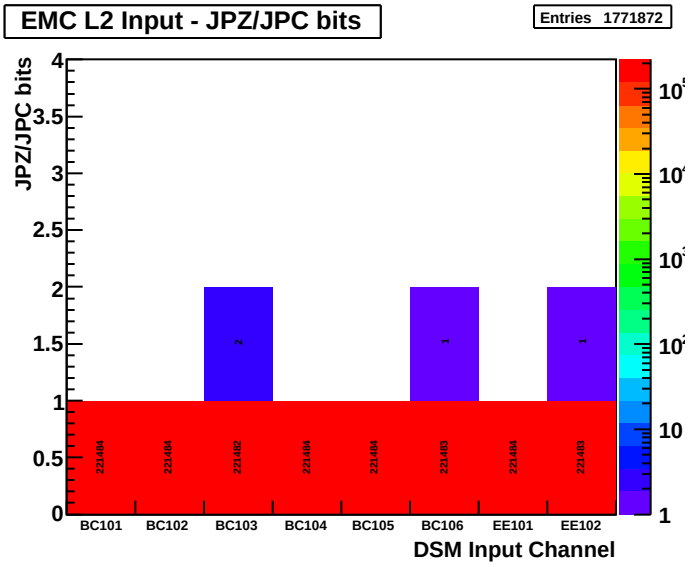

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

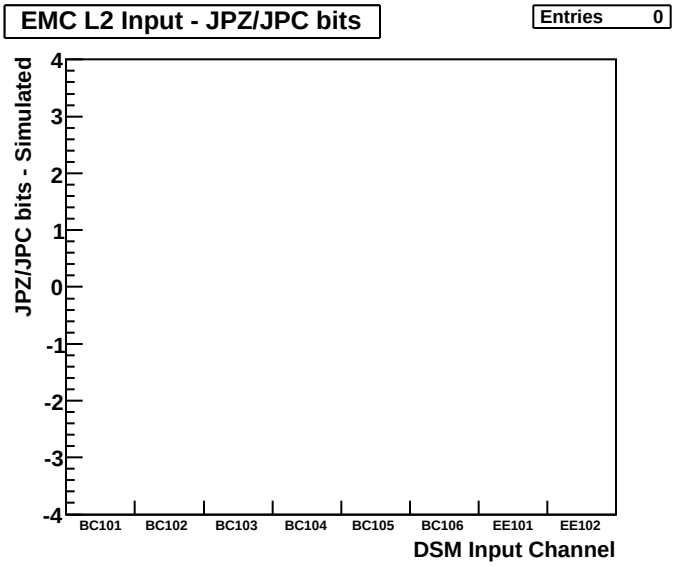

EMC L2 Input - JPX/JPA bits
Entries 1771872
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

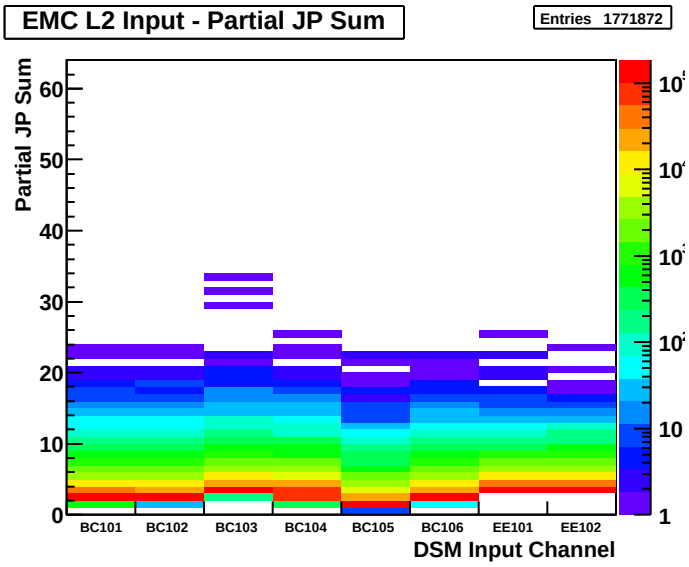

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

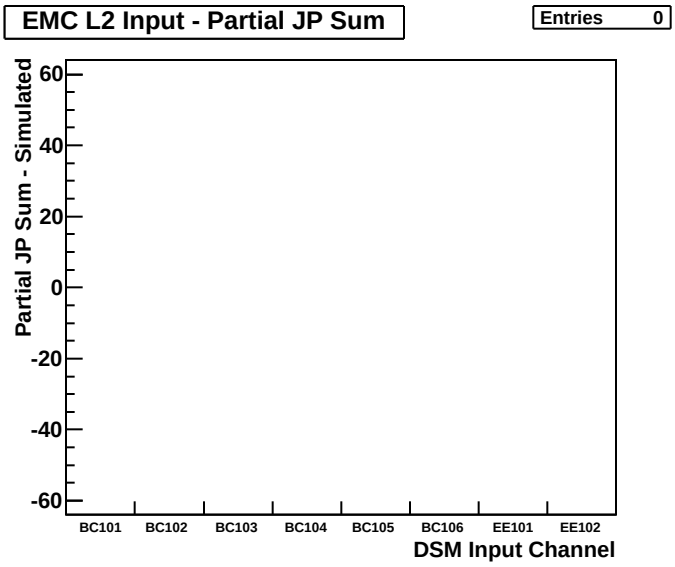

EMC L2 Input - JPY/JPB bits
Entries 1771872
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

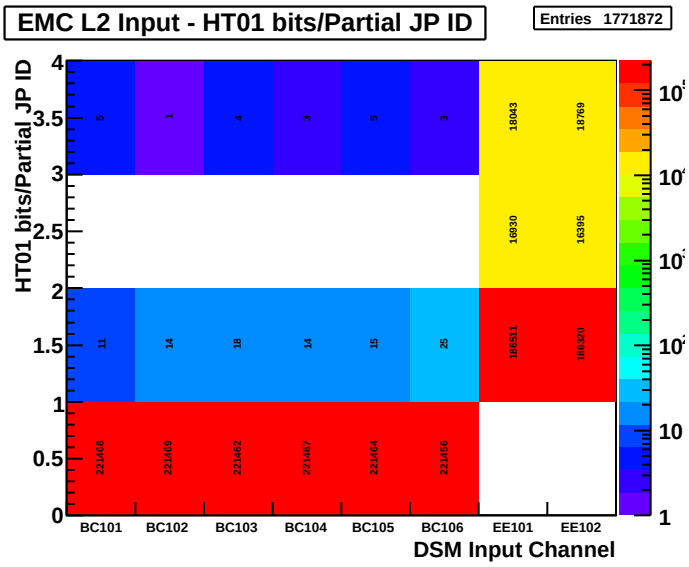

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

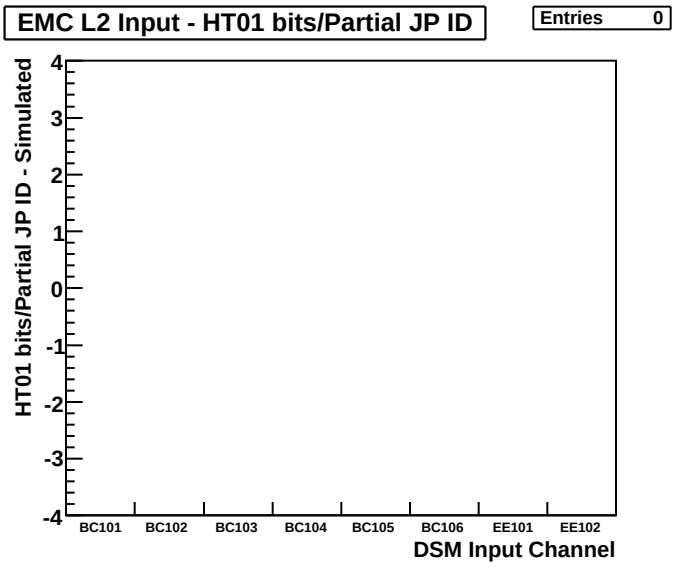

EMC L2 Input - JPZ/JPC bits
Entries 1771872
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

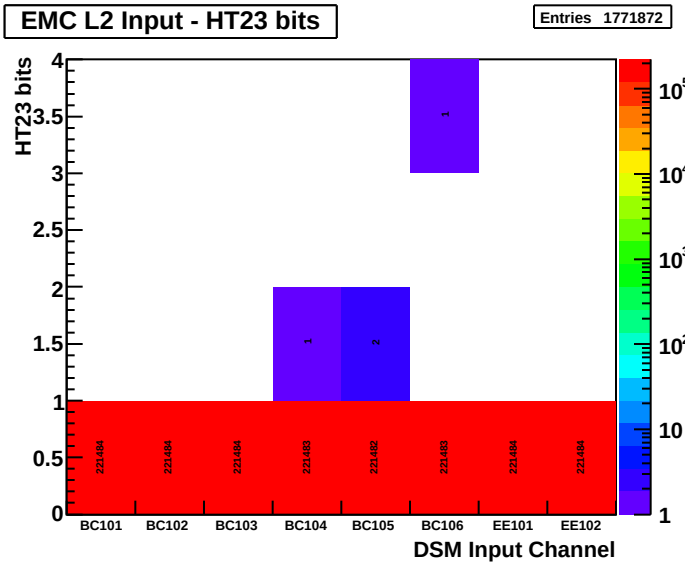

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

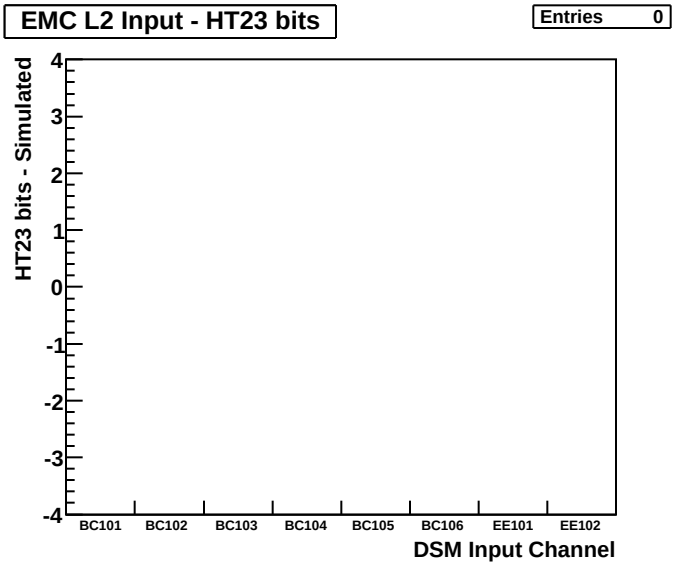

EMC L2 Input - Partial JP Sum
Entries 1771872
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

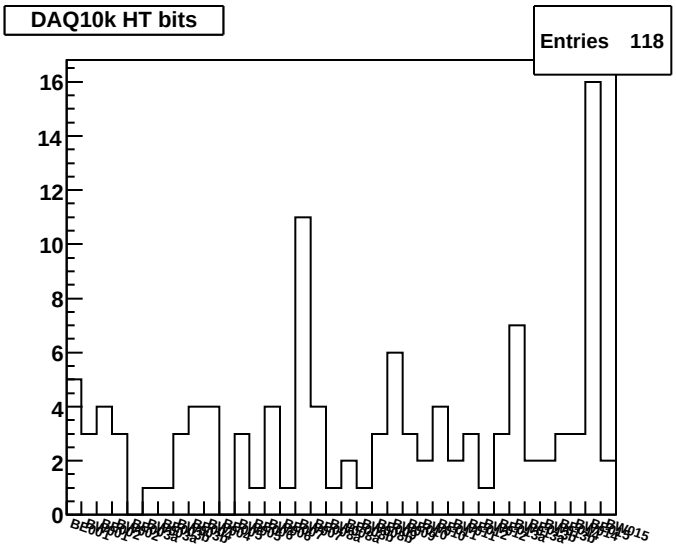

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

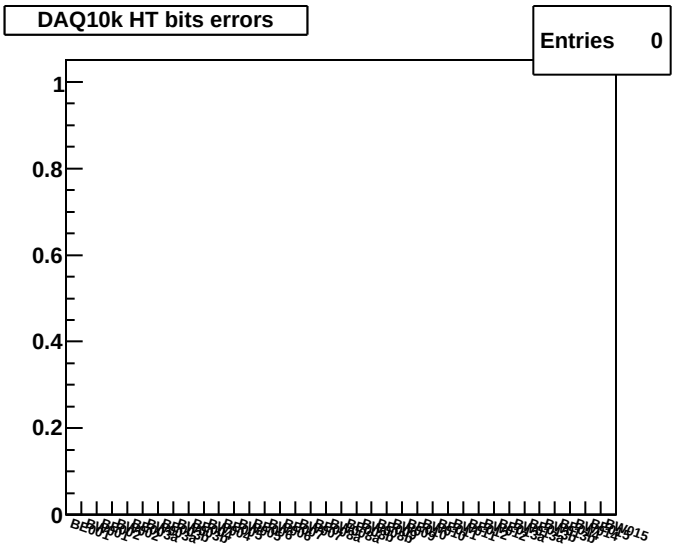

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1771872
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

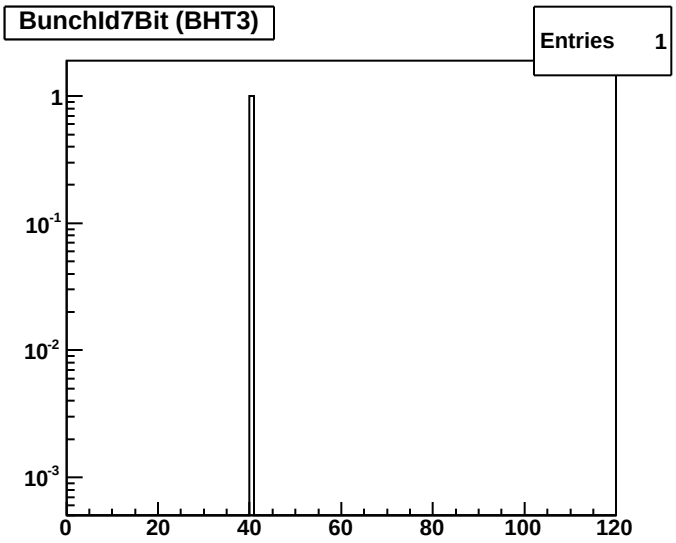

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

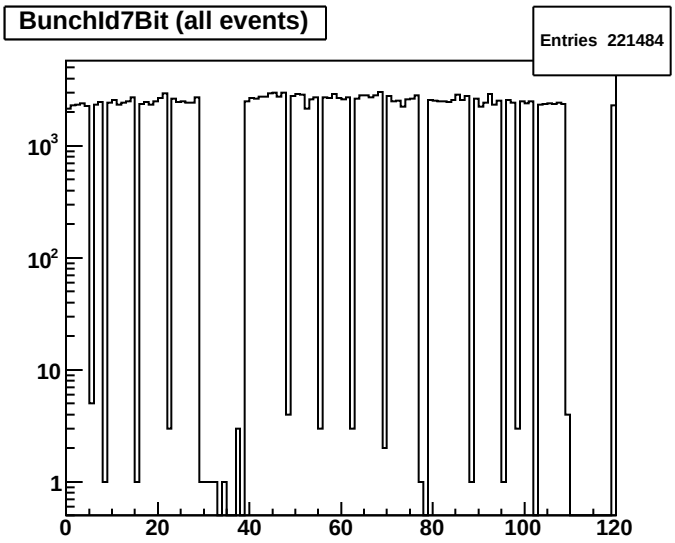

EMC L2 Input - HT23 bits
Entries 1771872
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

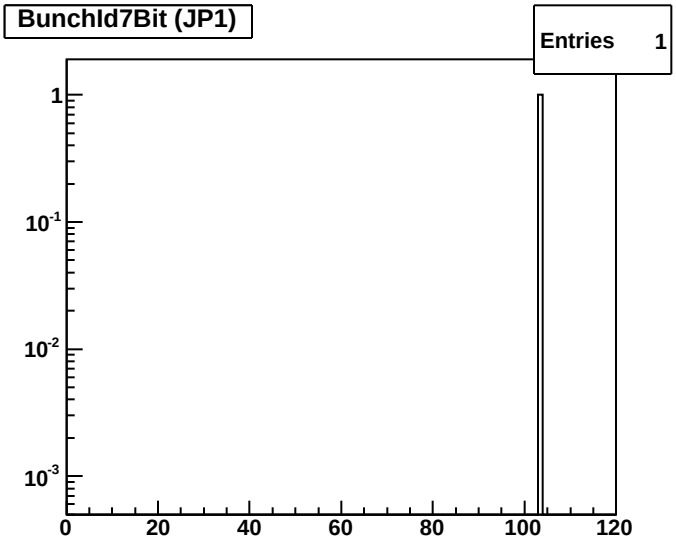

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

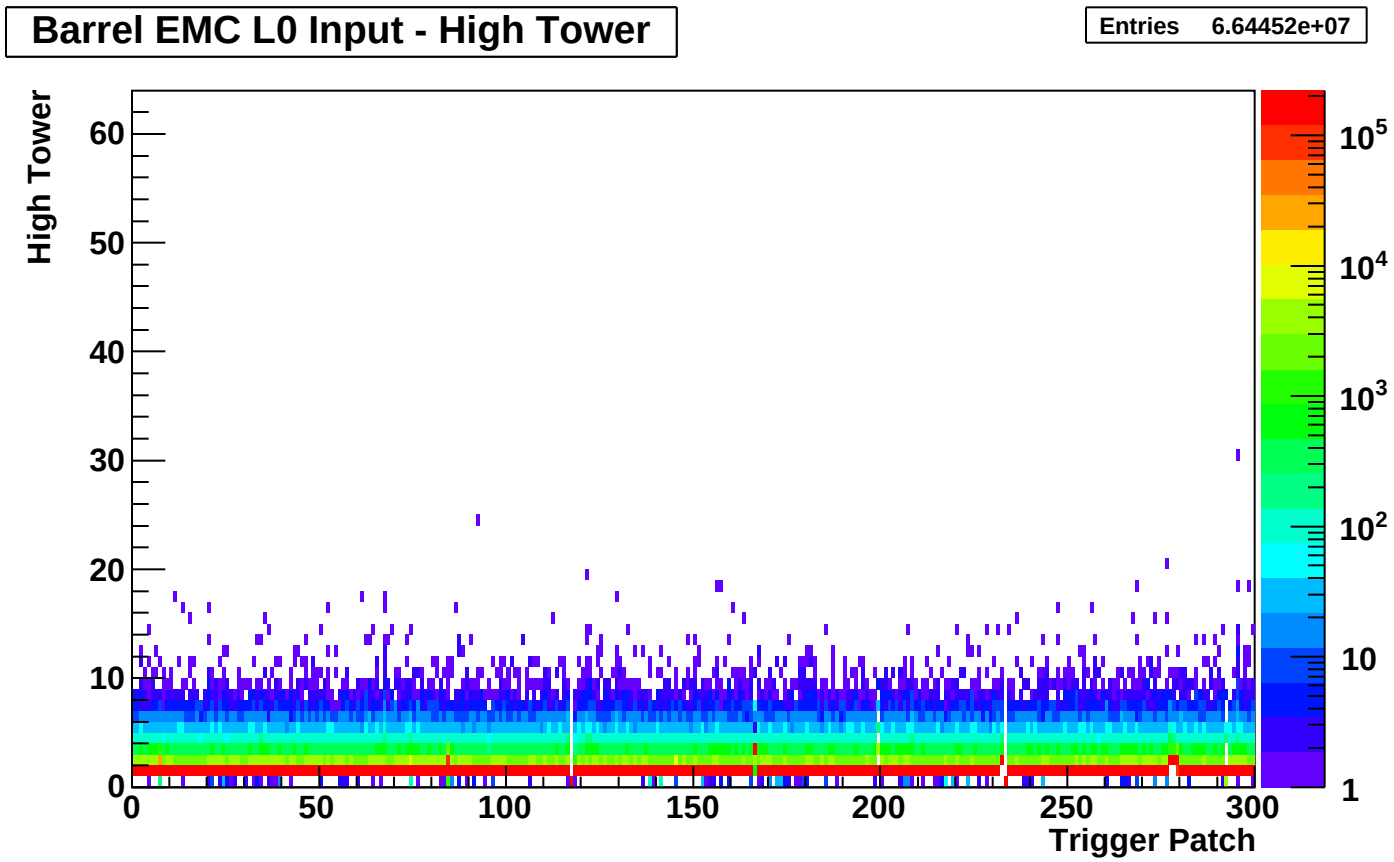

DAQ10k HT bits
Entries 118

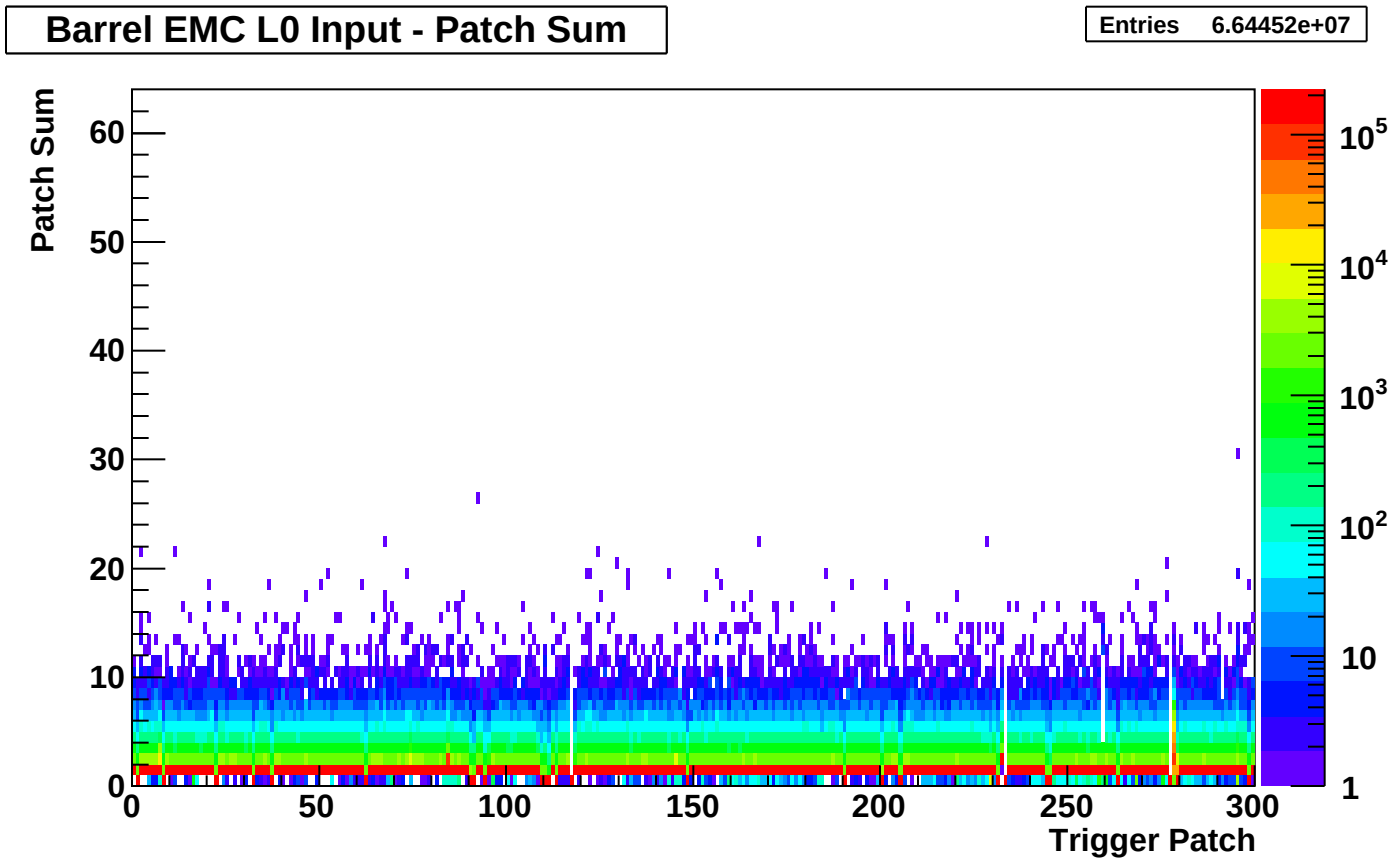

DAQ10k HT bits errors
Entries 0

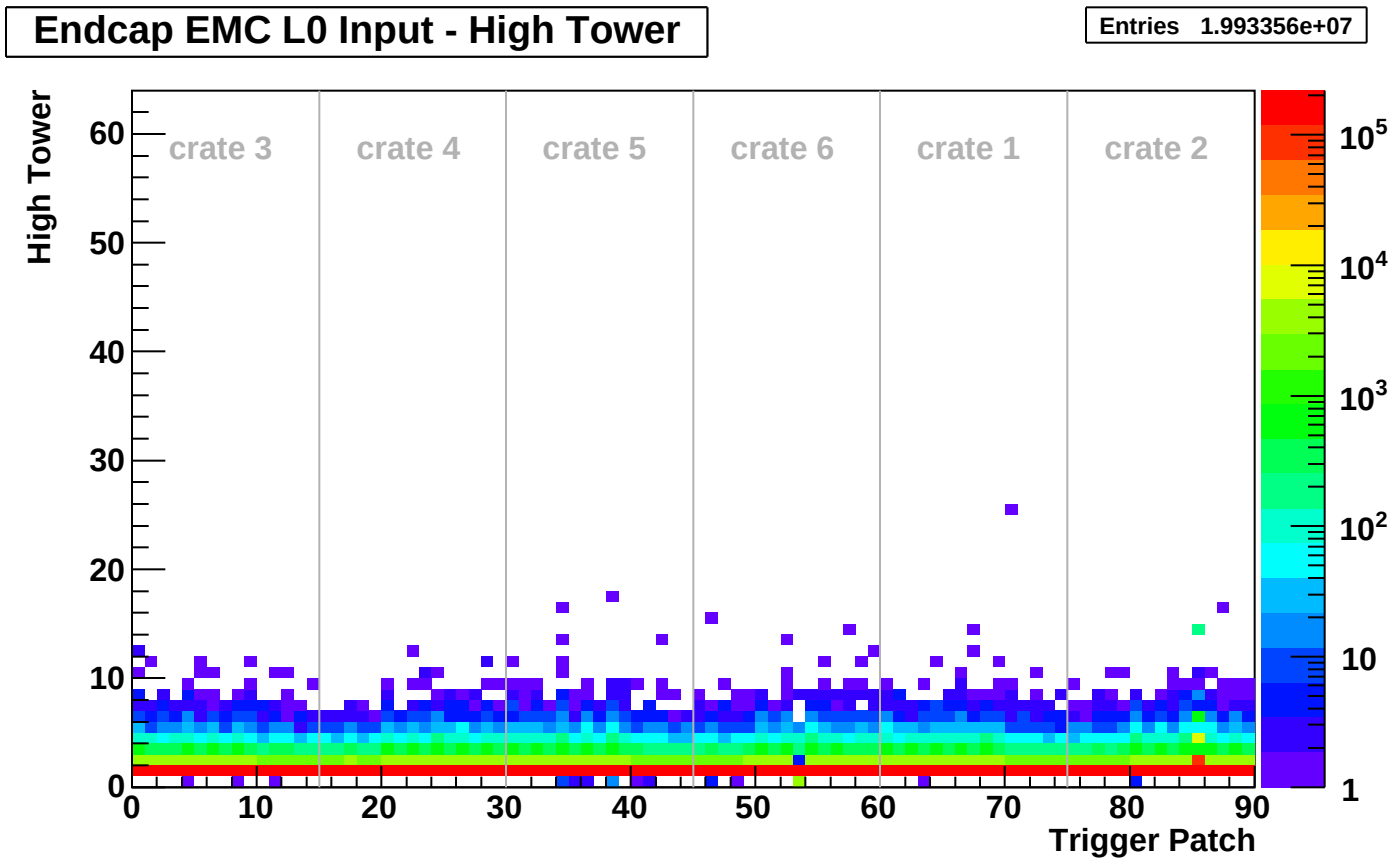

BunchId7Bit (BHT3)
Entries 1

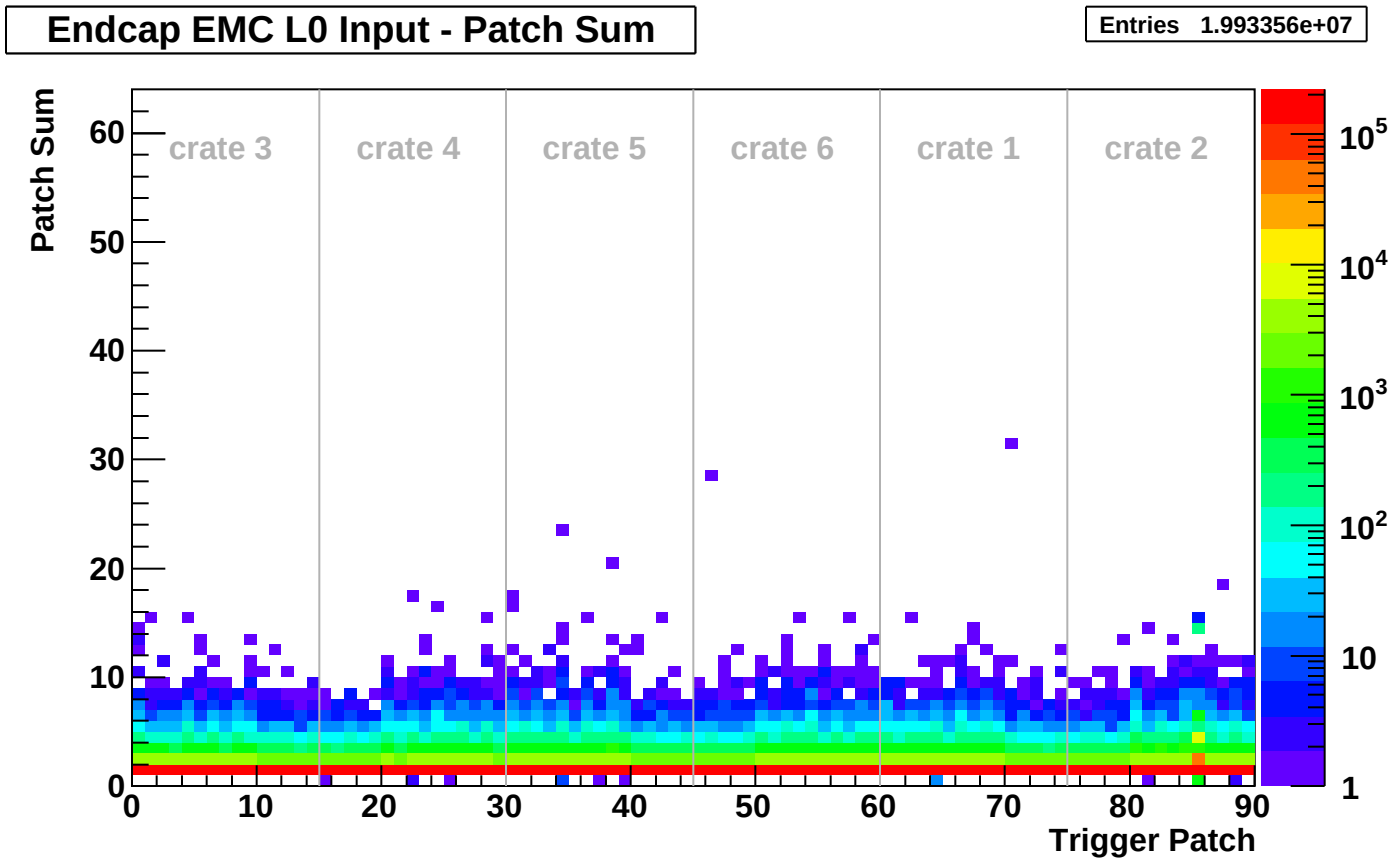

BunchId7Bit (all events)
Entries 221484

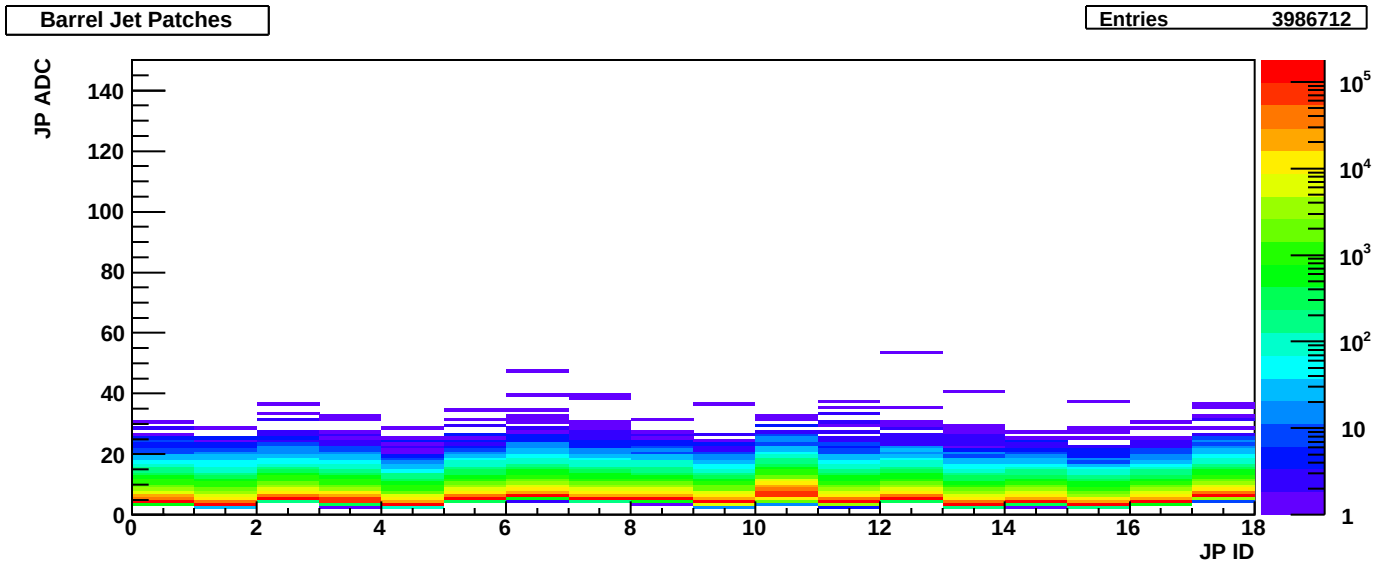

BunchId7Bit (JP1)
Entries 1

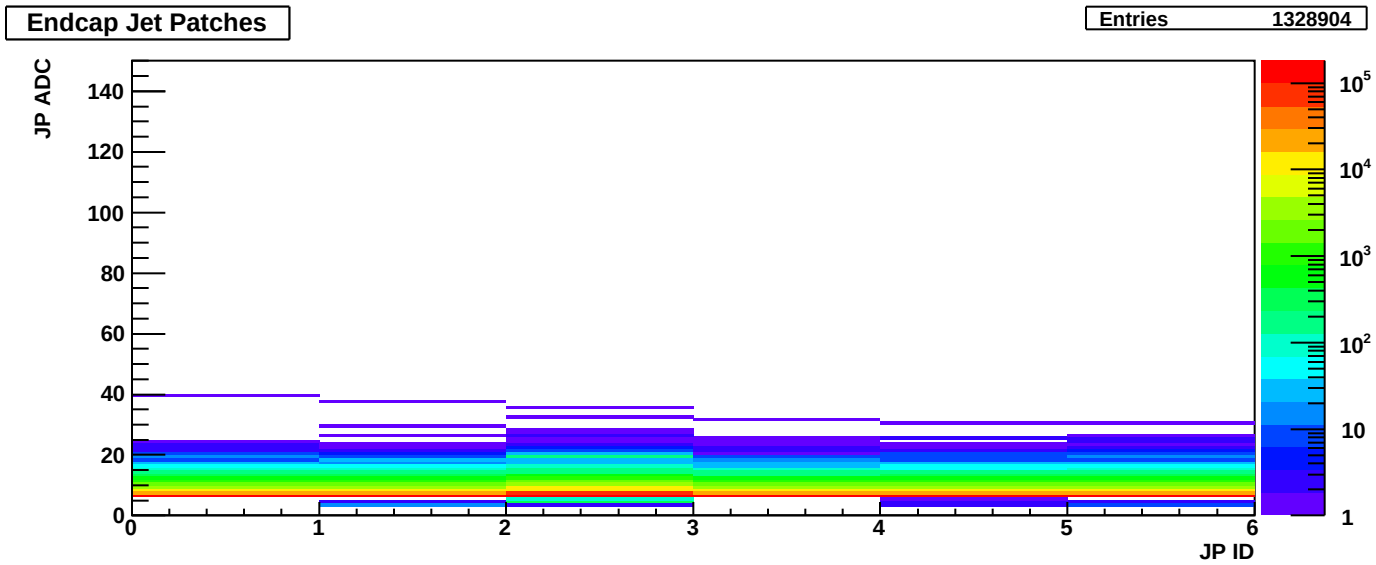

Barrel EMC L0 Input - High Tower
Entries 6.64452e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

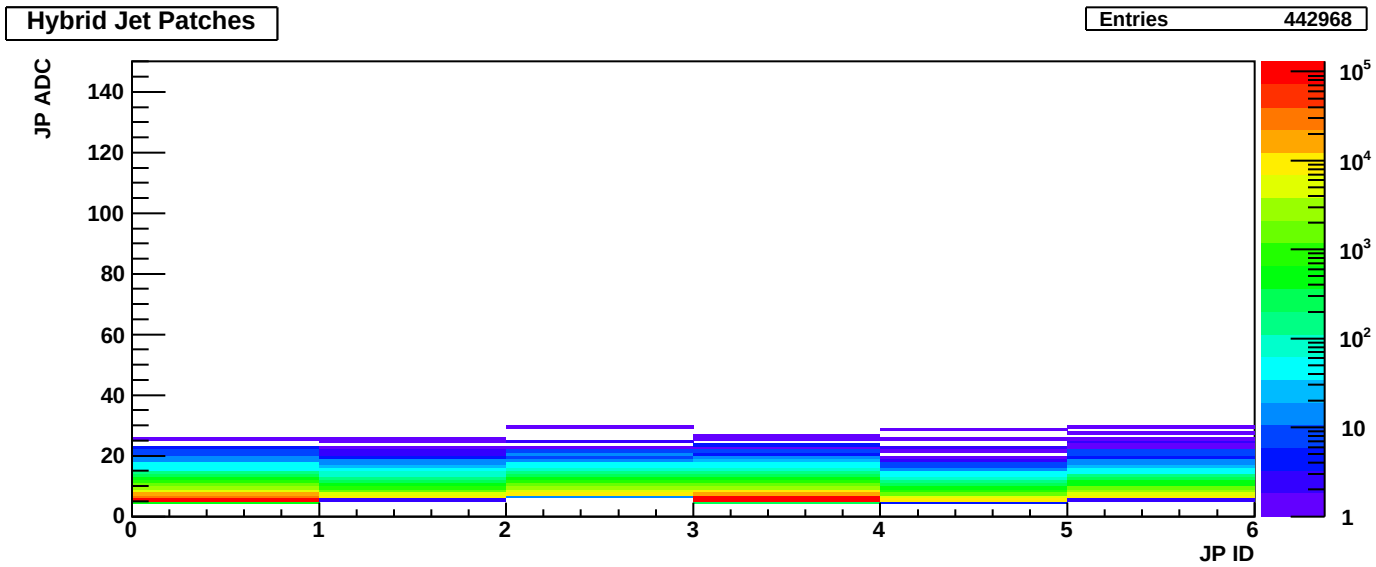

Barrel EMC L0 Input - Patch Sum
Entries 6.64452e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

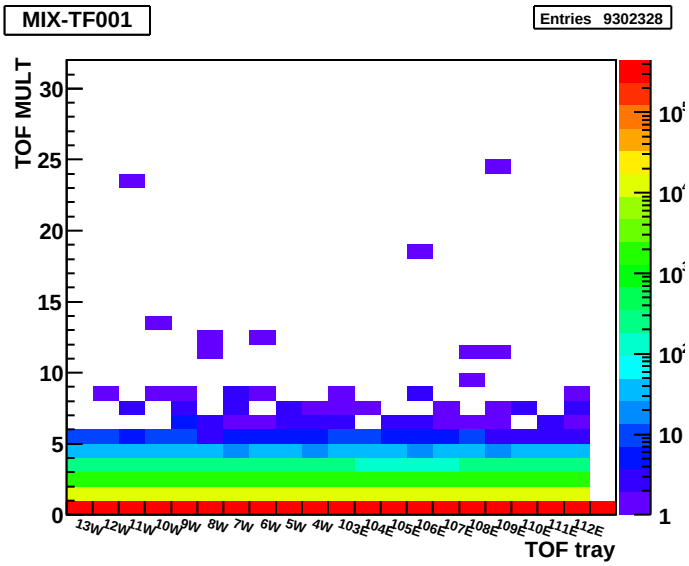

Endcap EMC L0 Input - High Tower
Entries 1.993356e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

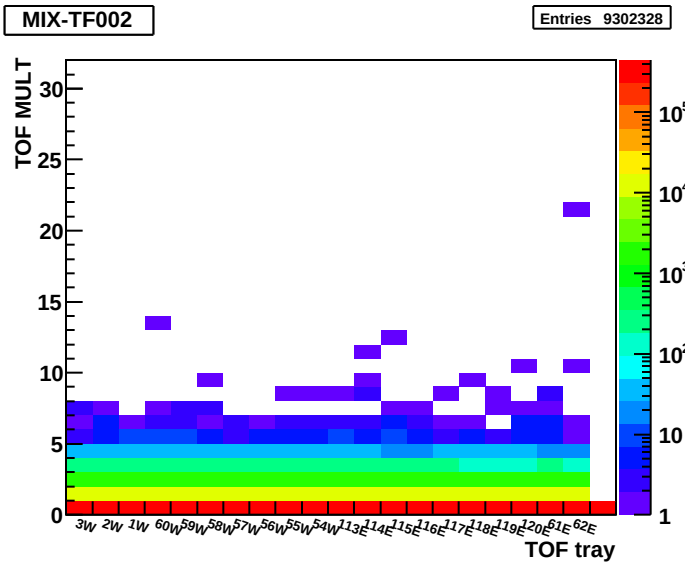

Endcap EMC L0 Input - Patch Sum
Entries 1.993356e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

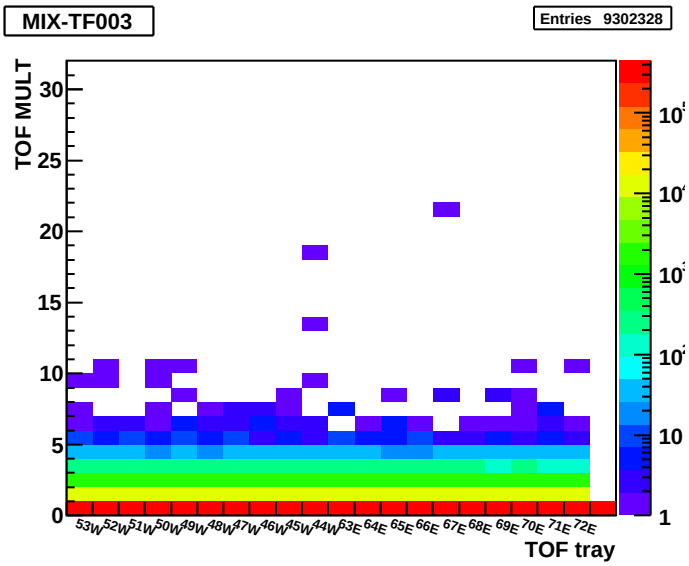

Barrel Jet Patches
Entries 3986712
JP ADC
JP ID

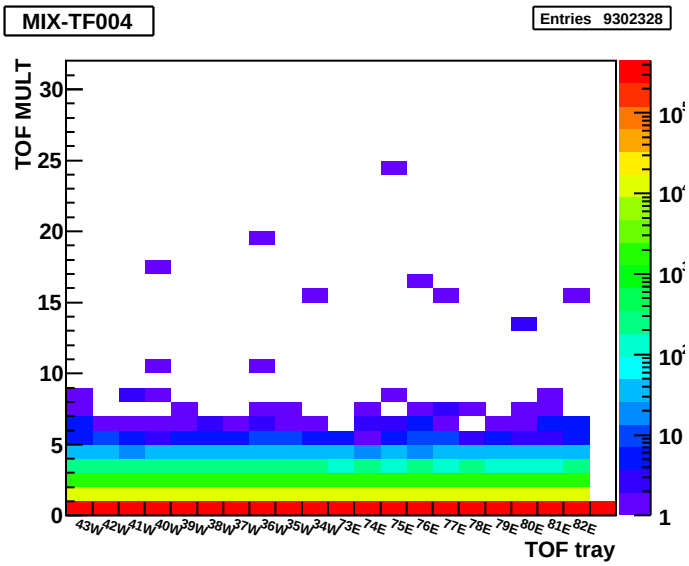

Endcap Jet Patches
Entries 1328904
JP ADC
JP ID

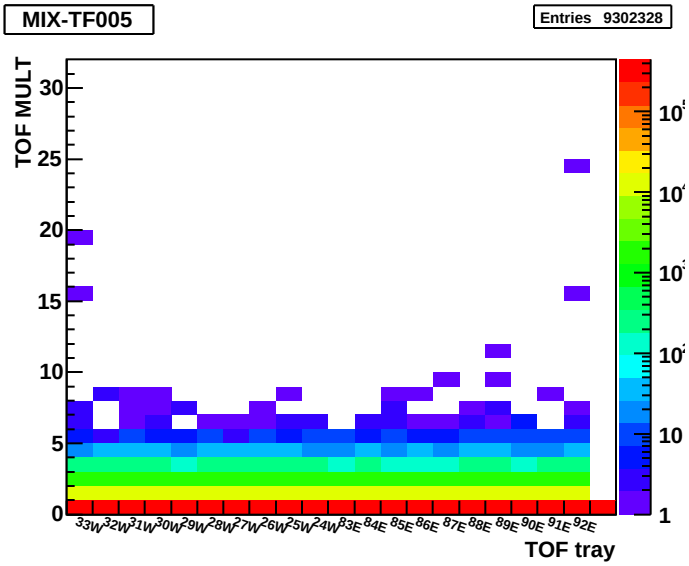

Hybrid Jet Patches
Entries 442968
JP ADC
JP ID

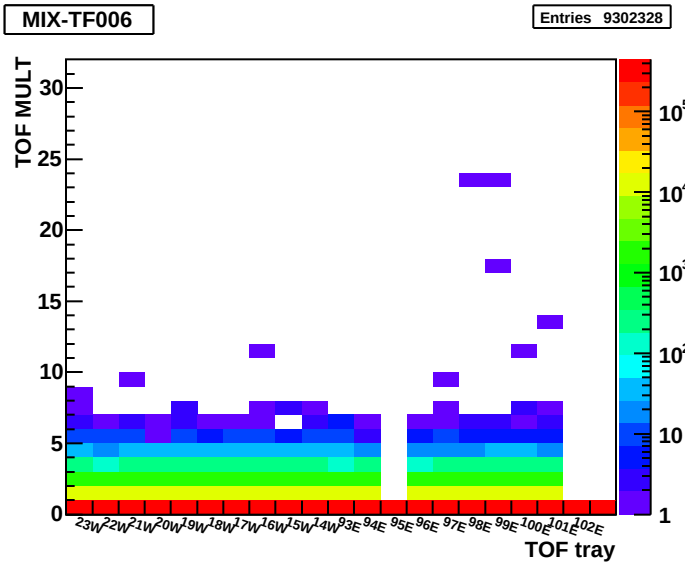

MIX-TF001
Entries 9302328
TOF MULT
TOF tray

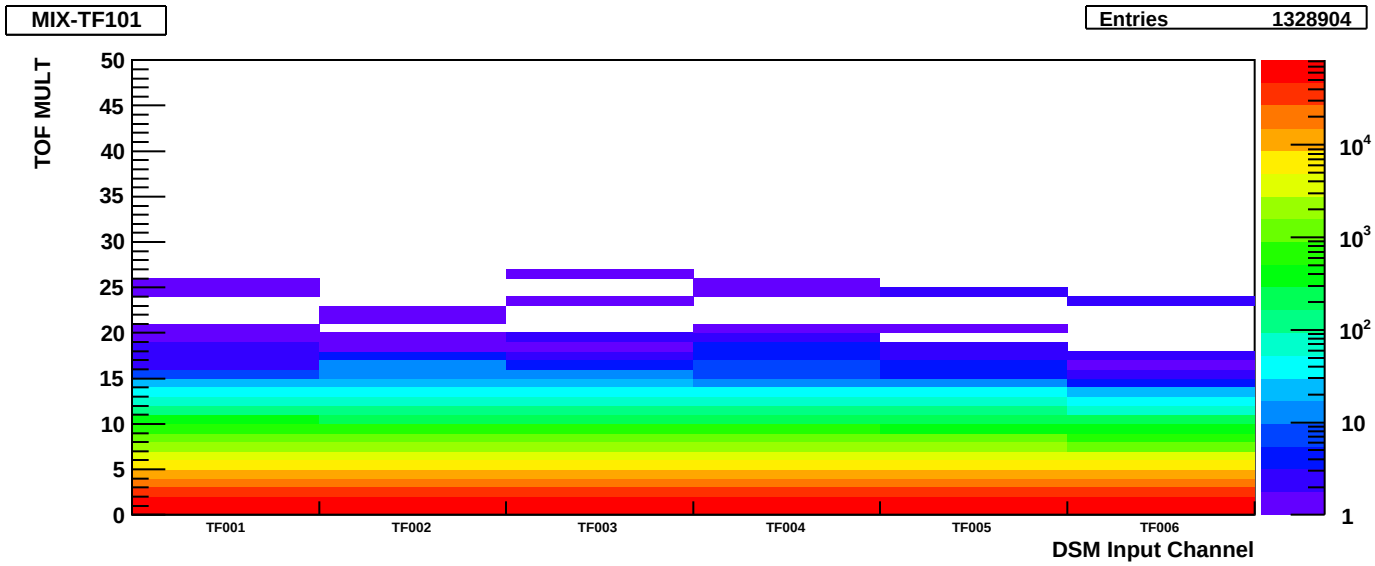

MIX-TF002
Entries 9302328
TOF MULT
TOF tray

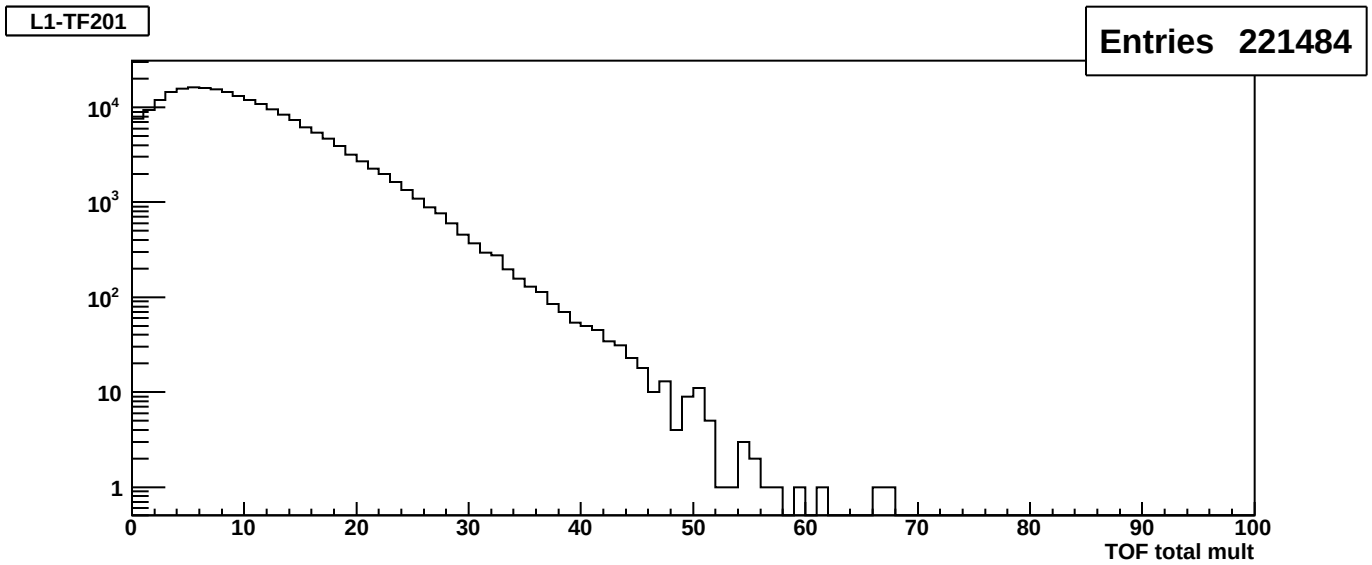

MIX-TF003
Entries 9302328
TOF MULT
TOF tray

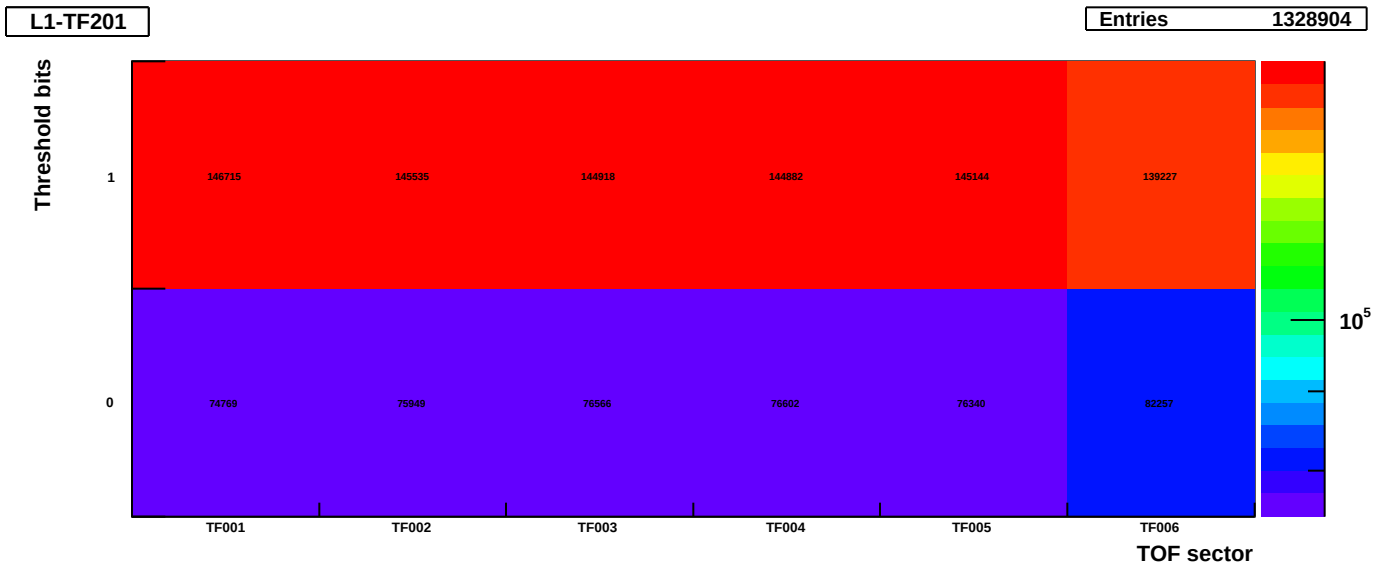

MIX-TF004
Entries 9302328
TOF MULT
TOF tray

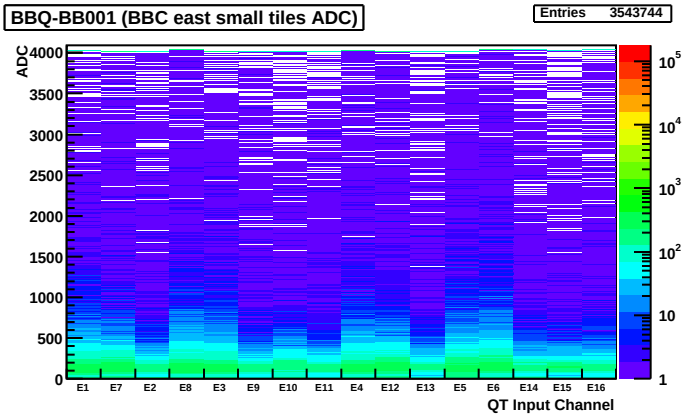

MIX-TF005
Entries 9302328
TOF MULT
TOF tray

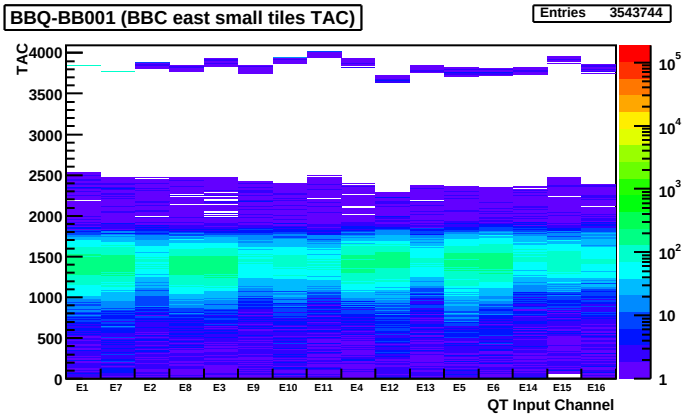

MIX-TF006
Entries 9302328
TOF MULT
TOF tray

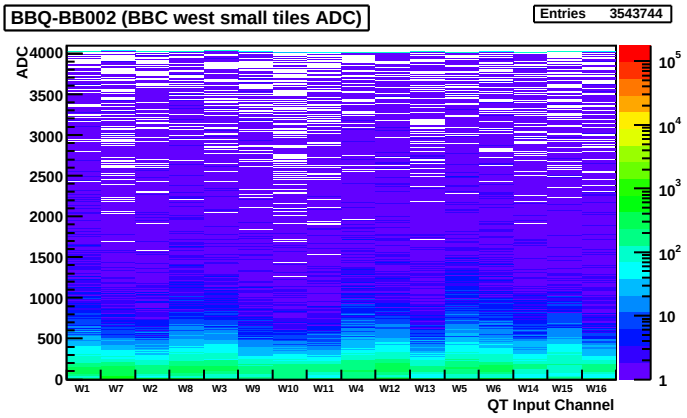

MIX-TF101
Entries 1328904
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

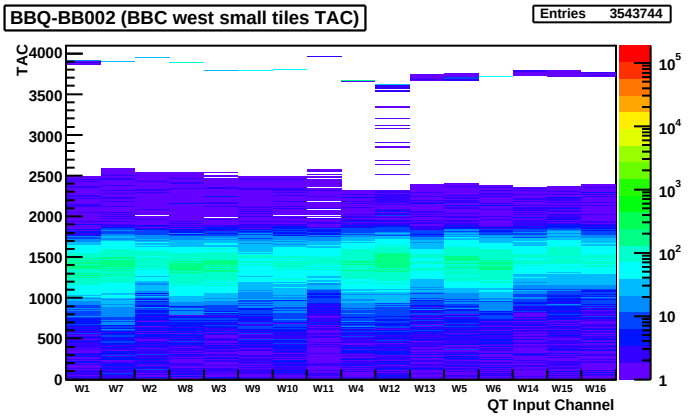

L1-TF201
Entries 221484
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

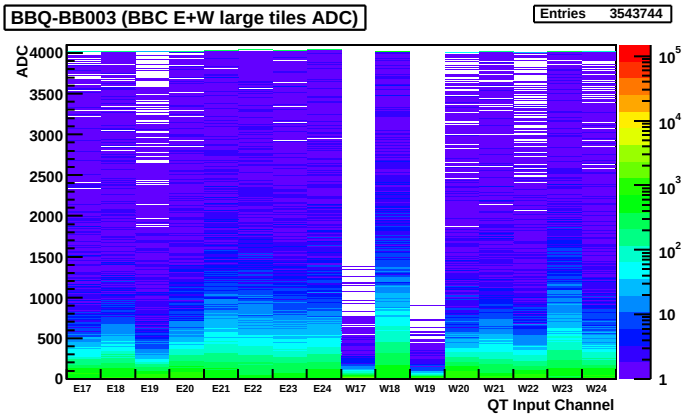

L1-TF201
Entries 1328904
Threshold bits
1
0
146715
145535
144918
144882
145144
139227
74769
75949
76566
76602
76340
82257
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

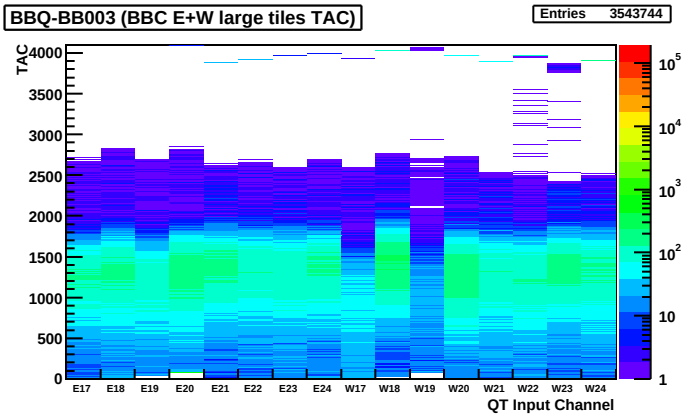

BBQ-BB001 (BBC east small tiles ADC)
Entries 3543744
ADC
QT Input Channel

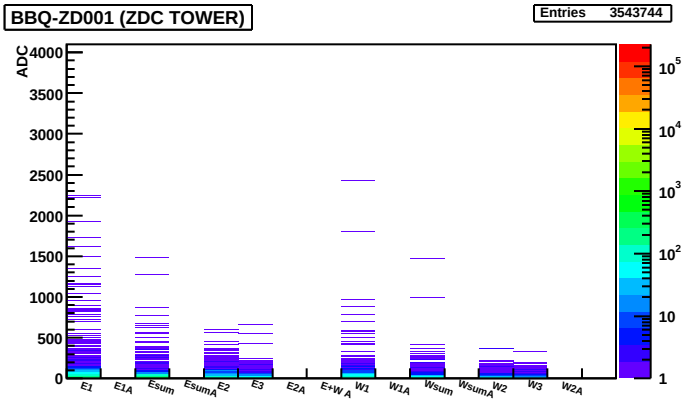

BBQ-BB001 (BBC east small tiles TAC)
Entries 3543744
TAC
QT Input Channel

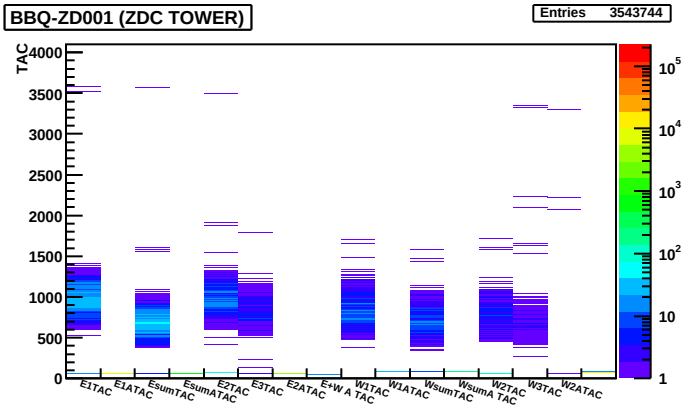

BBQ-BB002 (BBC west small tiles ADC)
Entries 3543744
ADC
QT Input Channel

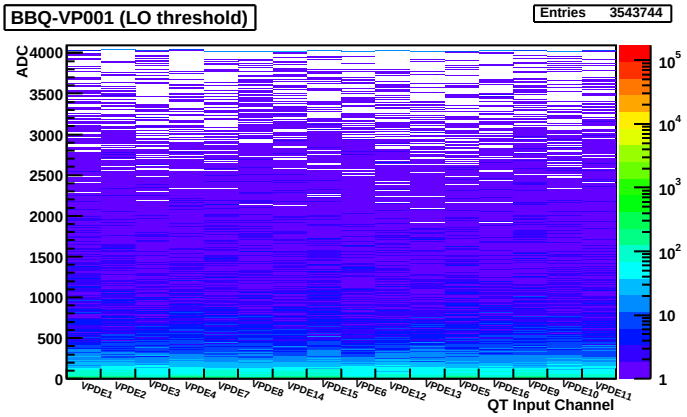

BBQ-BB002 (BBC west small tiles TAC)
Entries 3543744
TAC
QT Input Channel

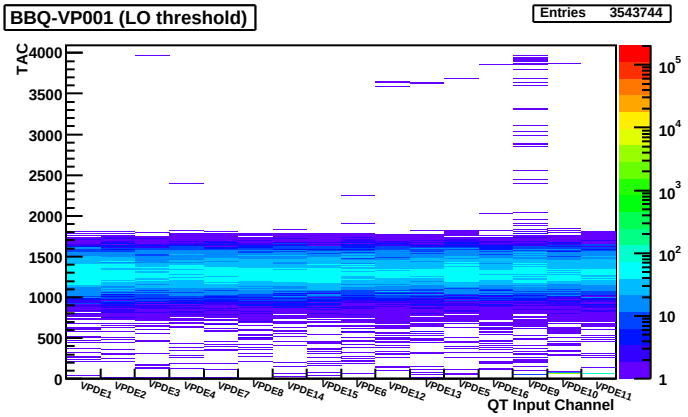

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3543744
ADC
QT Input Channel

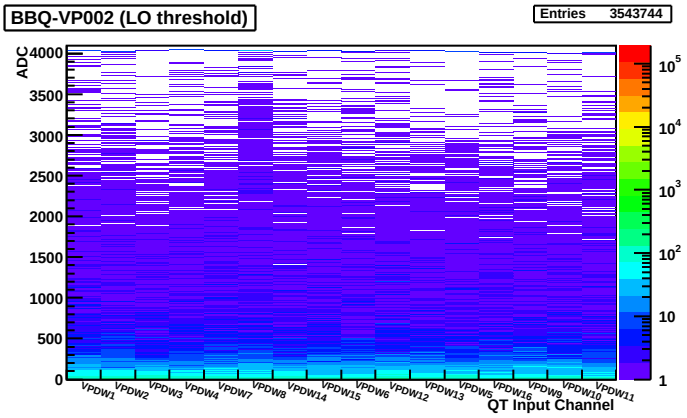

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3543744
TAC
QT Input Channel

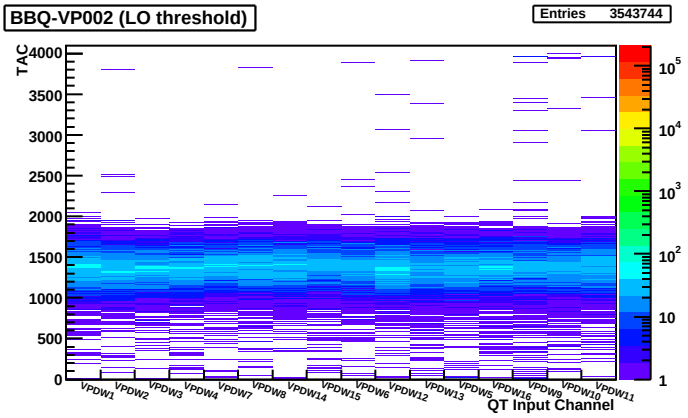

BBQ-ZD001 (ZDC TOWER)
Entries 3543744
ADC

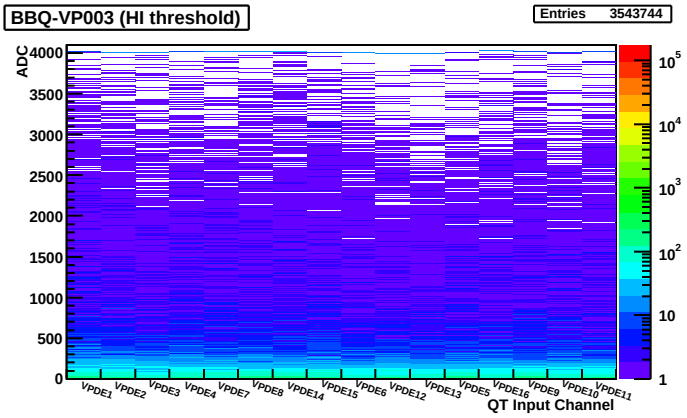

BBQ-ZD001 (ZDC TOWER)
Entries 3543744
TAC

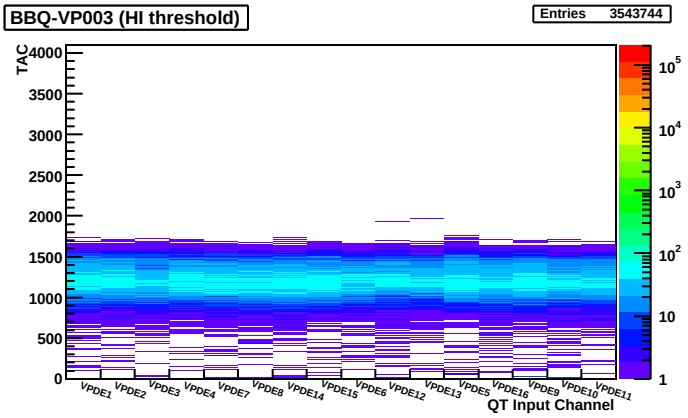

BBQ-VP001 (LO threshold)
Entries 3543744
ADC
QT Input Channel

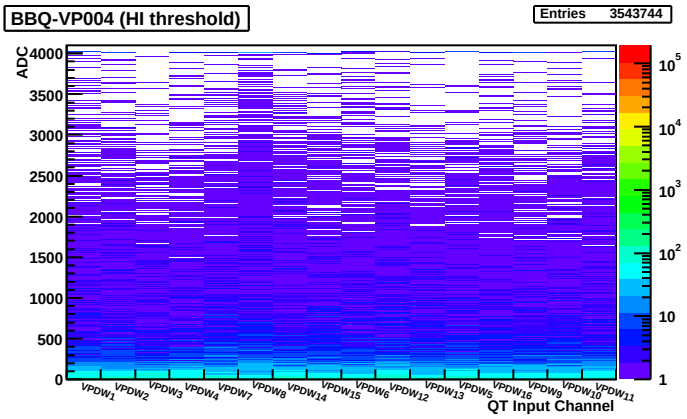

BBQ-VP001 (LO threshold)
Entries 3543744
TAC
QT Input Channel

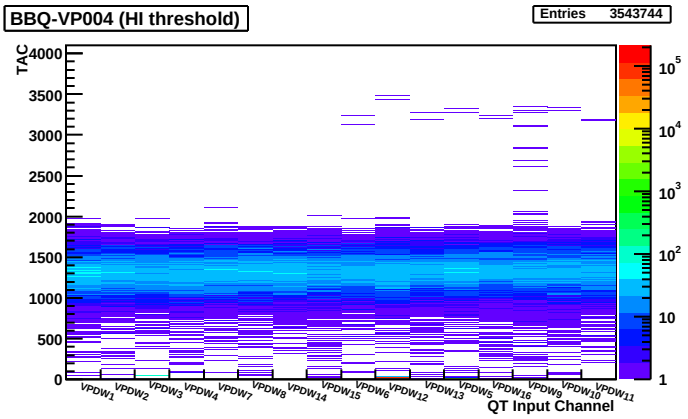

BBQ-VP002 (LO threshold)
Entries 3543744
ADC
QT Input Channel

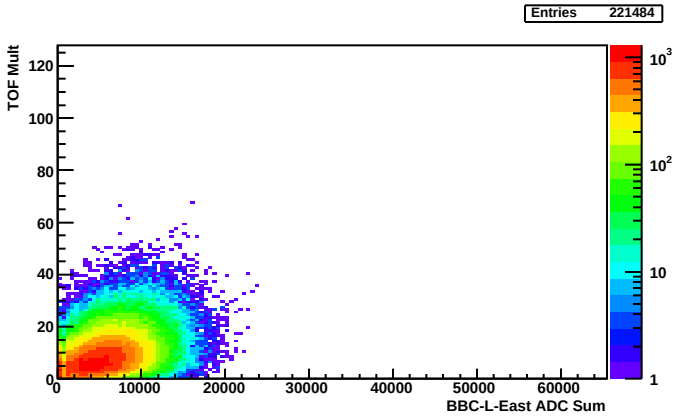

BBQ-VP002 (LO threshold)
Entries 3543744
TAC
QT Input Channel

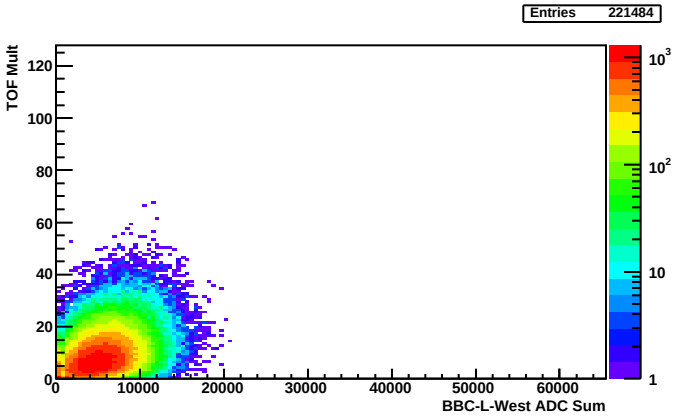

BBQ-VP003 (HI threshold)
Entries 3543744
ADC
QT Input Channel

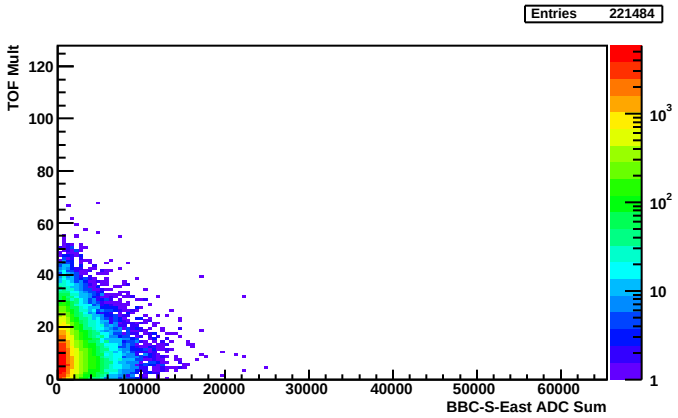

BBQ-VP003 (HI threshold)
Entries 3543744
TAC
QT Input Channel

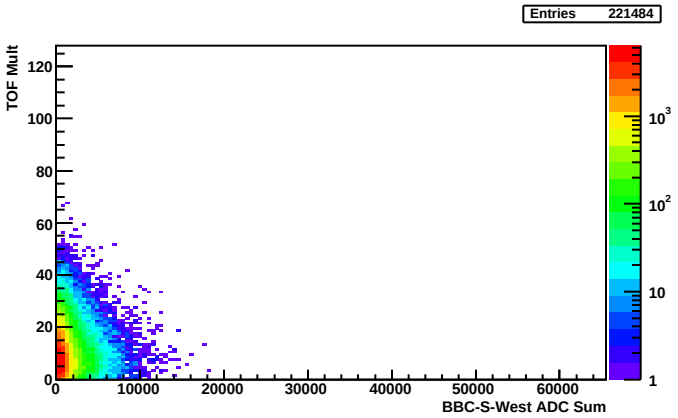

BBQ-VP004 (HI threshold)
Entries 3543744
ADC
QT Input Channel

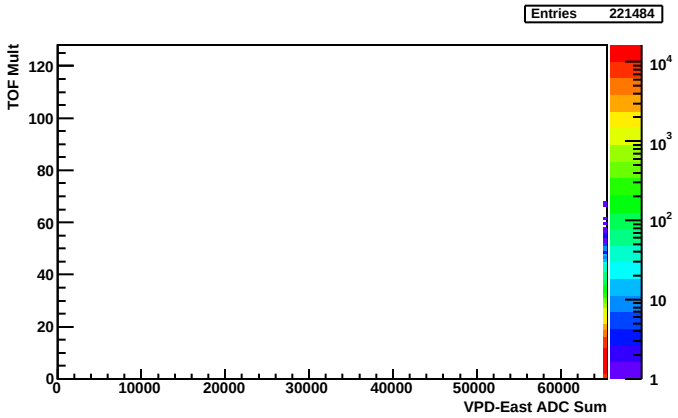

BBQ-VP004 (HI threshold)
Entries 3543744
TAC
QT Input Channel

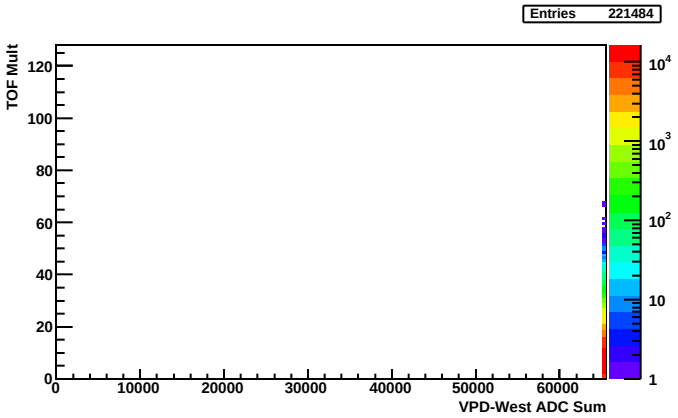

Entries 221484
TOF Mult
BBC-L-East ADC Sum

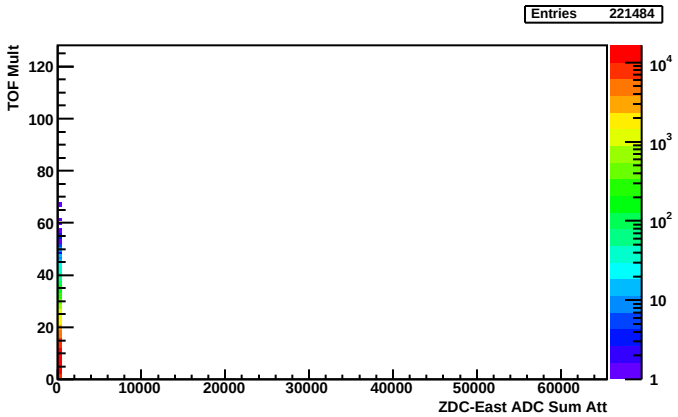

Entries 221484
TOF Mult
BBC-L-West ADC Sum

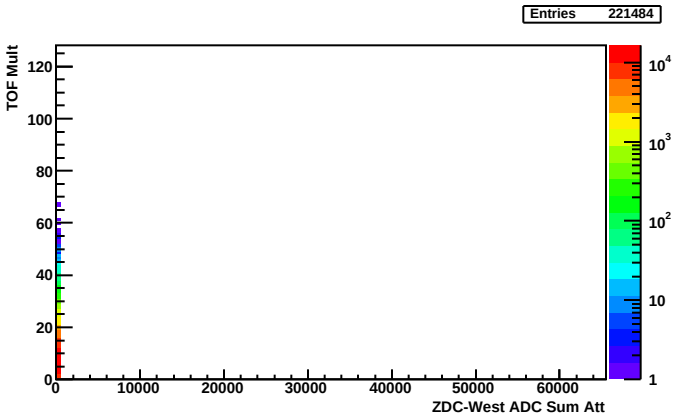

Entries 221484
TOF Mult
BBC-S-East ADC Sum

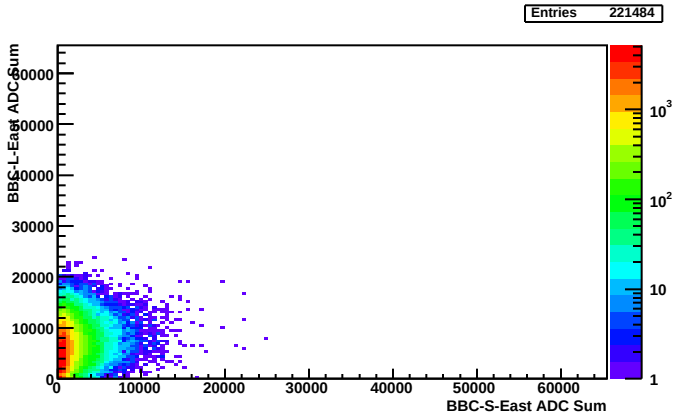

Entries 221484
TOF Mult
BBC-S-West ADC Sum

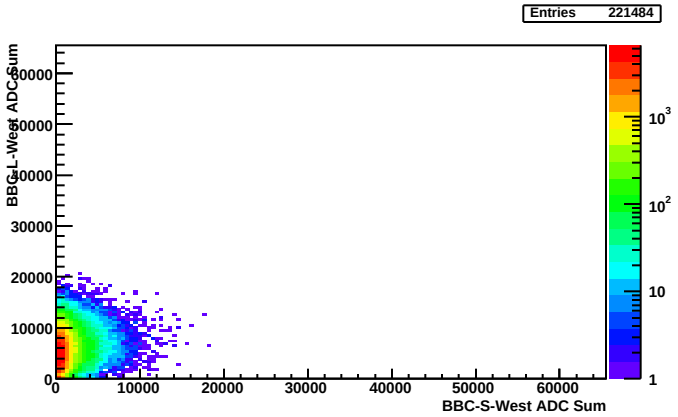

Entries 221484
TOF Mult
VPD-East ADC Sum

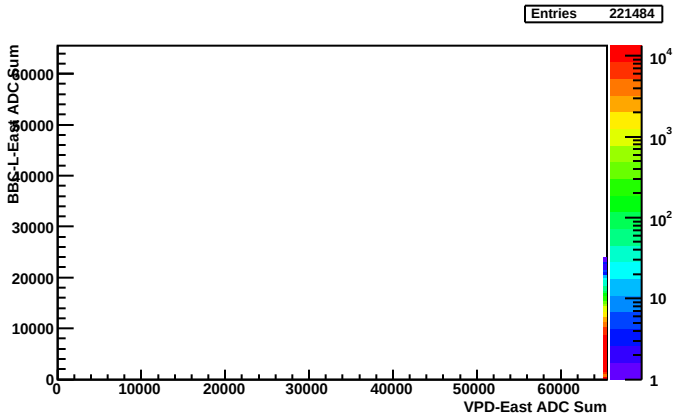

Entries 221484
TOF Mult
VPD-West ADC Sum

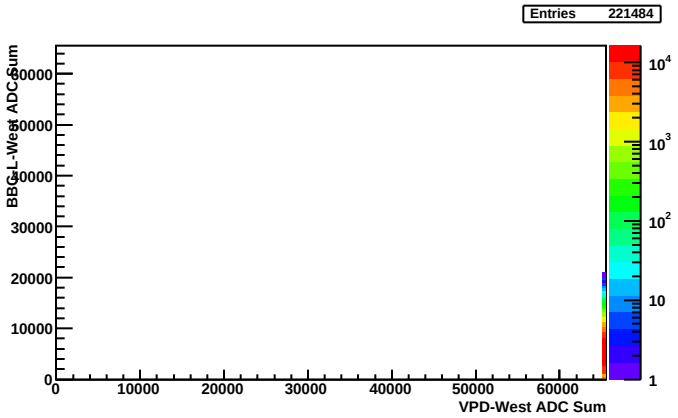

Entries 221484
TOF Mult
ZDC-East ADC Sum Att

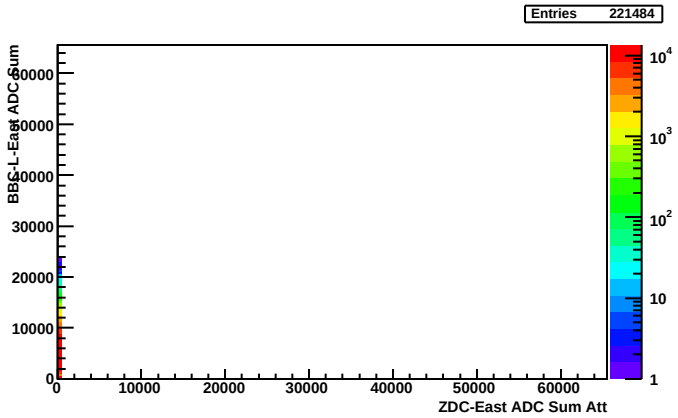

Entries 221484
TOF Mult
ZDC-West ADC Sum Att

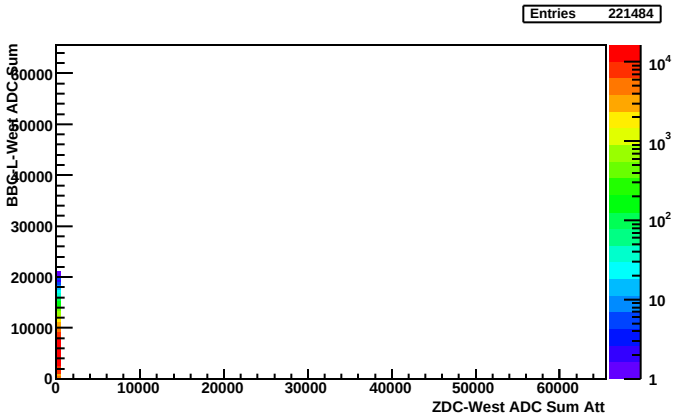

Entries 221484
BBC-L-East ADC Sum
BBC-S-East ADC Sum

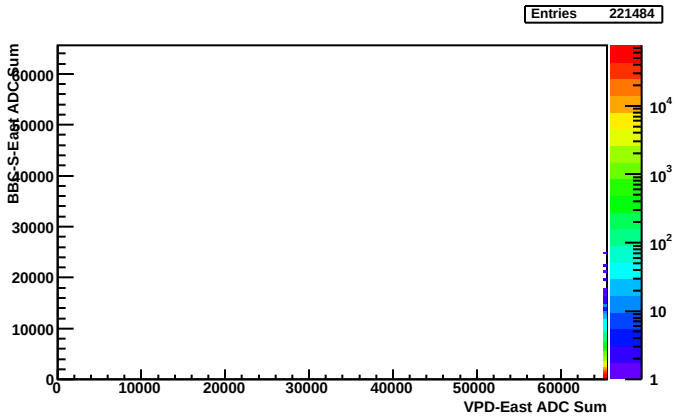

Entries 221484
BBC-L-West ADC Sum
BBC-S-West ADC Sum

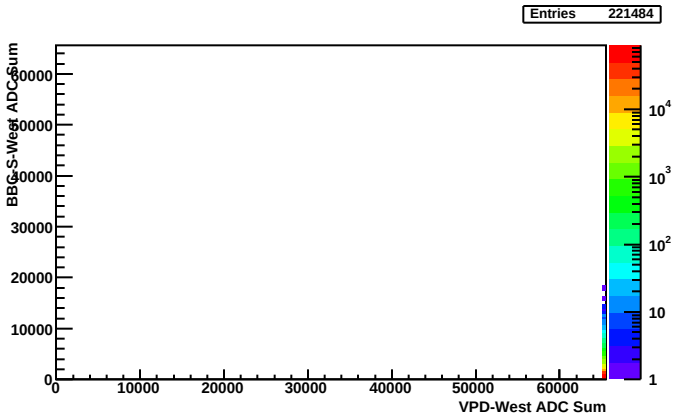

Entries 221484
BBC-L-East ADC Sum
VPD-East ADC Sum

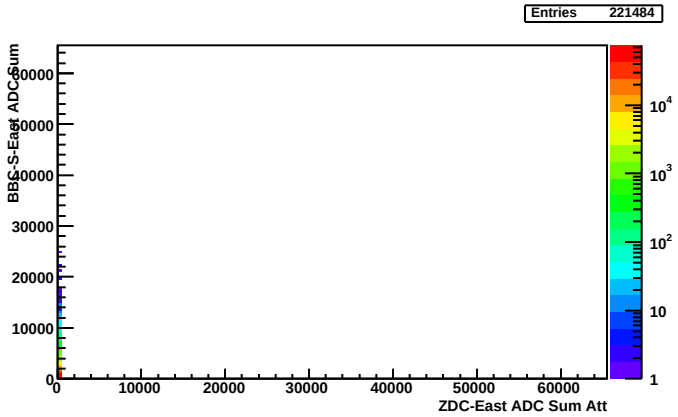

Entries 221484
BBC-L-West ADC Sum
VPD-West ADC Sum

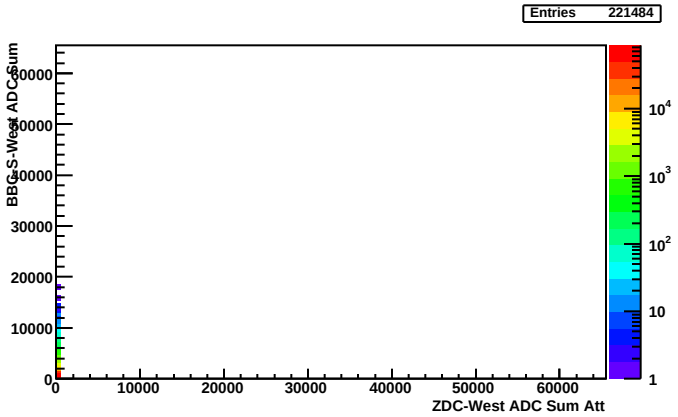

Entries 221484
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

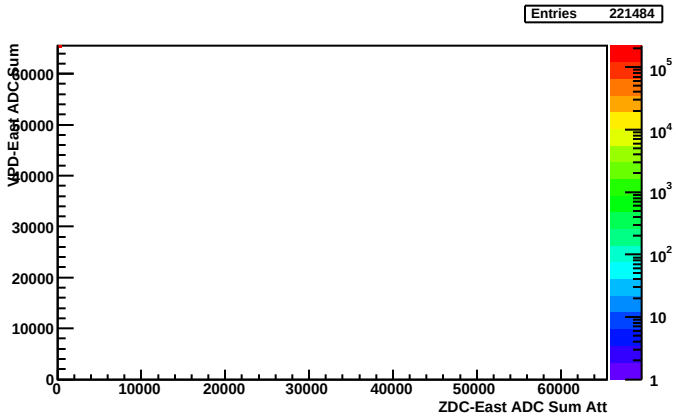

Entries 221484
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

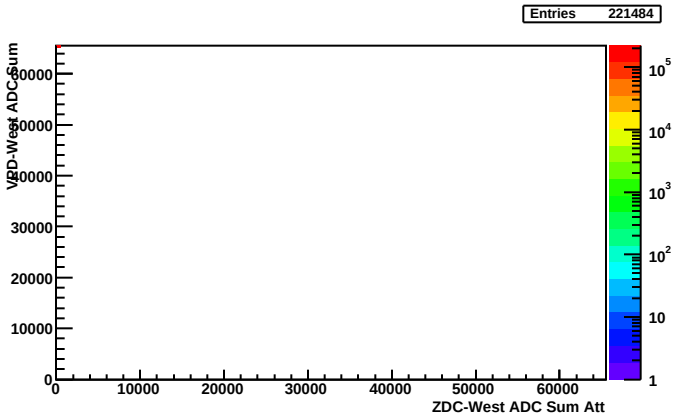

Entries 221484
BBC-S-East ADC Sum
VPD-East ADC Sum

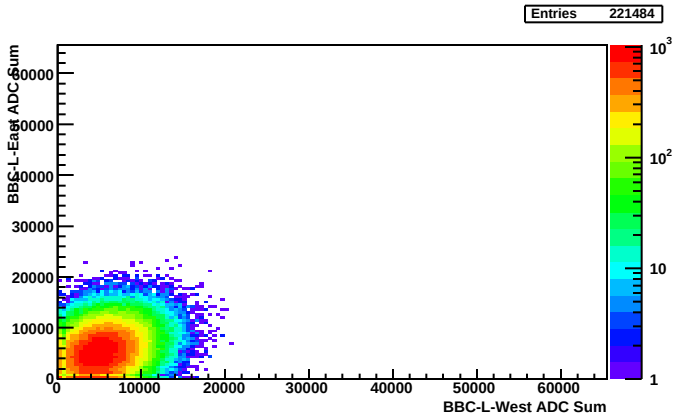

Entries 221484
BBC-S-West ADC Sum
VPD-West ADC Sum

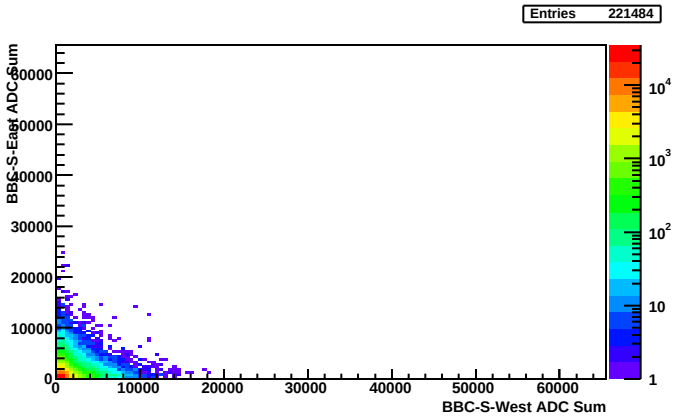

Entries 221484
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

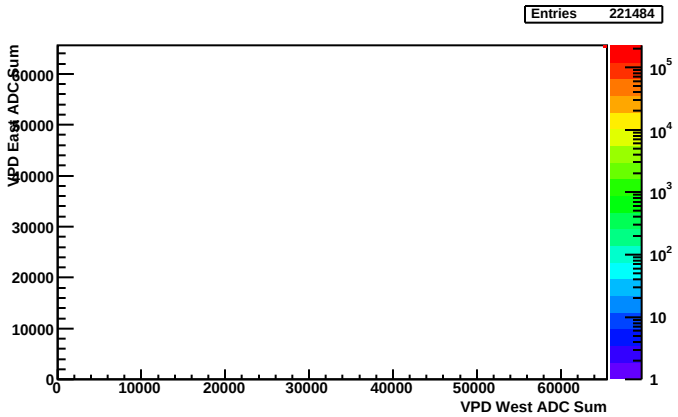

Entries 221484
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

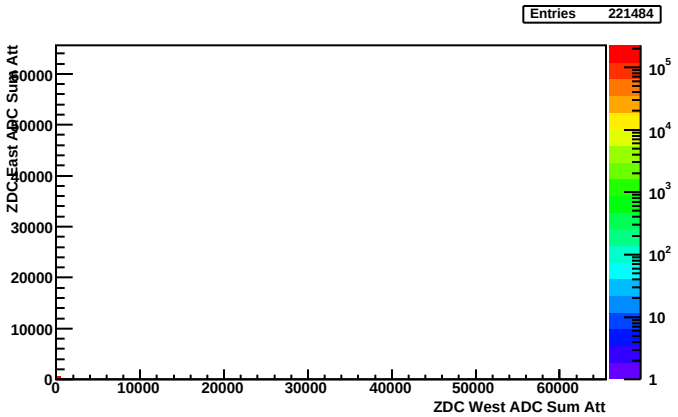

Entries 221484
VPD-East ADC Sum
ZDC-East ADC Sum Att

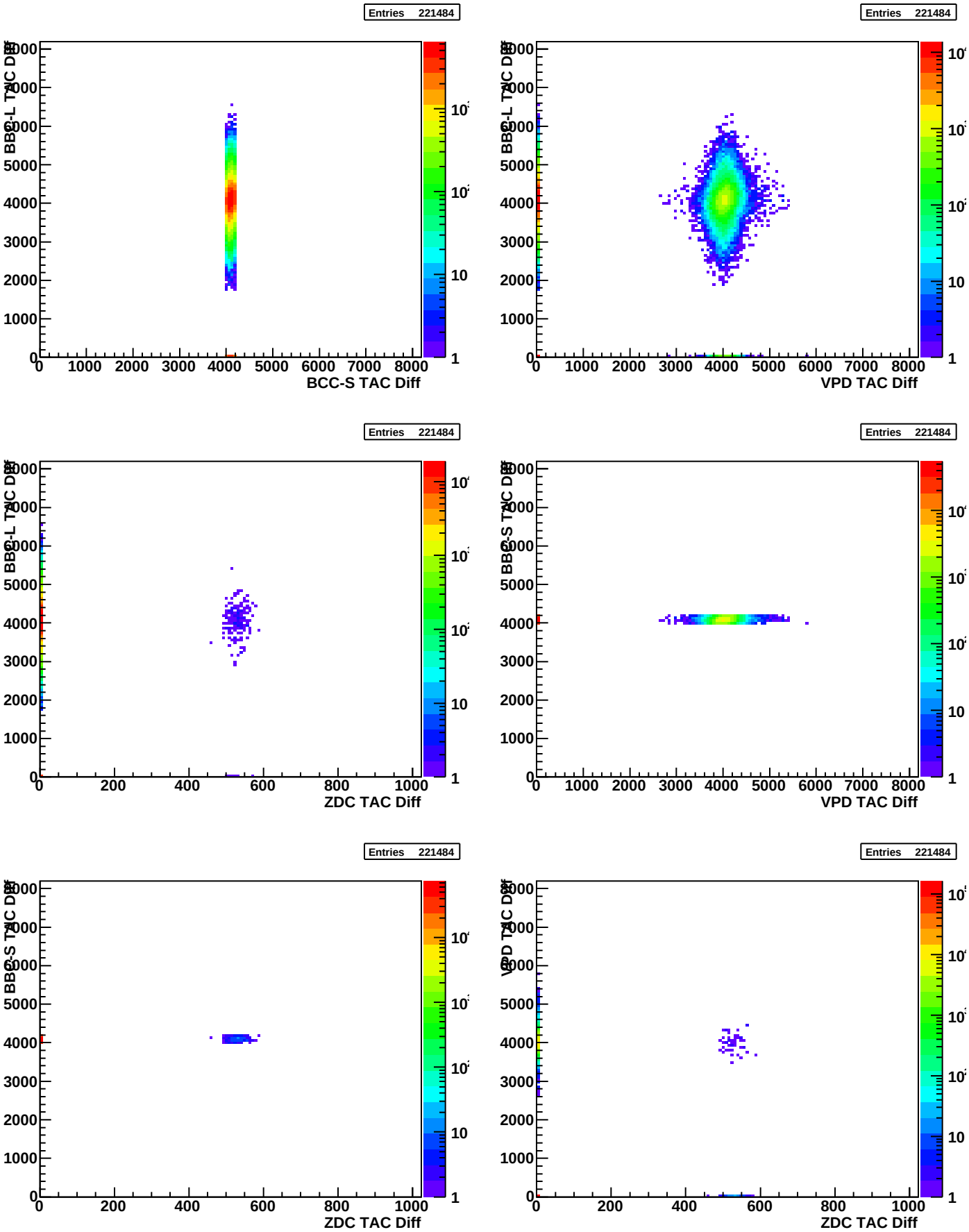

Entries 221484
VPD-West ADC Sum
ZDC-West ADC Sum Att

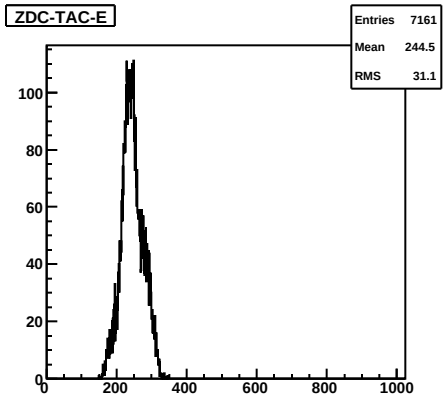

Entries 221484
BBC-L-East ADC Sum
BBC-L-West ADC Sum

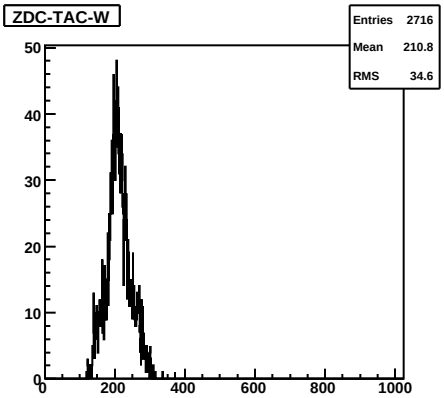

Entries 221484
BBC-S-East ADC Sum
BBC-S-West ADC Sum

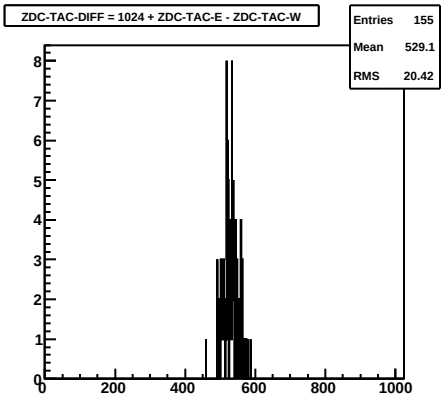

Entries 221484
VPD East ADC Sum
VPD West ADC Sum

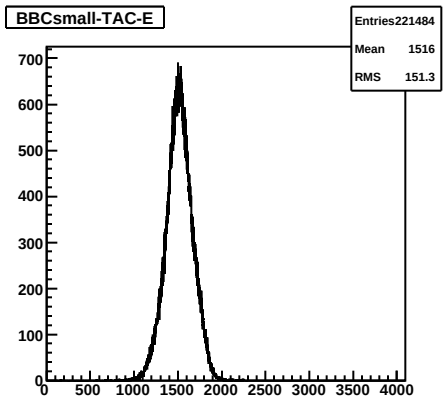

Entries 221484
ZDC East ADC Sum Att
ZDC West ADC Sum Att

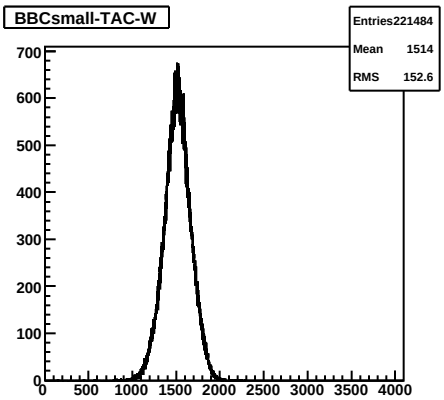

Entries 221484
BBC-L TAC Diff
BCC-S TAC Diff
Entries 221484
BBC-L TAC Diff
VPD TAC Diff
Entries 221484
BBC-L TAC Diff
ZDC TAC Diff
Entries 221484
BBC-S TAC Diff
VPD TAC Diff
Entries 221484
BBC-S TAC Diff
ZDC TAC Diff
Entries 221484
VPD TAC Diff
ZDC TAC Diff

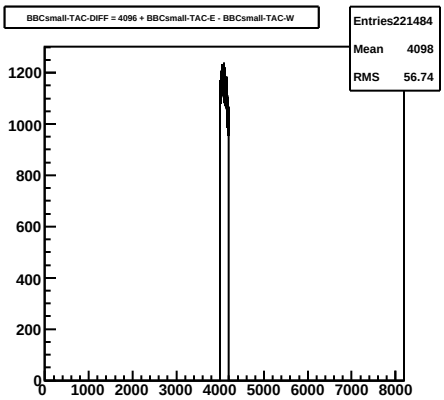

ZDC-TAC-E
Entries 7161
Mean 244.5
RMS 31.1

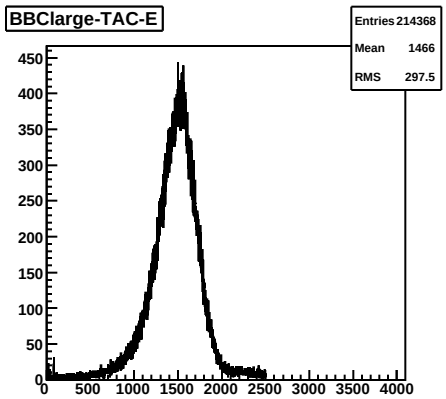

ZDC-TAC-W
Entries 2716
Mean 210.8
RMS 34.6

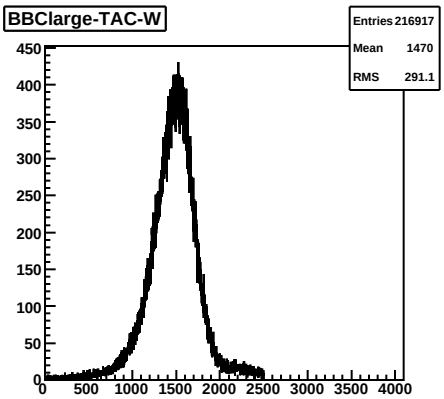

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 155
Mean 529.1
RMS 20.42

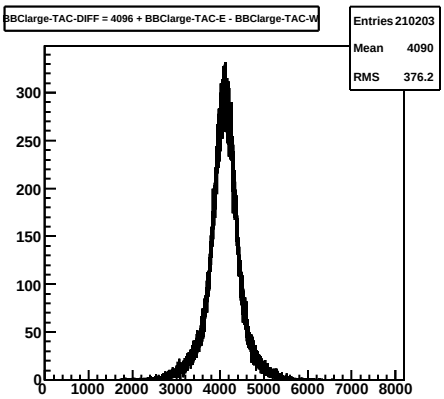

BBCsmall-TAC-E
Entries 221484
Mean 1516
RMS 151.3

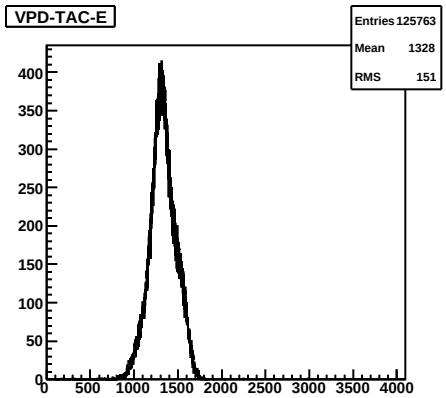

BBCsmall-TAC-W
Entries 221484
Mean 1514
RMS 152.6

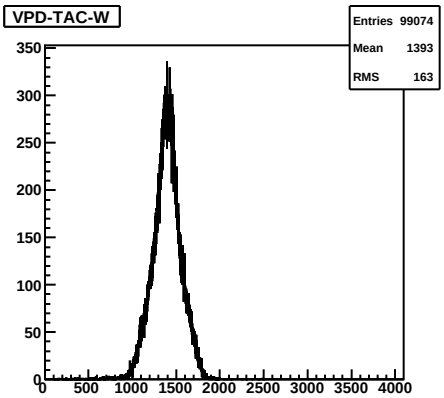

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 221484
Mean 4098
RMS 56.74

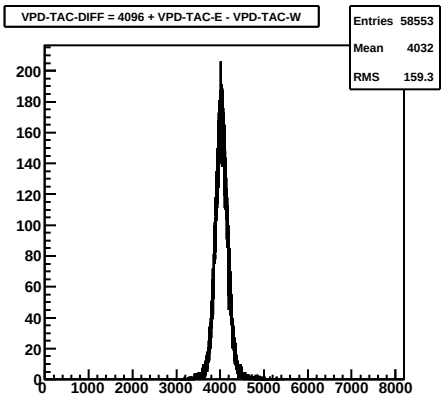

BBClarge-TAC-E
Entries 214368
Mean 1466
RMS 297.5

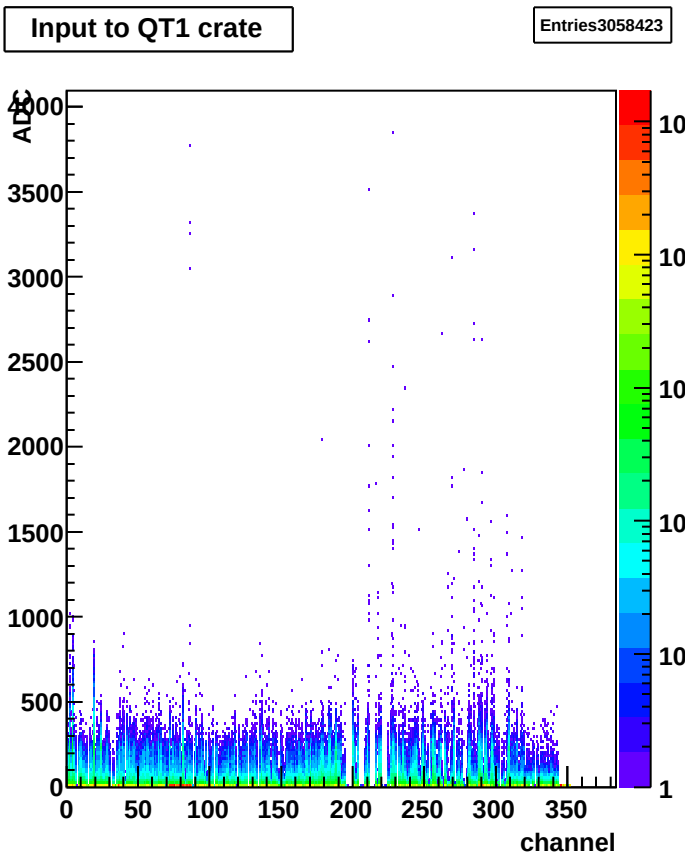

BBClarge-TAC-W
Entries 216917
Mean 1470
RMS 291.1

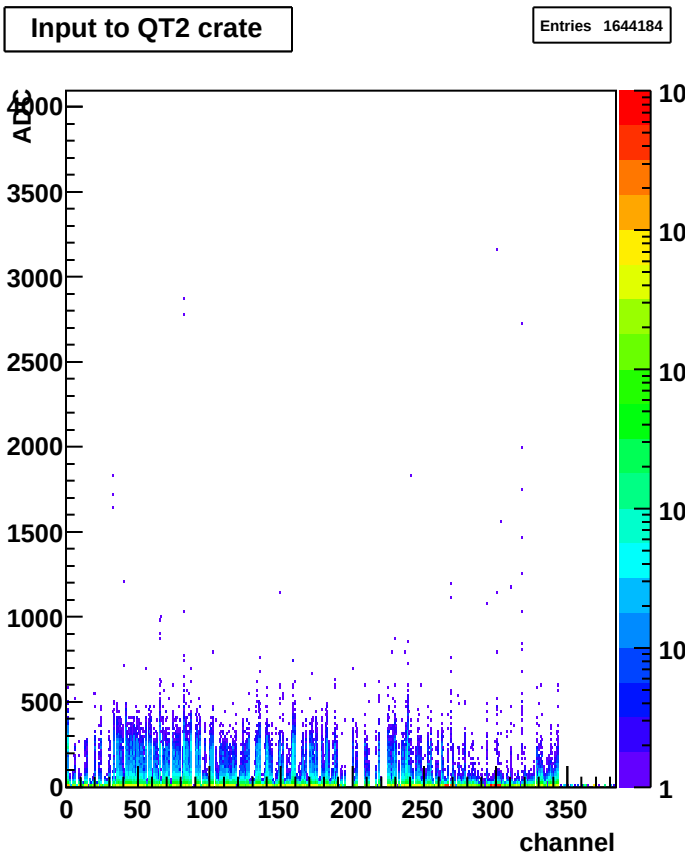

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 210203
Mean 4090
RMS 376.2

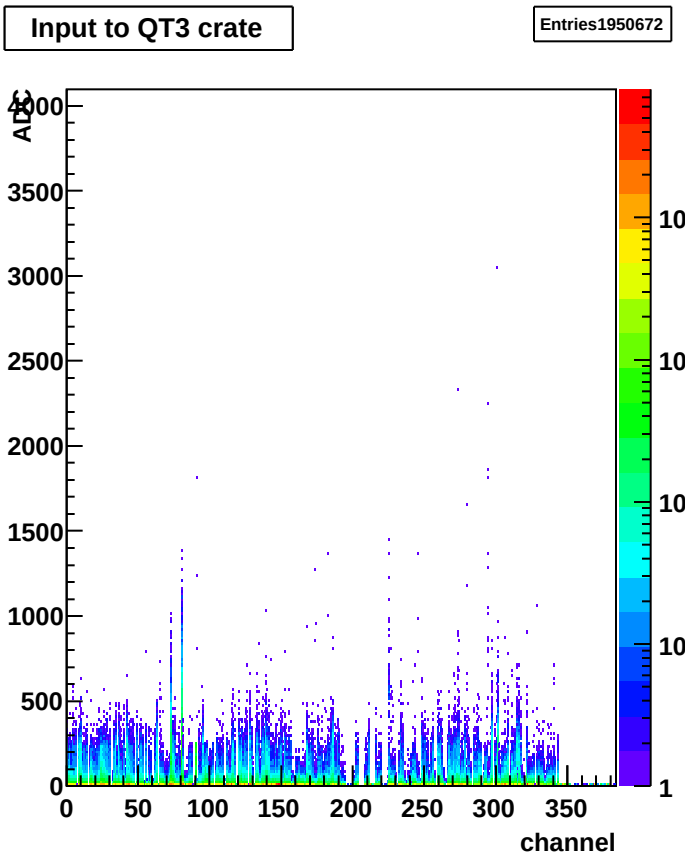

VPD-TAC-E
Entries 125763
Mean 1328
RMS 151

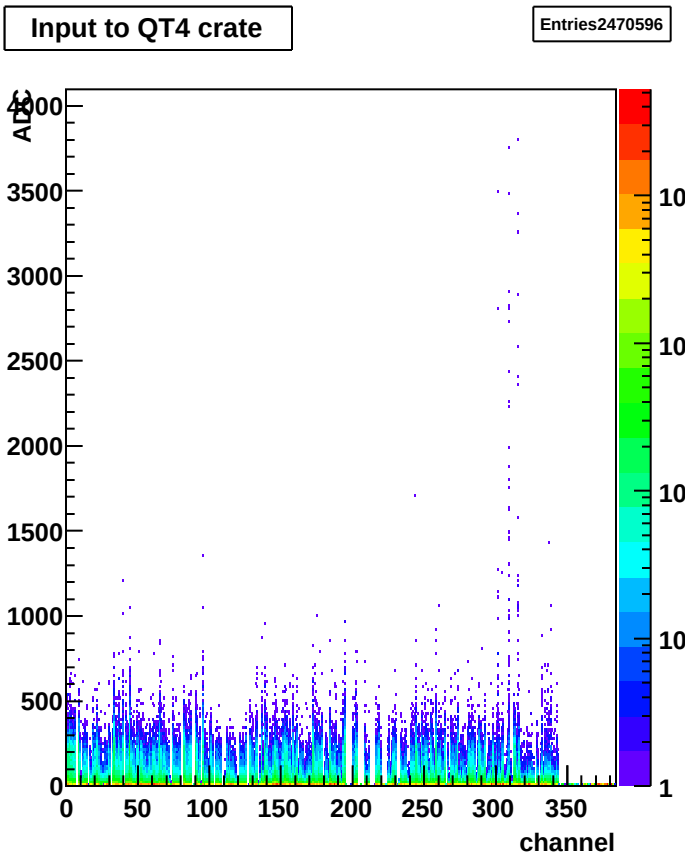

VPD-TAC-W
Entries 99074
Mean 1393
RMS 163

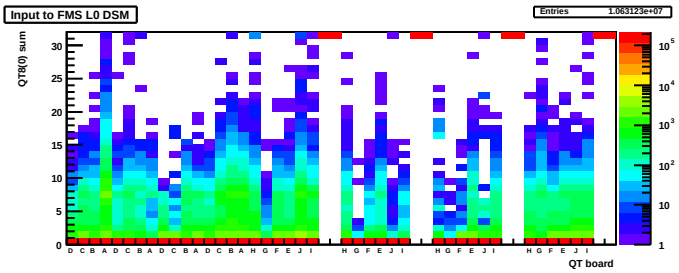

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 58553
Mean 4032
RMS 159.3

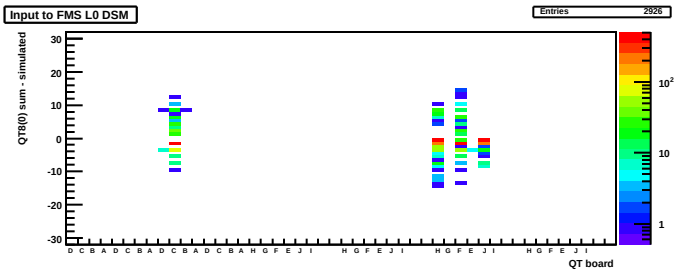

Input to QT1 crate
Entries3058423
ADC
channel

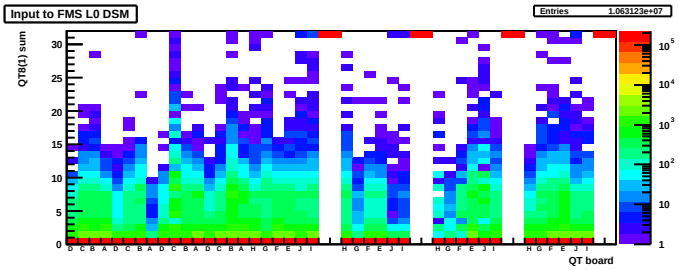

Input to QT2 crate
Entries 1644184
ADC
channel

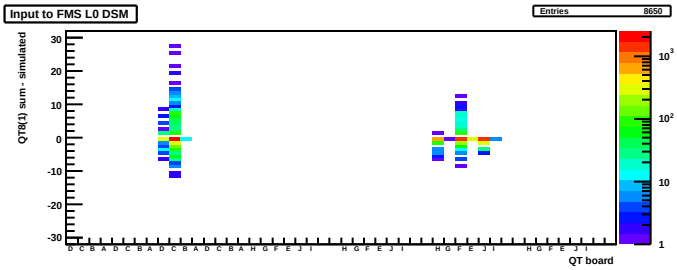

Input to QT3 crate
Entries1950672
ADC
channel

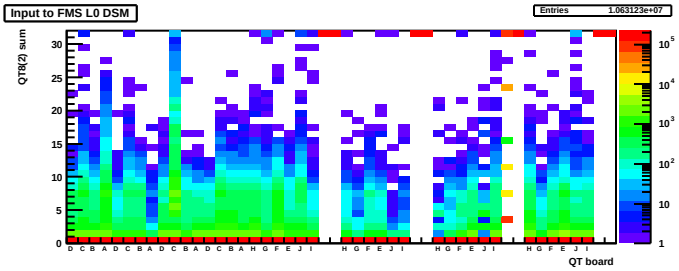

Input to QT4 crate
Entries2470596
ADC
channel

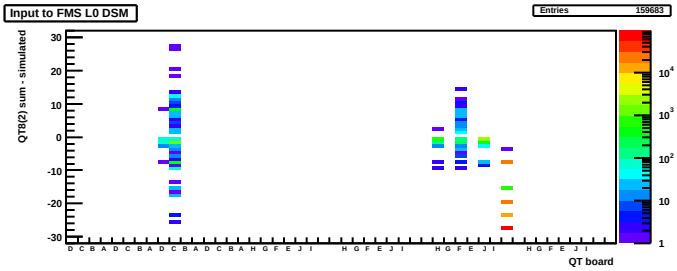

Input to FMS L0 DSM
Entries 1.063123e+07
QT8(0) sum
QT board

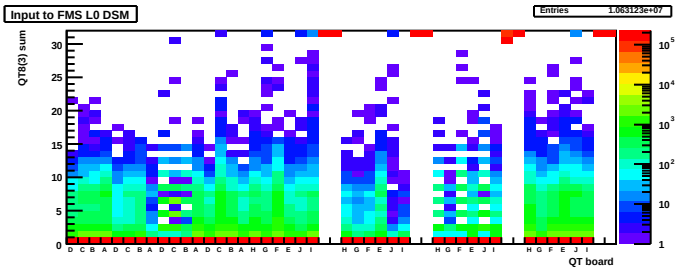

Input to FMS L0 DSM
Entries 2926
QT8(0) sum - simulated
QT board

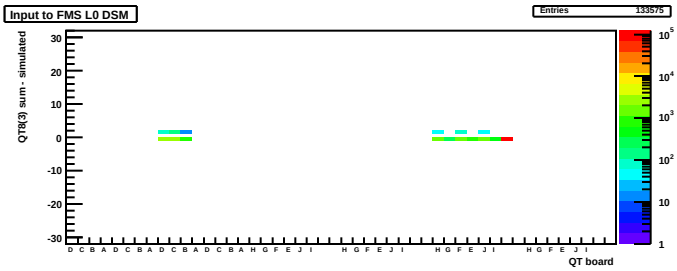

Input to FMS L0 DSM
Entries 1.063123e+07
QT8(1) sum
QT board

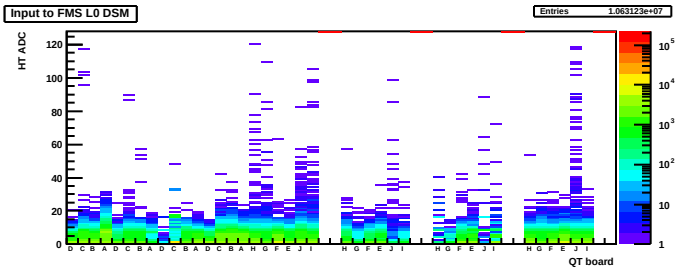

Input to FMS L0 DSM
Entries 8650
QT8(1) sum - simulated
QT board

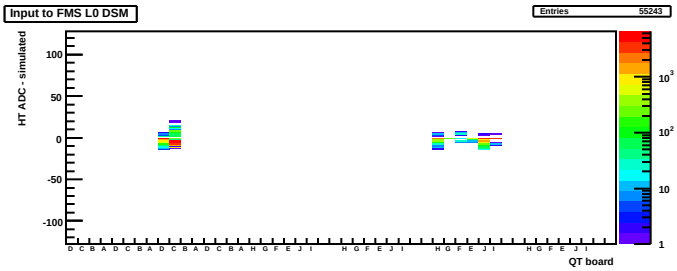

Input to FMS L0 DSM
Entries 1.063123e+07
QT8(2) sum
QT board

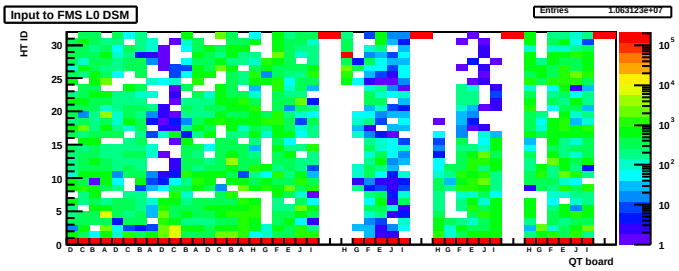

Input to FMS L0 DSM
Entries 159683
QT8(2) sum - simulated
QT board

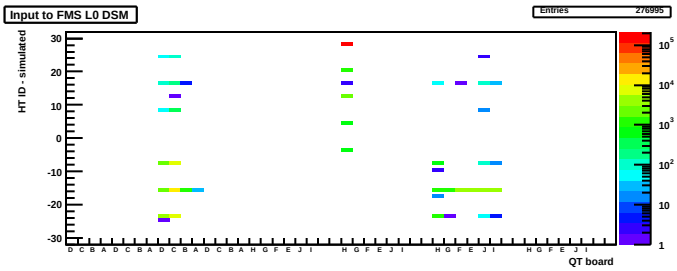

Input to FMS L0 DSM
Entries 1.063123e+07
QT8(3) sum
QT board

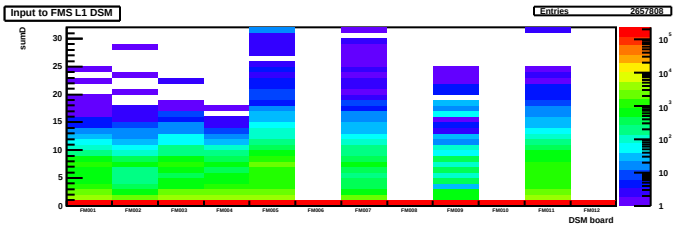

Input to FMS L0 DSM
Entries 133575
QT8(3) sum - simulated
QT board

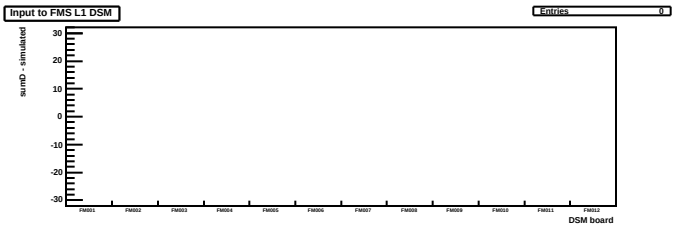

Input to FMS L0 DSM
Entries 1.063123e+07
HT ADC
QT board

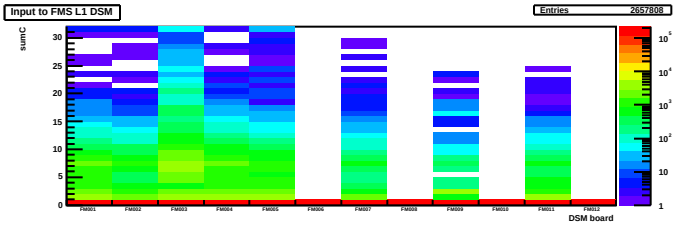

Input to FMS L0 DSM
Entries 55243
HT ADC - simulated
QT board

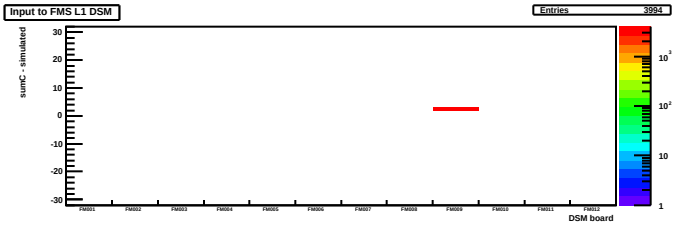

Input to FMS L0 DSM
Entries 1.063123e+07
HT ID
QT board

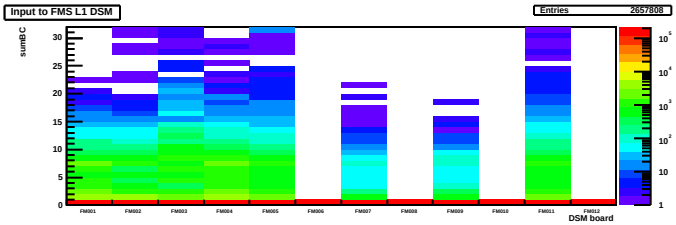

Input to FMS L0 DSM
Entries 276995
HT ID - simulated
QT board

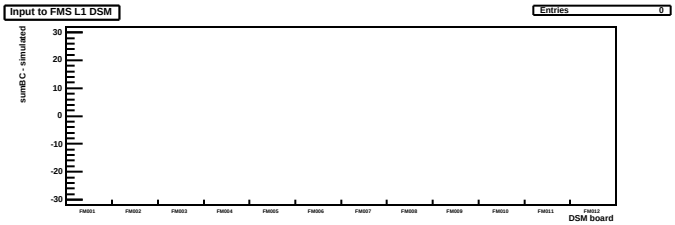

Input to FMS L1 DSM
Entries 2657808
sumD
DSM board

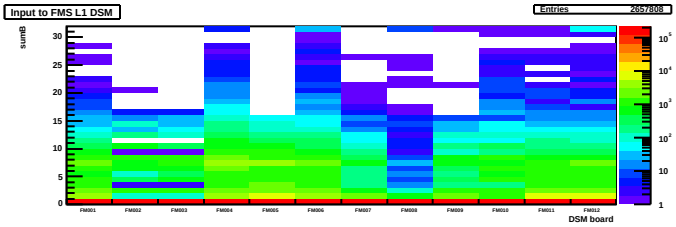

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

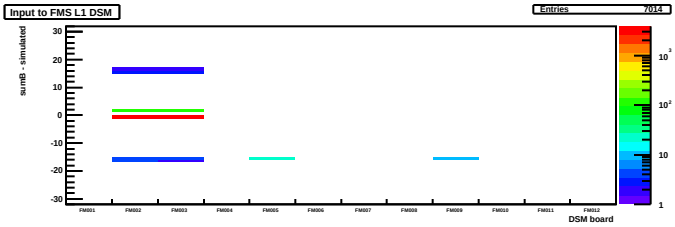

Input to FMS L1 DSM
Entries 2657808
sumC
DSM board

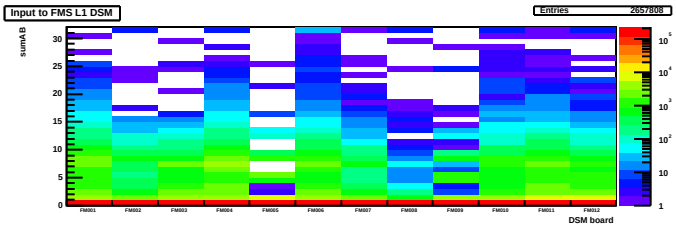

Input to FMS L1 DSM
Entries 3994
sumC - simulated
DSM board

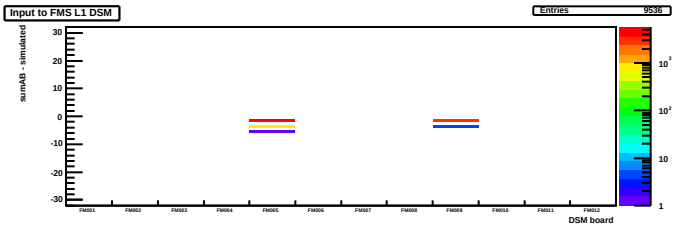

Input to FMS L1 DSM
Entries 2657808
sumBC
DSM board

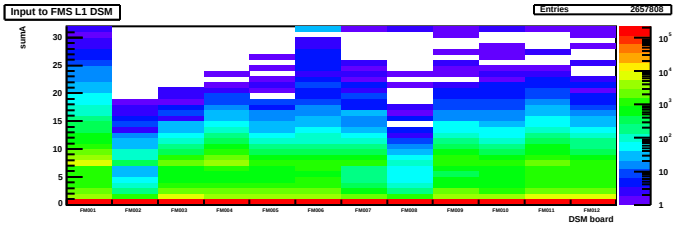

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

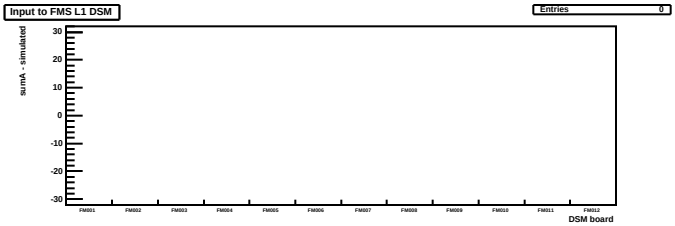

Input to FMS L1 DSM
Entries 2657808
sumB
DSM board

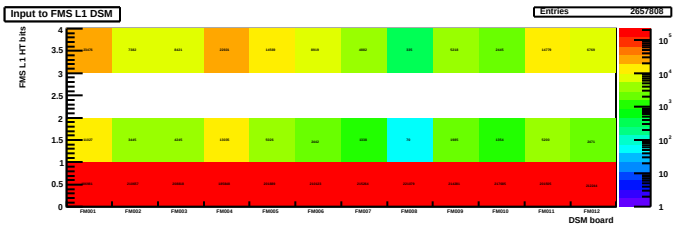

Input to FMS L1 DSM
Entries 7014
sumB - simulated
DSM board

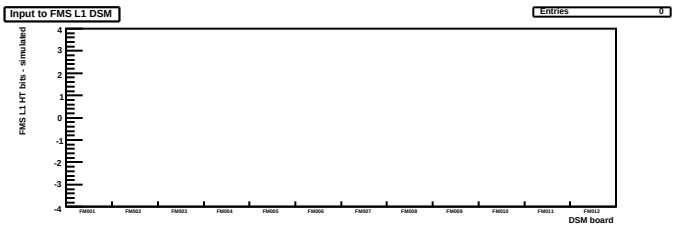

Input to FMS L1 DSM
Entries 2657808
sumAB
DSM board

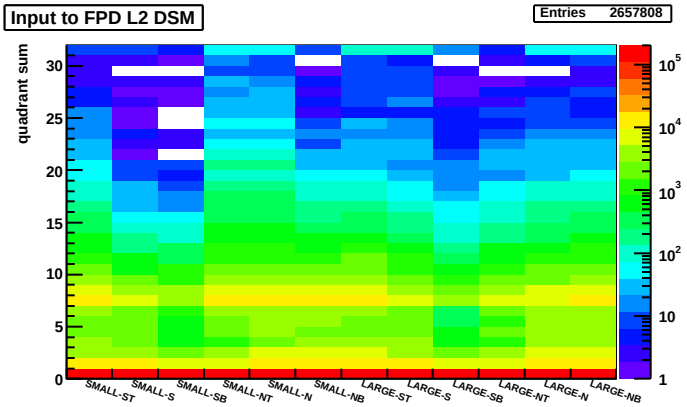

Input to FMS L1 DSM
Entries 9536
sumAB - simulated
DSM board

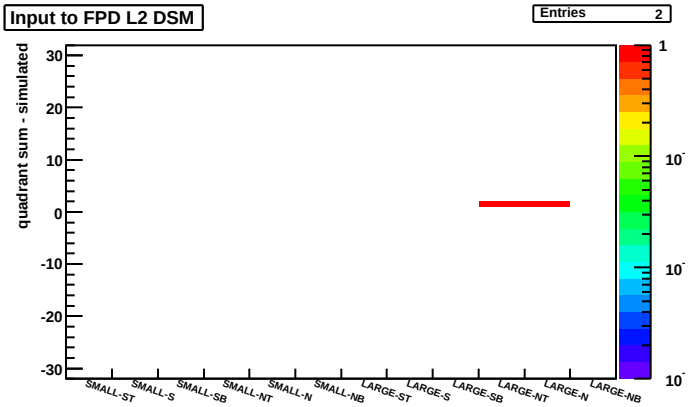

Input to FMS L1 DSM
Entries 2657808
sumA
DSM board

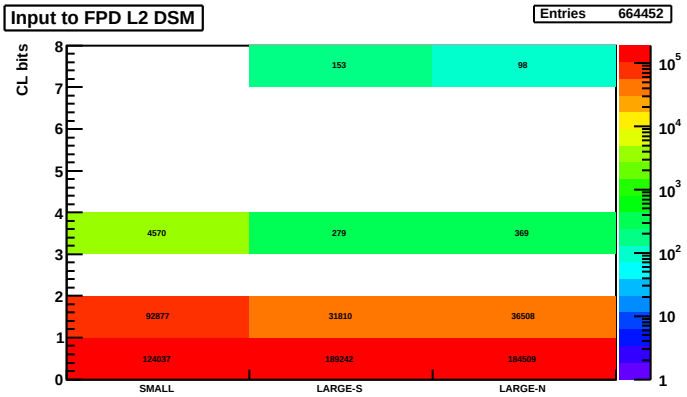

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

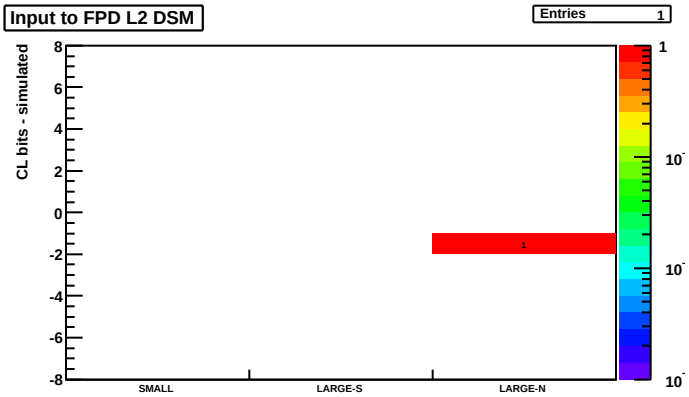

Input to FMS L1 DSM
Entries 2657808
FMS L1 HT bits
DSM board

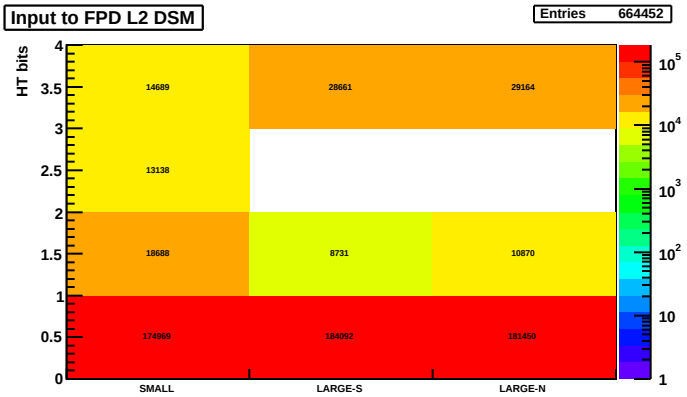

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

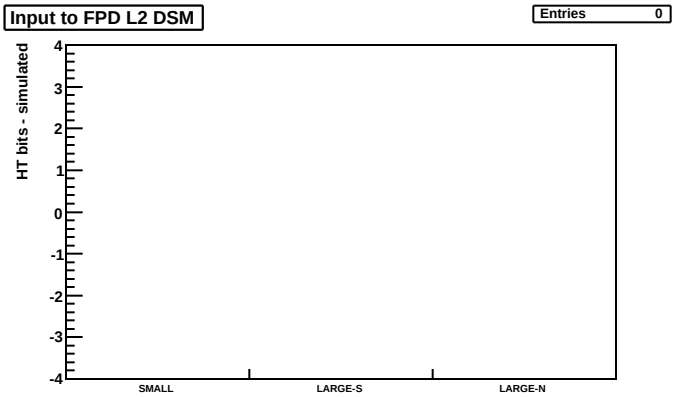

Input to FPD L2 DSM
Entries 2657808
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

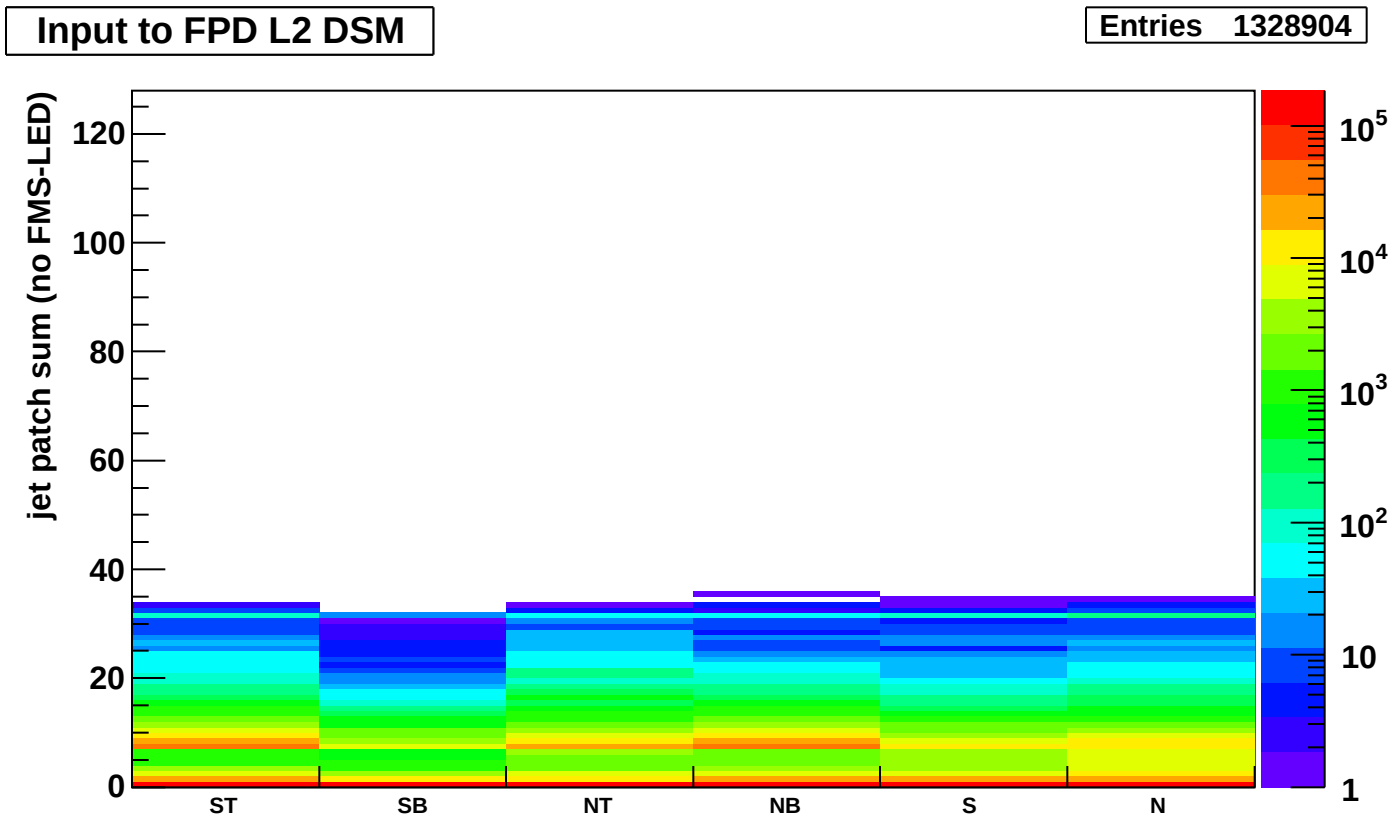

Input to FPD L2 DSM
Entries 2
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

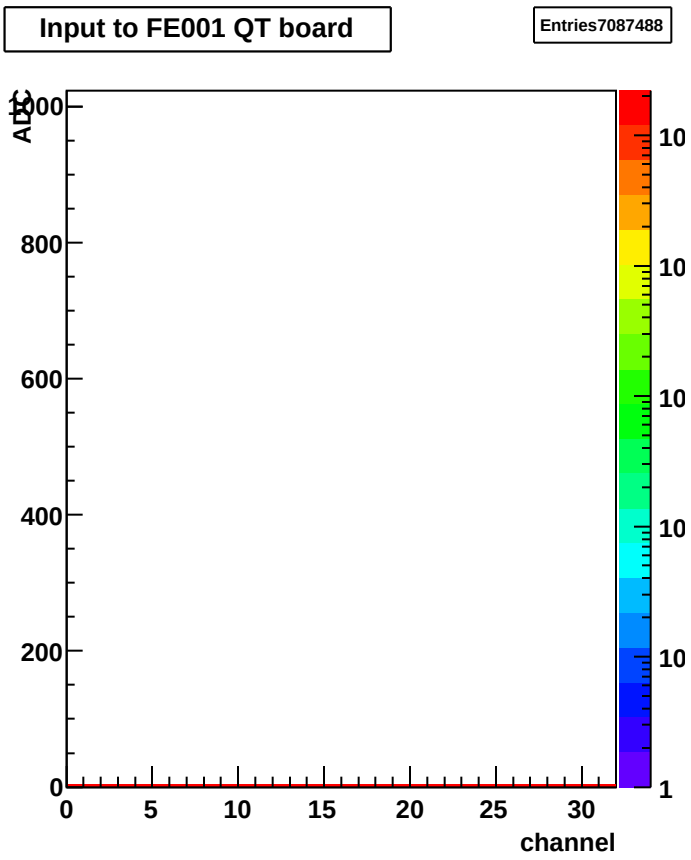

Input to FPD L2 DSM
Entries 664452
CL bits
153
98
4570
279
369
92877
31810
36508
124037
189242
184509
SMALL
LARGE-S
LARGE-N

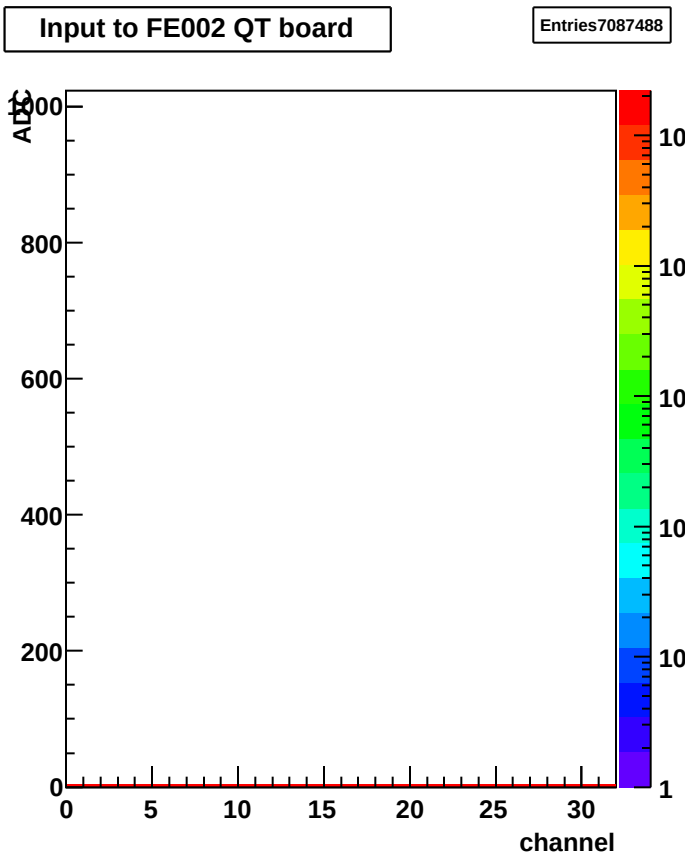

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

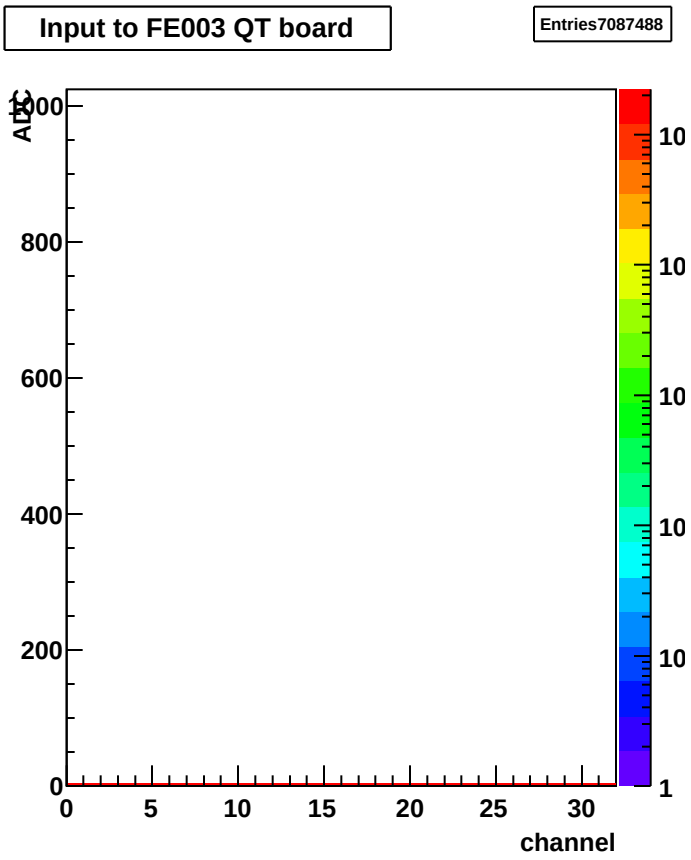

Input to FPD L2 DSM
Entries 664452
HT bits
14689
28661
29164
13138
18688
8731
10870
174969
184092
181450
SMALL
LARGE-S
LARGE-N

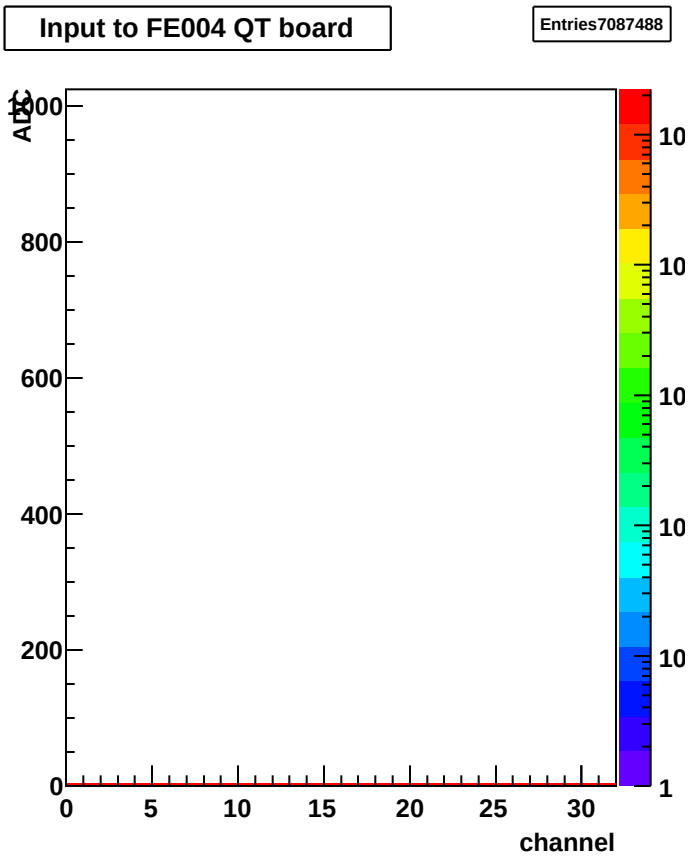

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

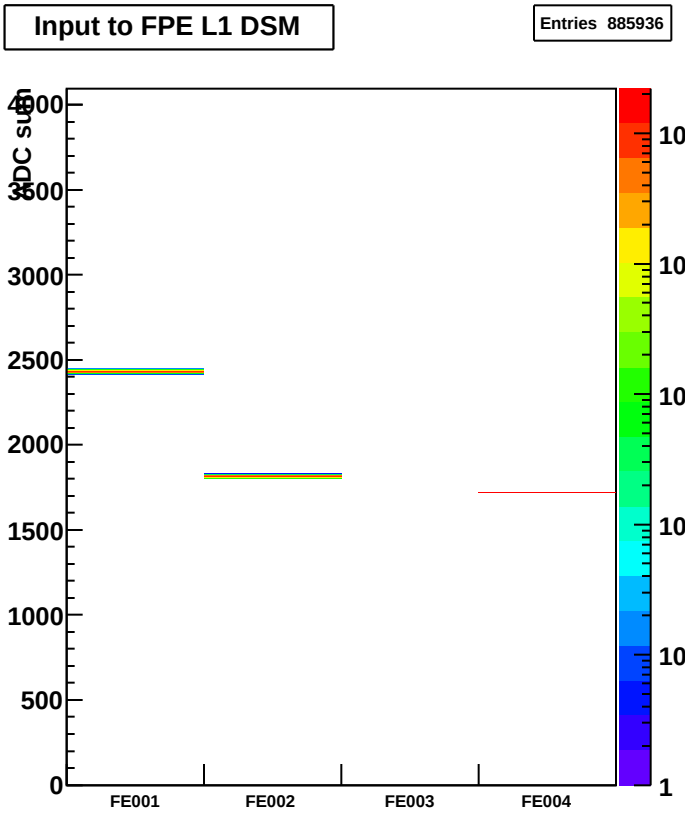

Input to FPD L2 DSM
Entries 1328904
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

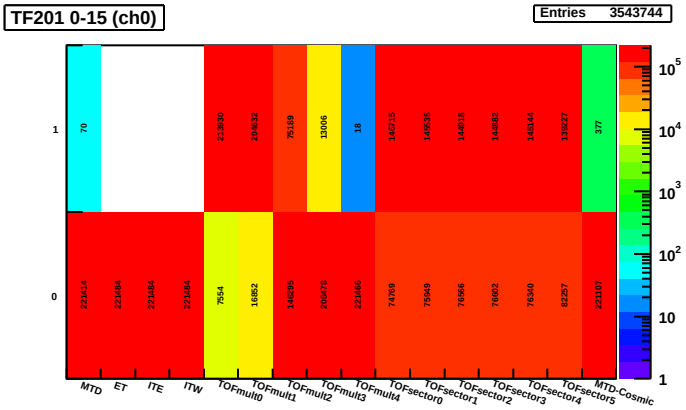

Input to FE001 QT board
Entries7087488
ADC
channel

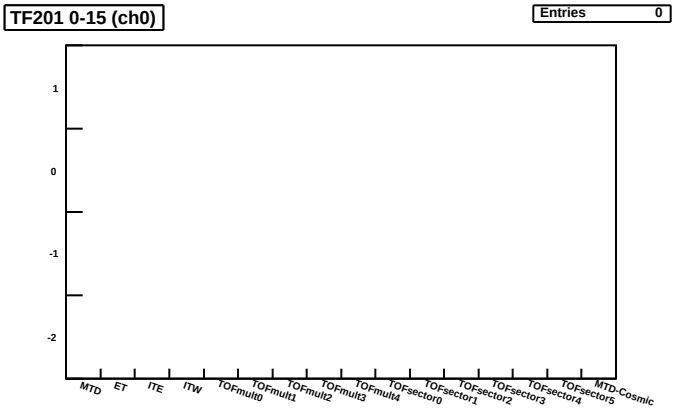

Input to FE002 QT board
Entries7087488
ADC
channel

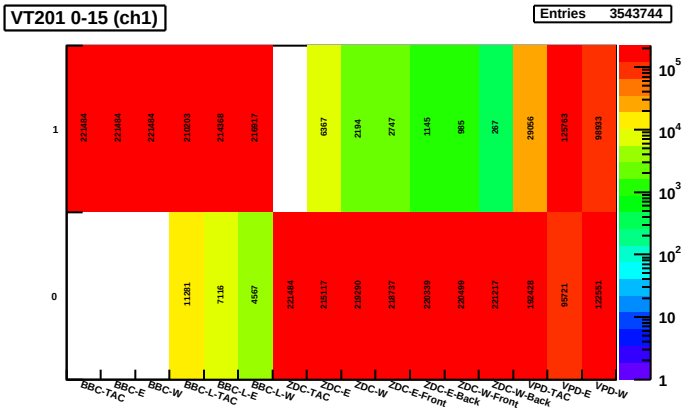

Input to FE003 QT board
Entries7087488
ADC
channel

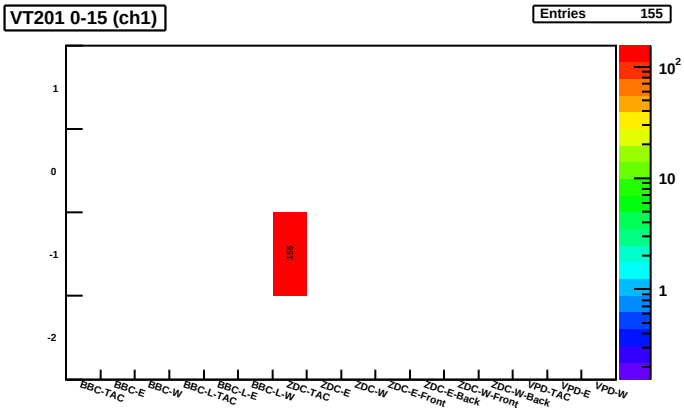

Input to FE004 QT board
Entries7087488
ADC
channel

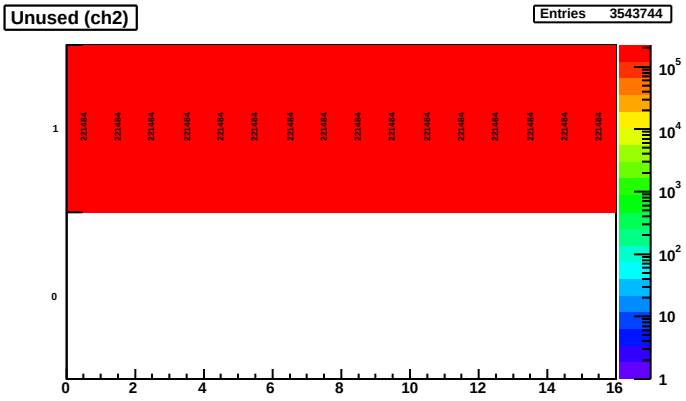

Input to FPE L1 DSM
Entries 885936
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

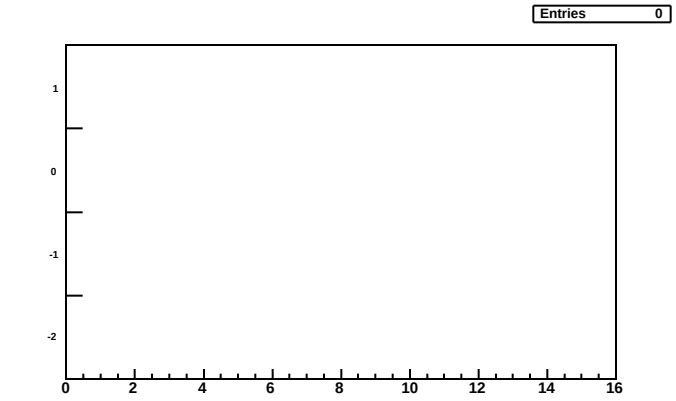

TF201 0-15 (ch0)
Entries 3543744
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

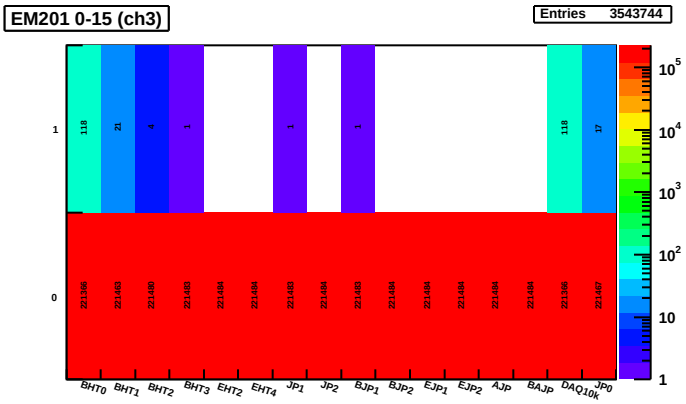

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

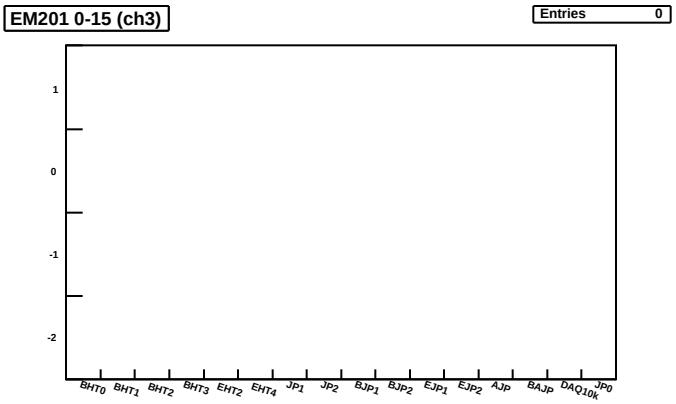

VT201 0-15 (ch1)
Entries 3543744
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 155
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

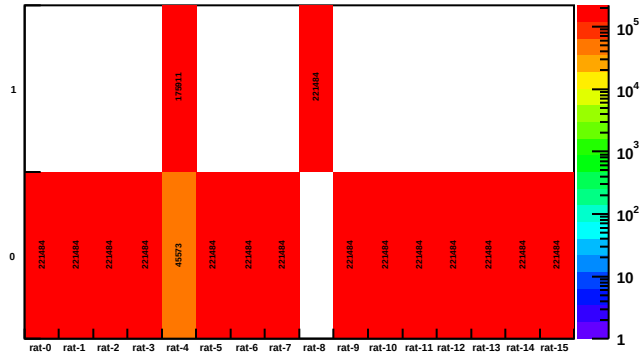

Unused (ch2)
Entries 3543744

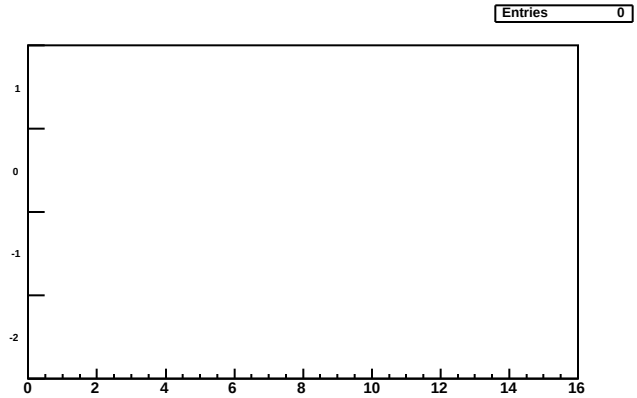

Entries 0


EM201 0-15 (ch3)
Entries 3543744
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

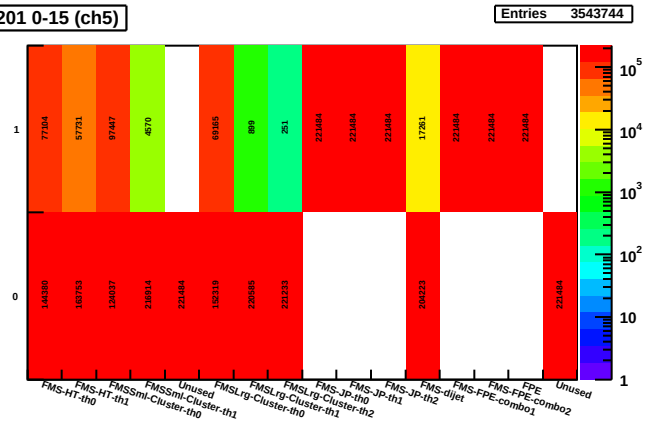

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

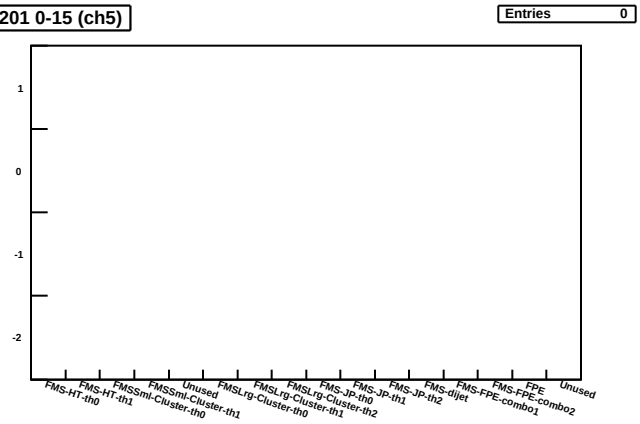




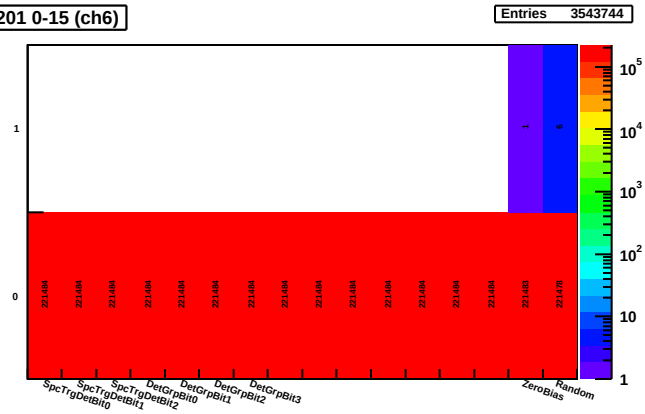
FP201 0-15 (ch5)

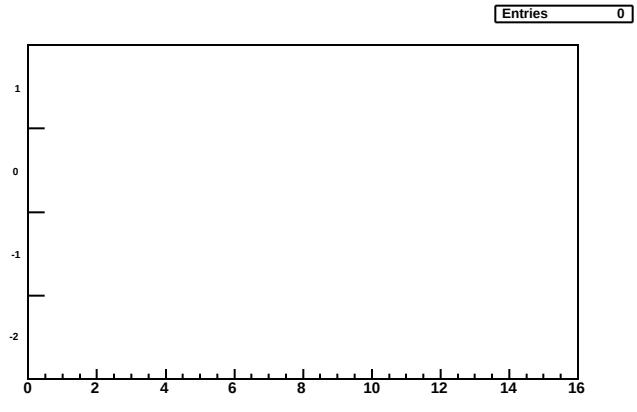
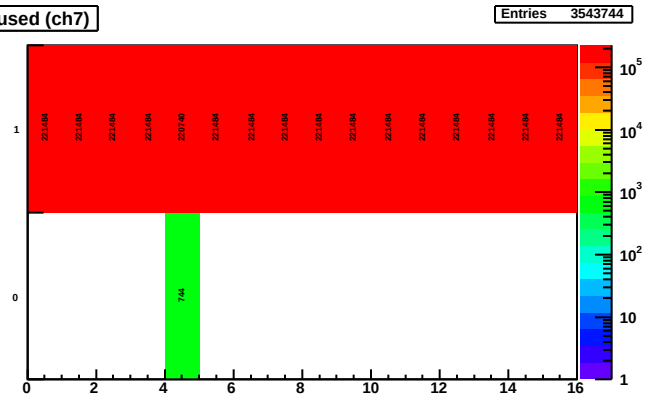
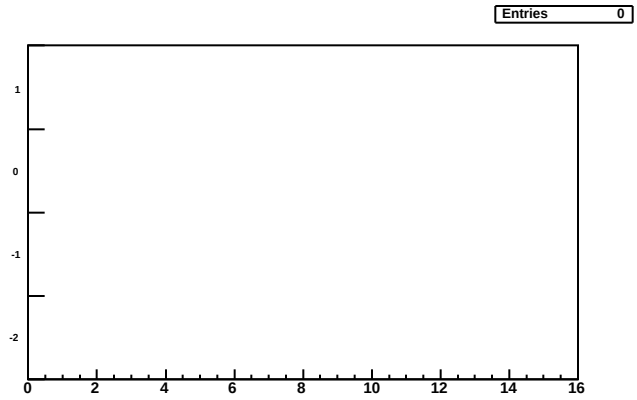


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)