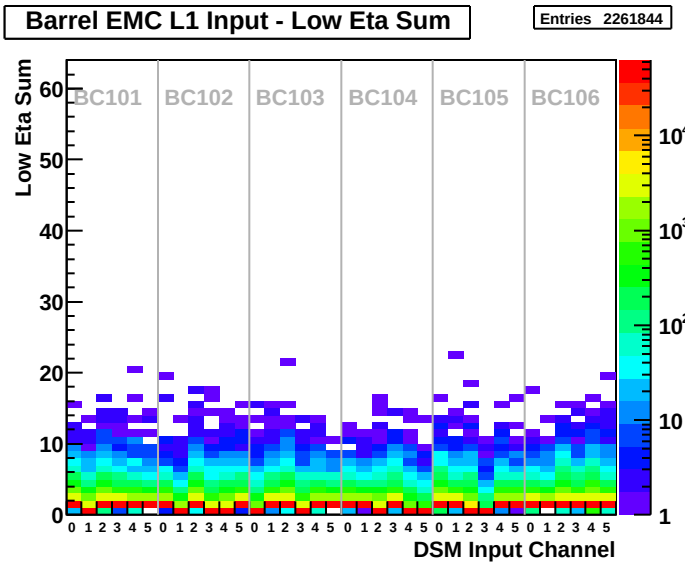
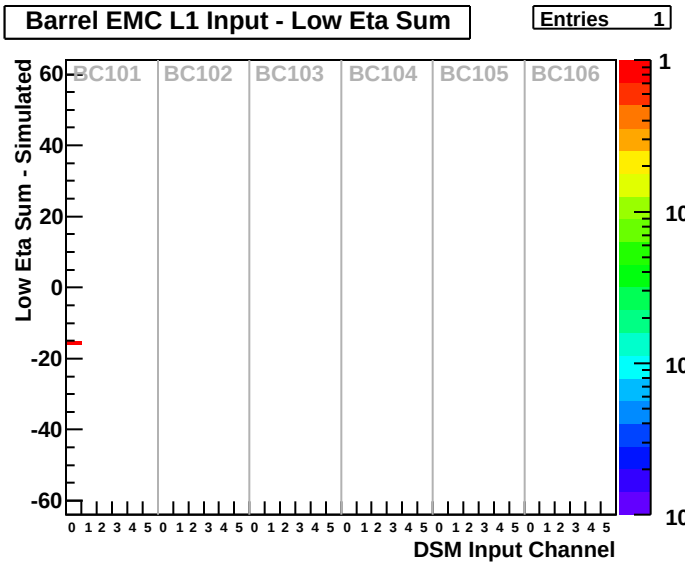
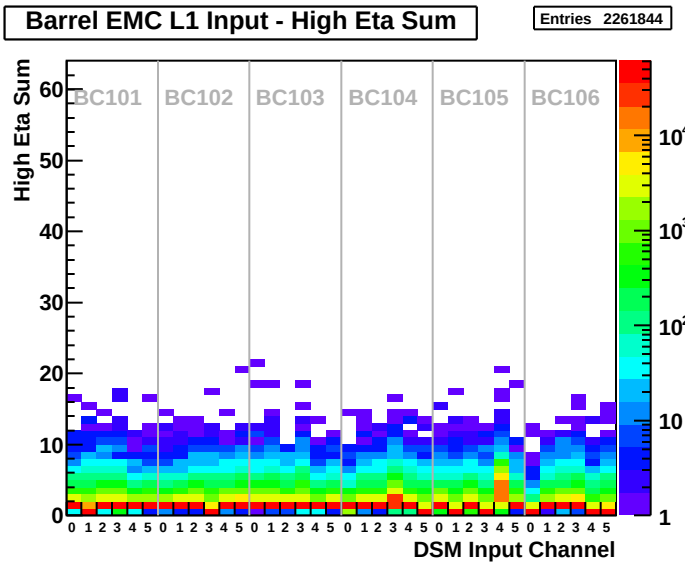
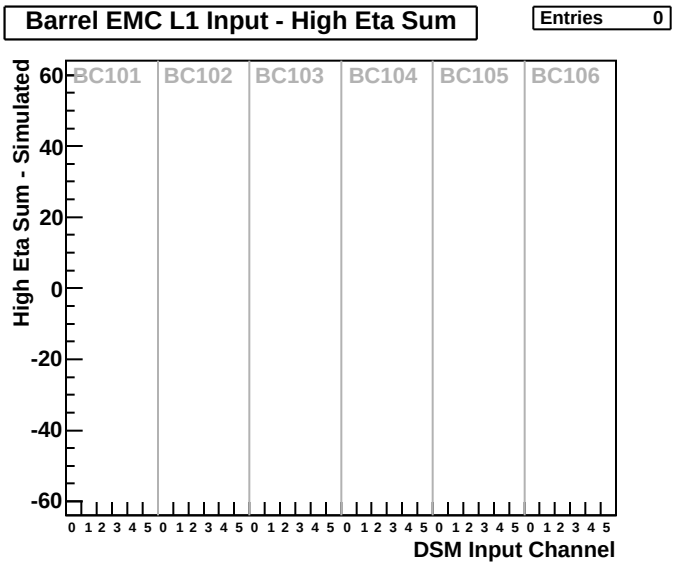
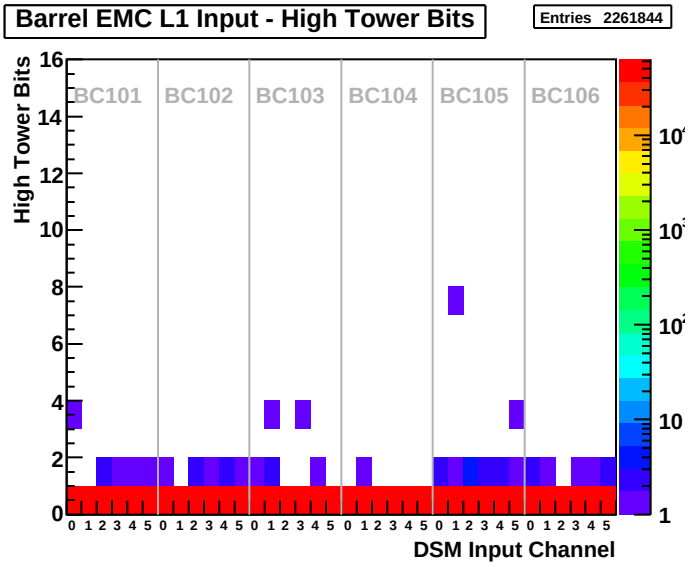
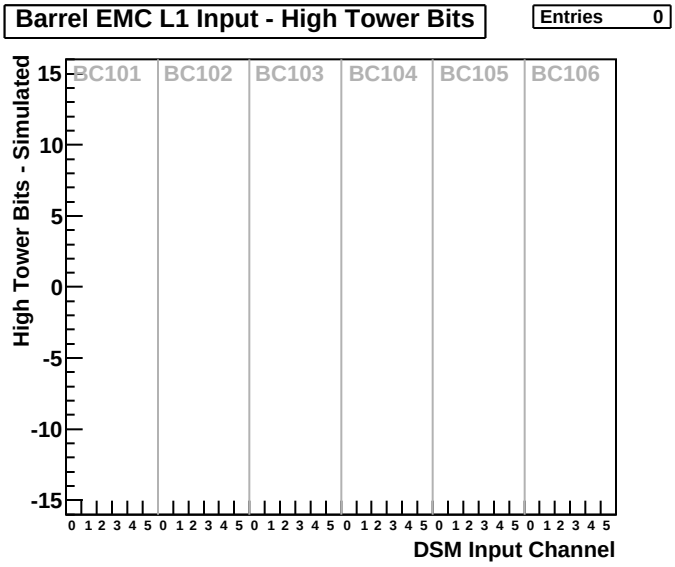

Barrel EMC L1 Input - Low Eta Sum
Entries 2261844
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

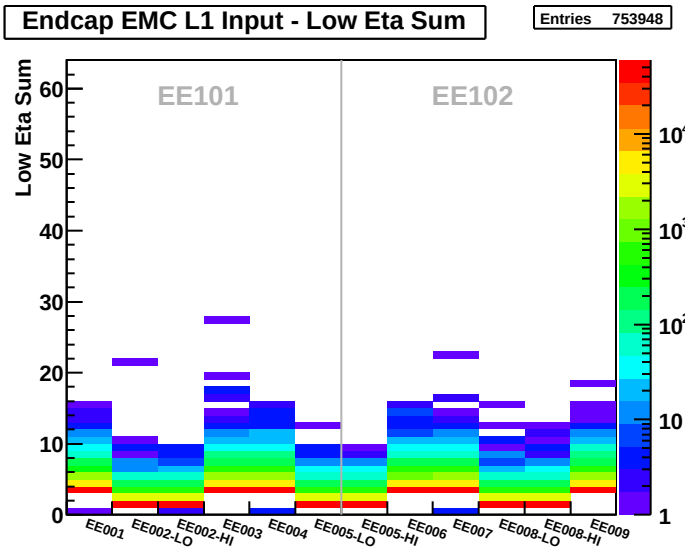

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

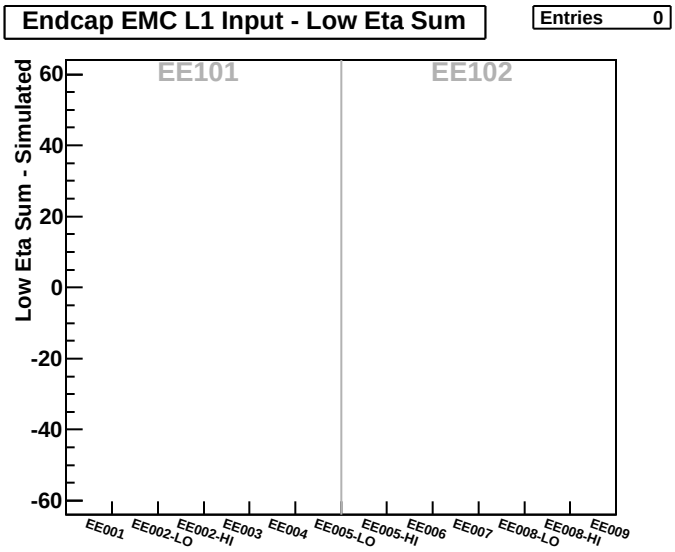

Barrel EMC L1 Input - High Eta Sum
Entries 2261844
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

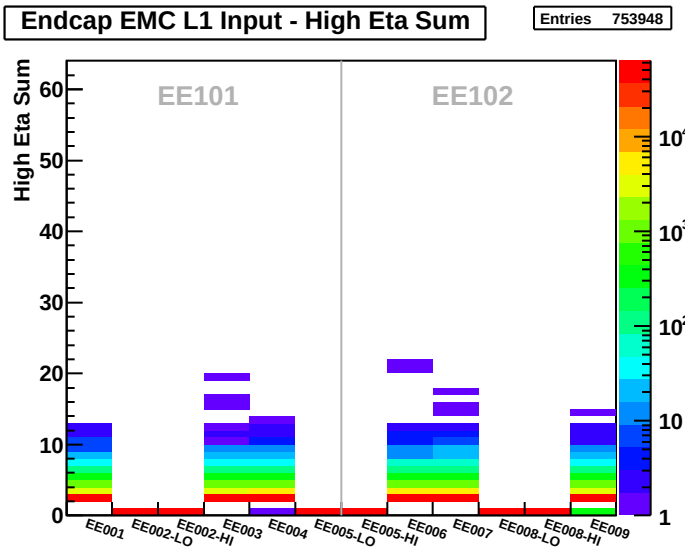

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

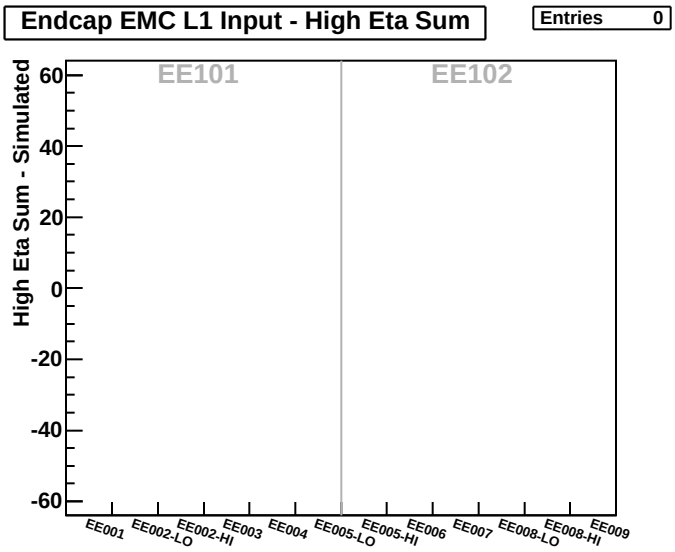

Barrel EMC L1 Input - High Tower Bits
Entries 2261844
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

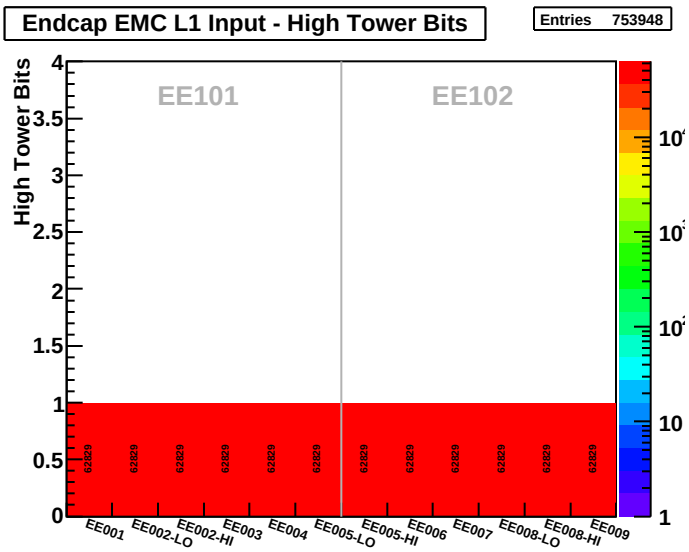

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

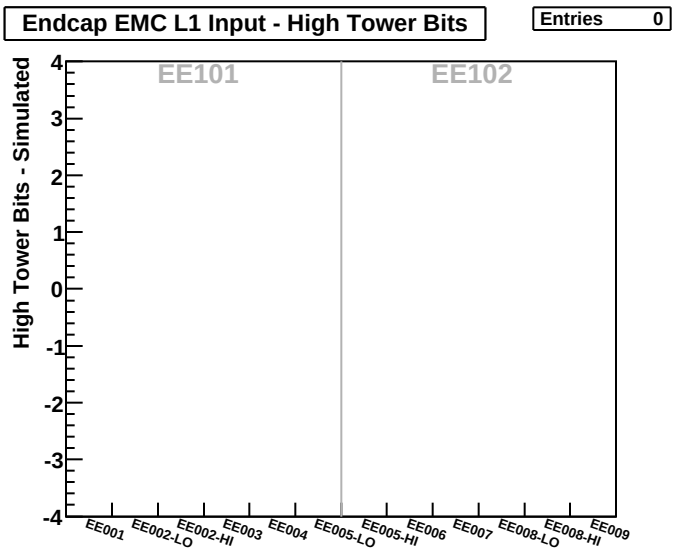

Endcap EMC L1 Input - Low Eta Sum
Entries 753948
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

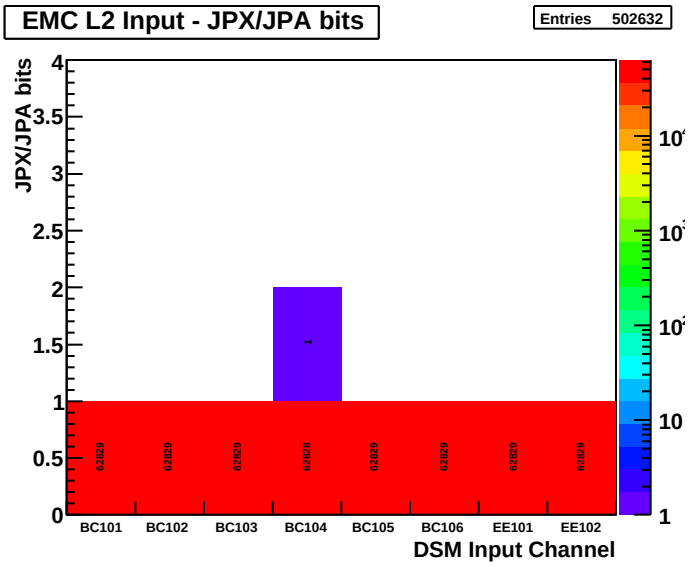

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

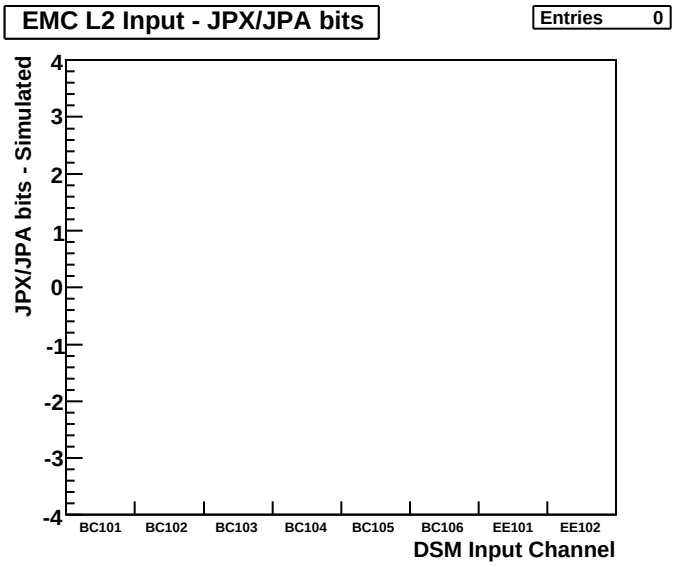

Endcap EMC L1 Input - High Eta Sum
Entries 753948
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

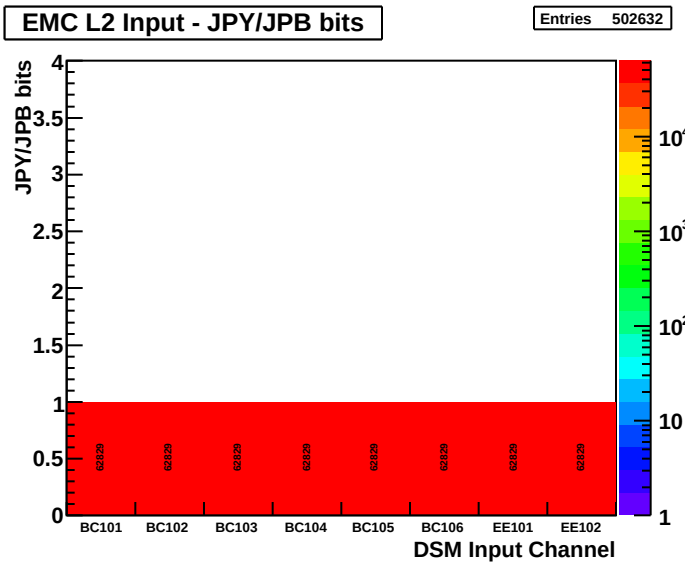

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

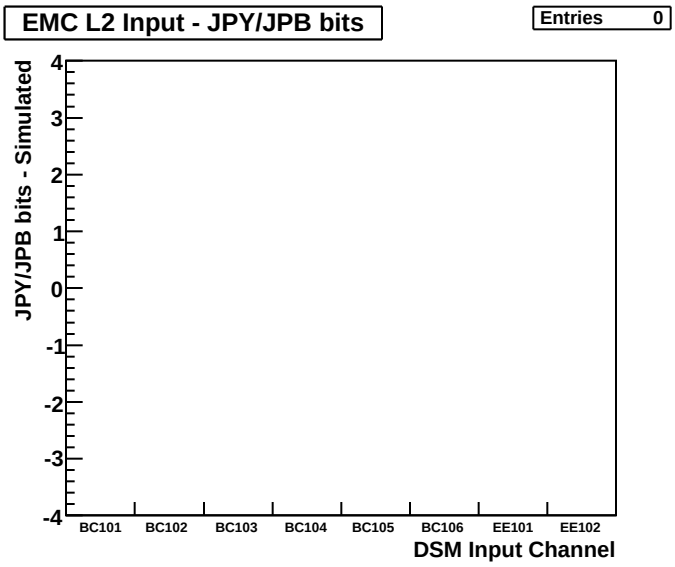

Endcap EMC L1 Input - High Tower Bits
Entries 753948
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

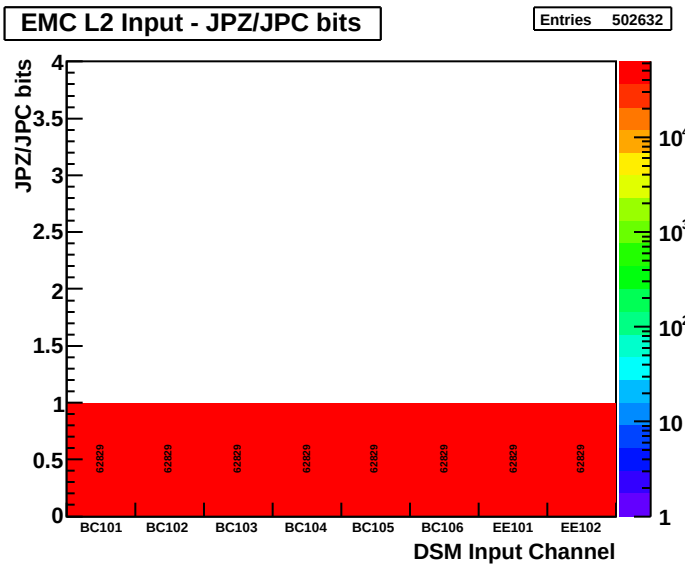

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

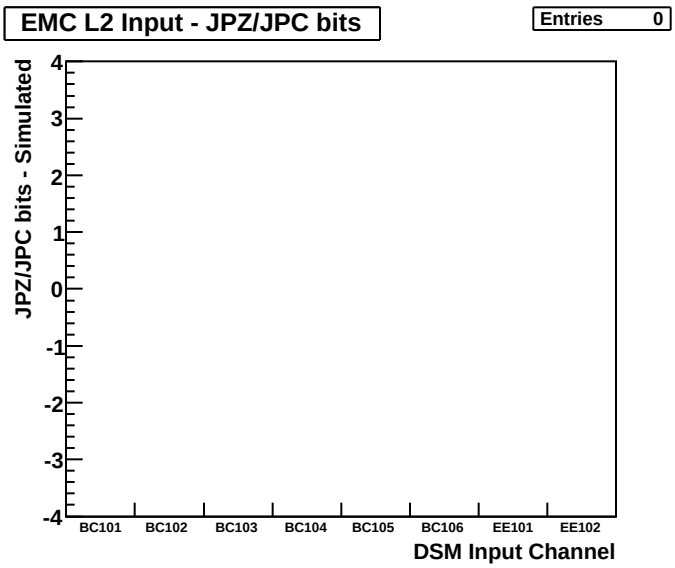

EMC L2 Input - JPX/JPA bits
Entries 502632
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

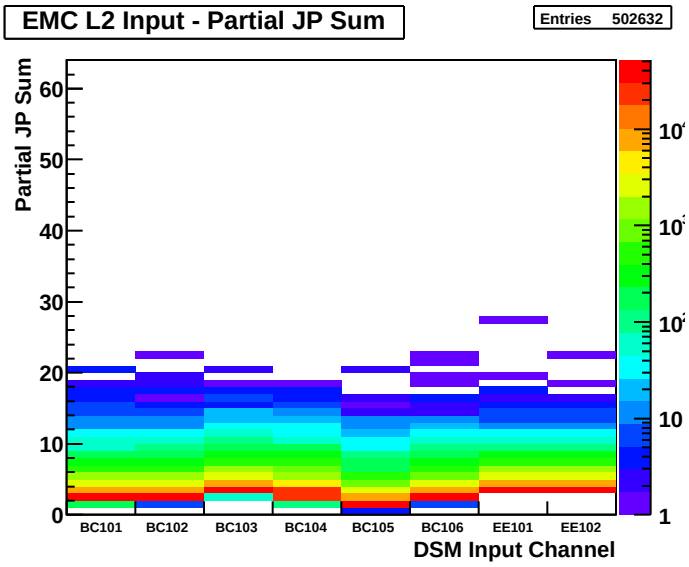

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

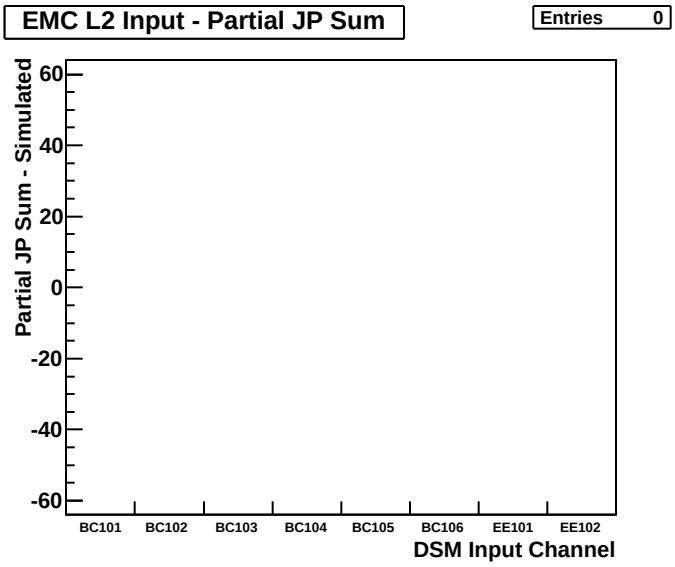

EMC L2 Input - JPY/JPB bits
Entries 502632
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

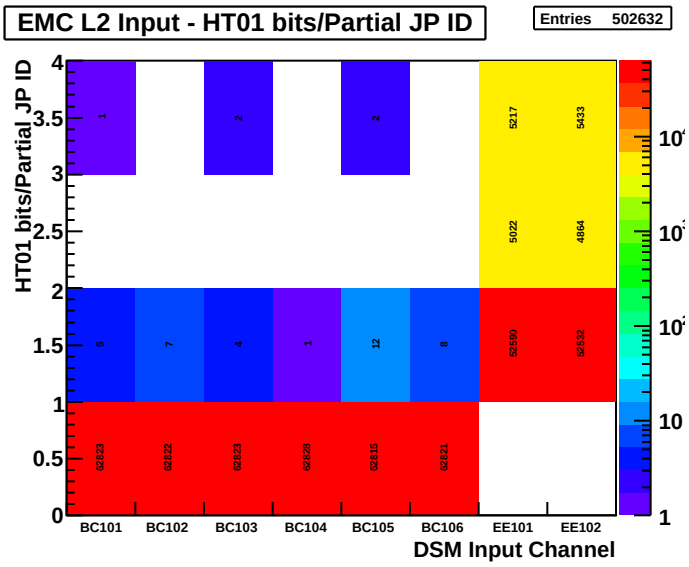

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

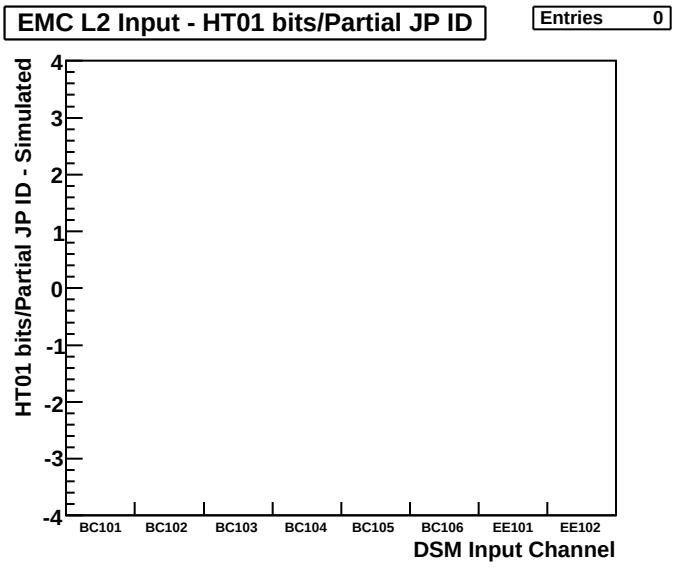

EMC L2 Input - JPZ/JPC bits
Entries 502632
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

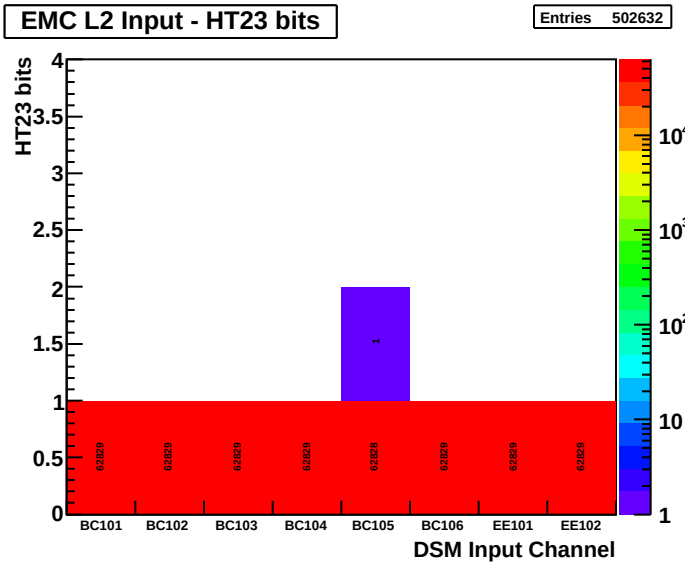

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

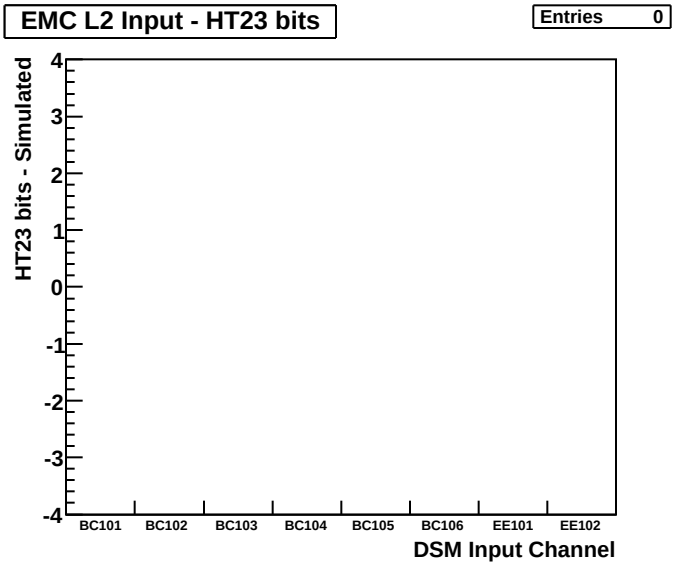

EMC L2 Input - Partial JP Sum
Entries 502632
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

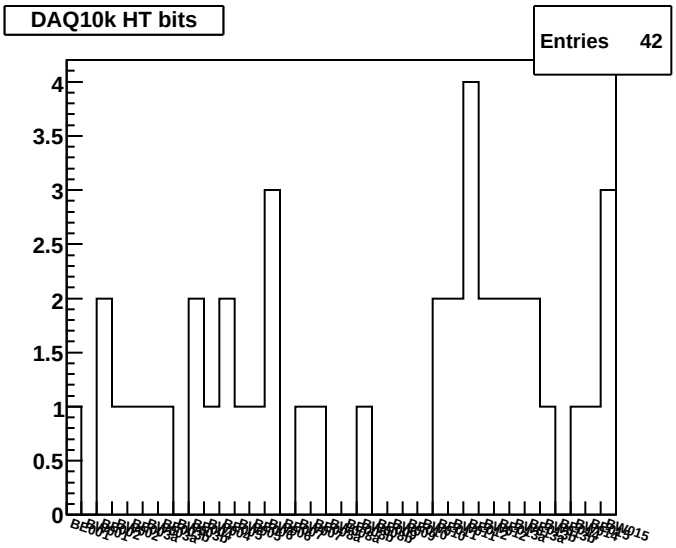

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

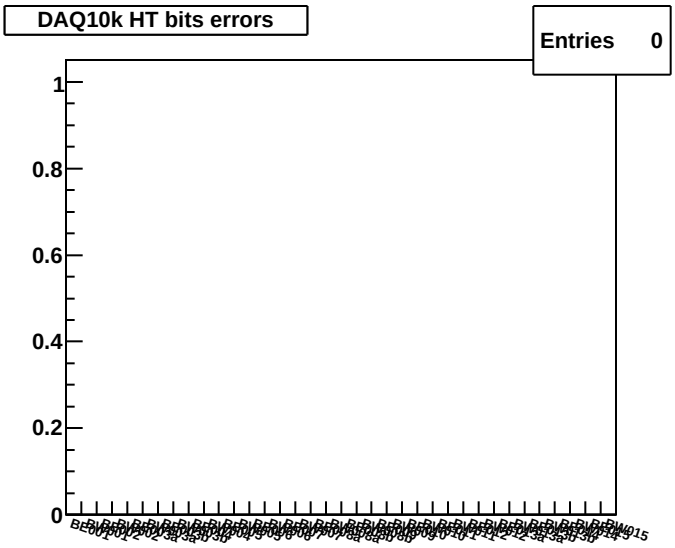

EMC L2 Input - HT01 bits/Partial JP ID
Entries 502632
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

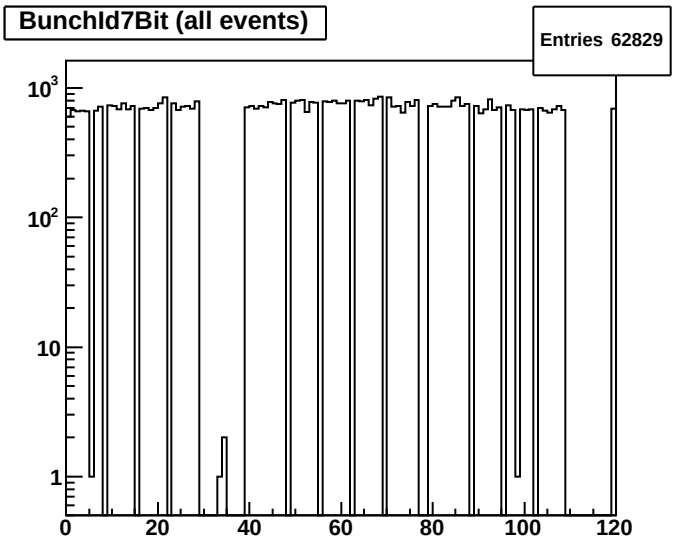

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

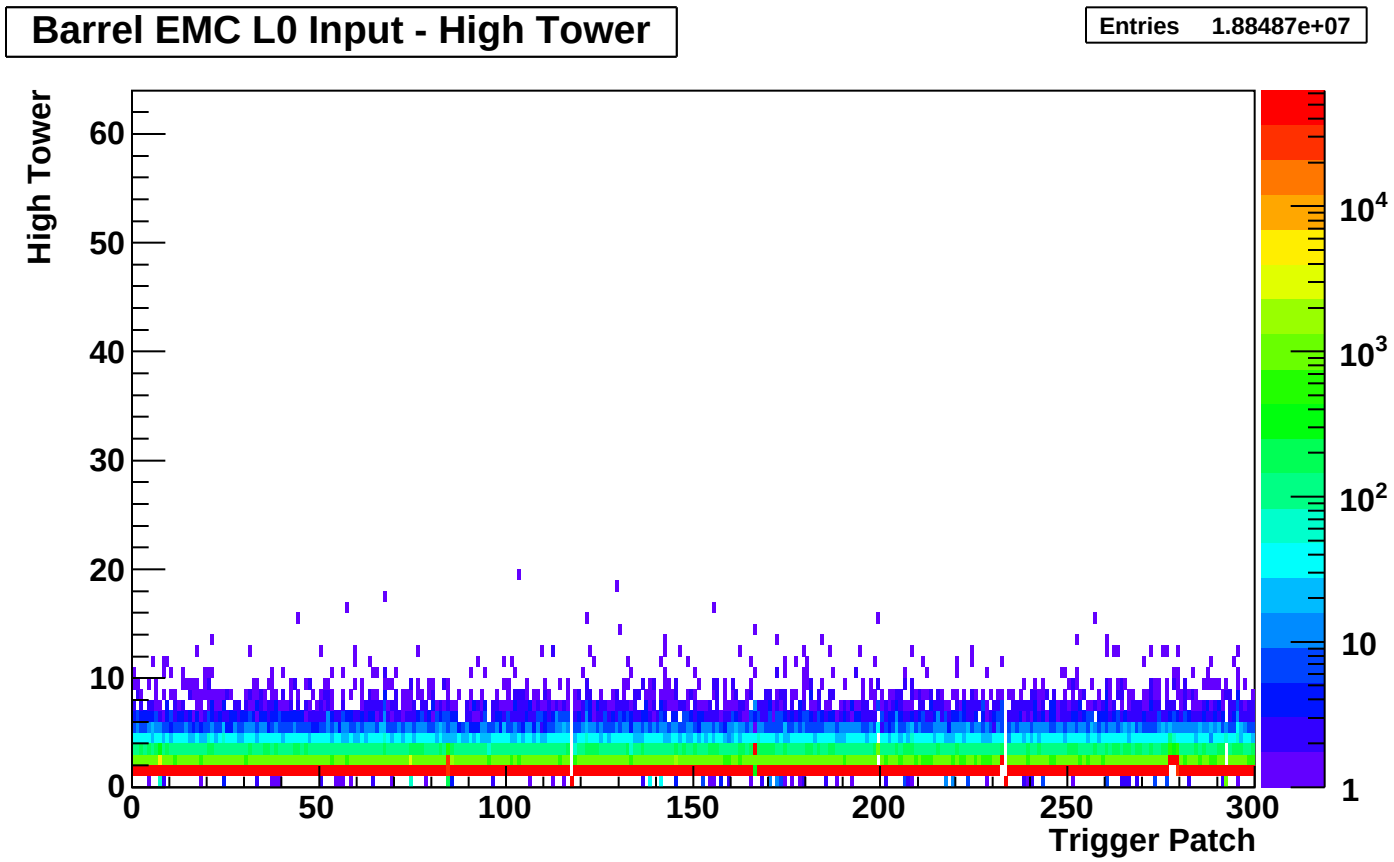

EMC L2 Input - HT23 bits
Entries 502632
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

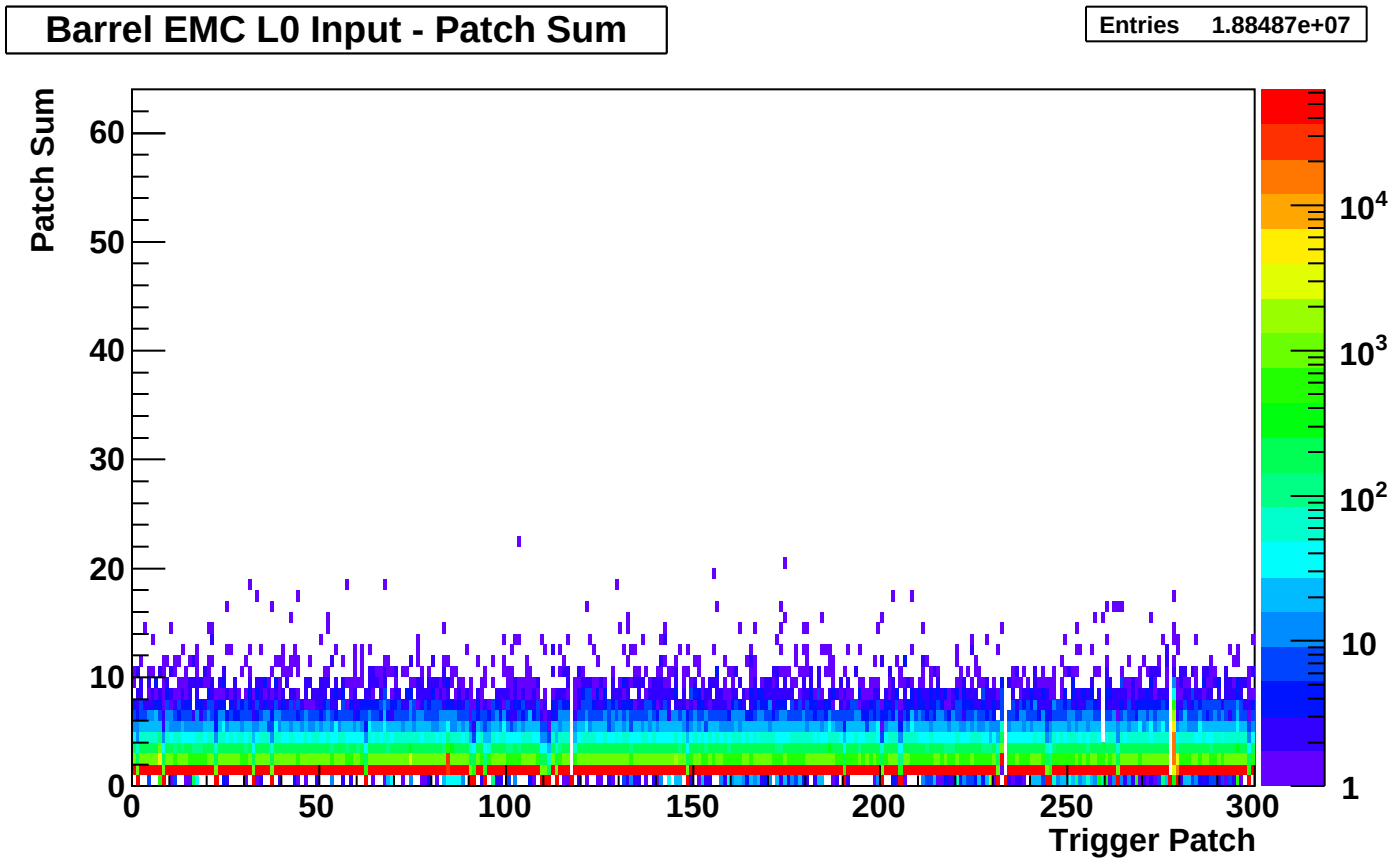

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

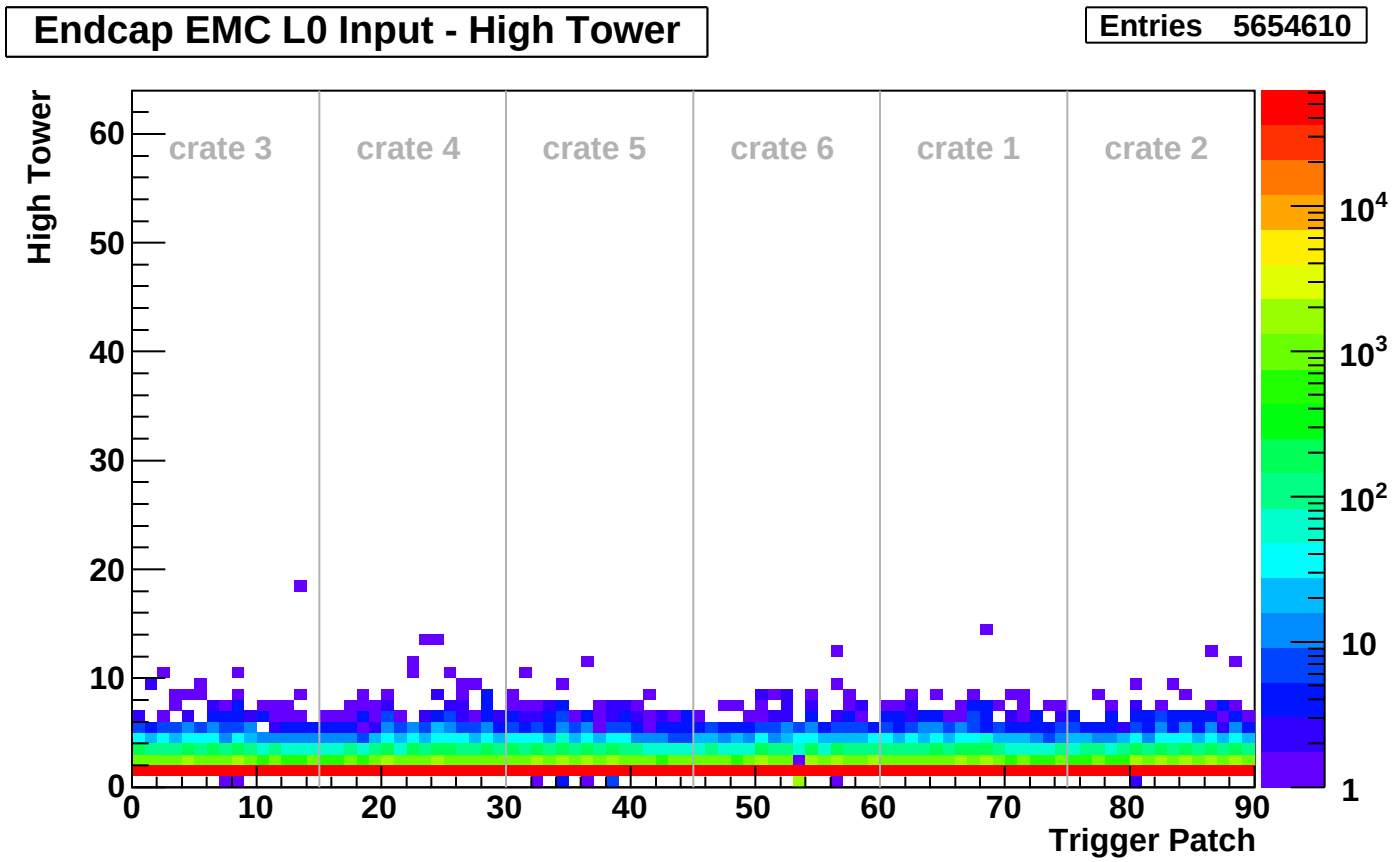

DAQ10k HT bits
Entries 42
4
3.5
3
2.5
2
1.5
1
0.5
0

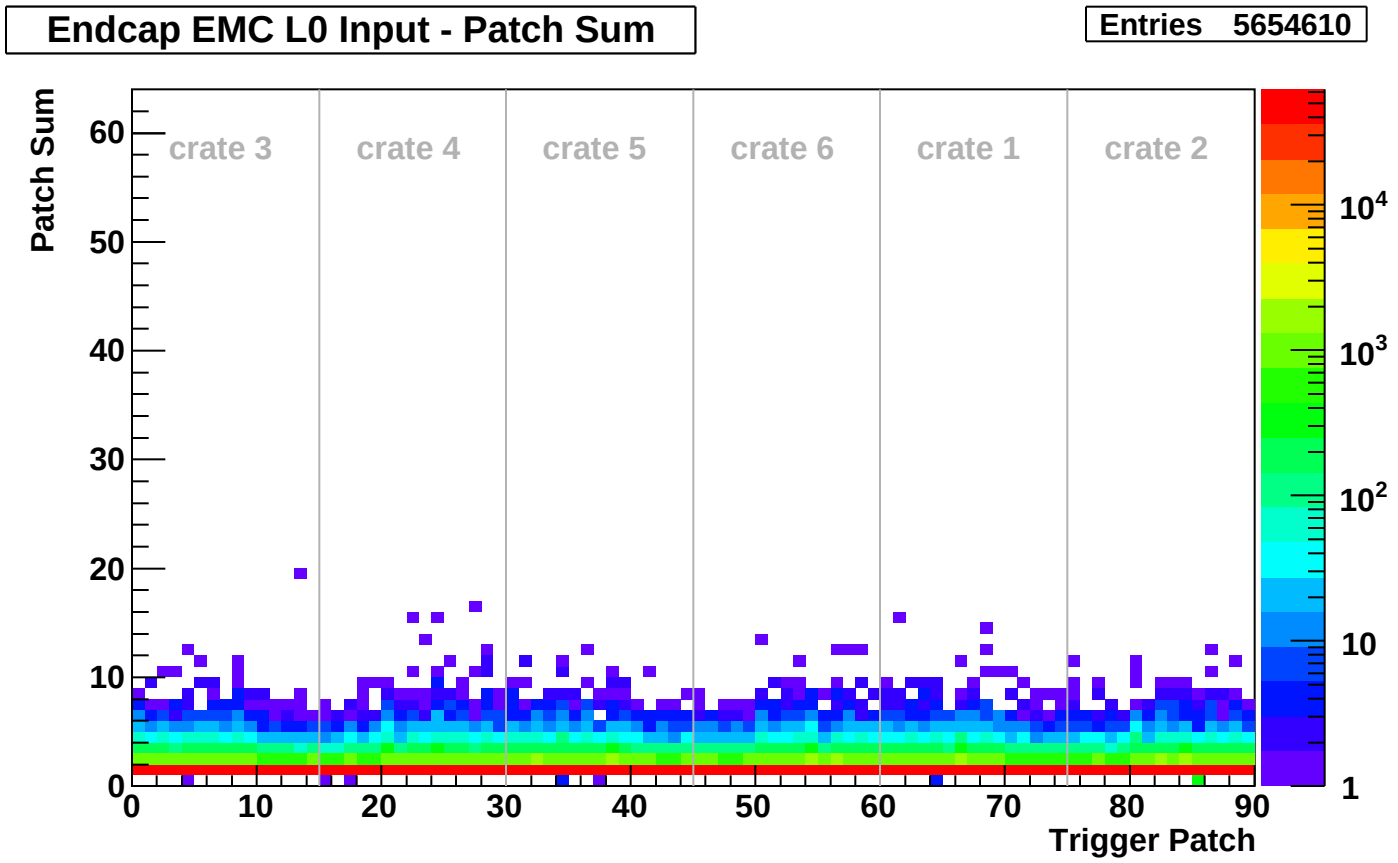

DAQ10k HT bits errors
Entries 0
1
0.8
0.6
0.4
0.2
0

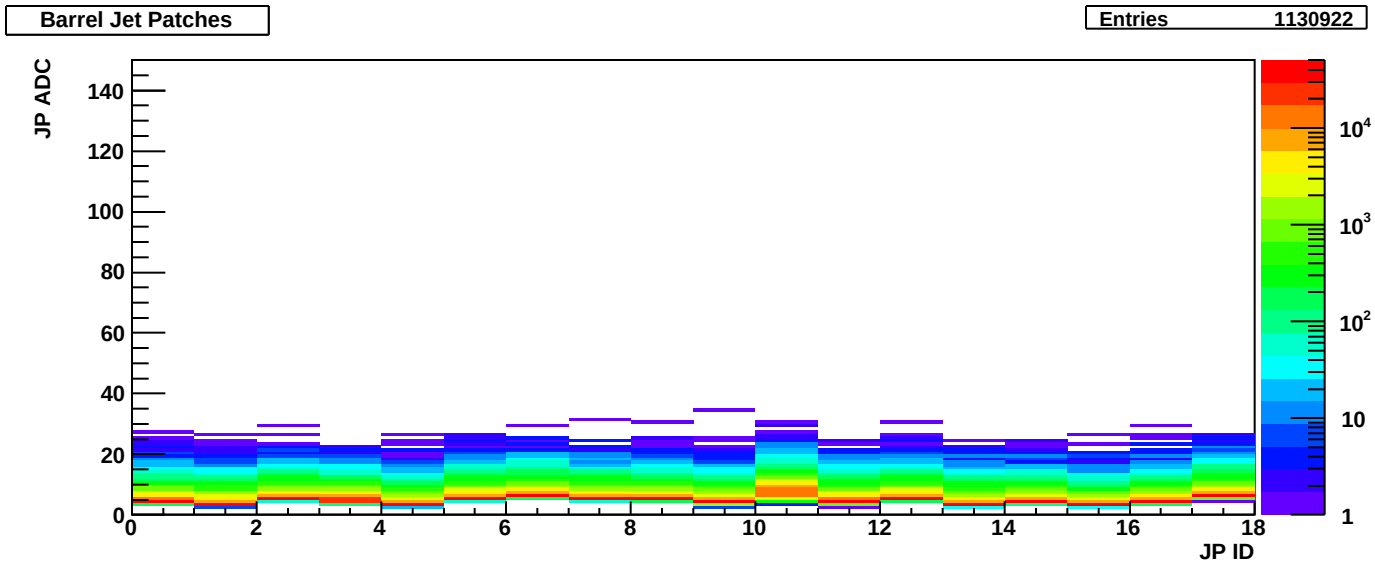

BunchId7Bit (all events)
Entries 62829
10^3
10^2
10
1
0
20
40
60
80
100
120

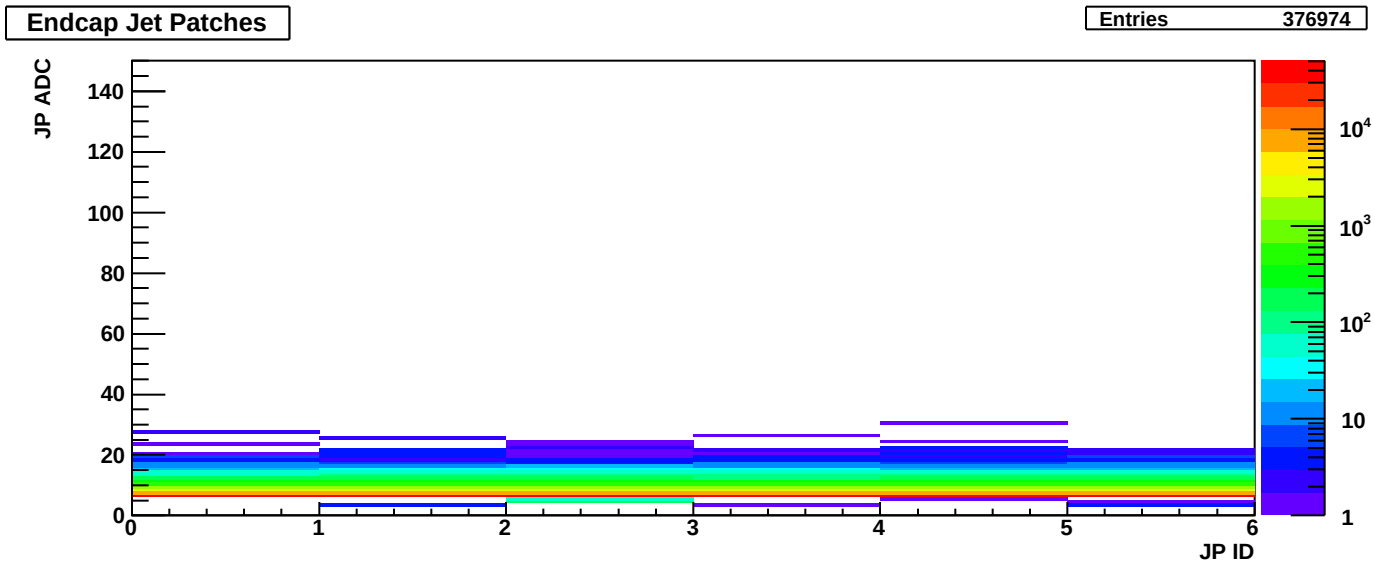

Barrel EMC L0 Input - High Tower
Entries 1.88487e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

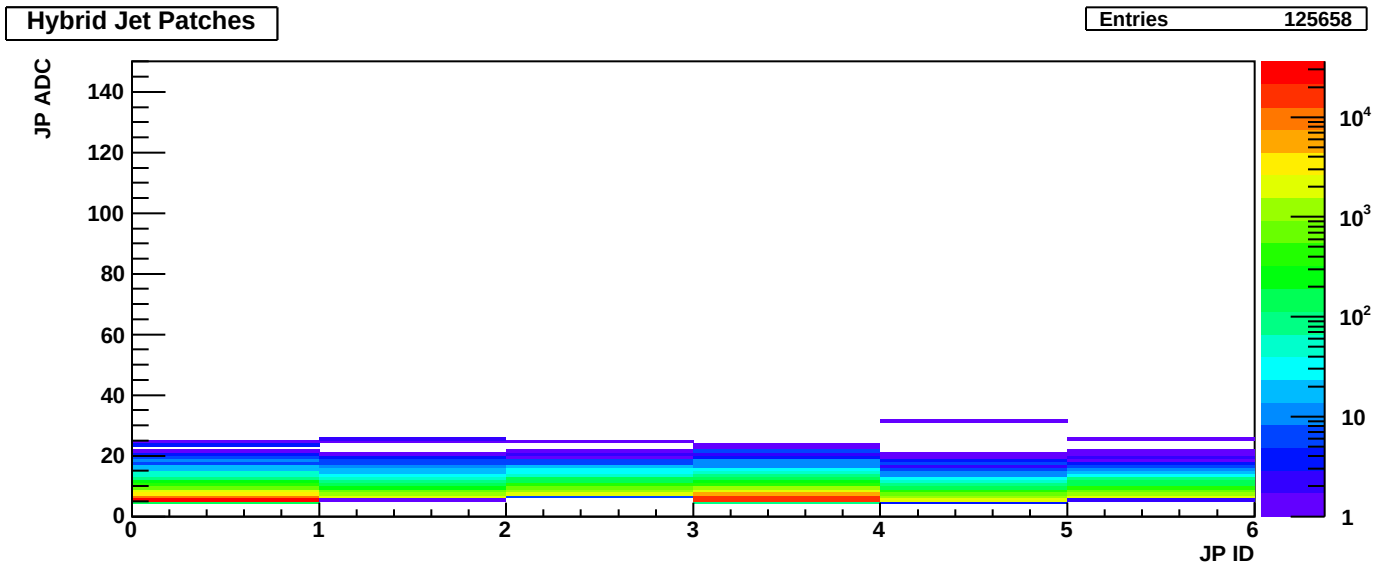

Barrel EMC L0 Input - Patch Sum
Entries 1.88487e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

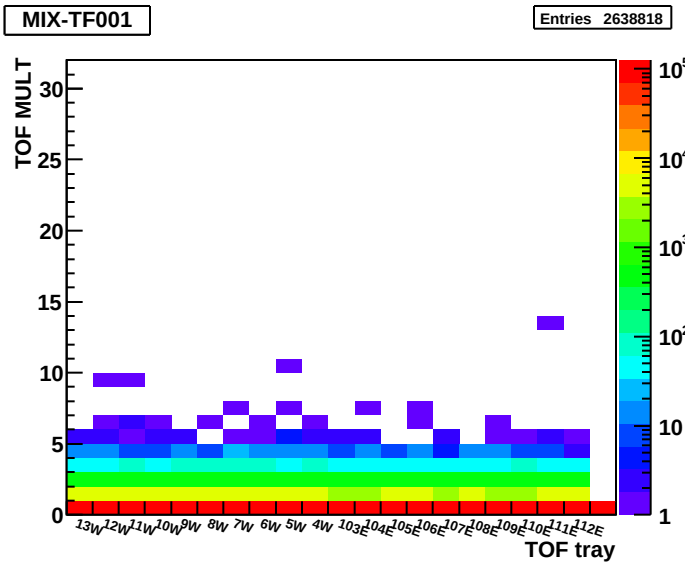

Endcap EMC L0 Input - High Tower
Entries 5654610
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

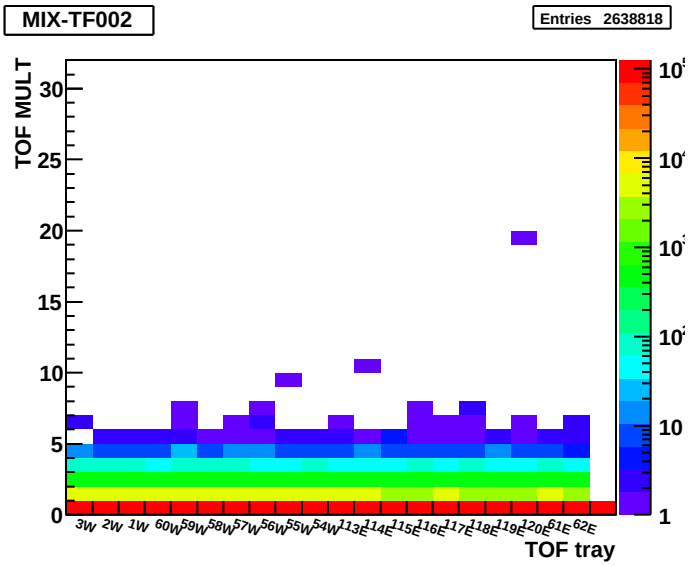

Endcap EMC L0 Input - Patch Sum
Entries 5654610
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

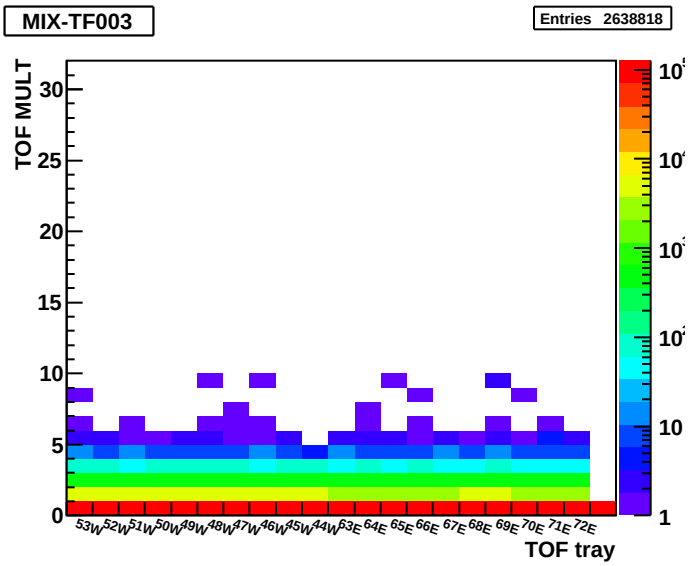

Barrel Jet Patches
Entries 1130922
JP ADC
JP ID

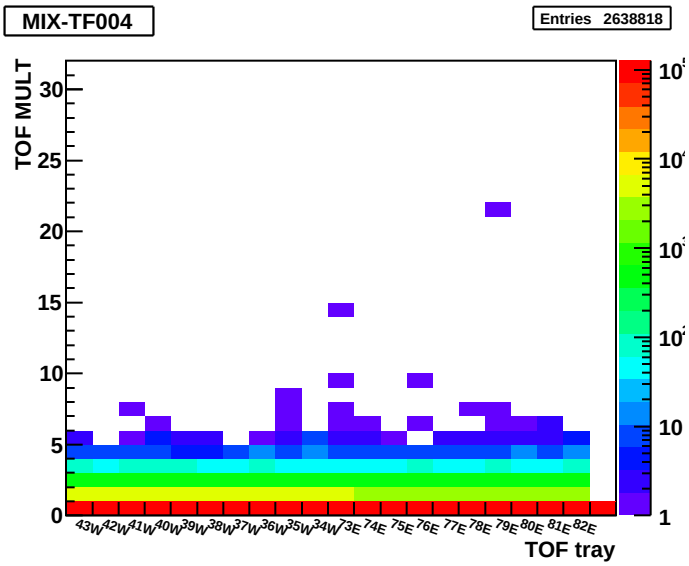

Endcap Jet Patches
Entries 376974
JP ADC
JP ID

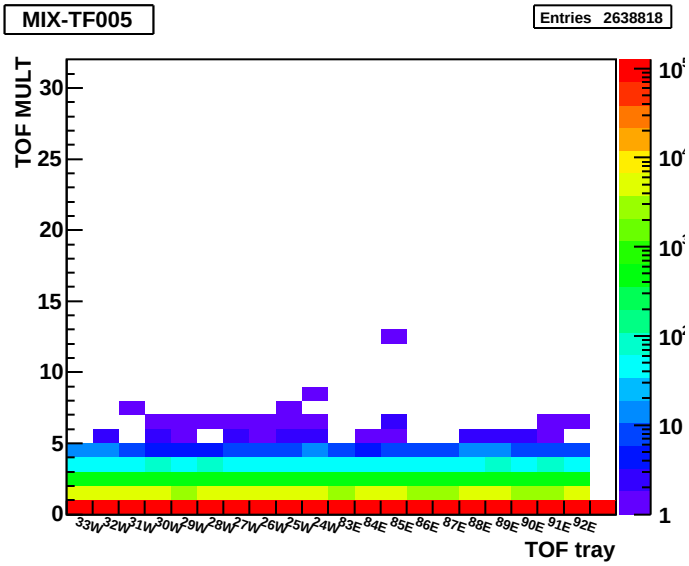

Hybrid Jet Patches
Entries 125658
JP ADC
JP ID

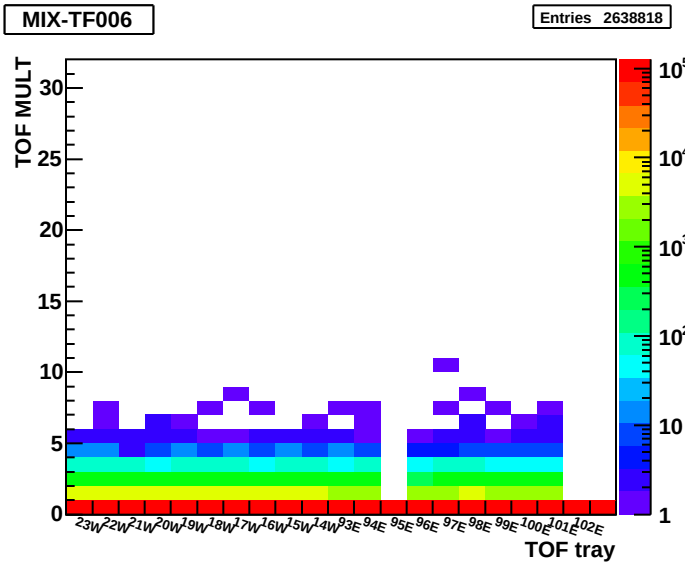

MIX-TF001
Entries 2638818
TOF MULT
TOF tray

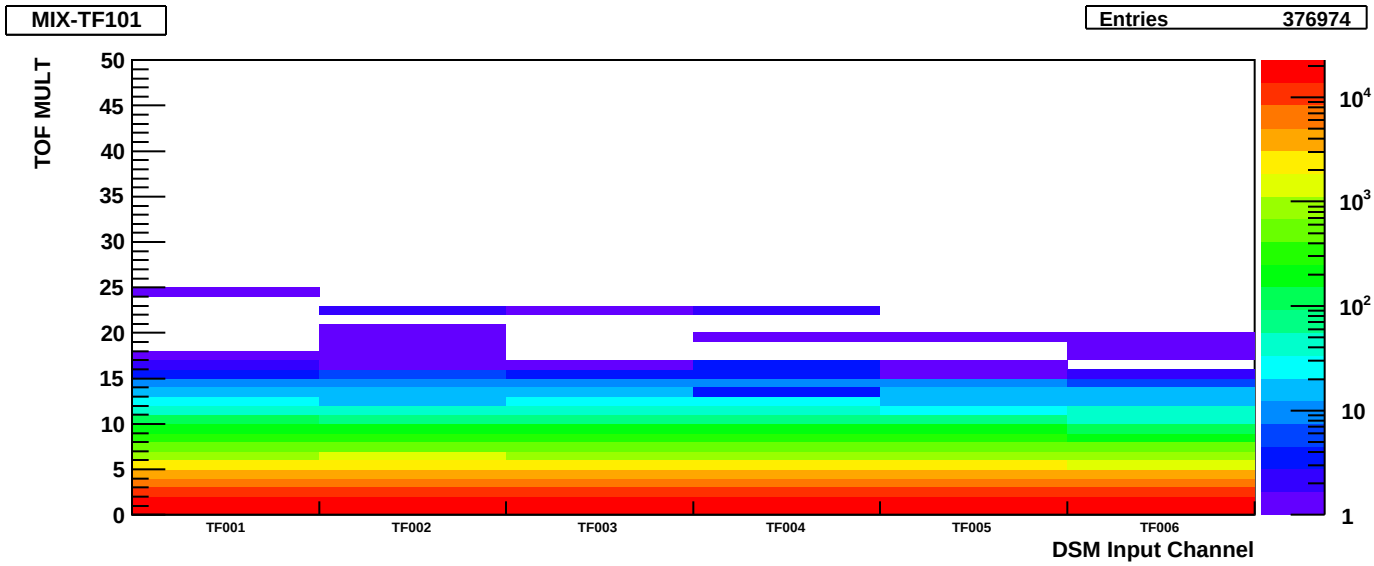

MIX-TF002
Entries 2638818
TOF MULT
TOF tray

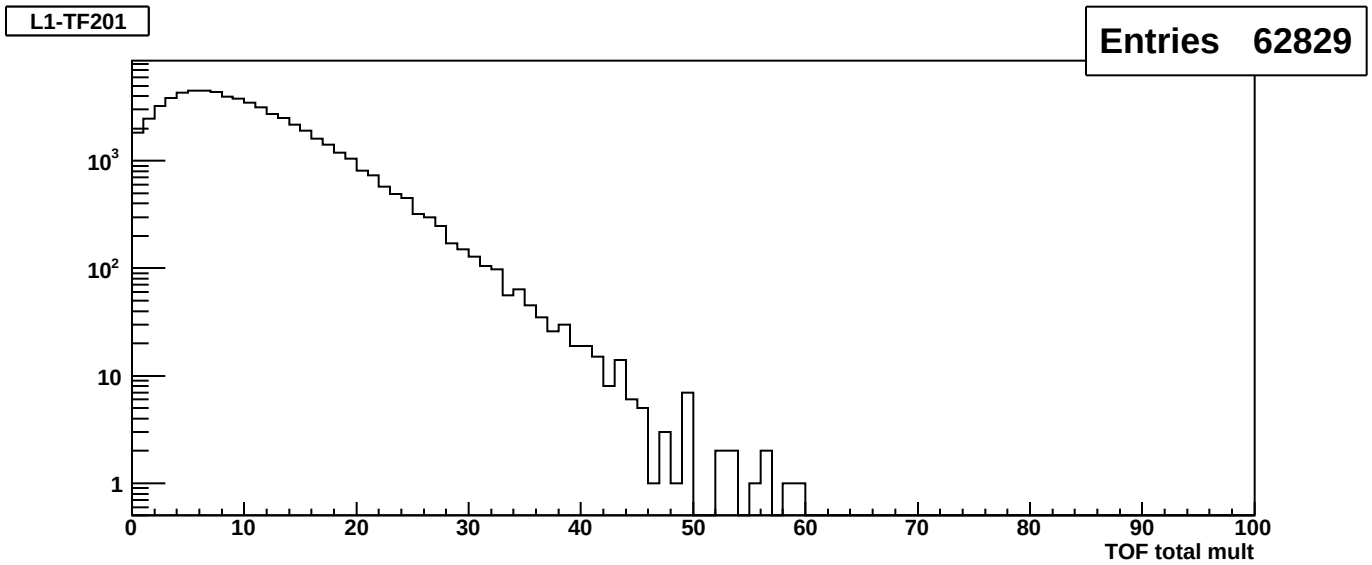

MIX-TF003
Entries 2638818
TOF MULT
TOF tray

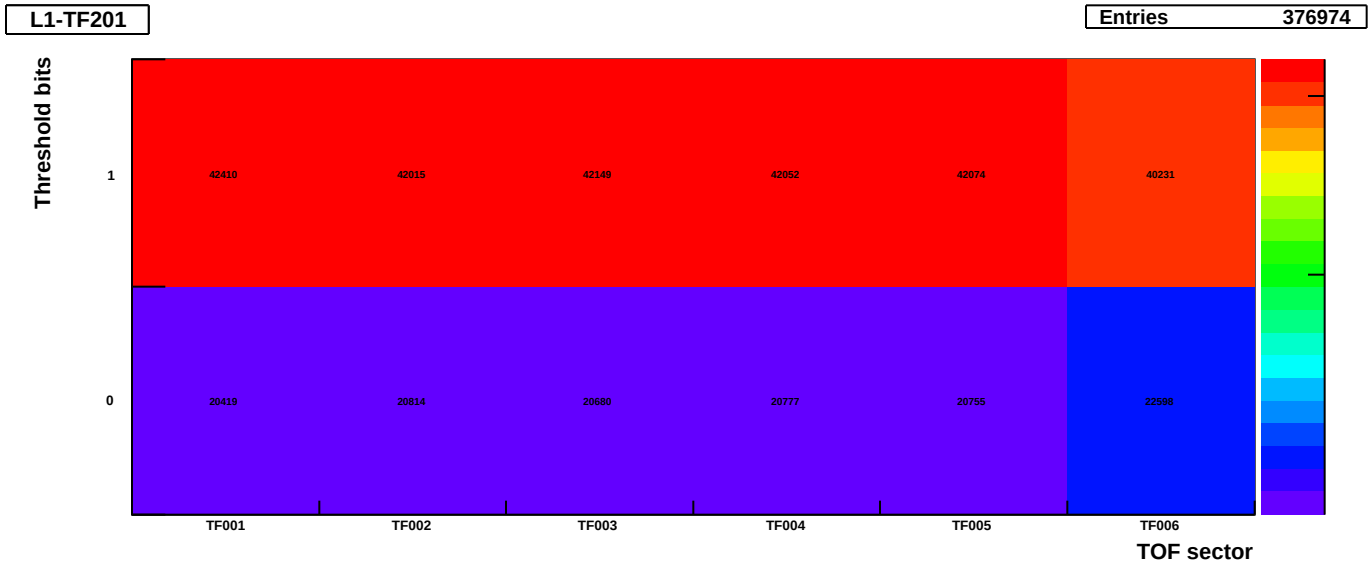

MIX-TF004
Entries 2638818
TOF MULT
TOF tray

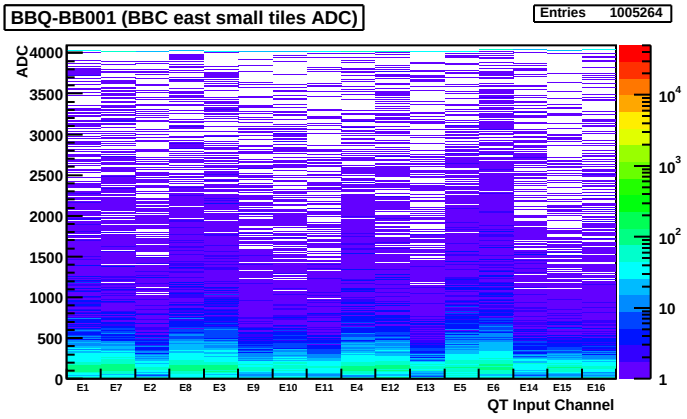

MIX-TF005
Entries 2638818
TOF MULT
TOF tray

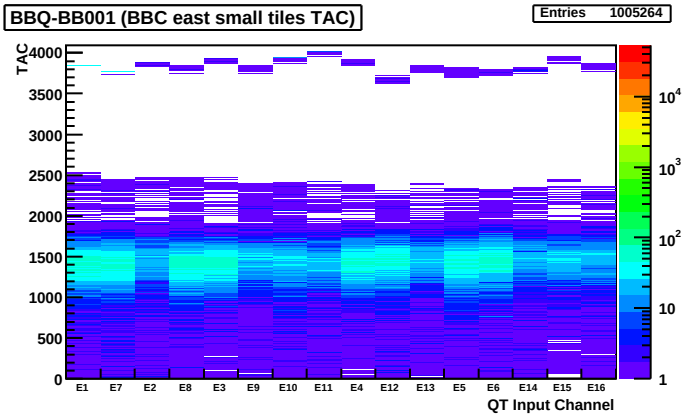

MIX-TF006
Entries 2638818
TOF MULT
TOF tray

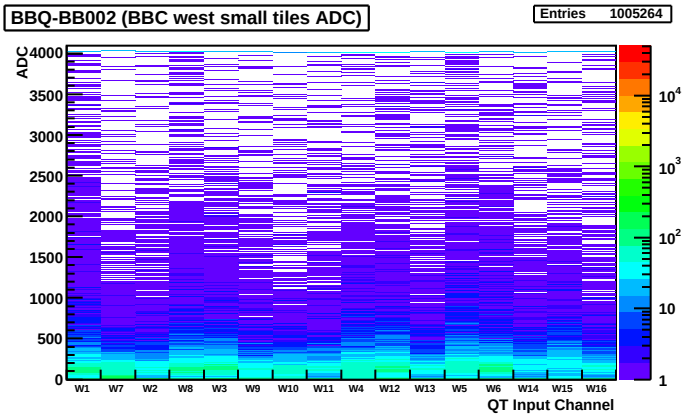

MIX-TF101
Entries 376974
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

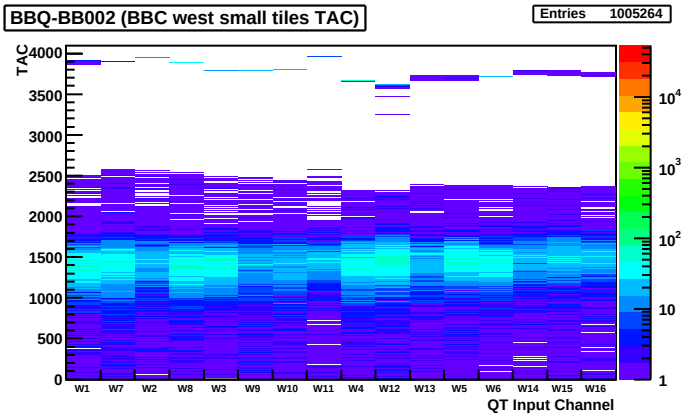

L1-TF201
Entries 62829
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

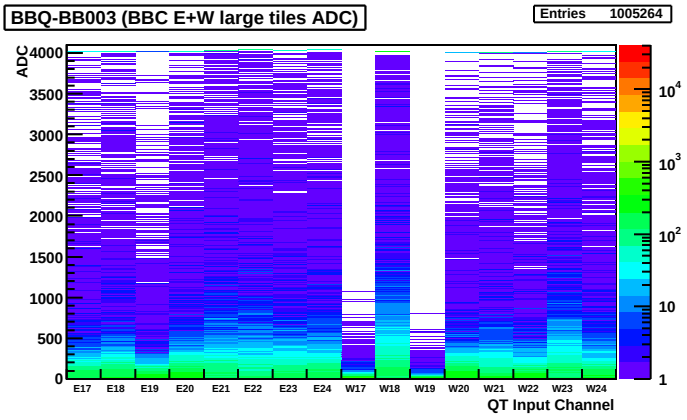

L1-TF201
Entries 376974
Threshold bits
1
0
42410
42015
42149
42052
42074
40231
20419
20814
20680
20777
20755
22598
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

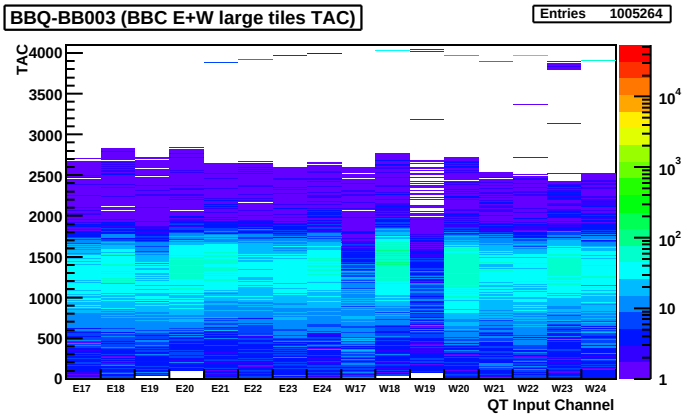

BBQ-BB001 (BBC east small tiles ADC)
Entries 1005264
ADC
QT Input Channel

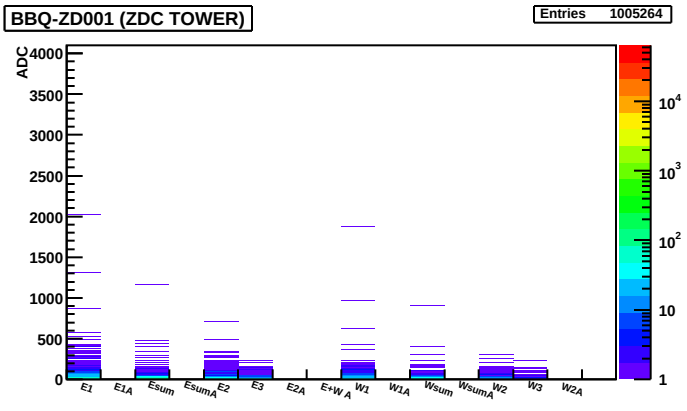

BBQ-BB001 (BBC east small tiles TAC)
Entries 1005264
TAC
QT Input Channel

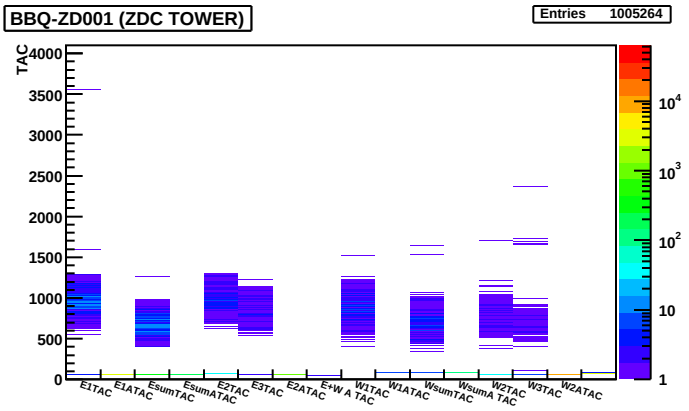

BBQ-BB002 (BBC west small tiles ADC)
Entries 1005264
ADC
QT Input Channel

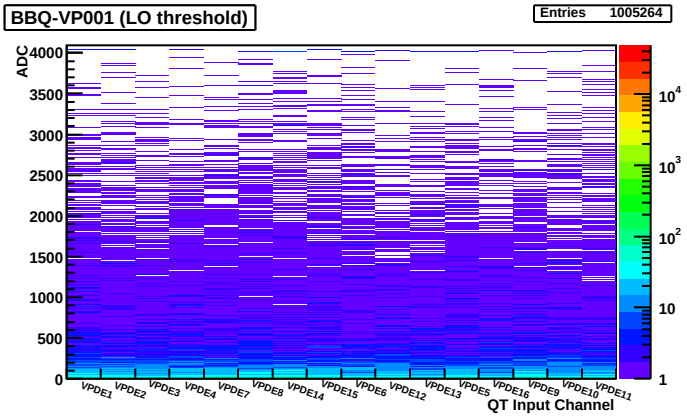

BBQ-BB002 (BBC west small tiles TAC)
Entries 1005264
TAC
QT Input Channel

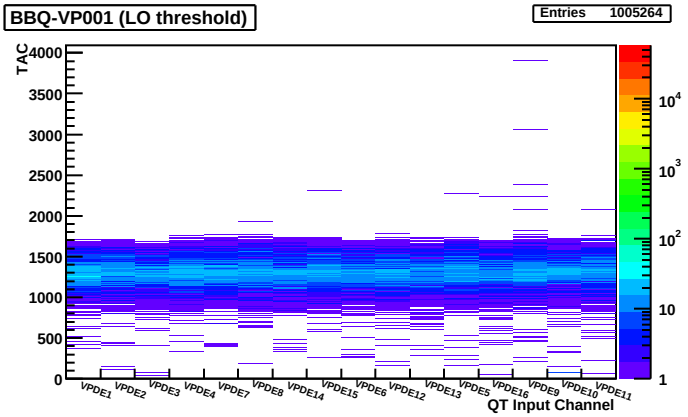

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1005264
ADC
QT Input Channel

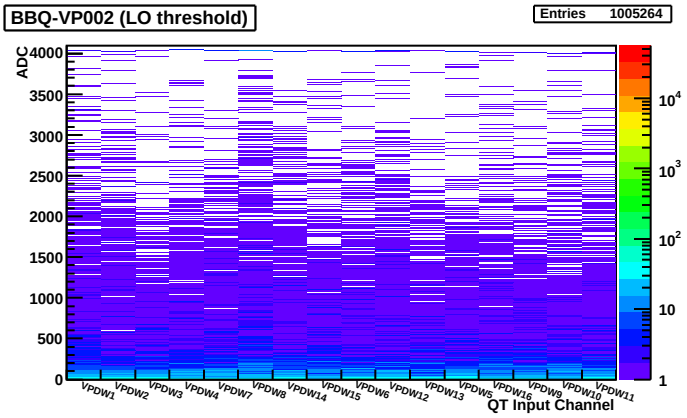

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1005264
TAC
QT Input Channel

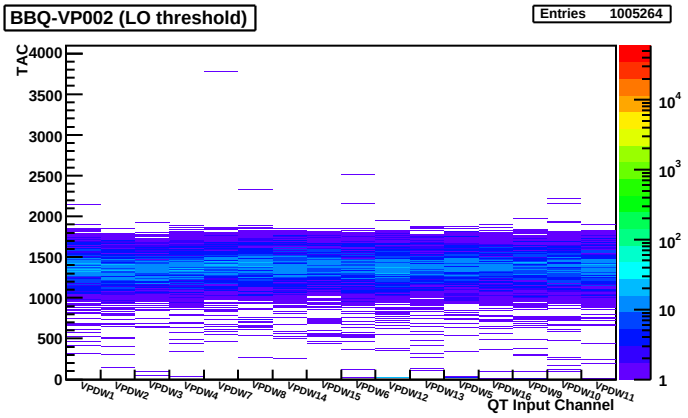

BBQ-ZD001 (ZDC TOWER)
Entries 1005264
ADC

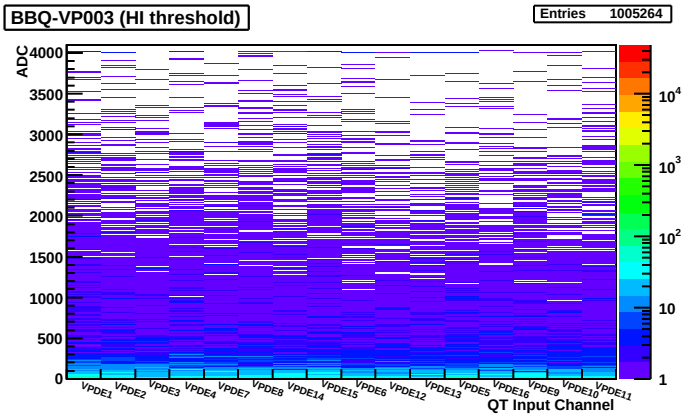

BBQ-ZD001 (ZDC TOWER)
Entries 1005264
TAC

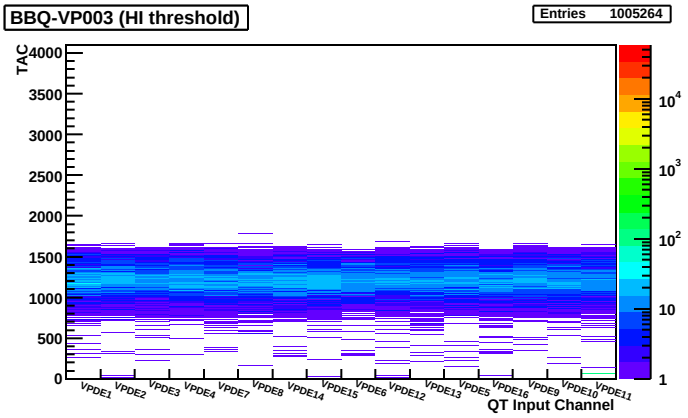

BBQ-VP001 (LO threshold)
Entries 1005264
ADC
QT Input Channel

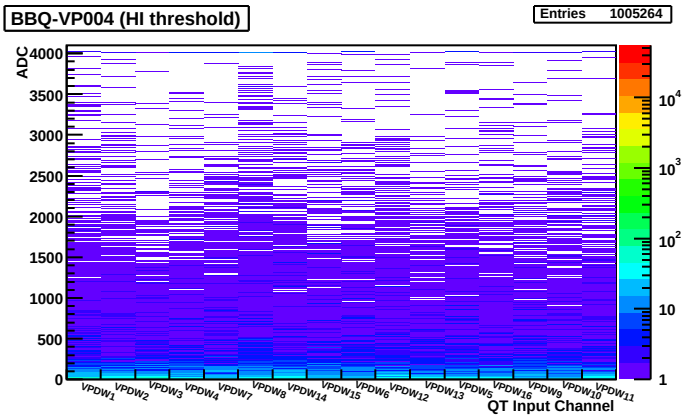

BBQ-VP001 (LO threshold)
Entries 1005264
TAC
QT Input Channel

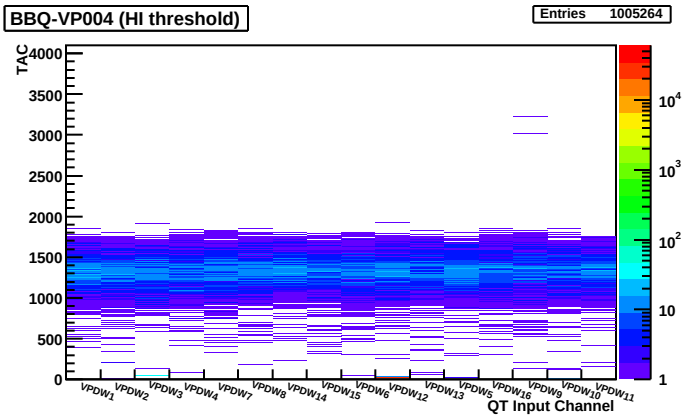

BBQ-VP002 (LO threshold)
Entries 1005264
ADC
QT Input Channel

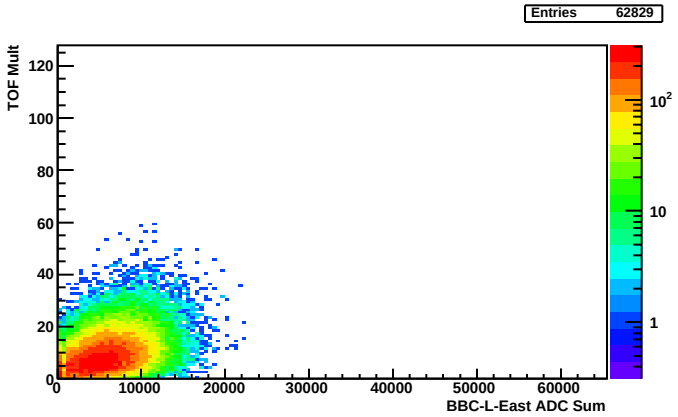

BBQ-VP002 (LO threshold)
Entries 1005264
TAC
QT Input Channel

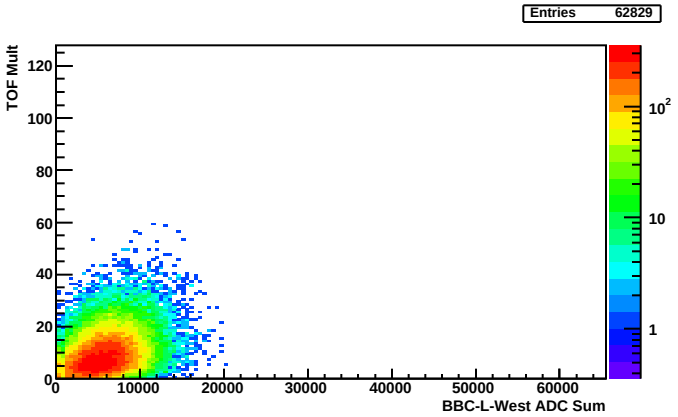

BBQ-VP003 (HI threshold)
Entries 1005264
ADC
QT Input Channel

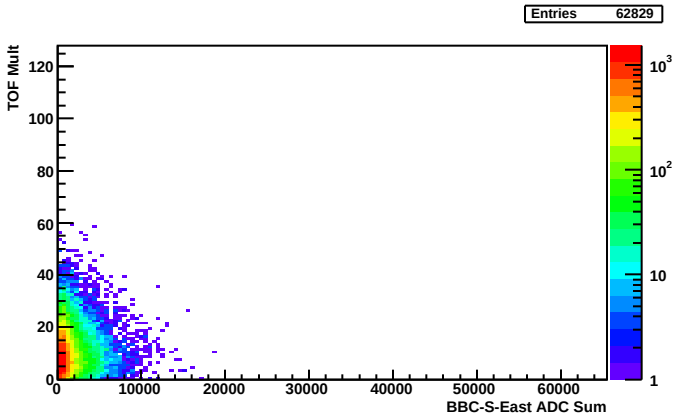

BBQ-VP003 (HI threshold)
Entries 1005264
TAC
QT Input Channel

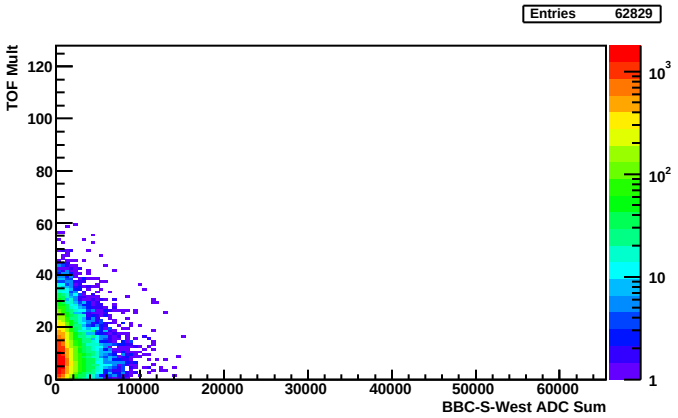

BBQ-VP004 (HI threshold)
Entries 1005264
ADC
QT Input Channel

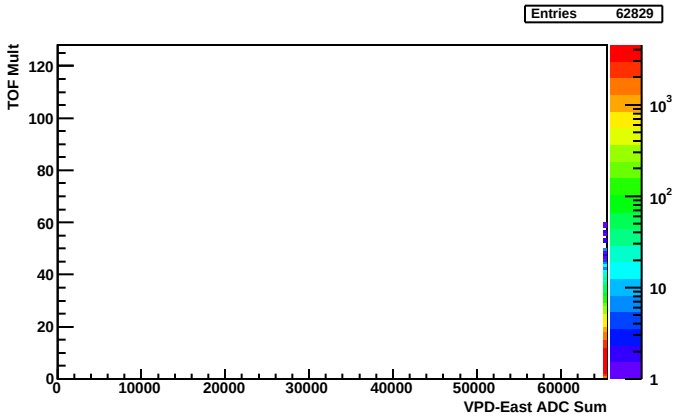

BBQ-VP004 (HI threshold)
Entries 1005264
TAC
QT Input Channel

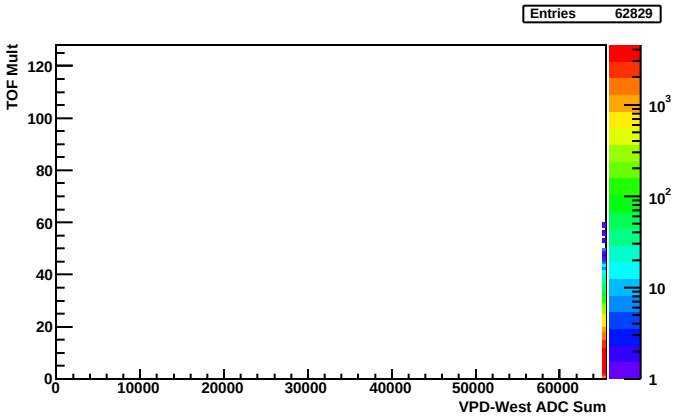

Entries 62829
TOF Mult
BBC-L-East ADC Sum

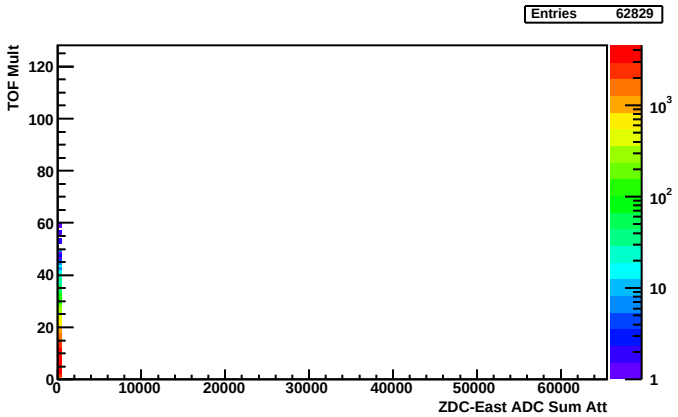

Entries 62829
TOF Mult
BBC-L-West ADC Sum

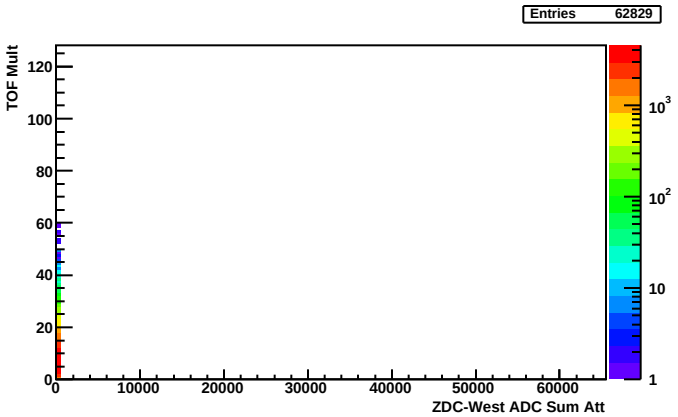

Entries 62829
TOF Mult
BBC-S-East ADC Sum

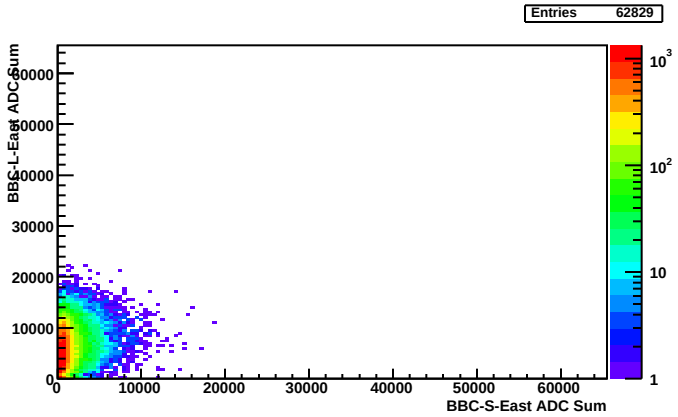

Entries 62829
TOF Mult
BBC-S-West ADC Sum

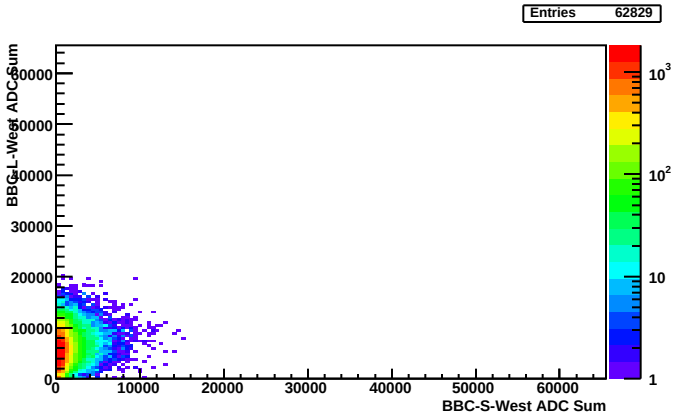

Entries 62829
TOF Mult
VPD-East ADC Sum

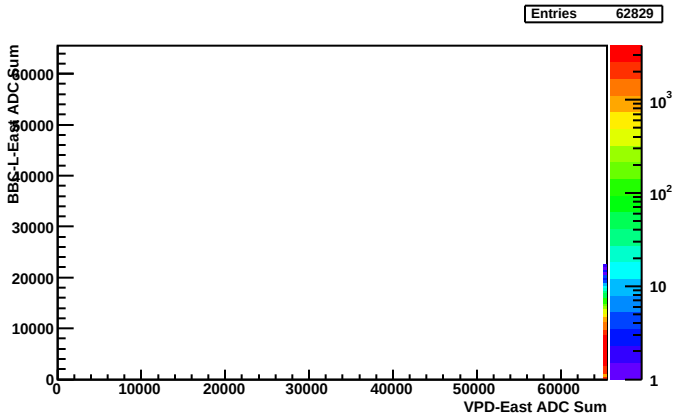

Entries 62829
TOF Mult
VPD-West ADC Sum

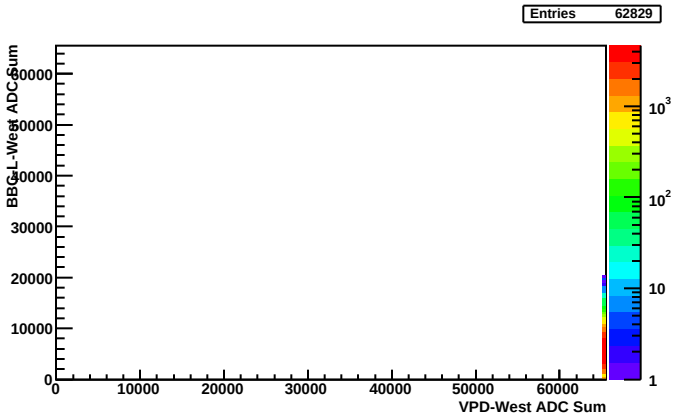

Entries 62829
TOF Mult
ZDC-East ADC Sum Att

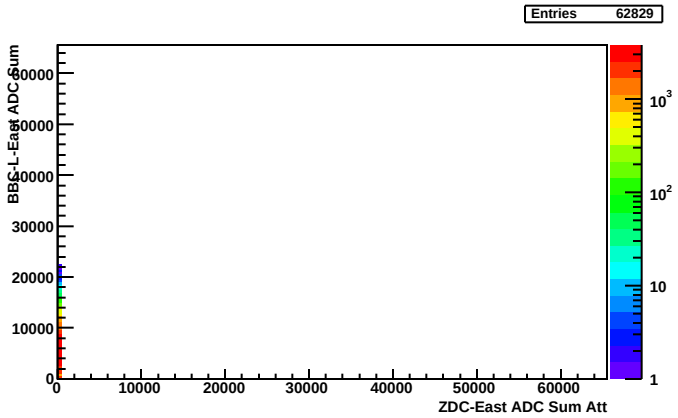

Entries 62829
TOF Mult
ZDC-West ADC Sum Att

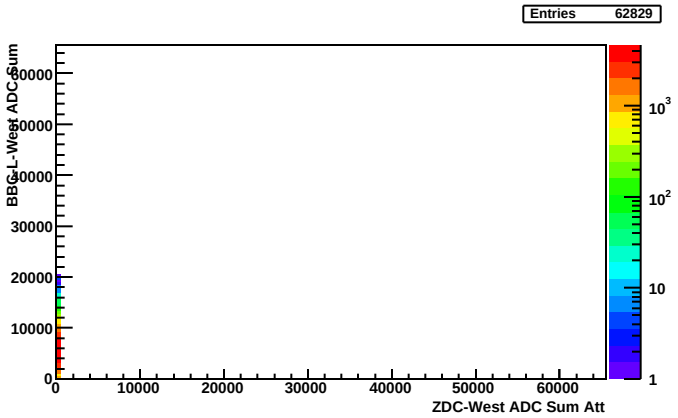

Entries 62829
BBC-L-East ADC Sum
BBC-S-East ADC Sum

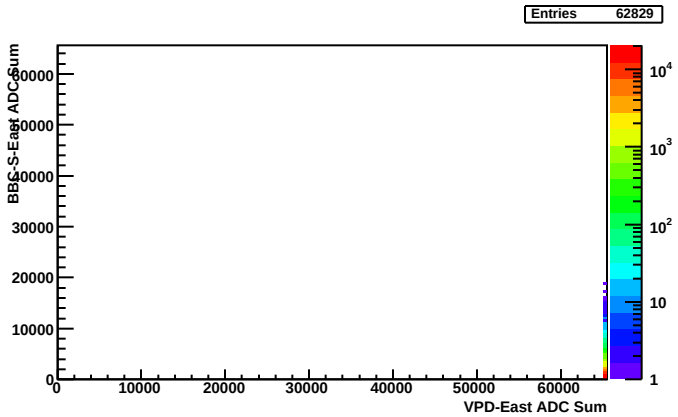

Entries 62829
BBC-L-West ADC Sum
BBC-S-West ADC Sum

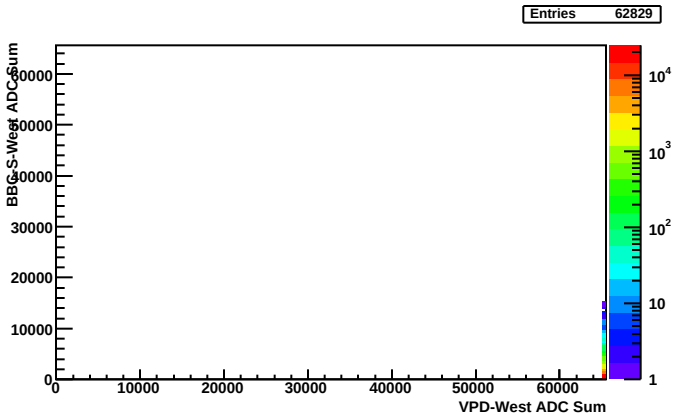

Entries 62829
BBC-L-East ADC Sum
VPD-East ADC Sum

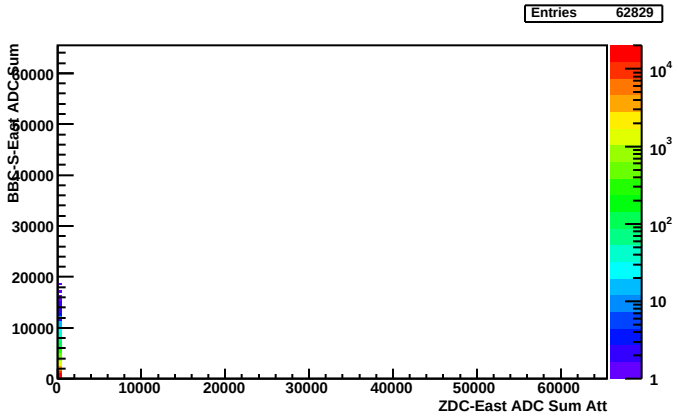

Entries 62829
BBC-L-West ADC Sum
VPD-West ADC Sum

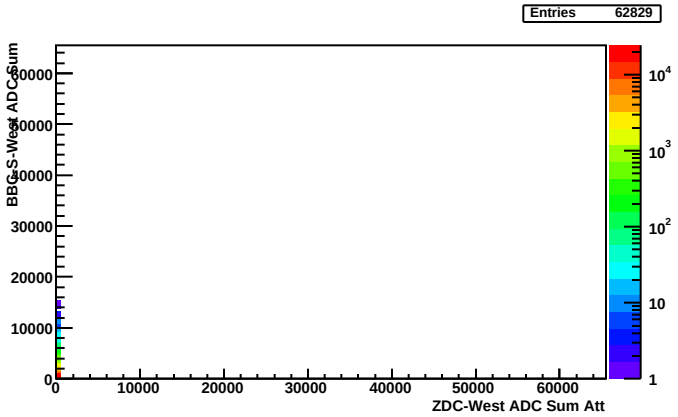

Entries 62829
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

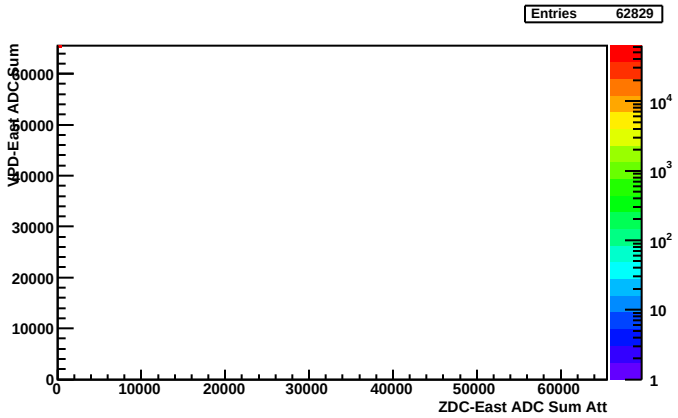

Entries 62829
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

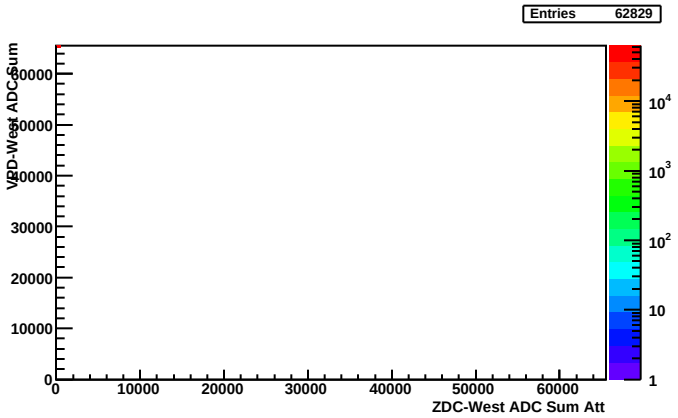

Entries 62829
BBC-S-East ADC Sum
VPD-East ADC Sum

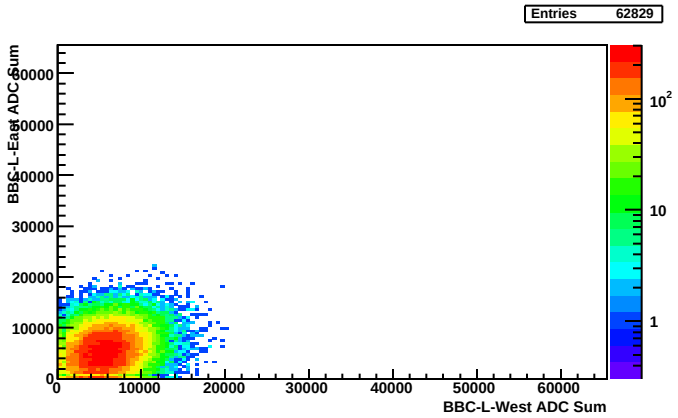

Entries 62829
BBC-S-West ADC Sum
VPD-West ADC Sum

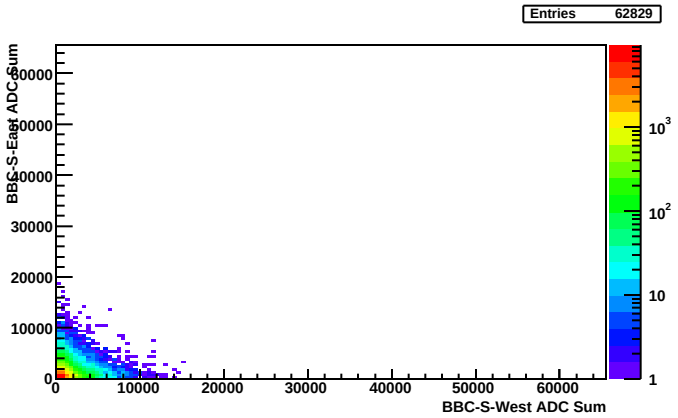

Entries 62829
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

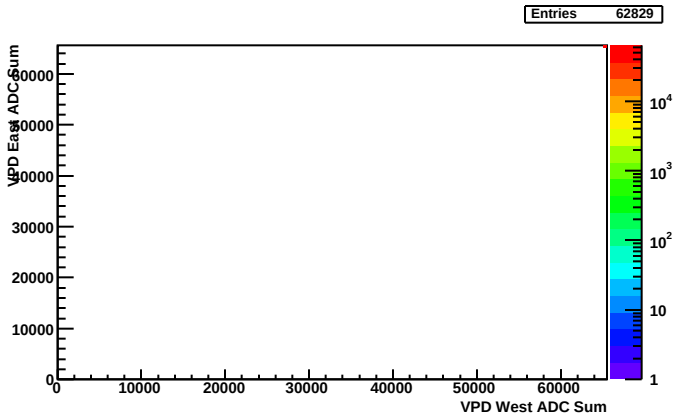

Entries 62829
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

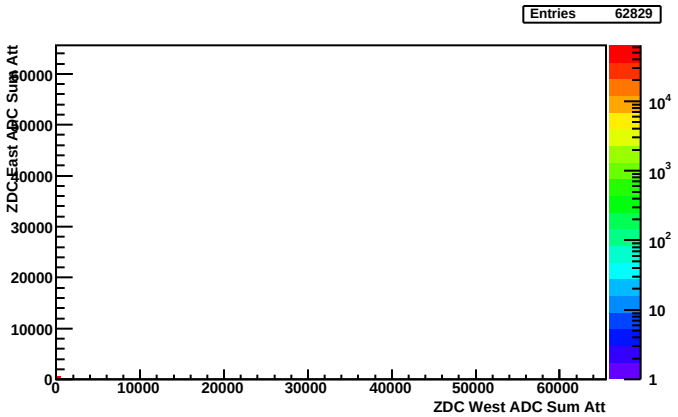

Entries 62829
VPD-East ADC Sum
ZDC-East ADC Sum Att

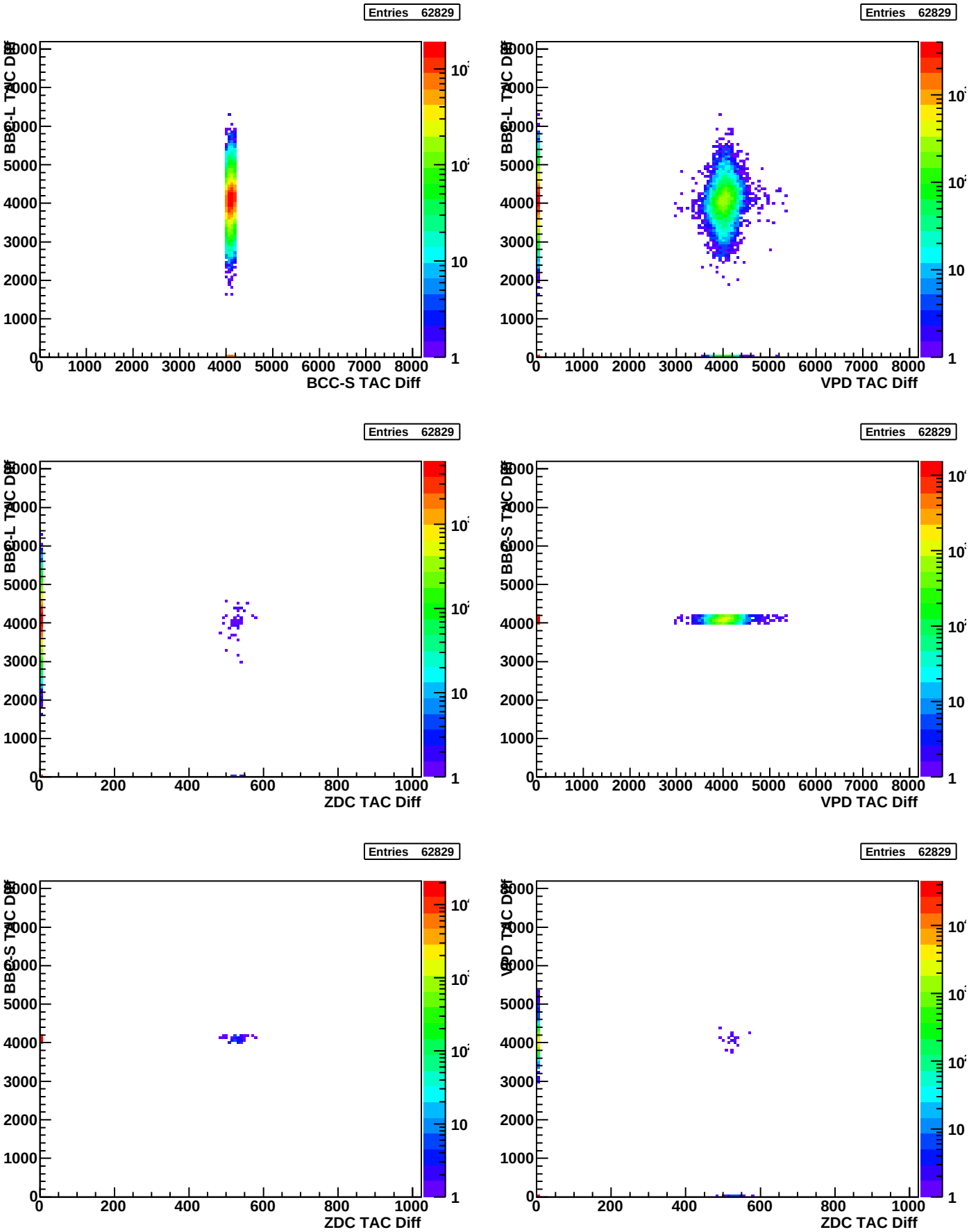

Entries 62829
VPD-West ADC Sum
ZDC-West ADC Sum Att

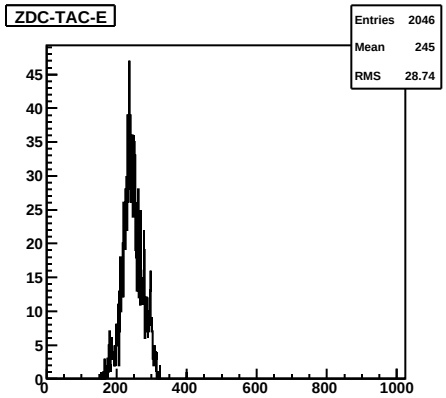

Entries 62829
BBC-L-East ADC Sum
BBC-L-West ADC Sum

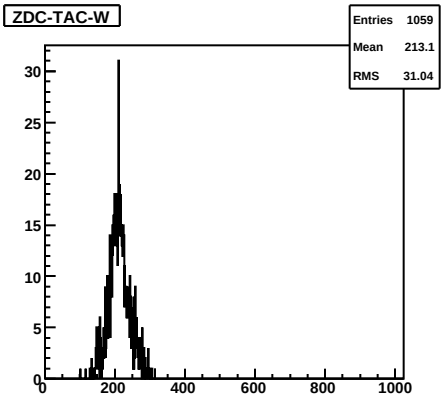

Entries 62829
BBC-S-East ADC Sum
BBC-S-West ADC Sum

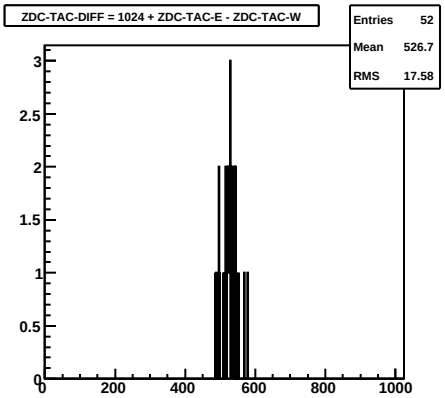

Entries 62829
VPD East ADC Sum
VPD West ADC Sum

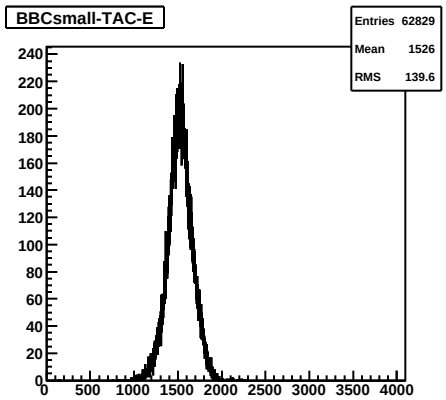

Entries 62829
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

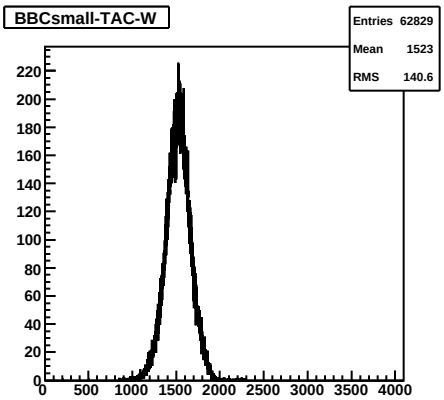

Entries 62829
BBC-L TAC Diff
BCC-S TAC Diff
Entries 62829
BBC-L TAC Diff
VPD TAC Diff
Entries 62829
BBC-L TAC Diff
ZDC TAC Diff
Entries 62829
BBC-S TAC Diff
VPD TAC Diff
Entries 62829
BBC-S TAC Diff
ZDC TAC Diff
Entries 62829
VPD TAC Diff
ZDC TAC Diff

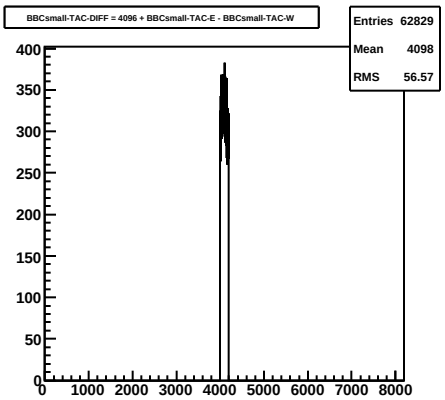

ZDC-TAC-E
Entries 2046
Mean 245
RMS 28.74

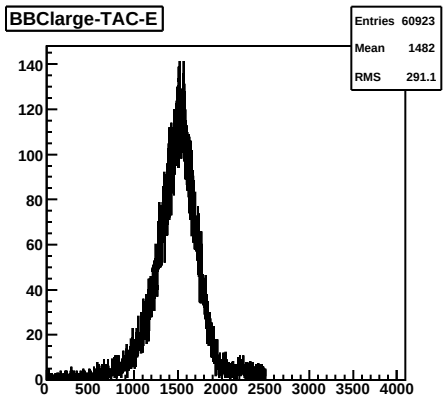

ZDC-TAC-W
Entries 1059
Mean 213.1
RMS 31.04

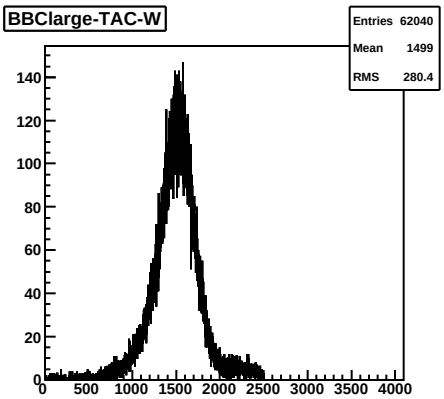

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 52
Mean 526.7
RMS 17.58

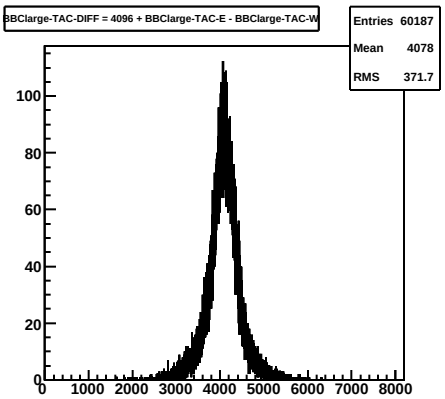

BBCsmall-TAC-E
Entries 62829
Mean 1526
RMS 139.6

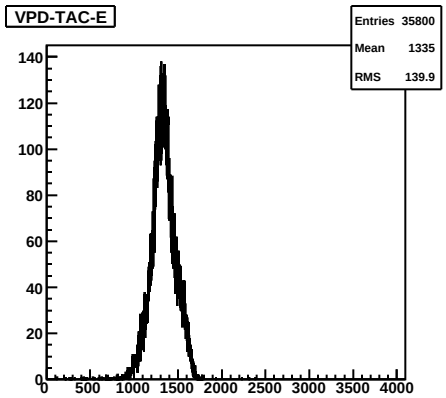

BBCsmall-TAC-W
Entries 62829
Mean 1523
RMS 140.6

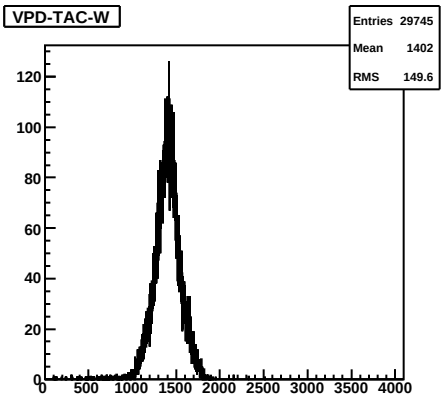

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 62829
Mean 4098
RMS 56.57

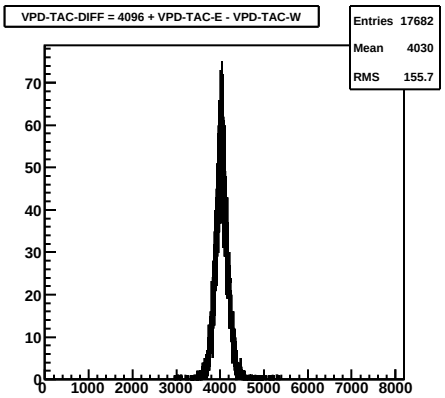

BBClarge-TAC-E
Entries 60923
Mean 1482
RMS 291.1

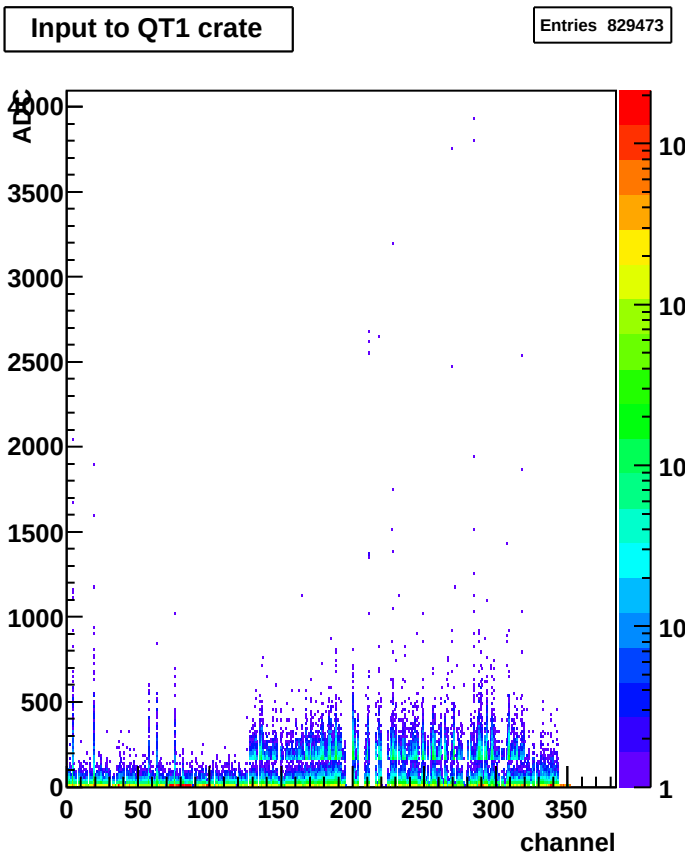

BBClarge-TAC-W
Entries 62040
Mean 1499
RMS 280.4

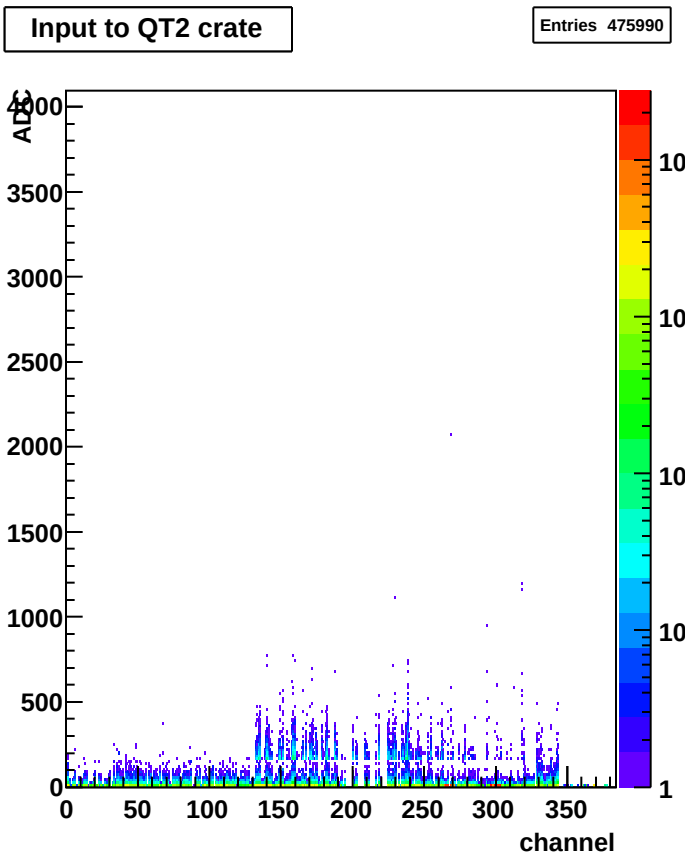

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 60187
Mean 4078
RMS 371.7

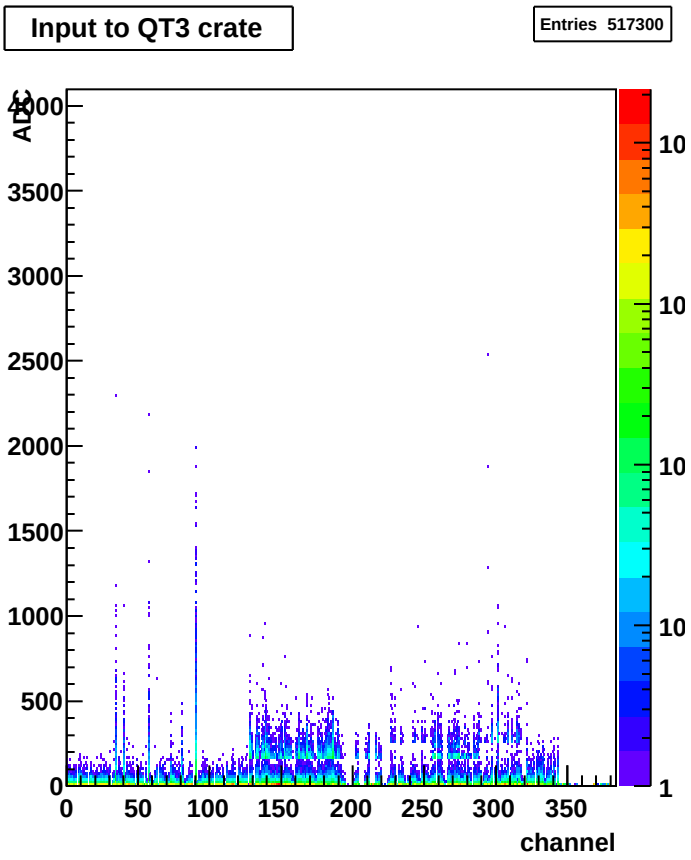

VPD-TAC-E
Entries 35800
Mean 1335
RMS 139.9

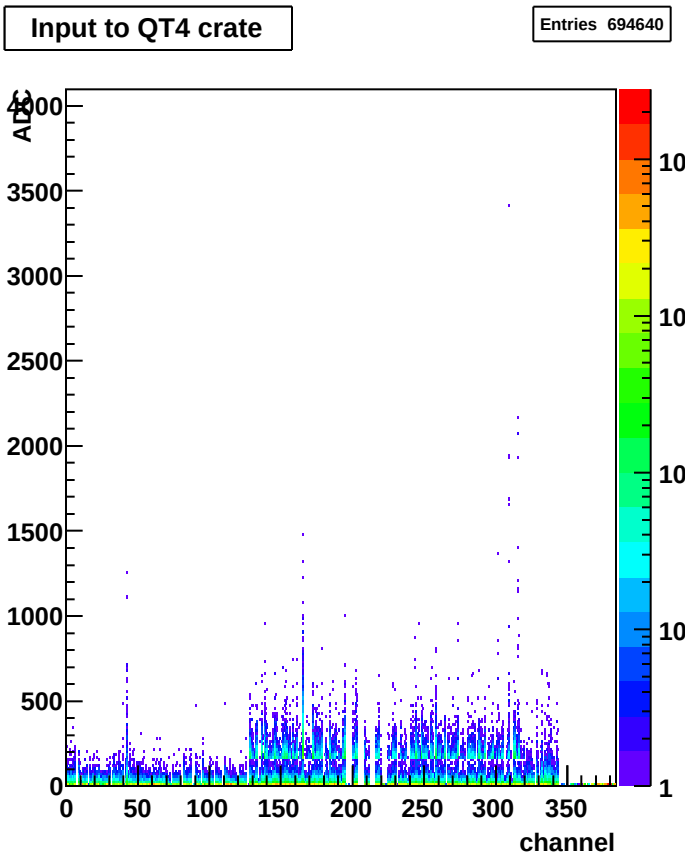

VPD-TAC-W
Entries 29745
Mean 1402
RMS 149.6

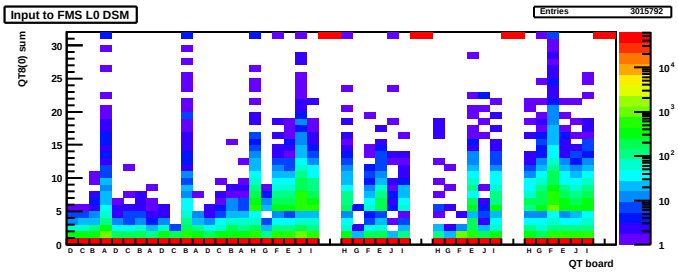

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 17682
Mean 4030
RMS 155.7

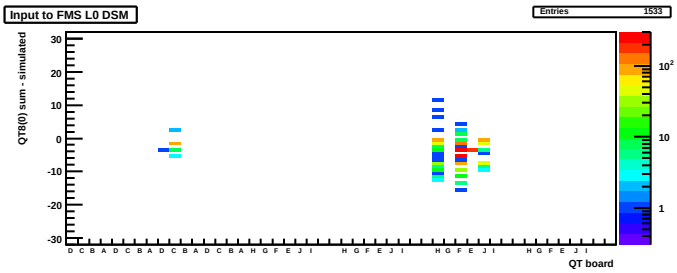

Input to QT1 crate
Entries 829473
ADC
channel

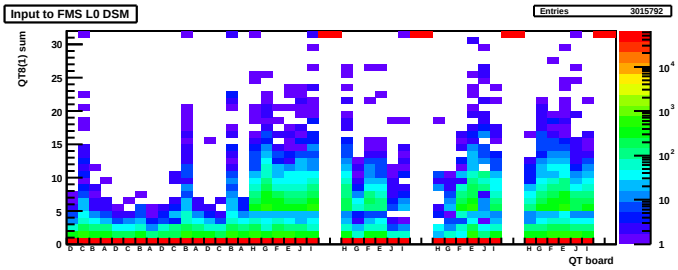

Input to QT2 crate
Entries 475990
ADC
channel

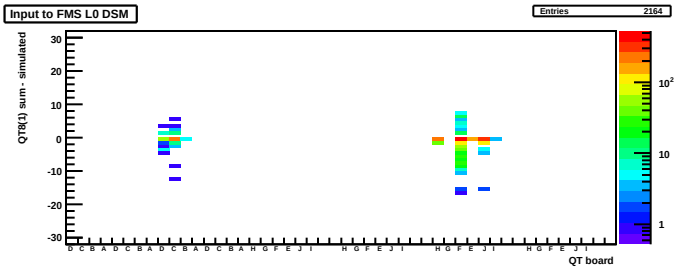

Input to QT3 crate
Entries 517300
ADC
channel

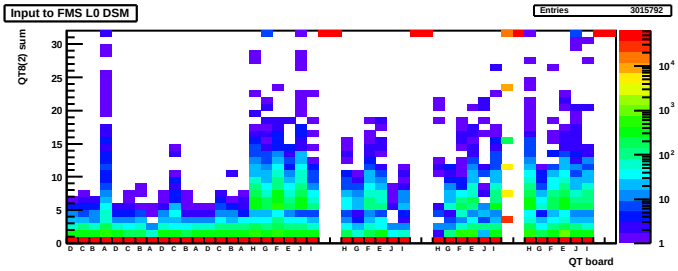

Input to QT4 crate
Entries 694640
ADC
channel

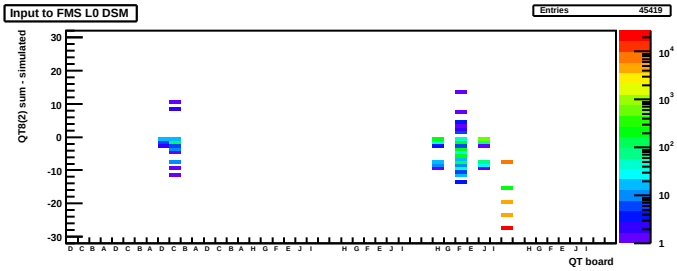

Input to FMS L0 DSM
Entries 3015792
QT8(0) sum
QT board

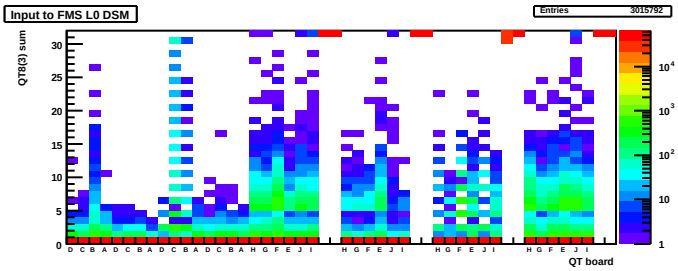

Input to FMS L0 DSM
Entries 1533
QT8(0) sum - simulated
QT board

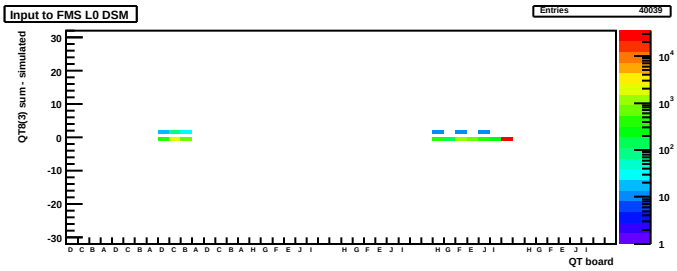

Input to FMS L0 DSM
Entries 3015792
QT8(1) sum
QT board

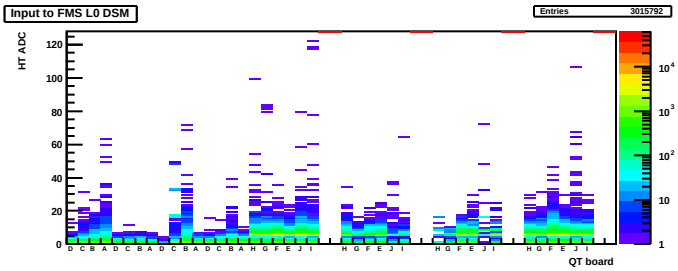

Input to FMS L0 DSM
Entries 2164
QT8(1) sum - simulated
QT board

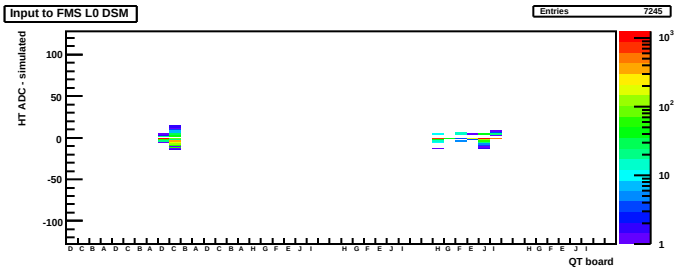

Input to FMS L0 DSM
Entries 3015792
QT8(2) sum
QT board

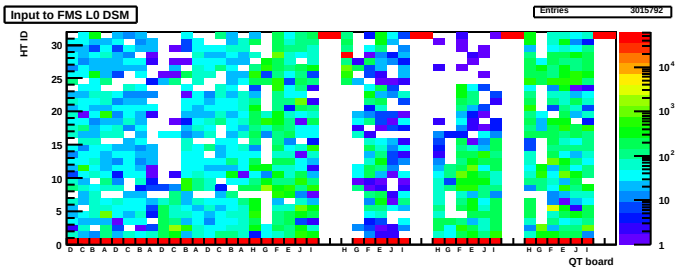

Input to FMS L0 DSM
Entries 45419
QT8(2) sum - simulated
QT board

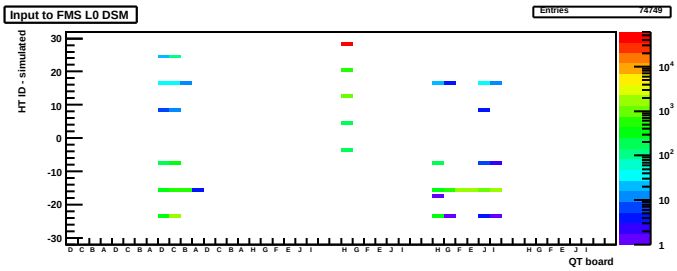

Input to FMS L0 DSM
Entries 3015792
QT8(3) sum
QT board

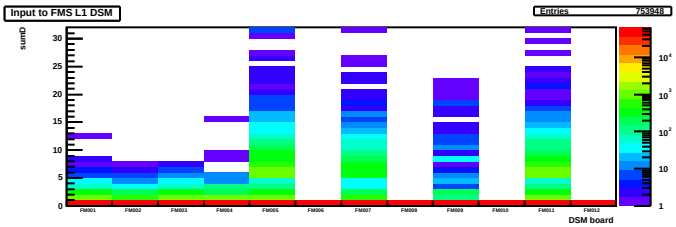

Input to FMS L0 DSM
Entries 40039
QT8(3) sum - simulated
QT board

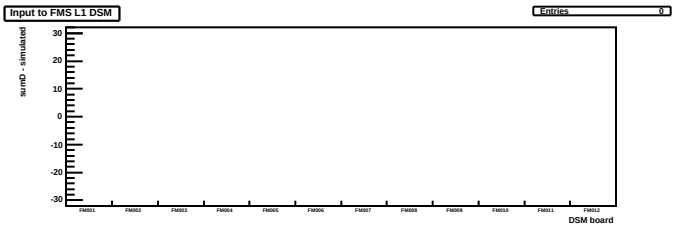

Input to FMS L0 DSM
Entries 3015792
HT ADC
QT board

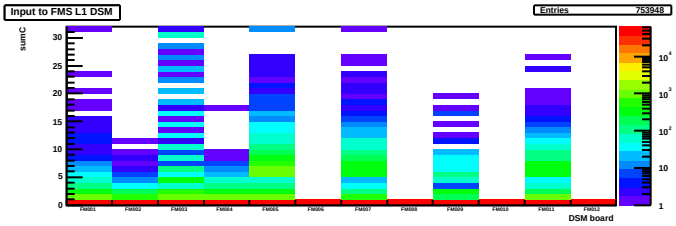

Input to FMS L0 DSM
Entries 7245
HT ADC - simulated
QT board

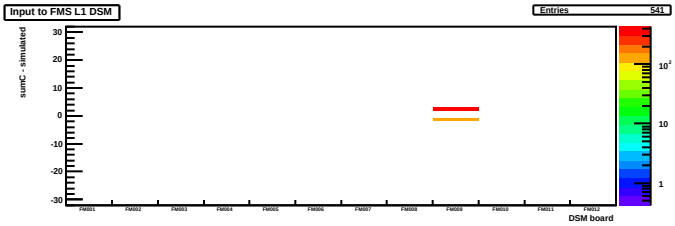

Input to FMS L0 DSM
Entries 3015792
HT ID
QT board

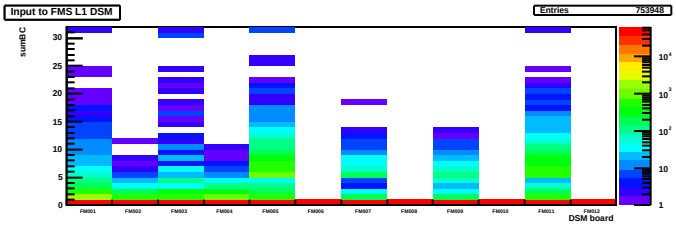

Input to FMS L0 DSM
Entries 74749
HT ID - simulated
QT board

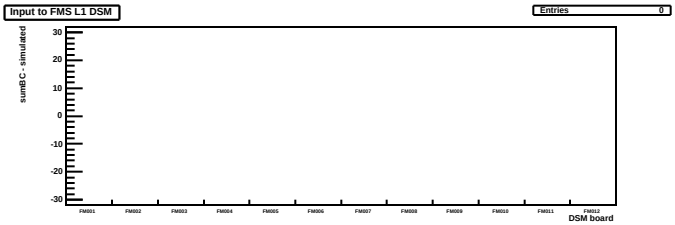

Input to FMS L1 DSM
Entries 753948
sumD
DSM board

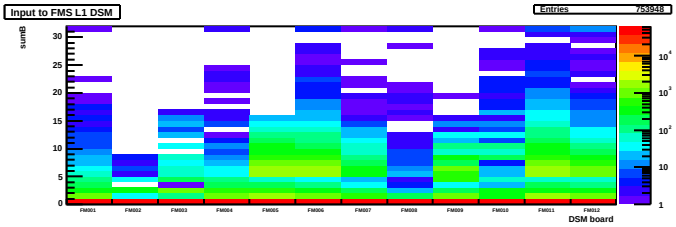

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

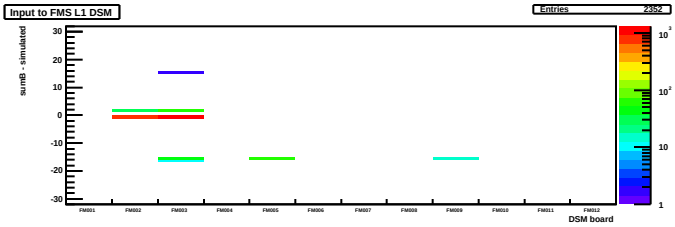

Input to FMS L1 DSM
Entries 753948
sumC
DSM board

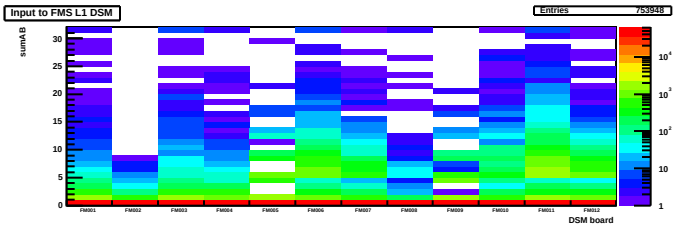

Input to FMS L1 DSM
Entries 541
sumC - simulated
DSM board

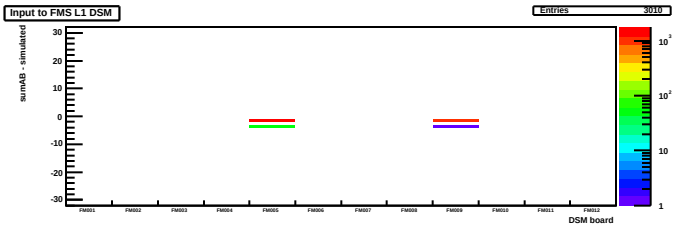

Input to FMS L1 DSM
Entries 753948
sumBC
DSM board

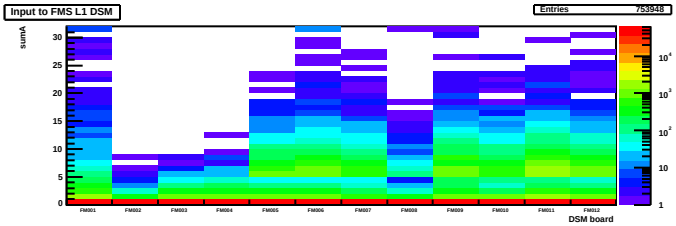

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

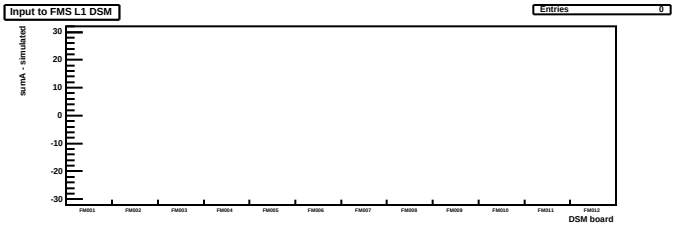

Input to FMS L1 DSM
Entries 753948
sumB
DSM board

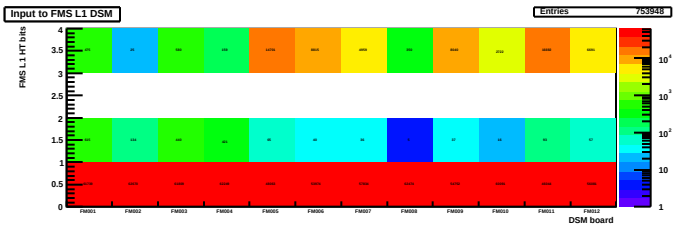

Input to FMS L1 DSM
Entries 2352
sumB - simulated
DSM board

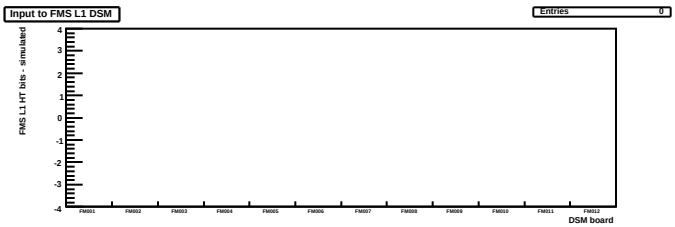

Input to FMS L1 DSM
Entries 753948
sumAB
DSM board

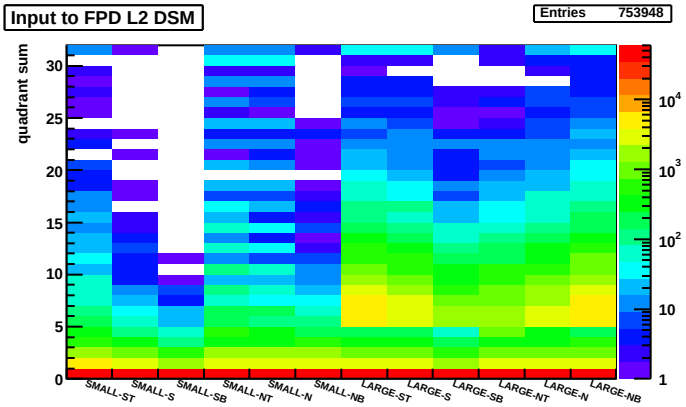

Input to FMS L1 DSM
Entries 3010
sumAB - simulated
DSM board

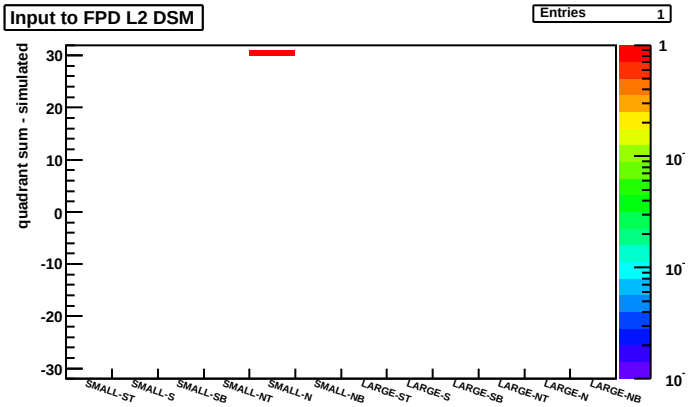

Input to FMS L1 DSM
Entries 753948
sumA
DSM board

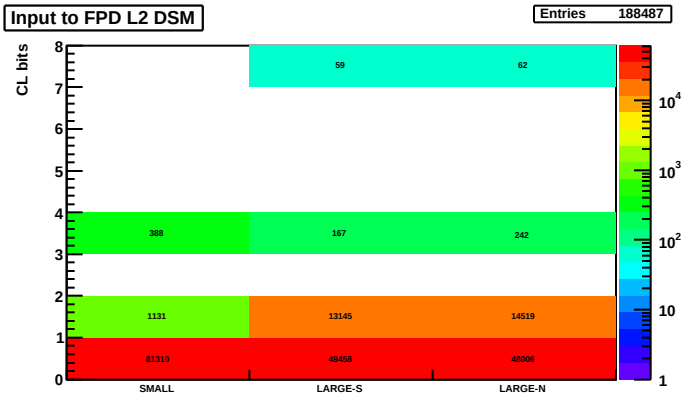

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

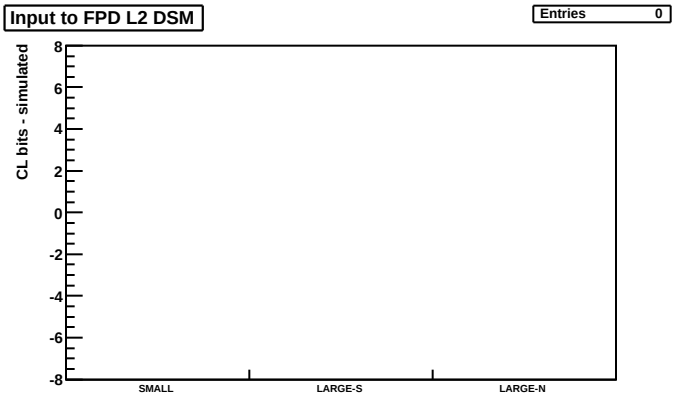

Input to FMS L1 DSM
Entries 753948
FMS L1 HT bits
DSM board

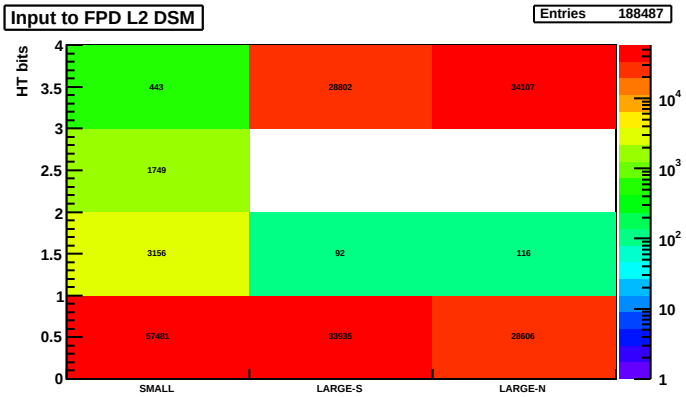

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

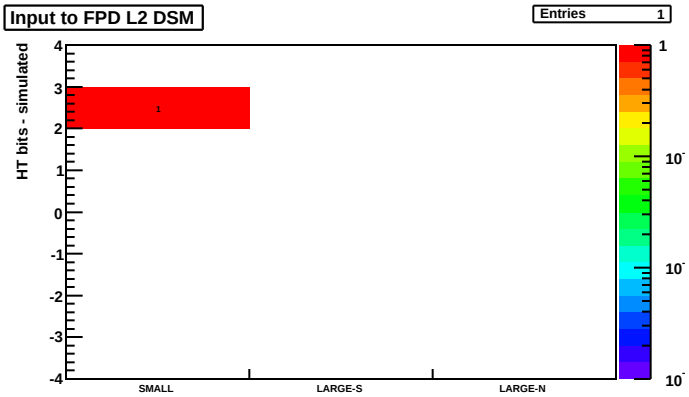

Input to FPD L2 DSM
Entries 753948
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

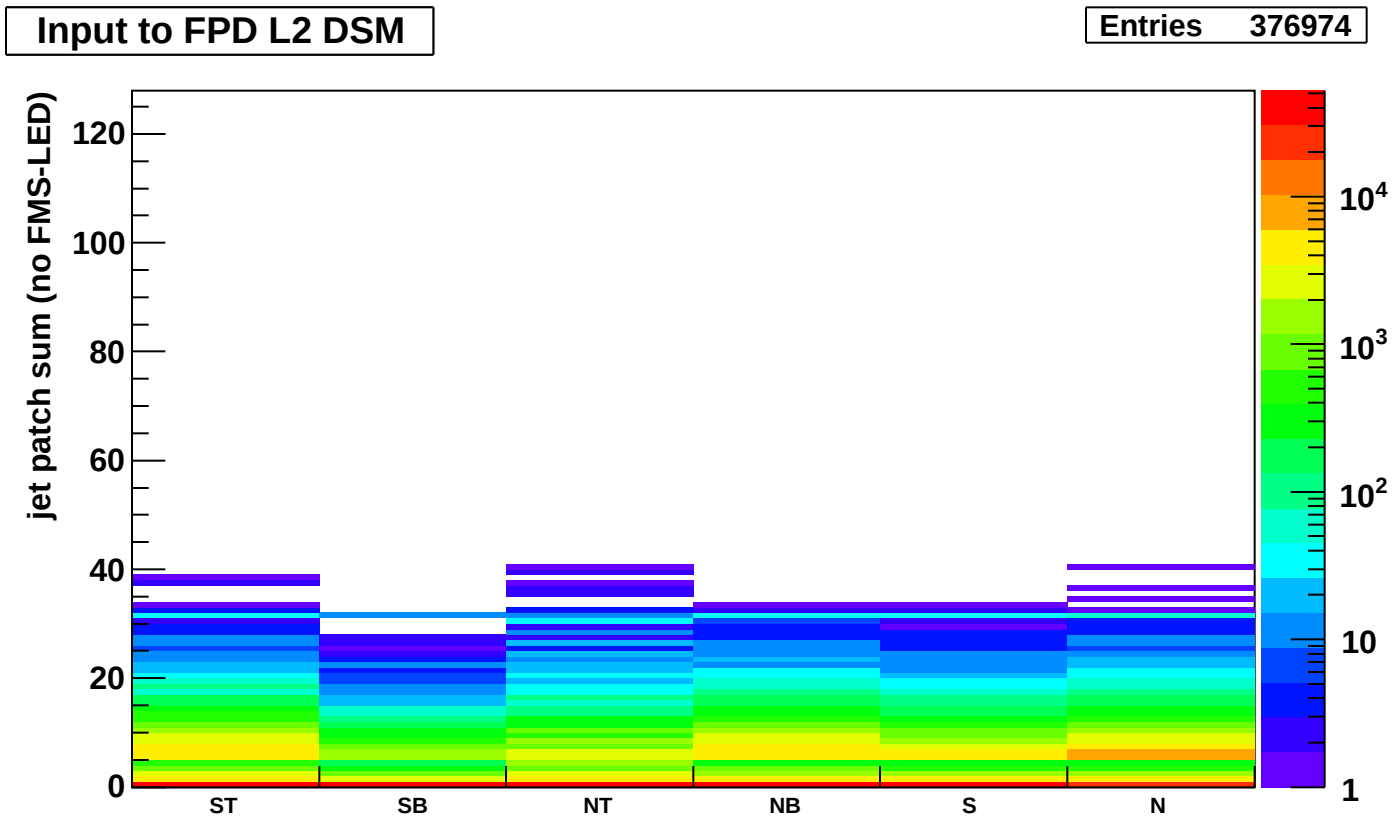

Input to FPD L2 DSM
Entries 1
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

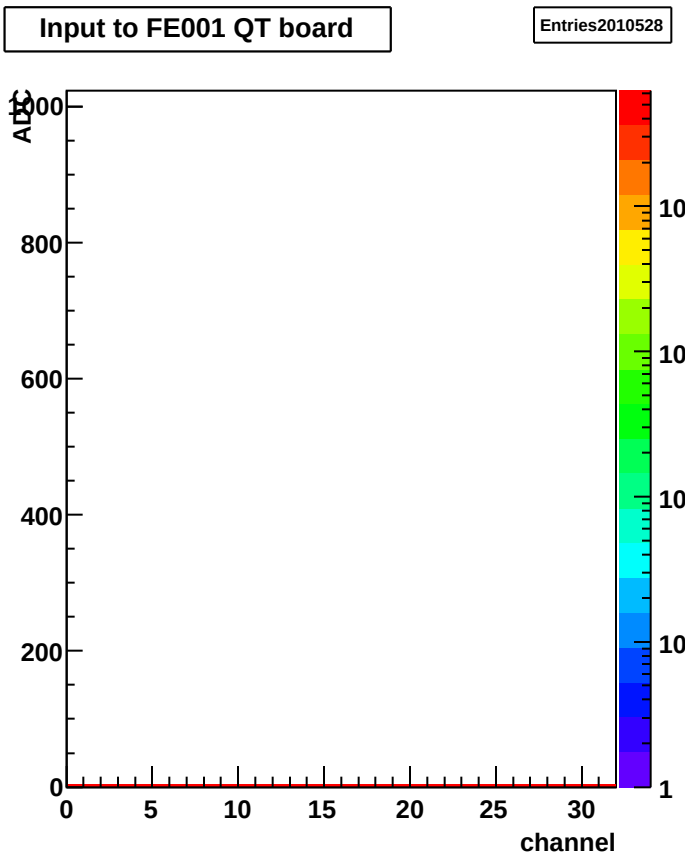

Input to FPD L2 DSM
Entries 188487
CL bits
59
62
388
167
242
1131
13145
14519
61310
49458
48006
SMALL
LARGE-S
LARGE-N

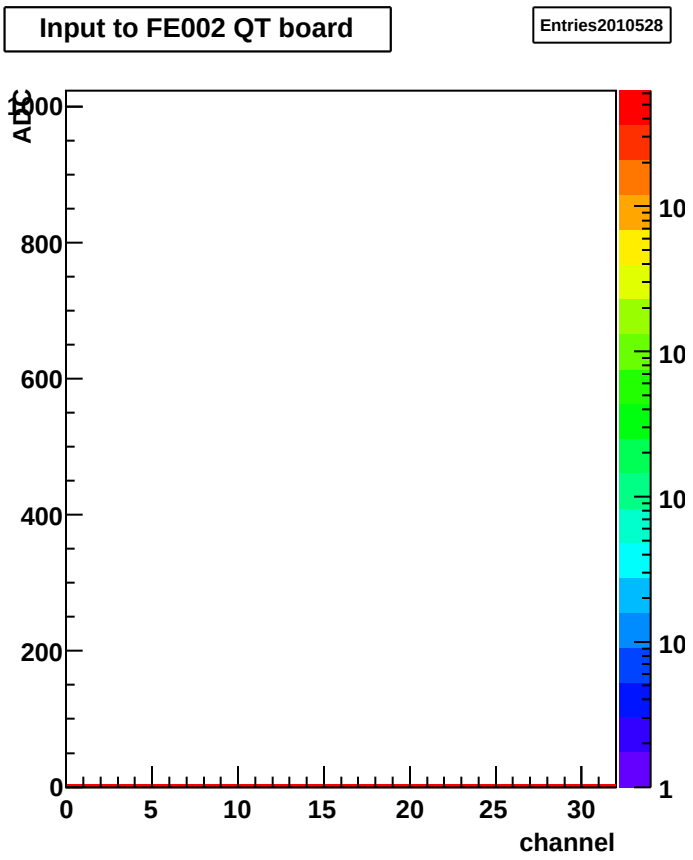

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

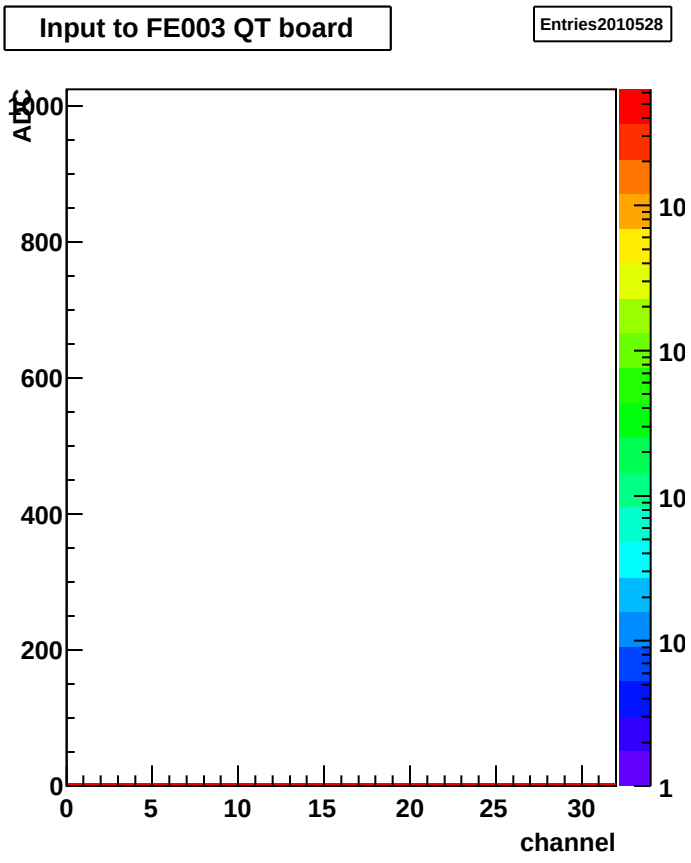

Input to FPD L2 DSM
Entries 188487
HT bits
443
28802
34107
1749
3156
92
116
57481
33935
28606
SMALL
LARGE-S
LARGE-N

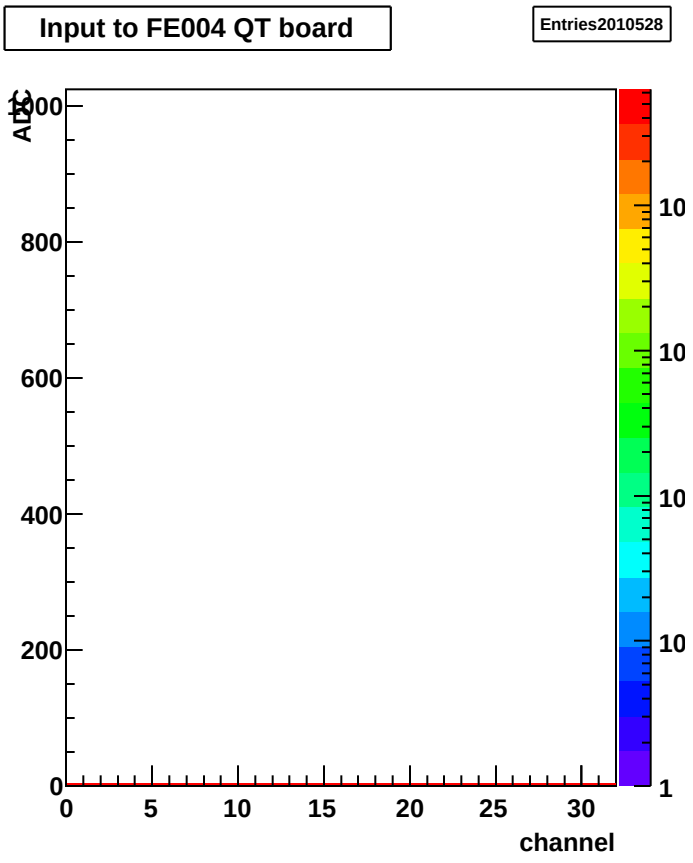

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

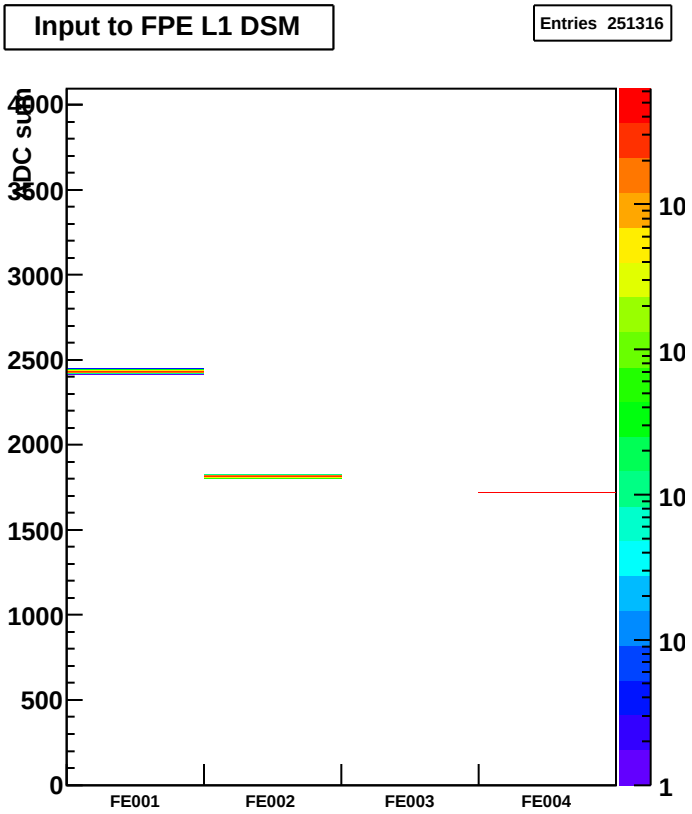

Input to FPD L2 DSM
Entries 376974
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

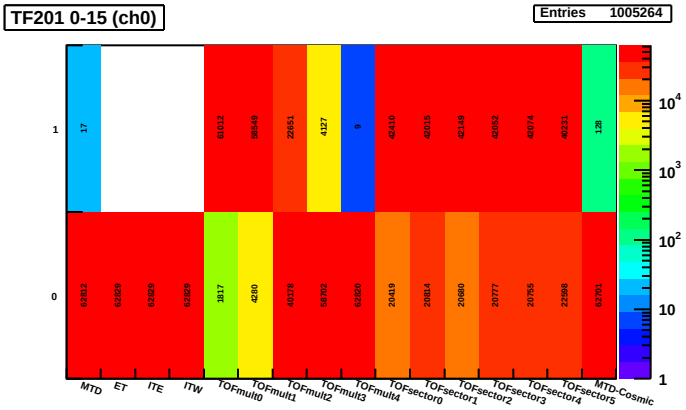

Input to FE001 QT board
Entries2010528
ADC
channel

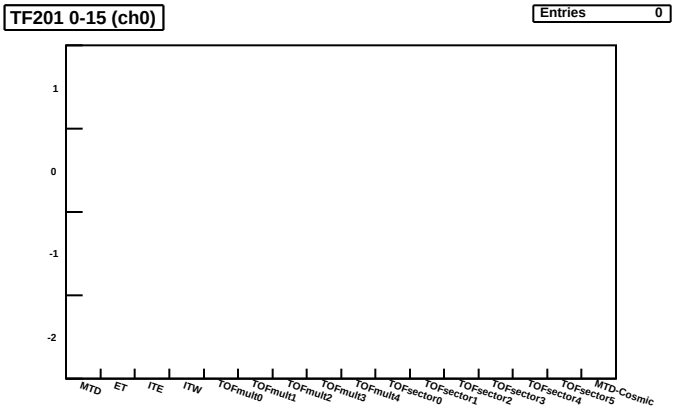

Input to FE002 QT board
Entries2010528
ADC
channel

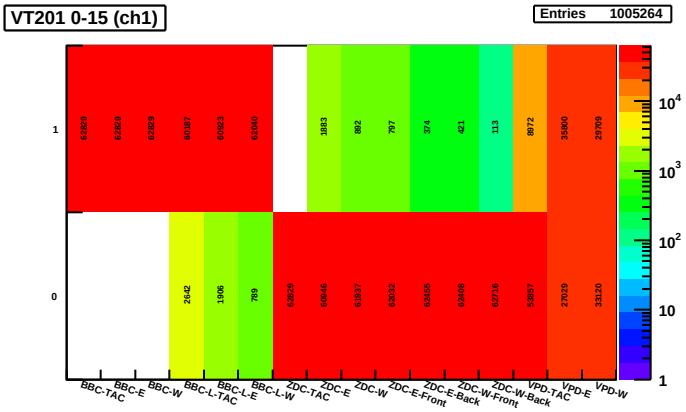

Input to FE003 QT board
Entries2010528
ADC
channel

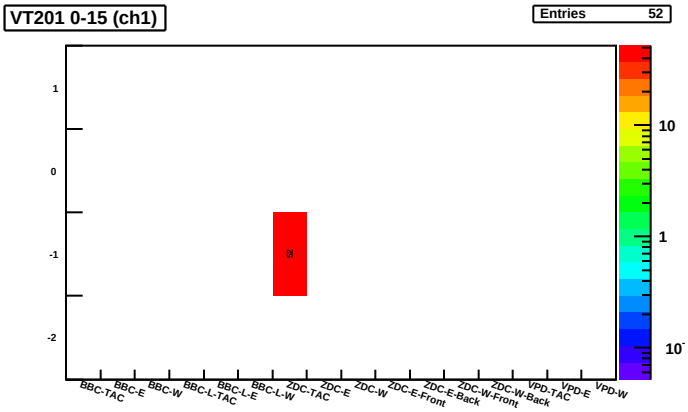

Input to FE004 QT board
Entries2010528
ADC
channel

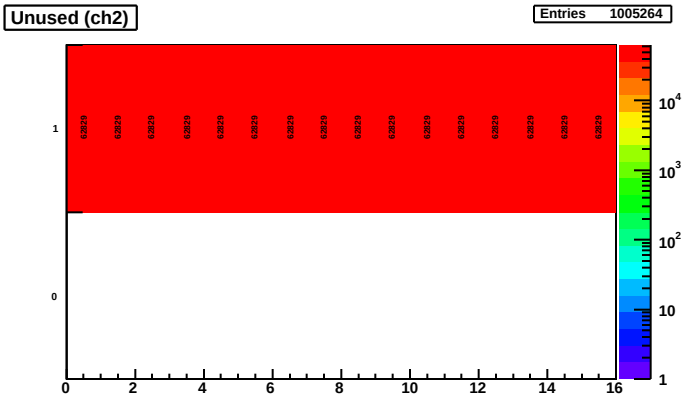

Input to FPE L1 DSM
Entries 251316
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

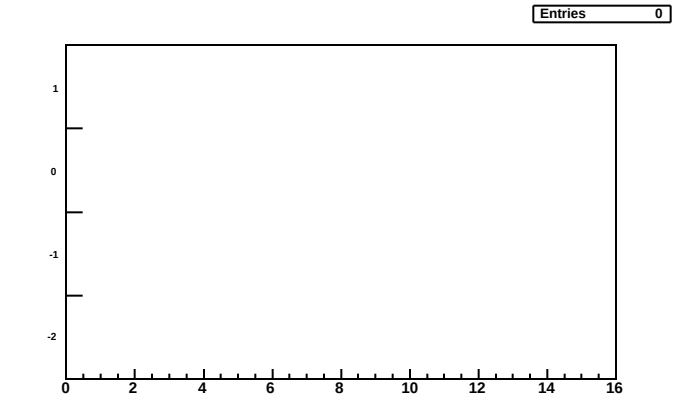

TF201 0-15 (ch0)
Entries 1005264
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

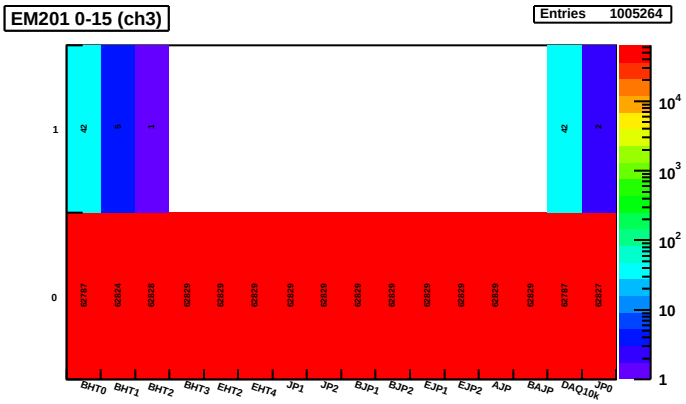

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

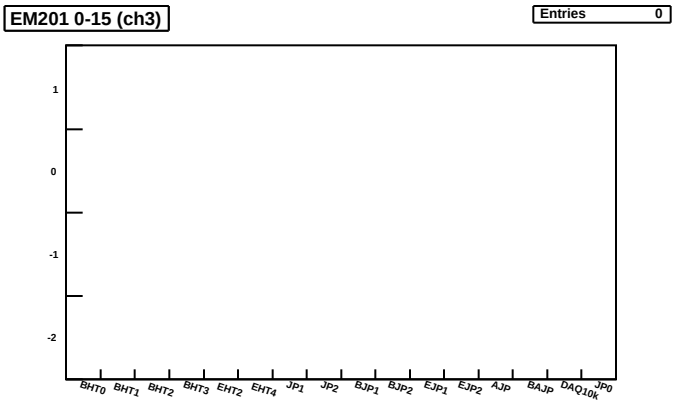

VT201 0-15 (ch1)
Entries 1005264
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 52
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

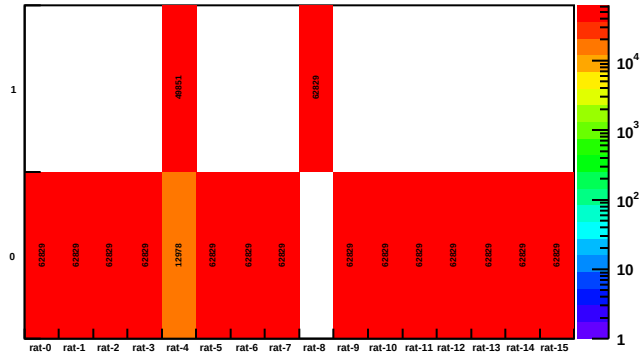

Unused (ch2)
Entries 1005264

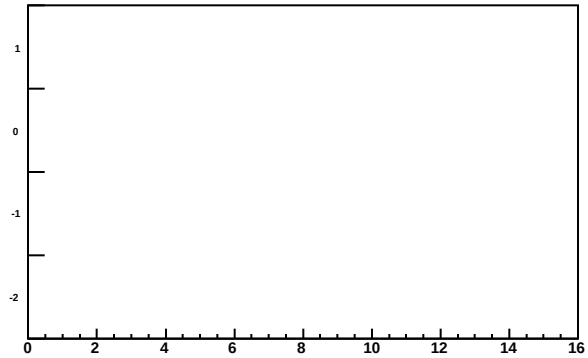

Entries 0


EM201 0-15 (ch3)
Entries 1005264
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

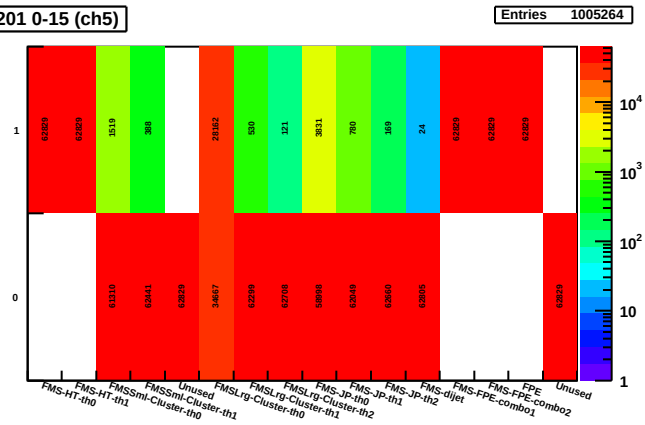

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

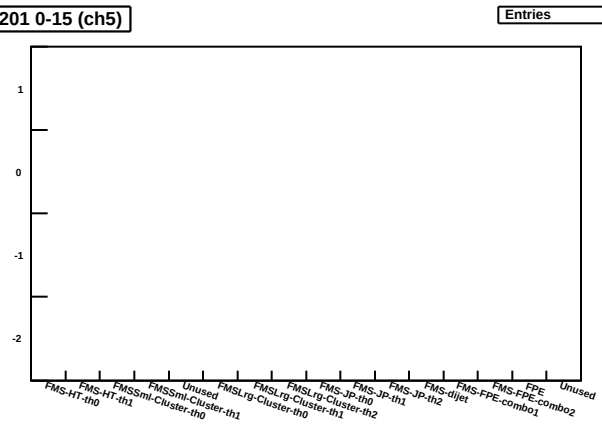




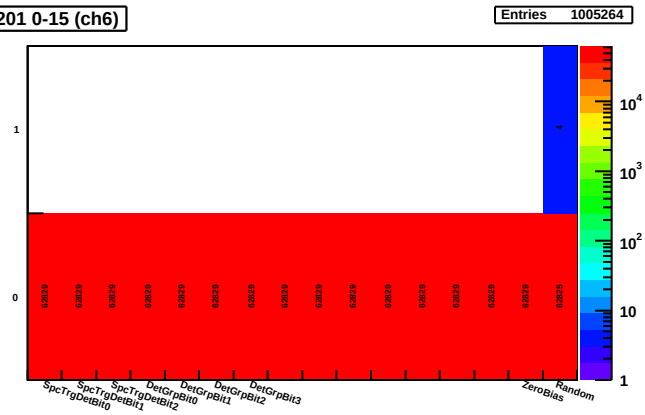
FP201 0-15 (ch5)

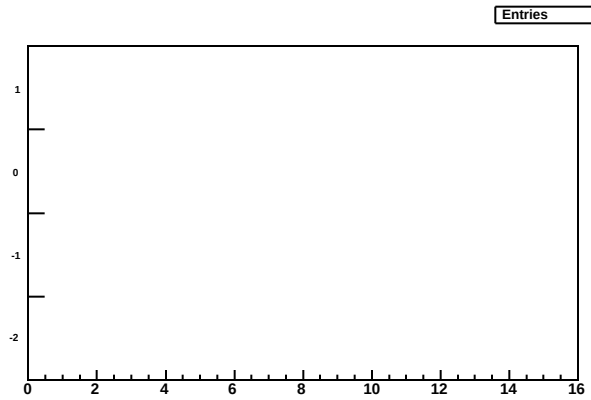
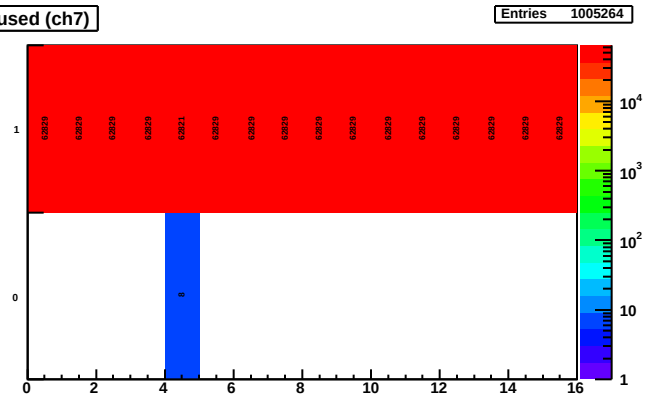
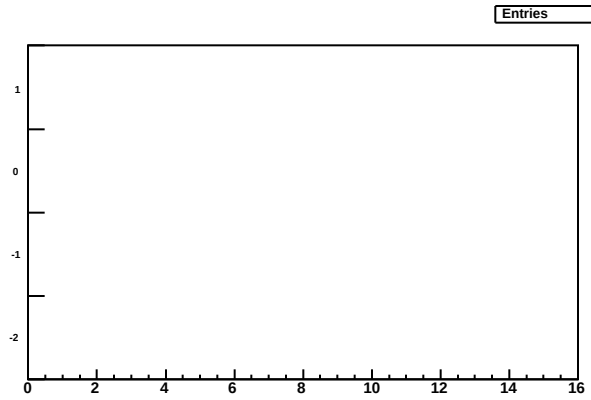


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)