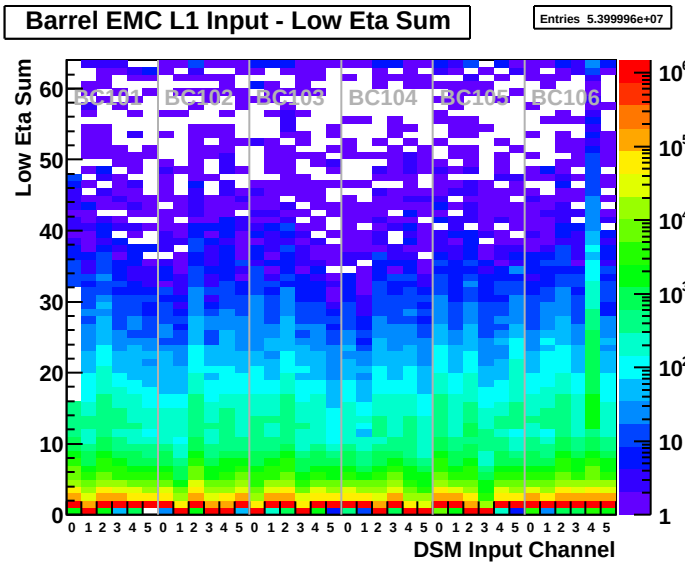
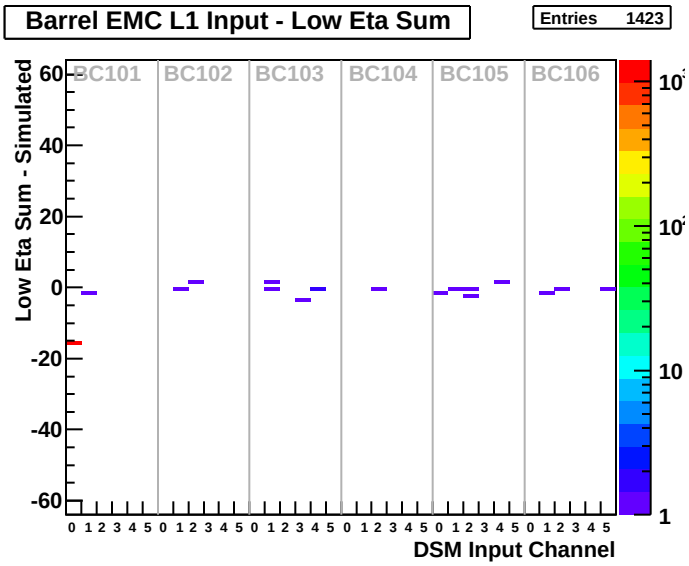
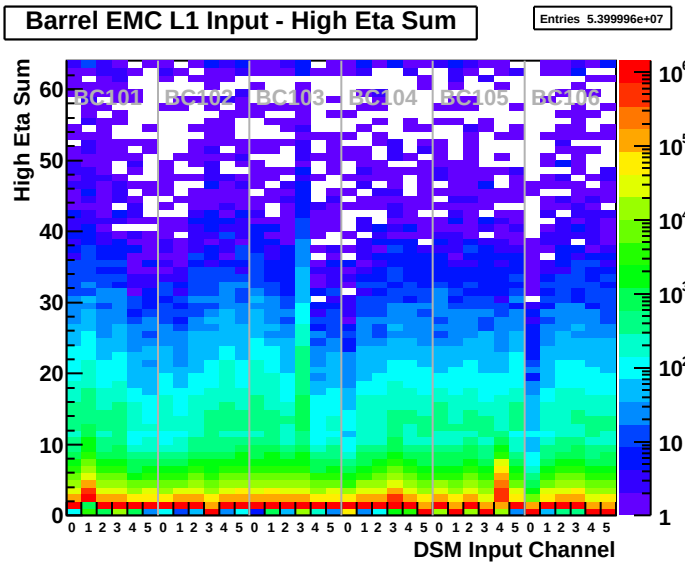
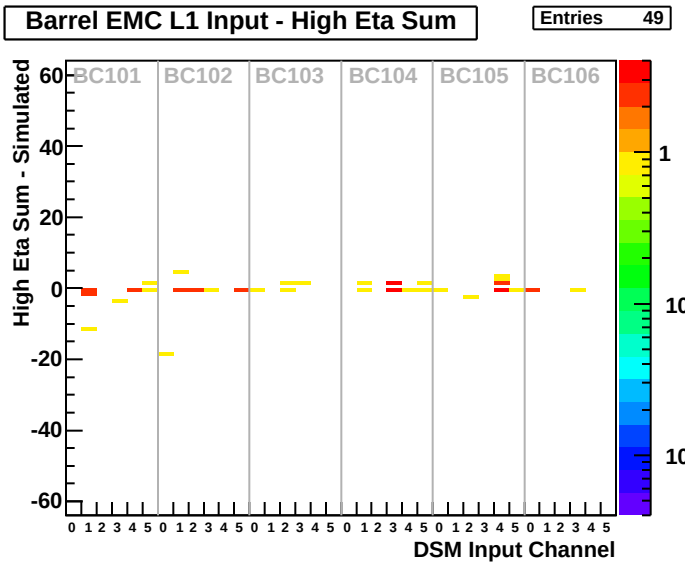
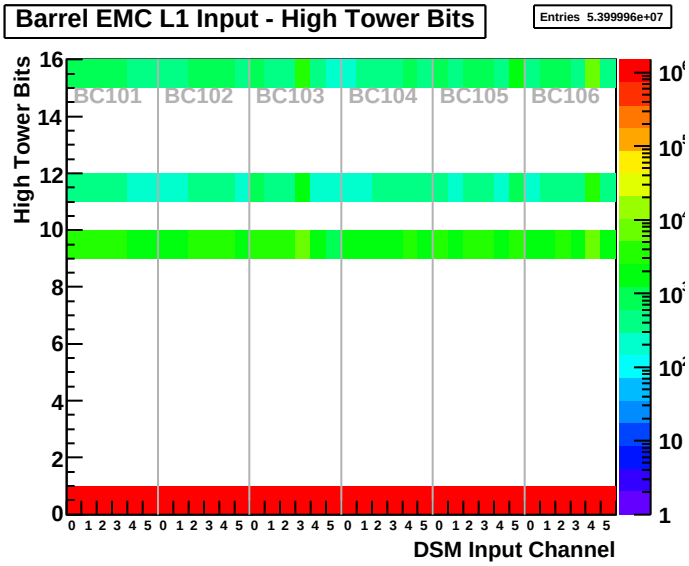
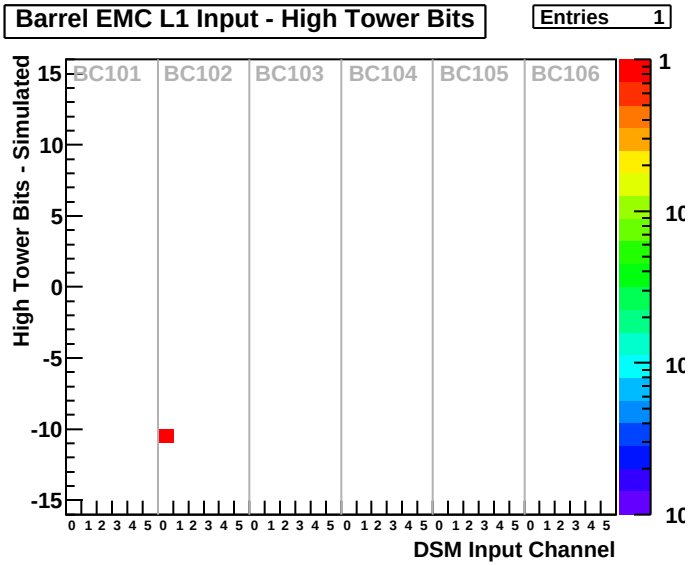

Barrel EMC L1 Input - Low Eta Sum
Entries 5.399996e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

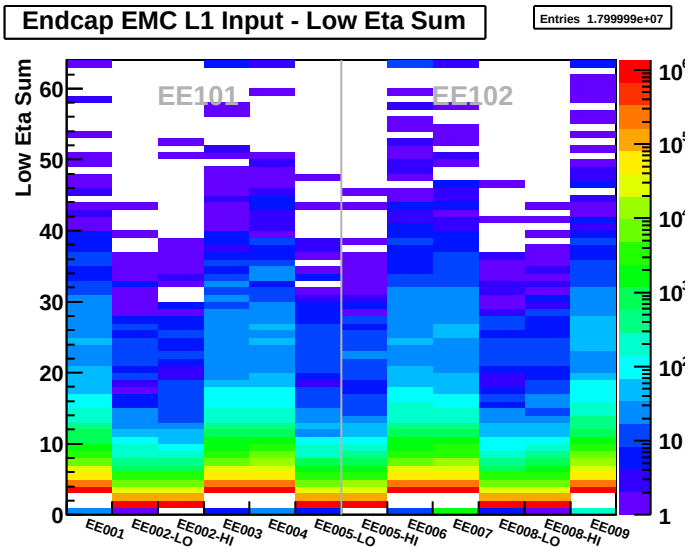

Barrel EMC L1 Input - Low Eta Sum
Entries 1423
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

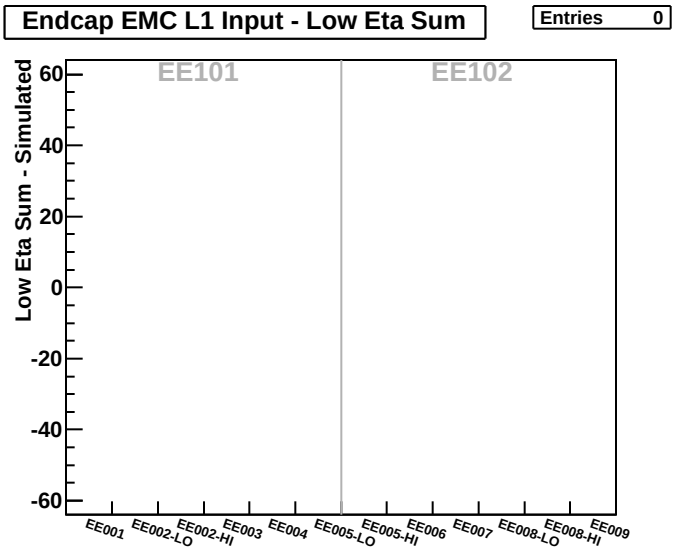

Barrel EMC L1 Input - High Eta Sum
Entries 5.399996e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

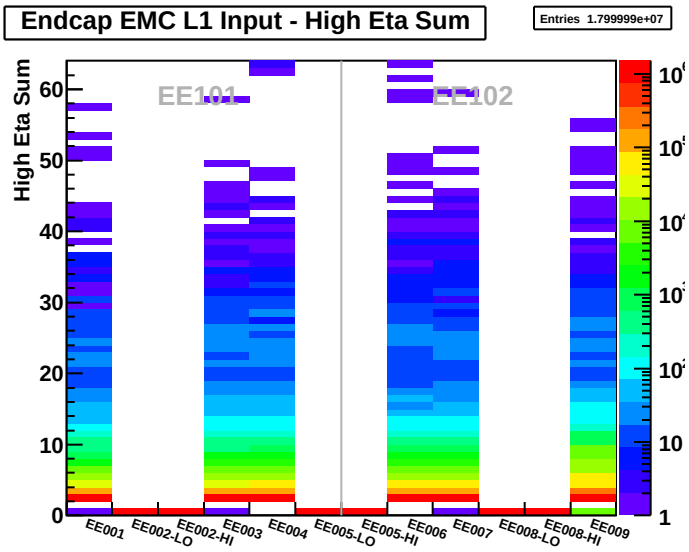

Barrel EMC L1 Input - High Eta Sum
Entries 49
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

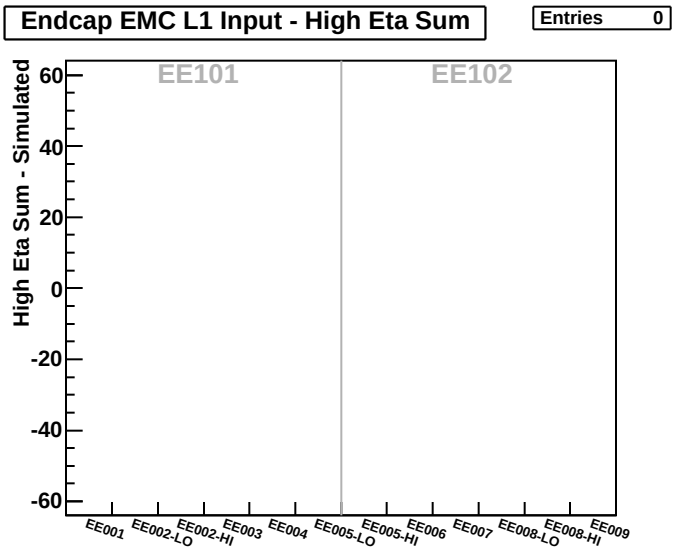

Barrel EMC L1 Input - High Tower Bits
Entries 5.399996e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

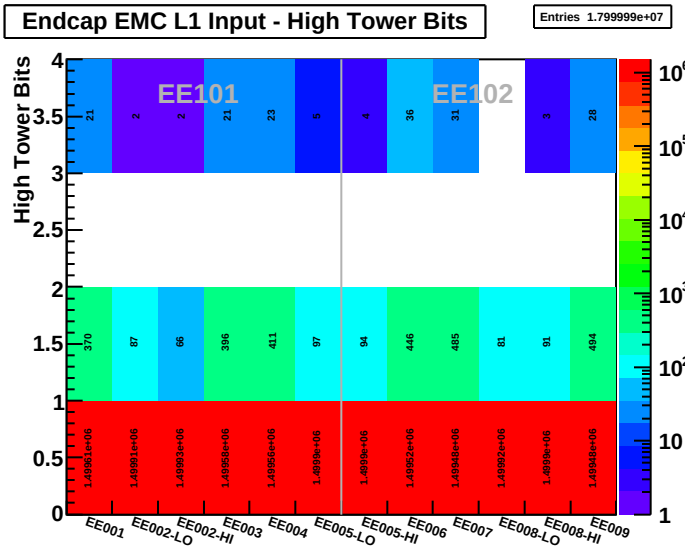

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

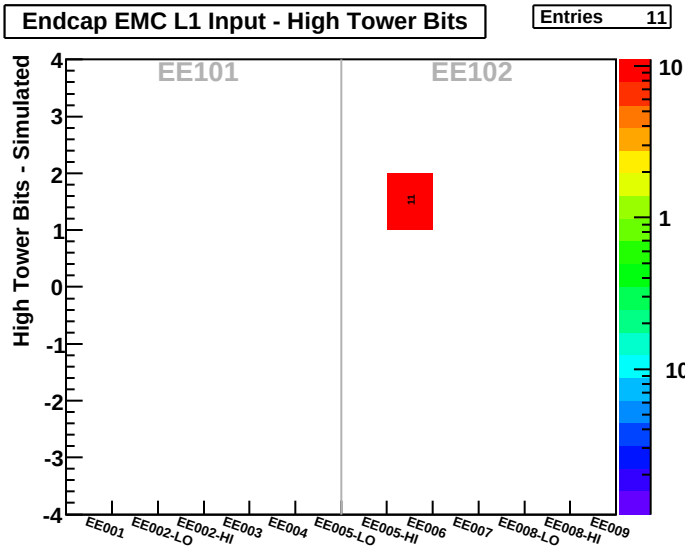

Endcap EMC L1 Input - Low Eta Sum
Entries 1.799999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

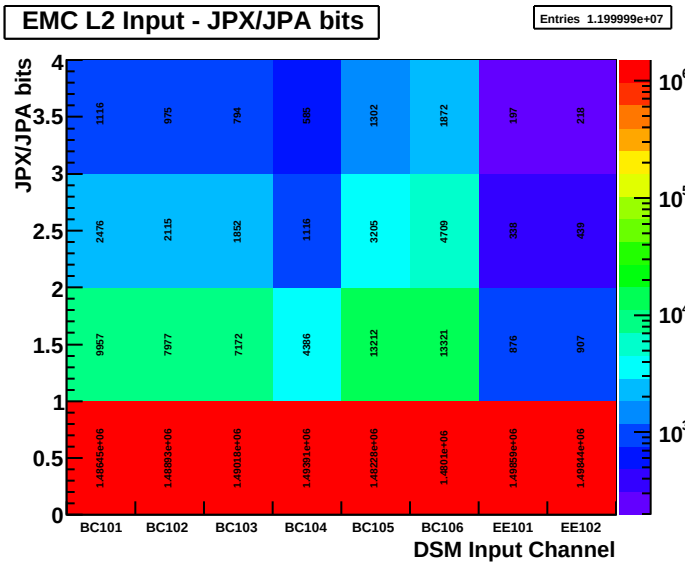

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

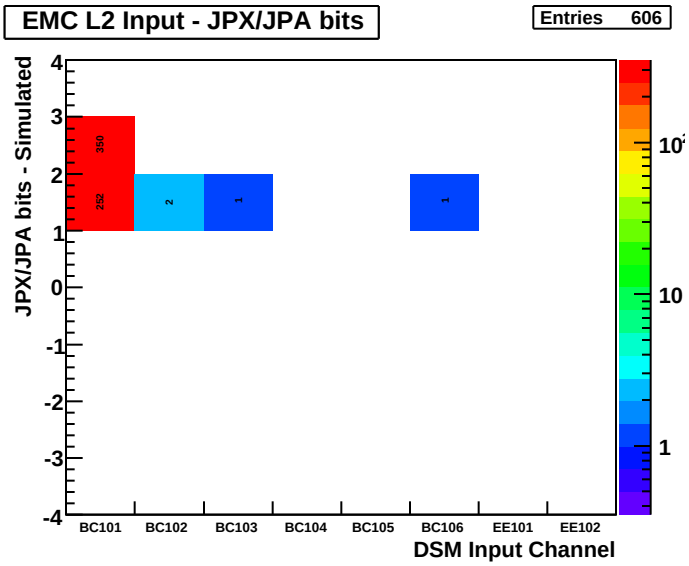

Endcap EMC L1 Input - High Eta Sum
Entries 1.799999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

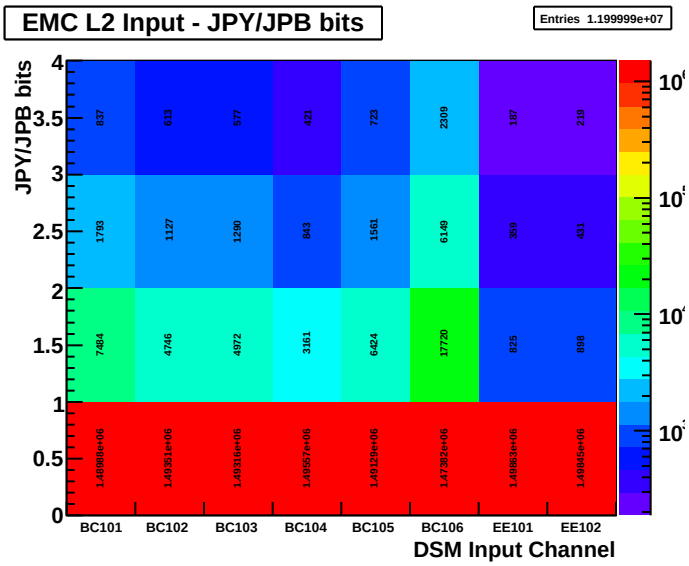

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

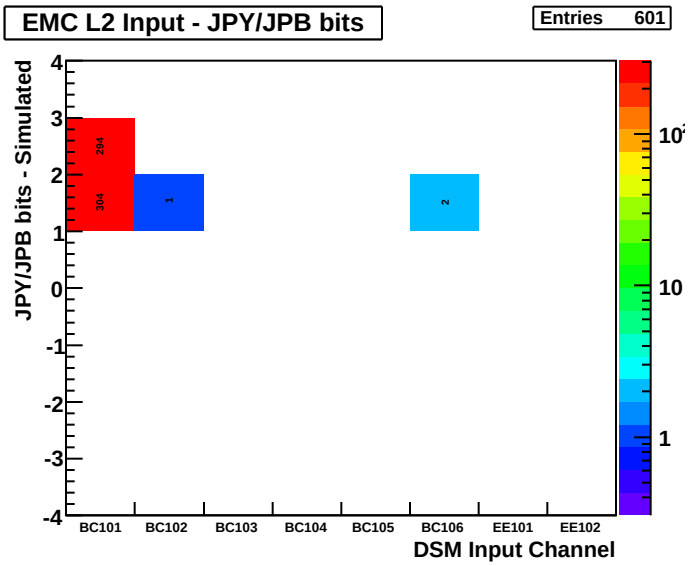

Endcap EMC L1 Input - High Tower Bits
Entries 1.799999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

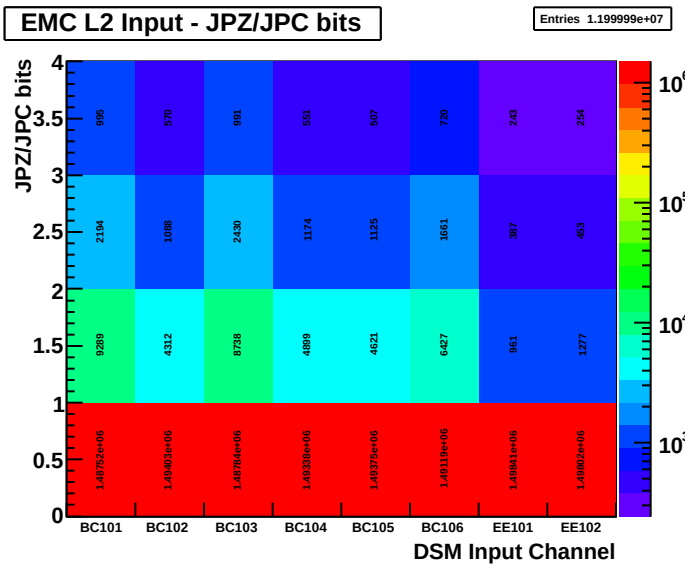

Endcap EMC L1 Input - High Tower Bits
Entries 11
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

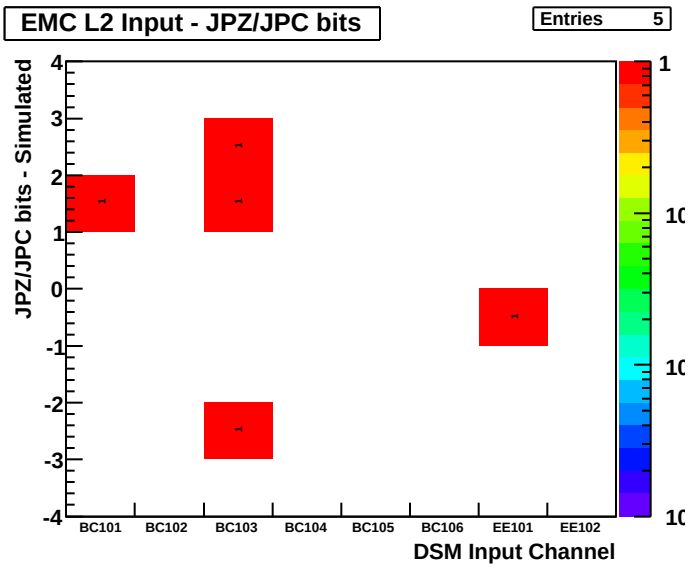

EMC L2 Input - JPX/JPA bits
Entries 1.199999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1116 975 794 585 1302 1872 197 218
2476 2115 1852 1116 3205 4709 338 439
9957 7977 7172 4386 13212 13321 876 907
1.48645e+06 1.48833e+06 1.49018e+06 1.49391e+06 1.48228e+06 1.4891e+06 1.49859e+06 1.49844e+06

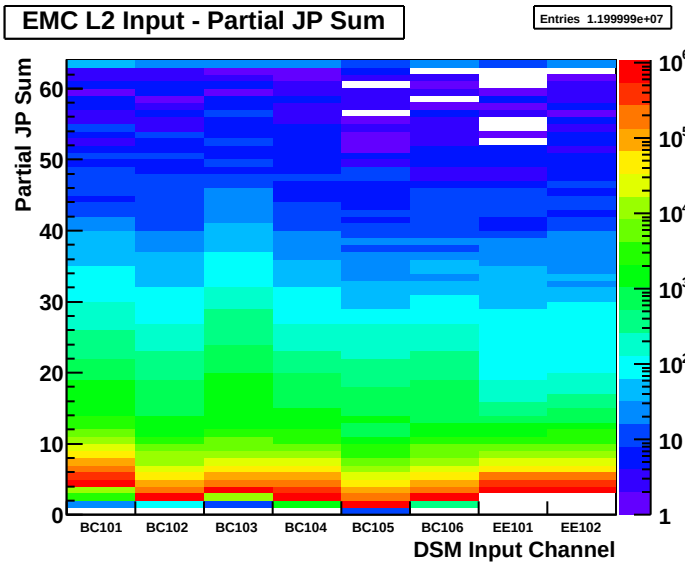

EMC L2 Input - JPX/JPA bits
Entries 606
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
350
252
2
1
1

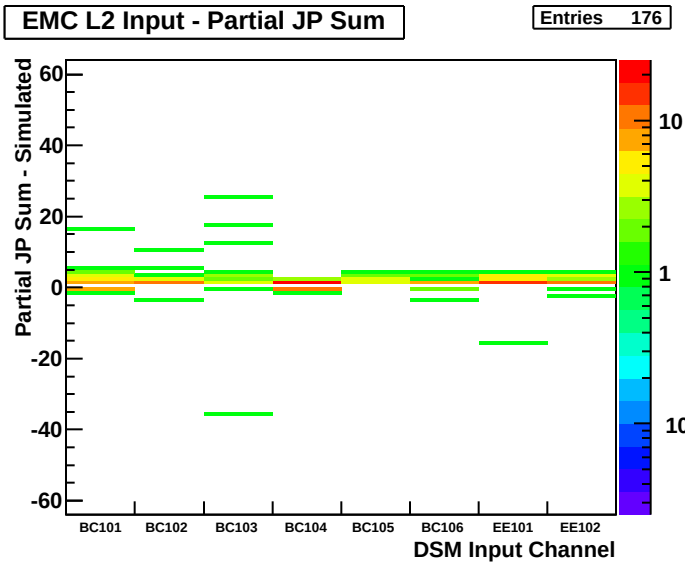

EMC L2 Input - JPY/JPB bits
Entries 1.199999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
837 613 577 421 723 2309 187 219
1793 1127 1290 843 1561 6149 359 431
7484 4746 4972 3161 6424 17720 825 898
1.48988e+06 1.49351e+06 1.49316e+06 1.49557e+06 1.49129e+06 1.47392e+06 1.49883e+06 1.49845e+06

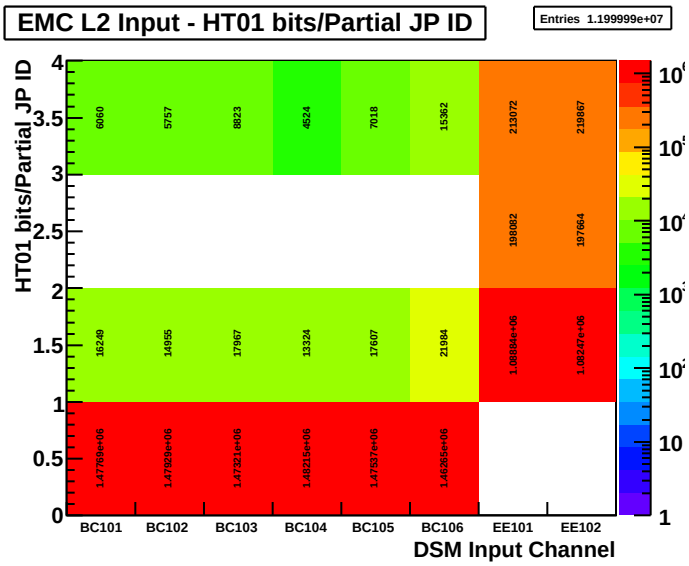

EMC L2 Input - JPY/JPB bits
Entries 601
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
294
304
1
2

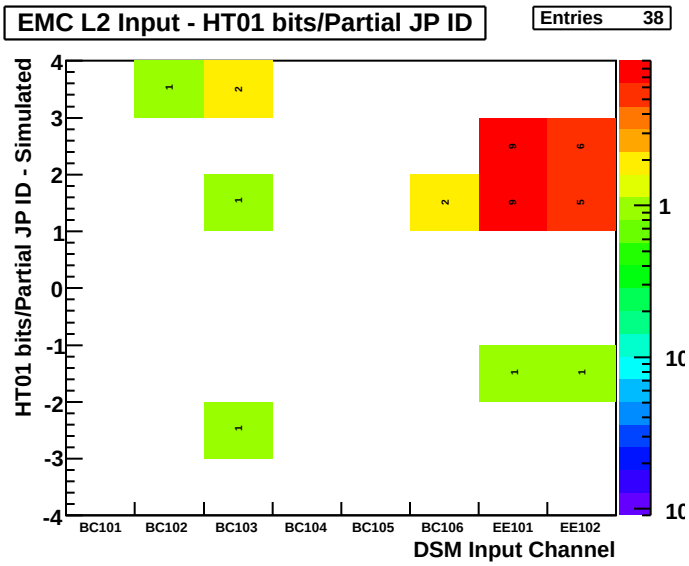

EMC L2 Input - JPZ/JPC bits
Entries 1.199999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
995 570 991 551 507 720 243 254
2194 1088 2430 1174 1125 1661 337 453
9289 4312 8738 4899 4621 6427 961 1277
1.48752e+06 1.49403e+06 1.48784e+06 1.49338e+06 1.49375e+06 1.49119e+06 1.49841e+06 1.49802e+06

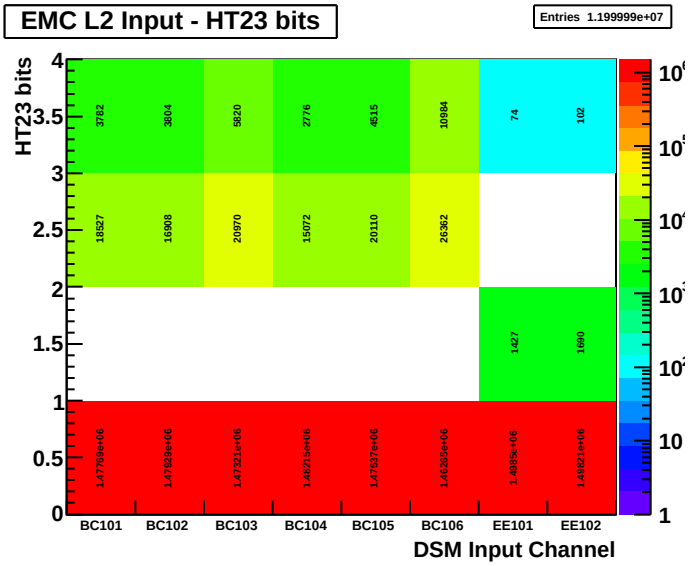

EMC L2 Input - JPZ/JPC bits
Entries 5
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1
1
1
1

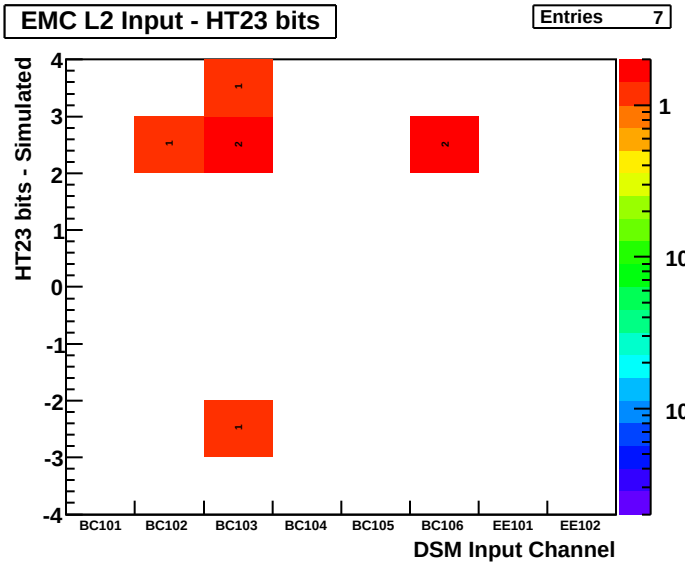

EMC L2 Input - Partial JP Sum
Entries 1.199999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

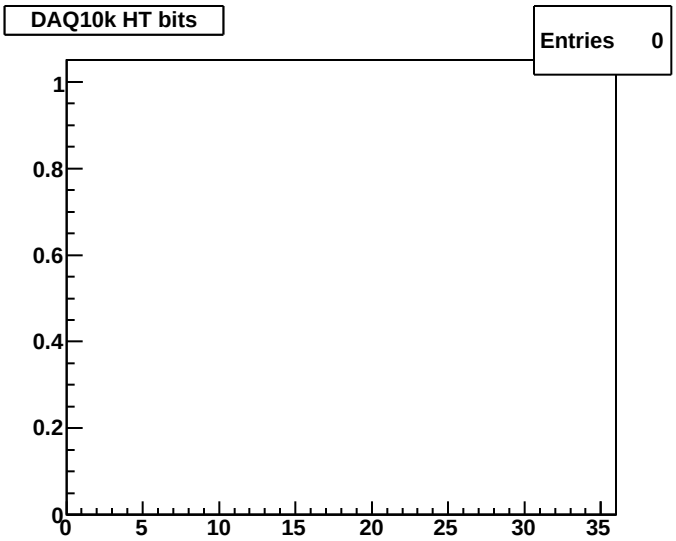

EMC L2 Input - Partial JP Sum
Entries 176
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

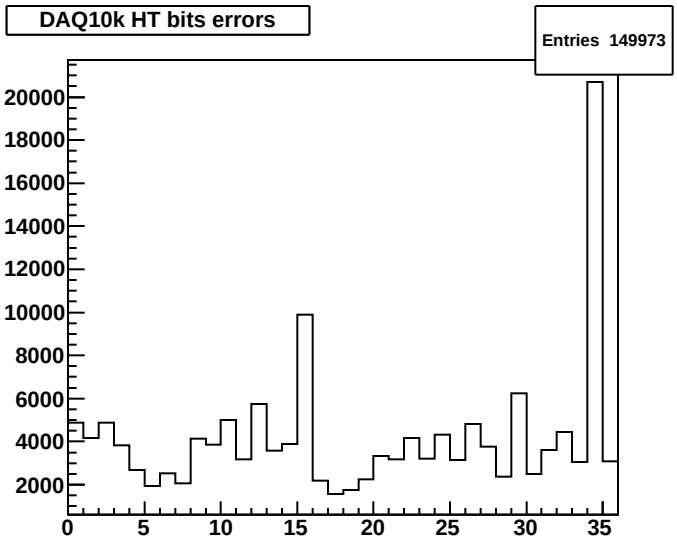

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.199999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

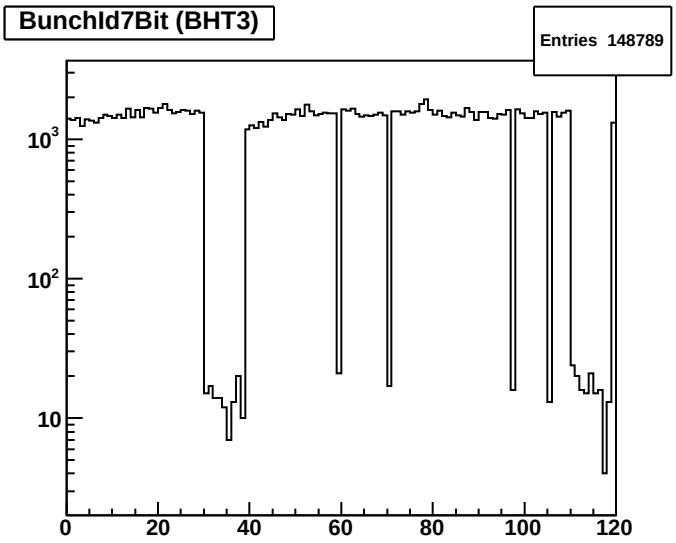

EMC L2 Input - HT01 bits/Partial JP ID
Entries 38
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

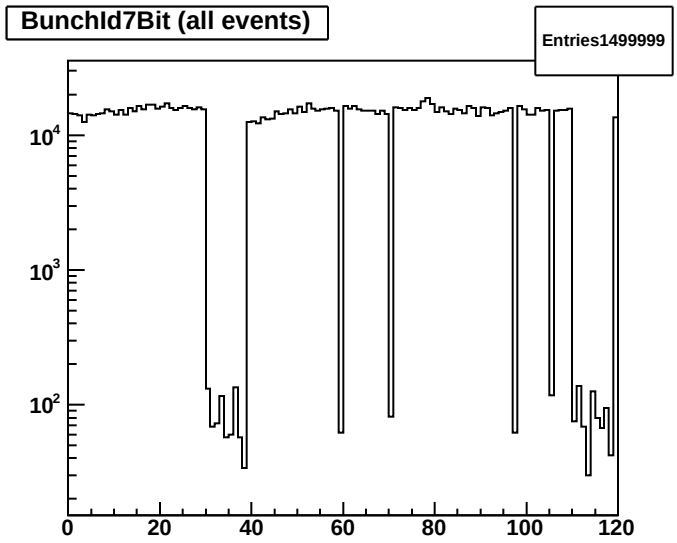

EMC L2 Input - HT23 bits
Entries 1.199999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

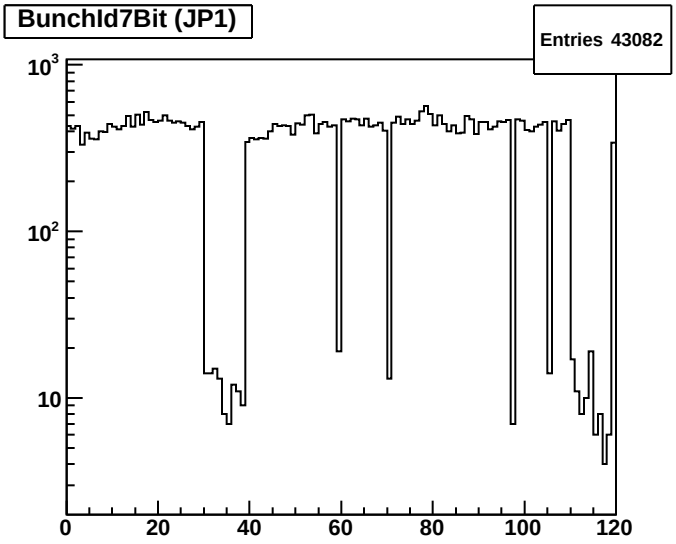

EMC L2 Input - HT23 bits
Entries 7
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

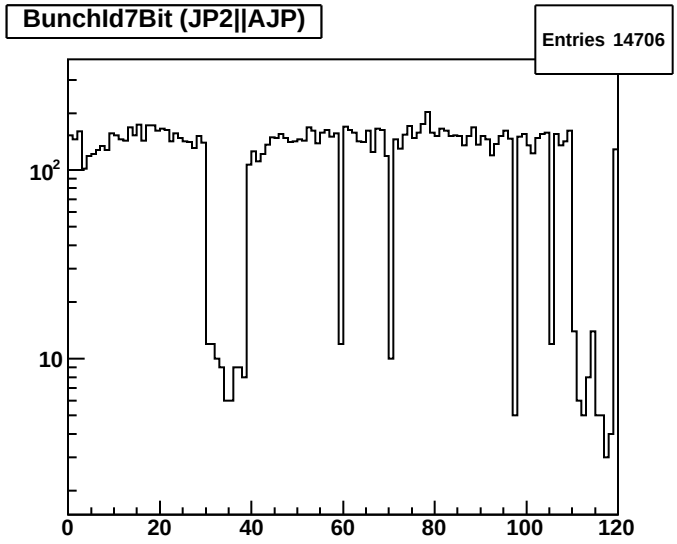

DAQ10k HT bits
Entries 0

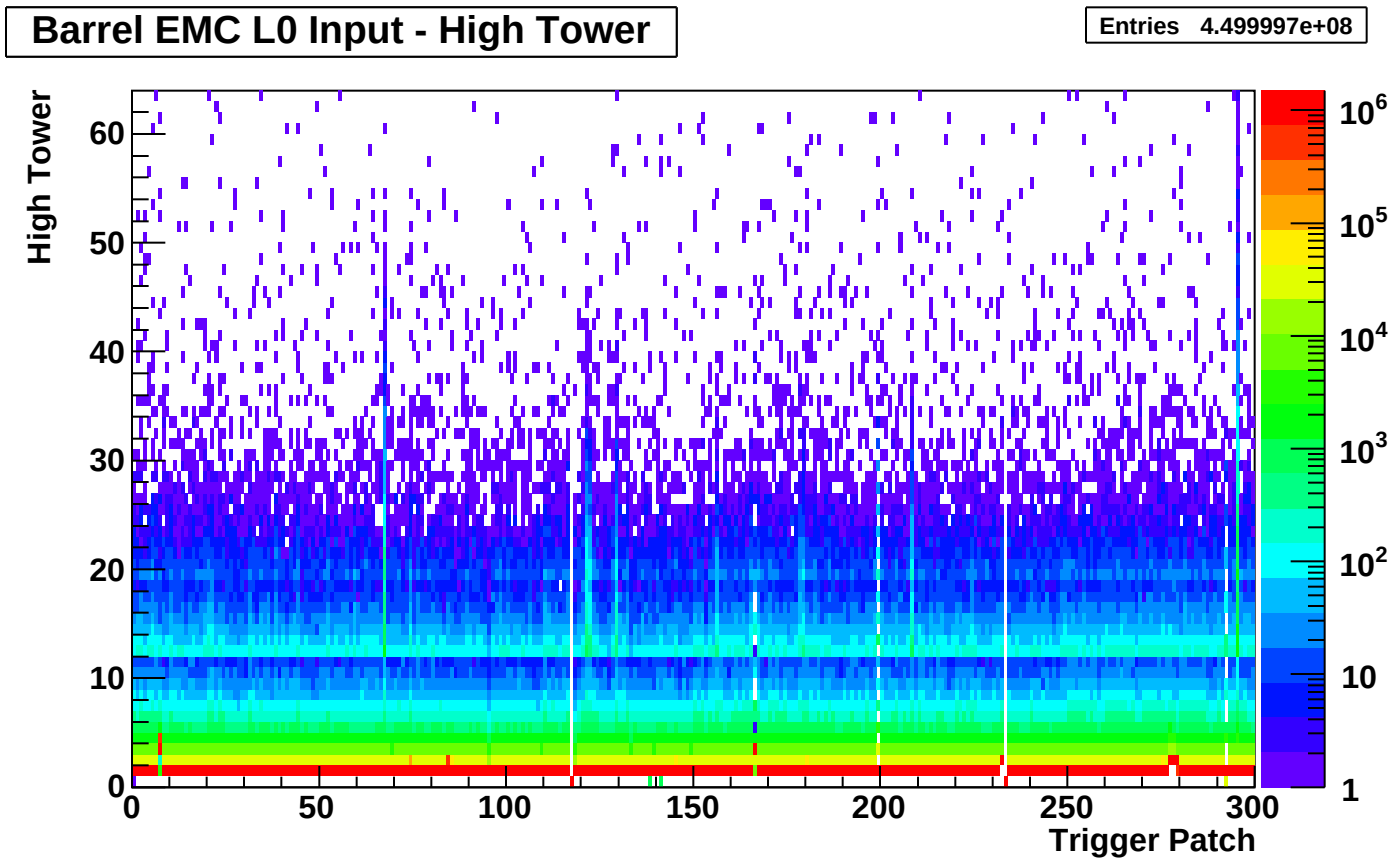

DAQ10k HT bits errors
Entries 149973

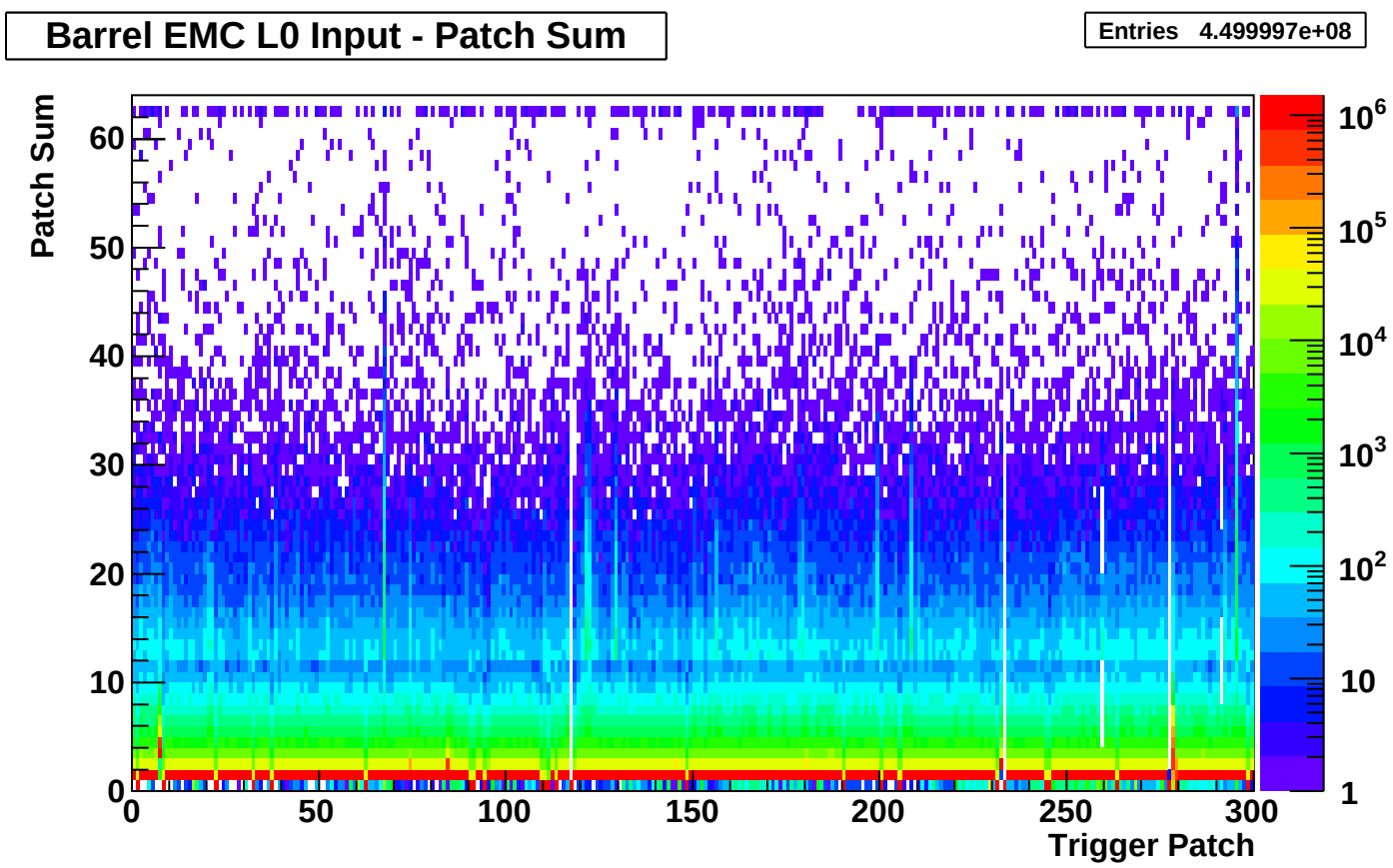

BunchId7Bit (BHT3)
Entries 148789

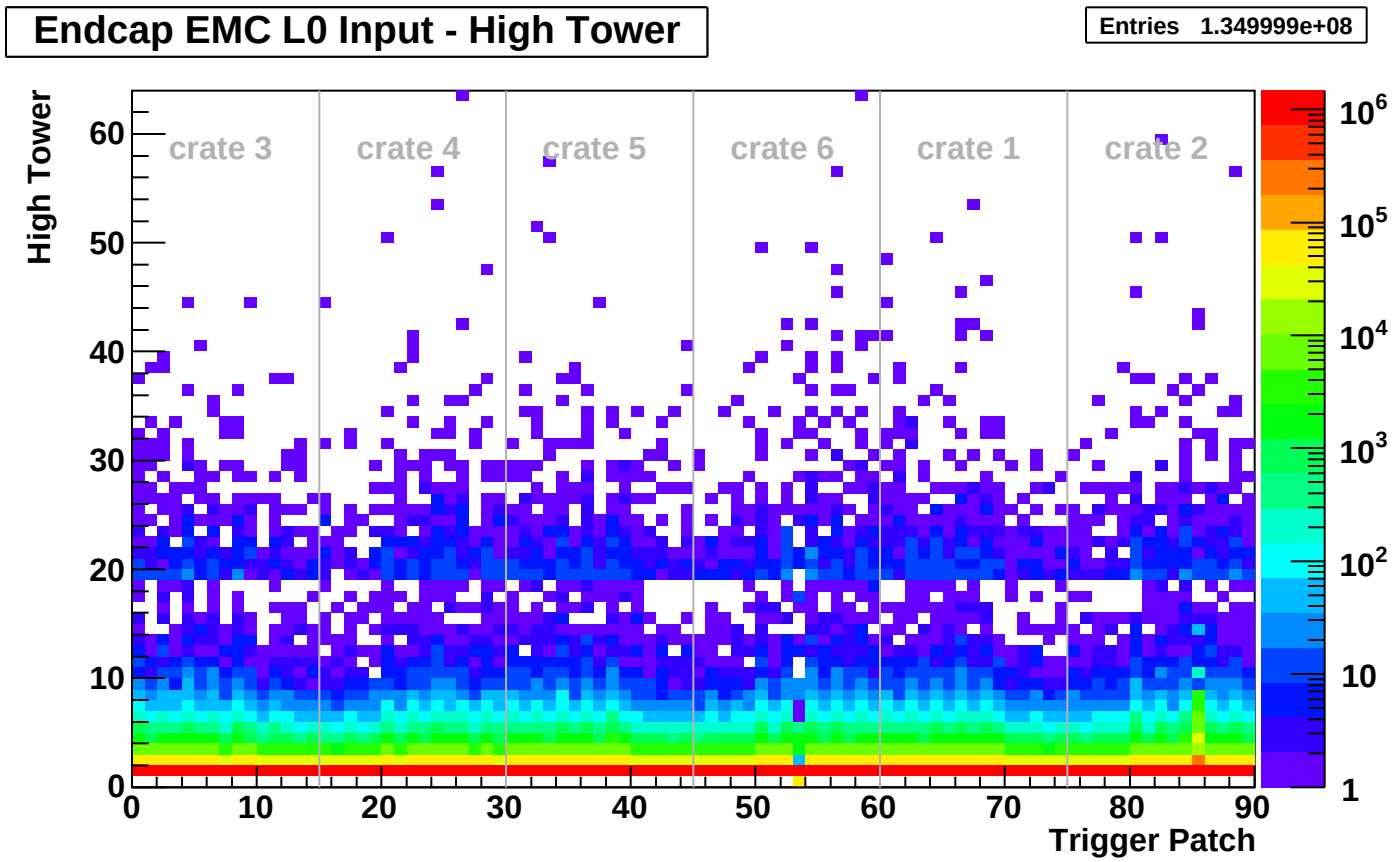

BunchId7Bit (all events)
Entries1499999

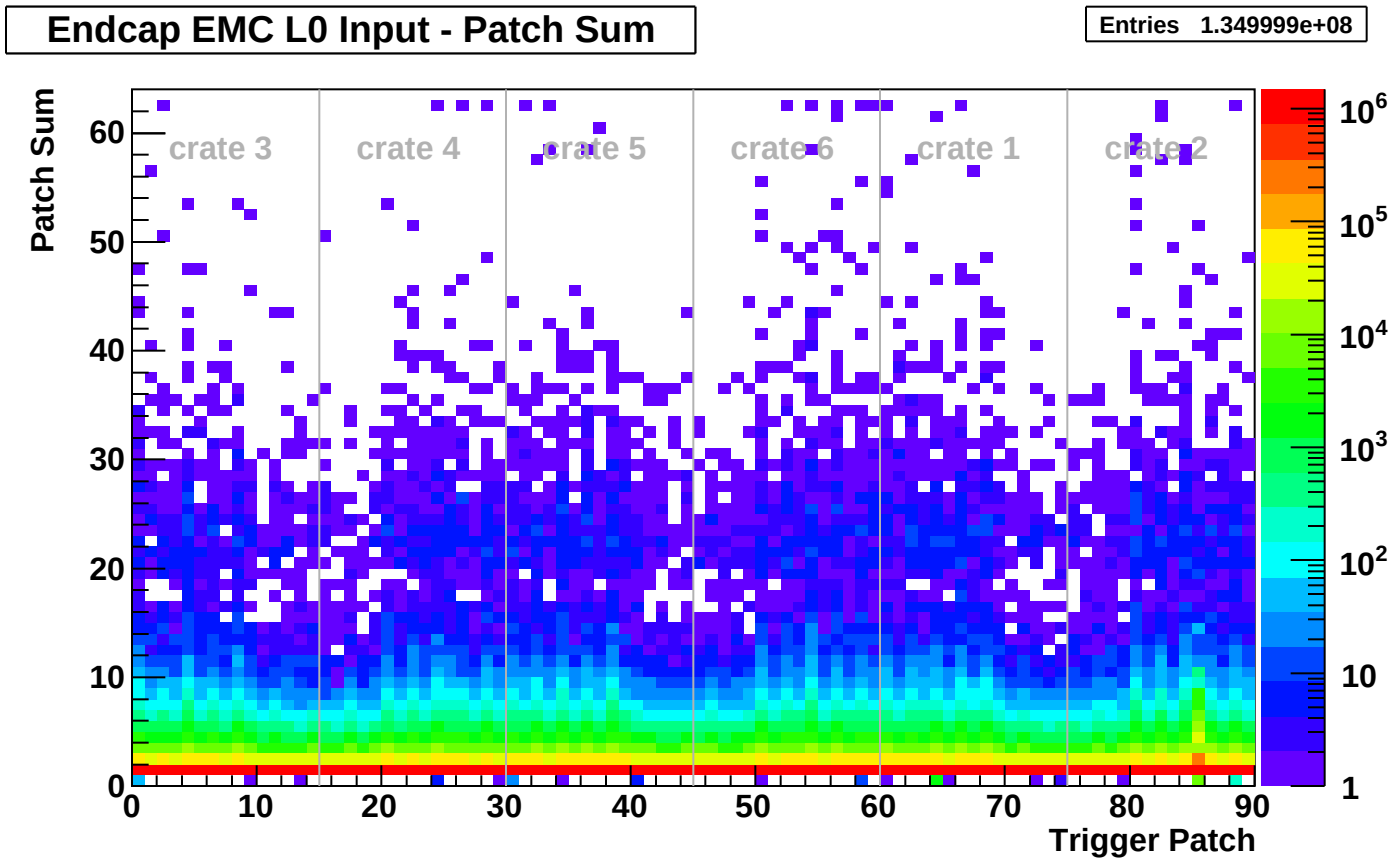

BunchId7Bit (JP1)
Entries 43082

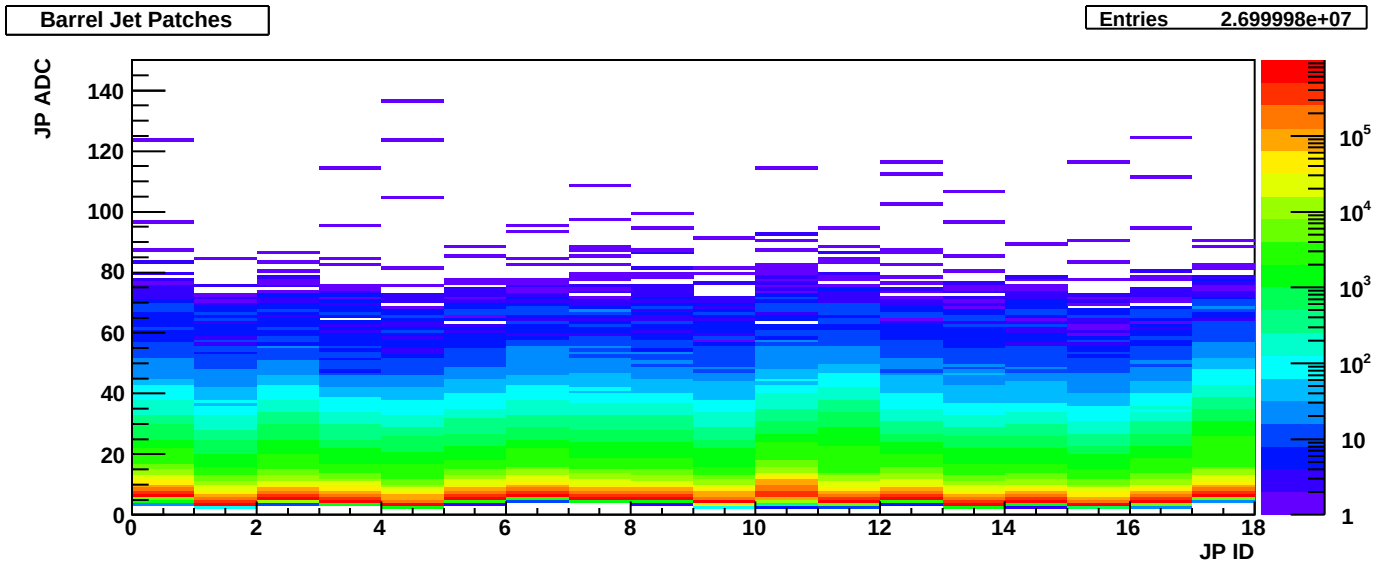

BunchId7Bit (JP2||AJP)
Entries 14706

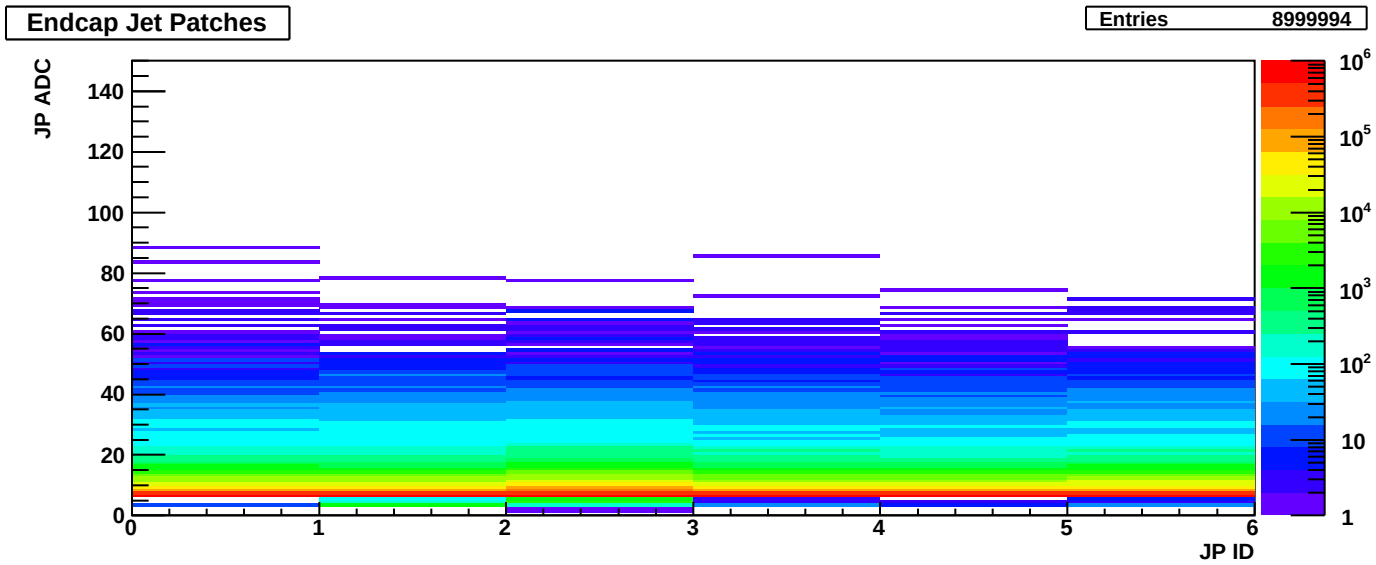

Barrel EMC L0 Input - High Tower
Entries 4.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

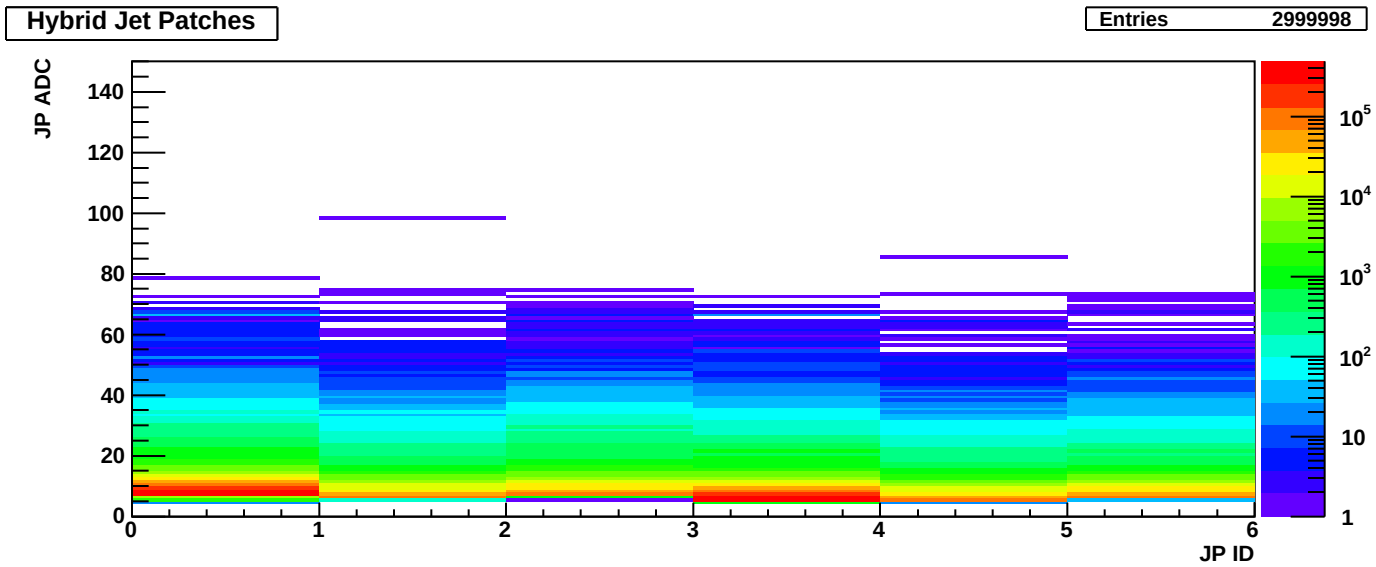

Barrel EMC L0 Input - Patch Sum
Entries 4.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

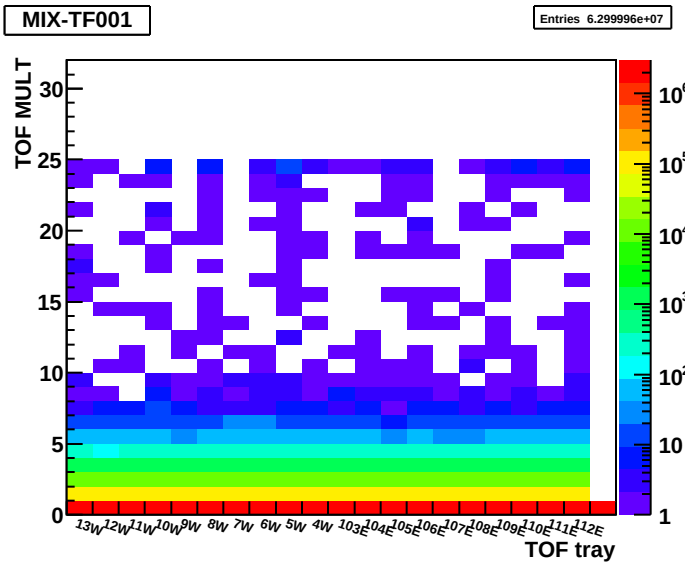

Endcap EMC L0 Input - High Tower
Entries 1.349999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

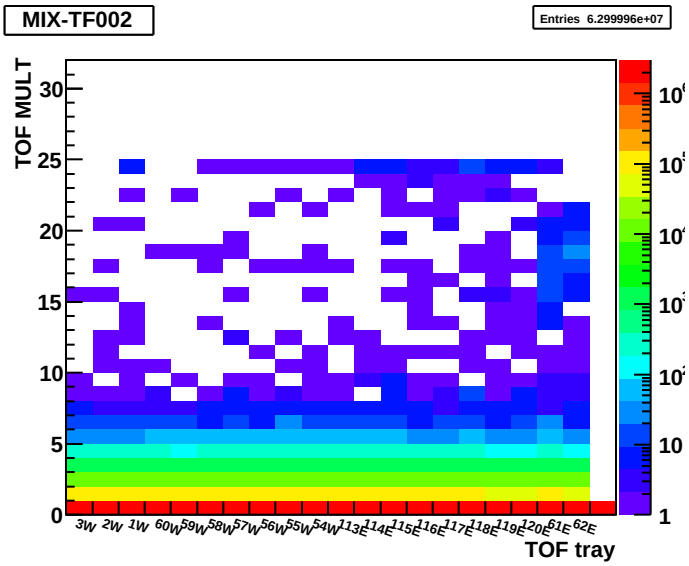

Endcap EMC L0 Input - Patch Sum
Entries 1.349999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

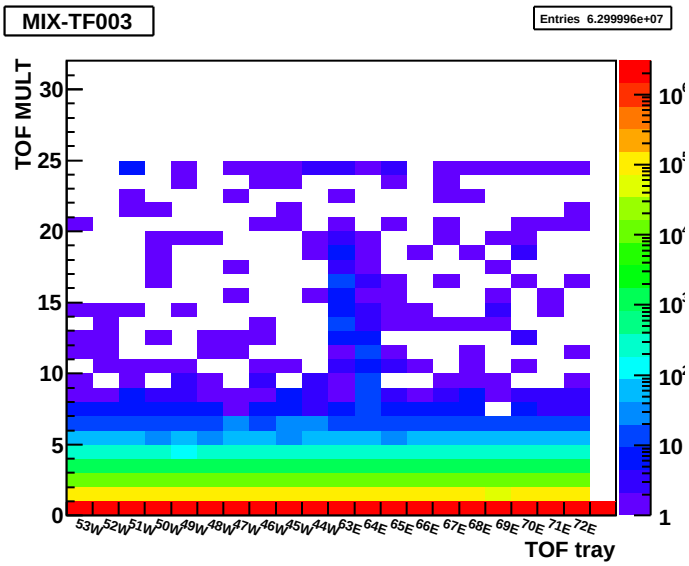

Barrel Jet Patches
Entries 2.699998e+07
JP ADC
JP ID

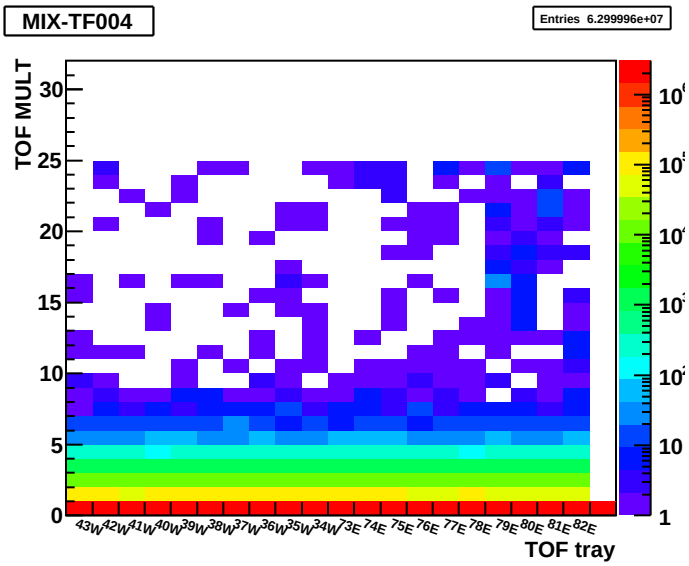

Endcap Jet Patches
Entries 8999994
JP ADC
JP ID

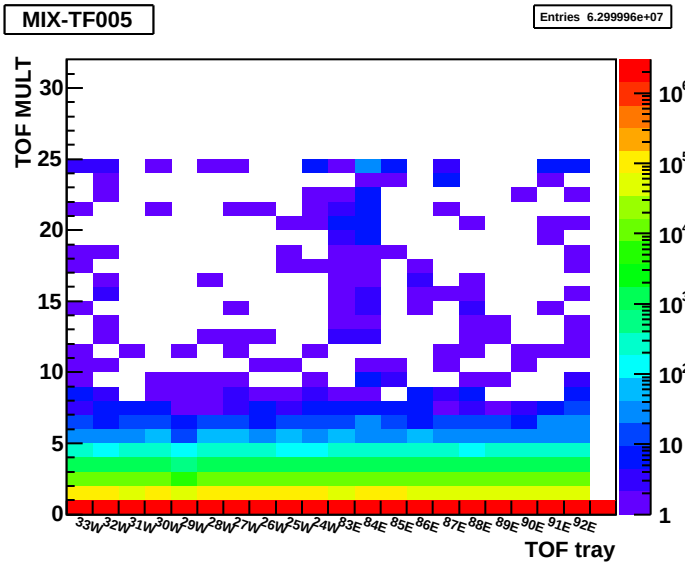

Hybrid Jet Patches
Entries 2999998
JP ADC
JP ID

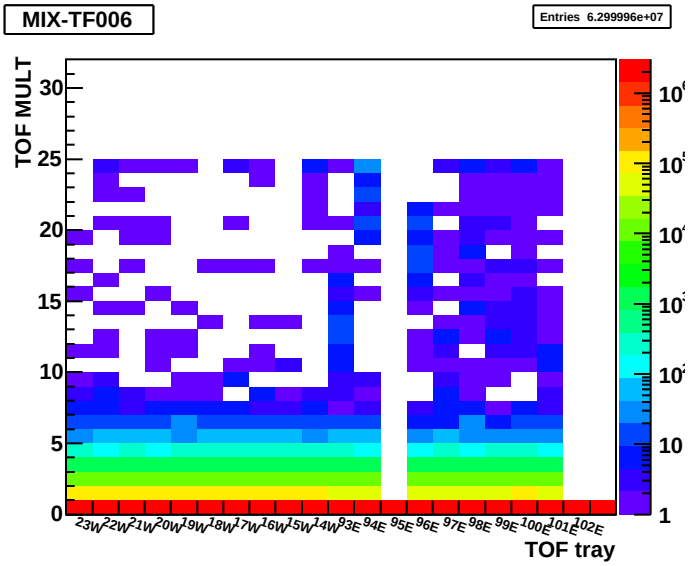

MIX-TF001
Entries 6.299996e+07
TOF MULT
TOF tray

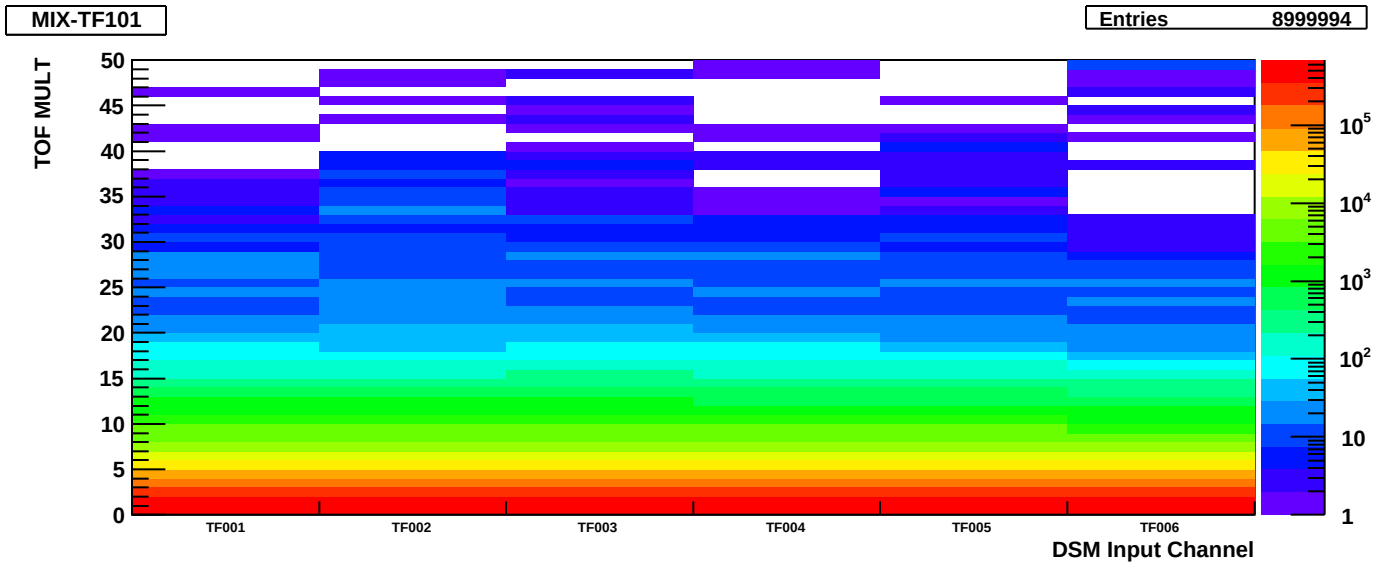

MIX-TF002
Entries 6.299996e+07
TOF MULT
TOF tray

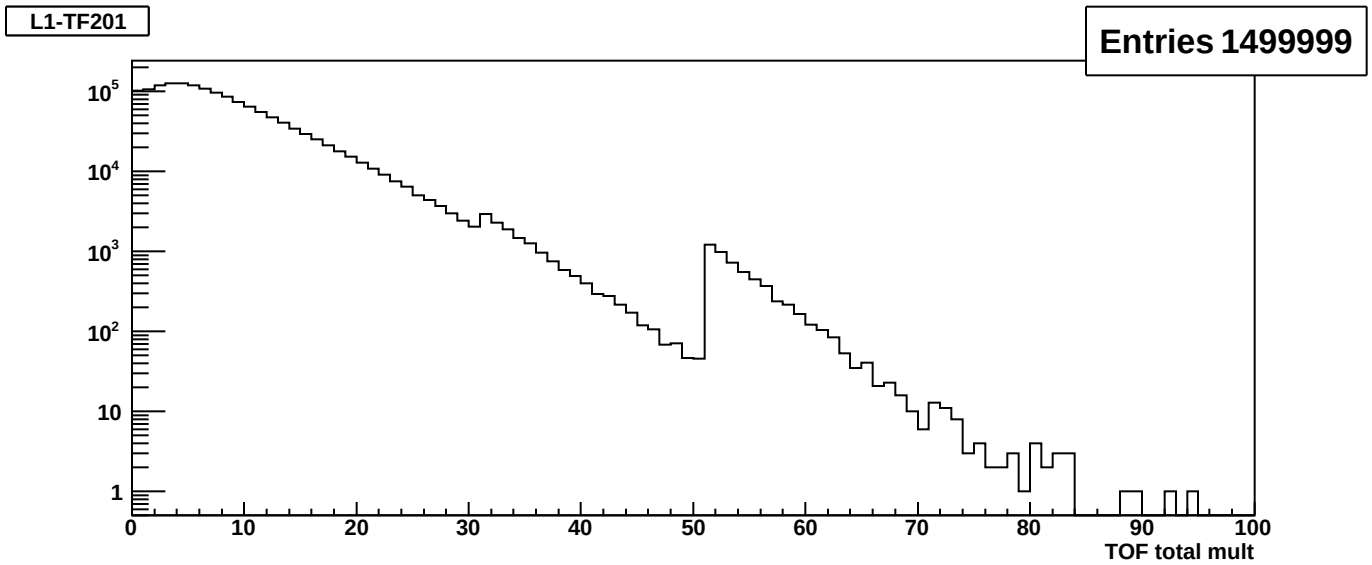

MIX-TF003
Entries 6.299996e+07
TOF MULT
TOF tray

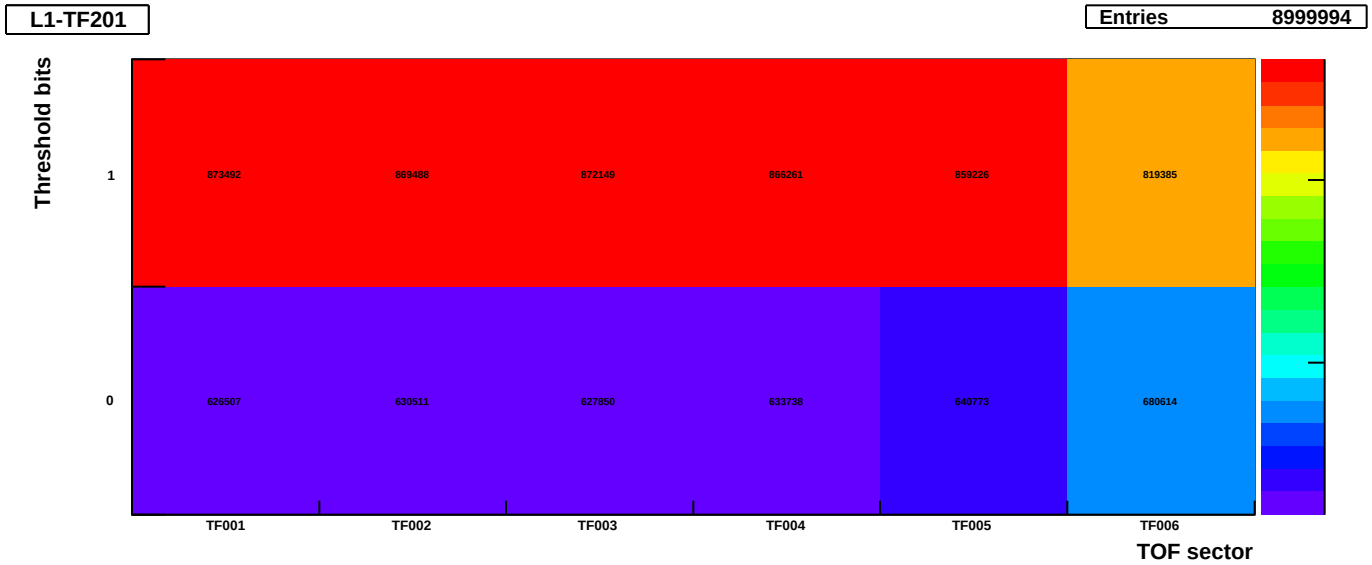

MIX-TF004
Entries 6.299996e+07
TOF MULT
TOF tray

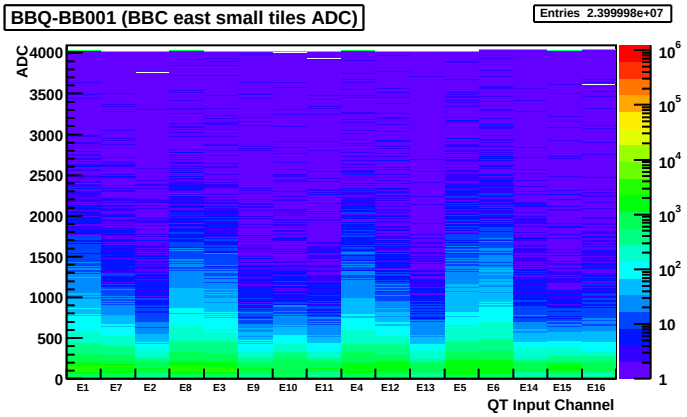

MIX-TF005
Entries 6.299996e+07
TOF MULT
TOF tray

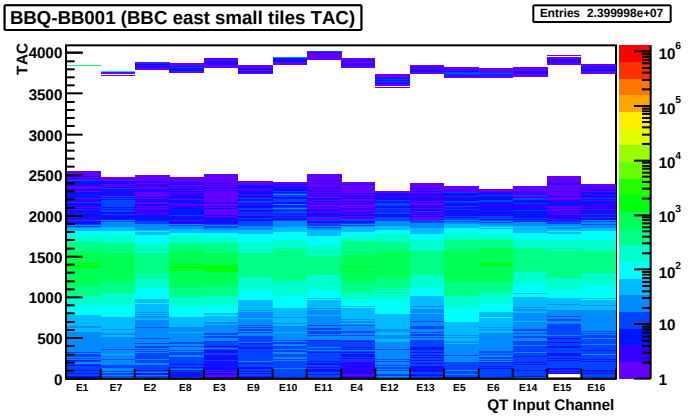

MIX-TF006
Entries 6.299996e+07
TOF MULT
TOF tray

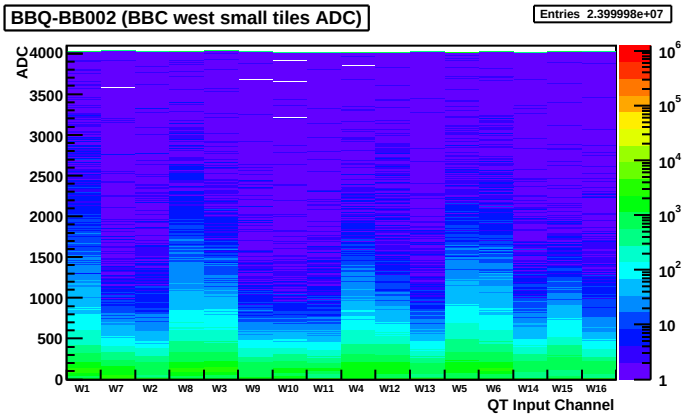

MIX-TF101
Entries 8999994
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

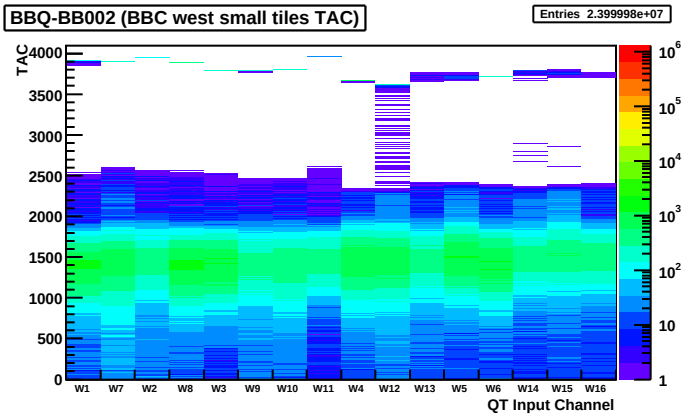

L1-TF201
Entries 1499999
TOF total mult

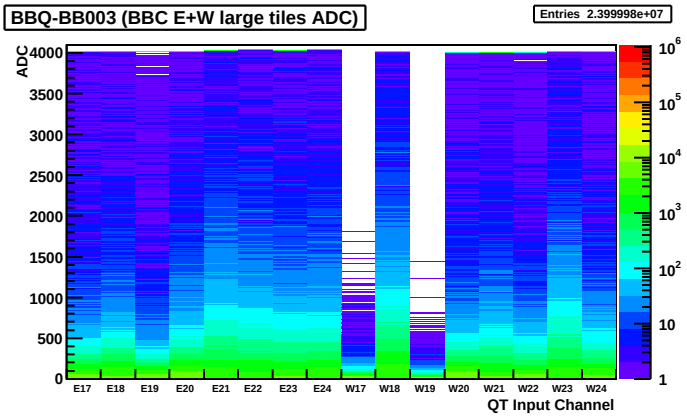

L1-TF201
Entries 8999994
Threshold bits
873492
869488
872149
866261
859226
819385
626507
630511
627850
633738
640773
680614
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

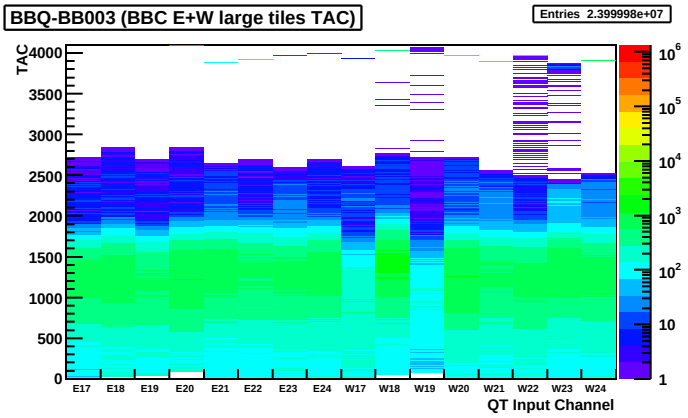

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.399998e+07
ADC
QT Input Channel

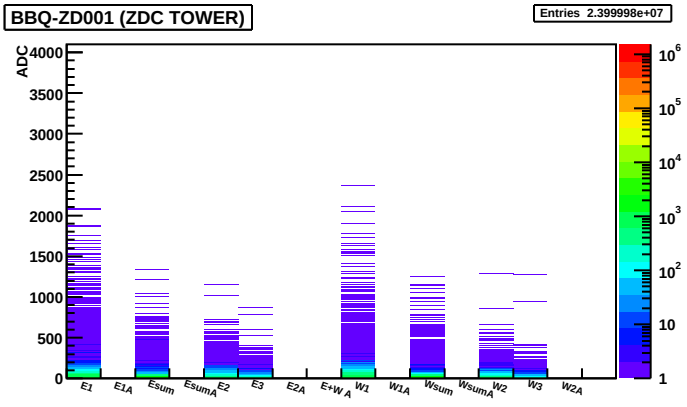

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.399998e+07
TAC
QT Input Channel

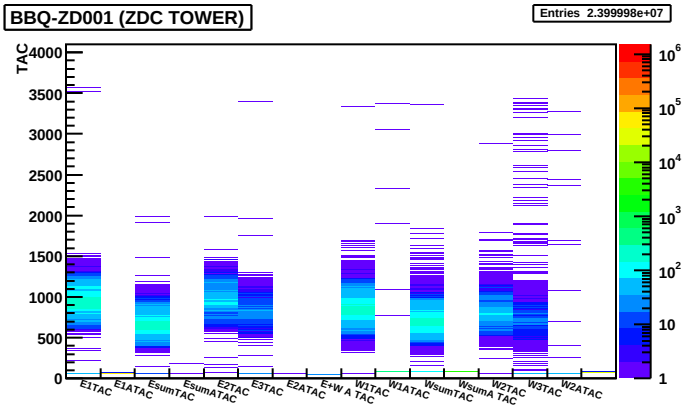

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.399998e+07
ADC
QT Input Channel

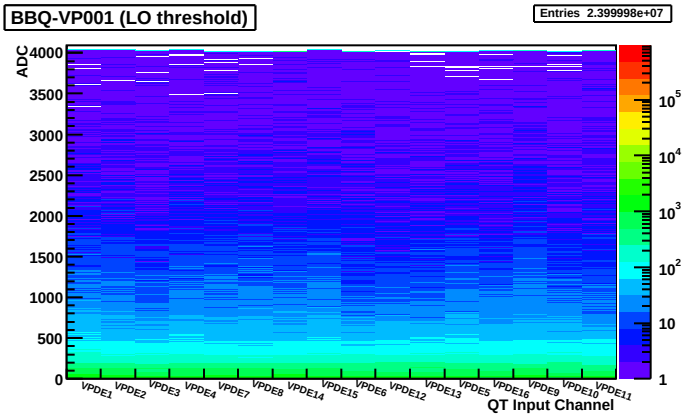

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.399998e+07
TAC
QT Input Channel

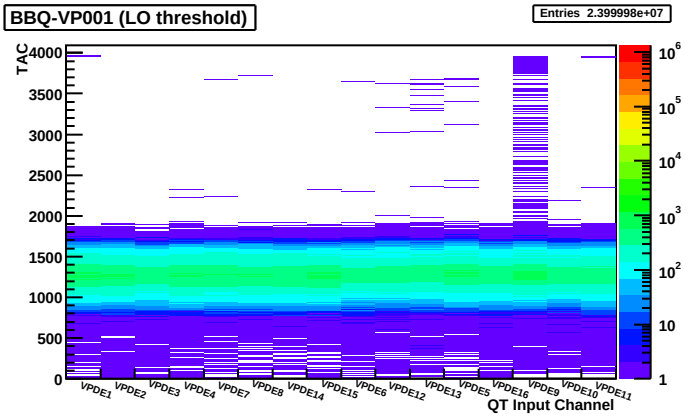

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.399998e+07
ADC
QT Input Channel

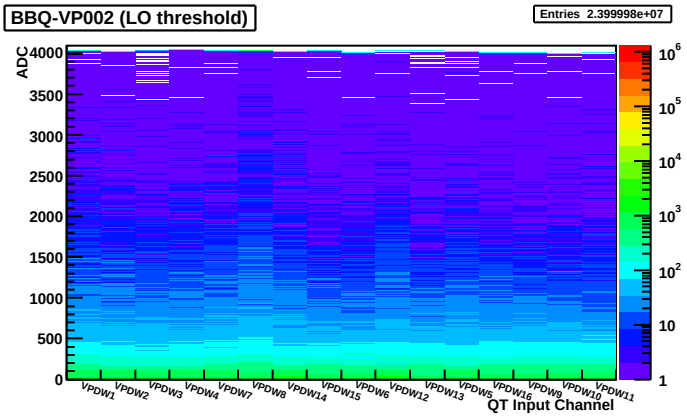

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.399998e+07
TAC
QT Input Channel

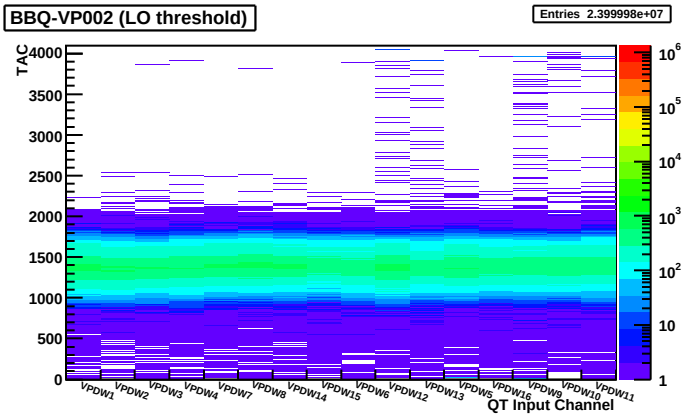

BBQ-ZD001 (ZDC TOWER)
Entries 2.399998e+07
ADC

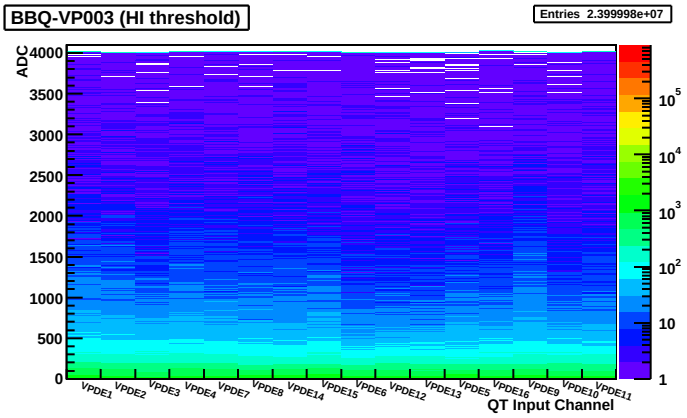

BBQ-ZD001 (ZDC TOWER)
Entries 2.399998e+07
TAC

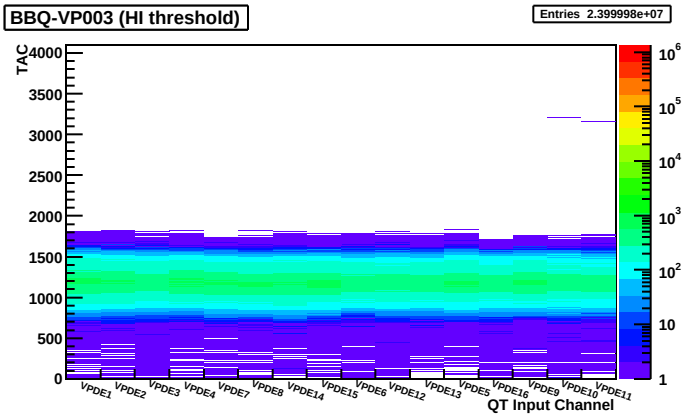

BBQ-VP001 (LO threshold)
Entries 2.399998e+07
ADC
QT Input Channel

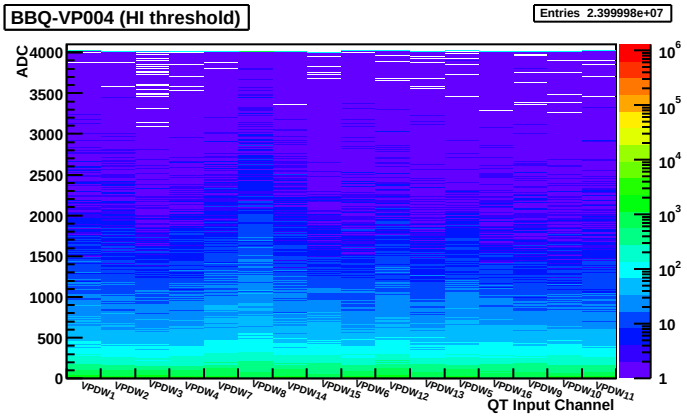

BBQ-VP001 (LO threshold)
Entries 2.399998e+07
TAC
QT Input Channel

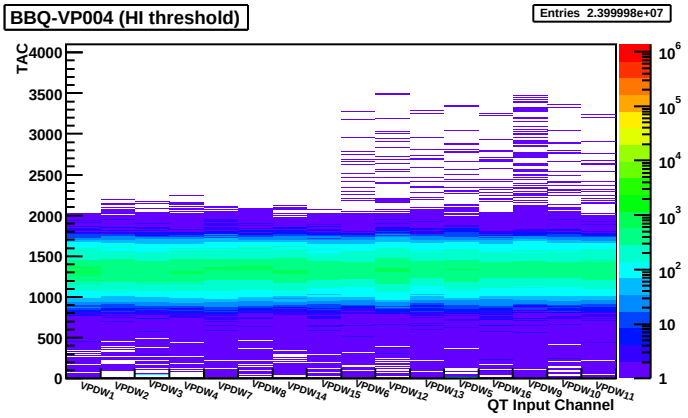

BBQ-VP002 (LO threshold)
Entries 2.399998e+07
ADC
QT Input Channel

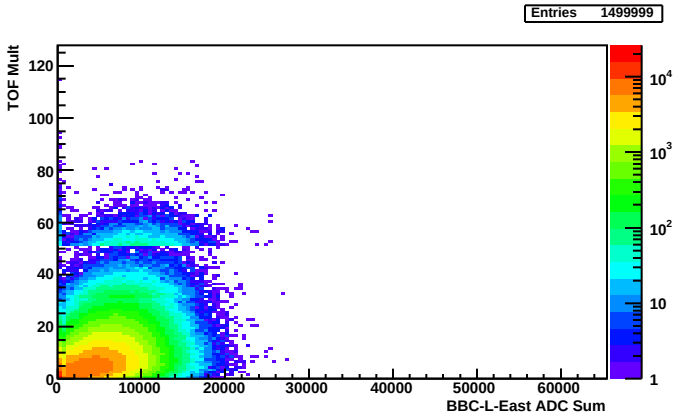

BBQ-VP002 (LO threshold)
Entries 2.399998e+07
TAC
QT Input Channel

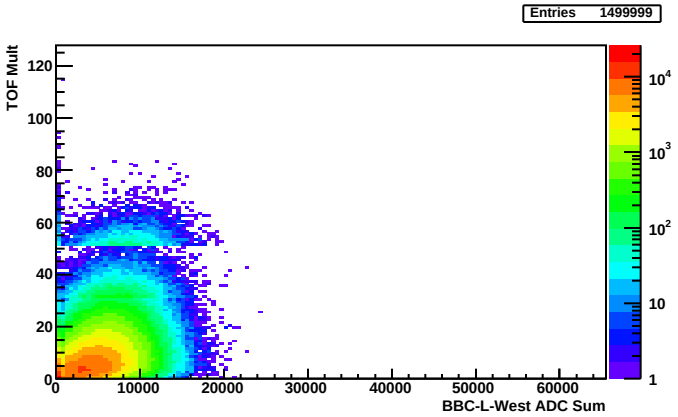

BBQ-VP003 (HI threshold)
Entries 2.399998e+07
ADC
QT Input Channel

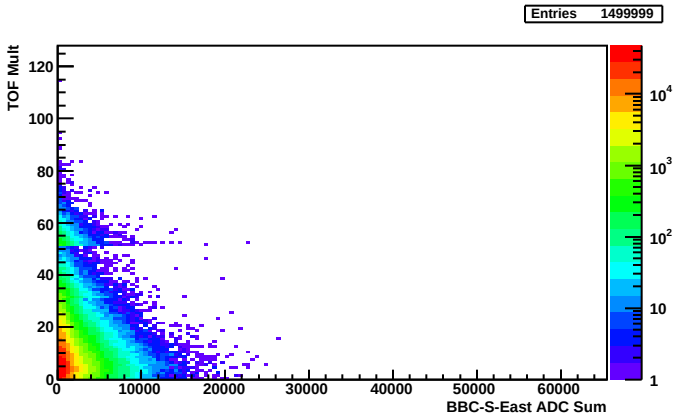

BBQ-VP003 (HI threshold)
Entries 2.399998e+07
TAC
QT Input Channel

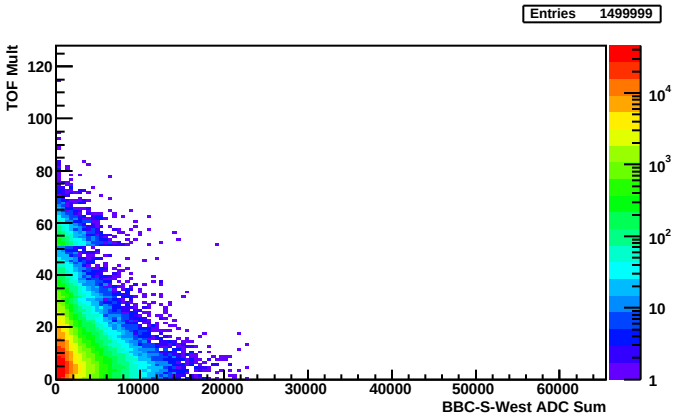

BBQ-VP004 (HI threshold)
Entries 2.399998e+07
ADC
QT Input Channel

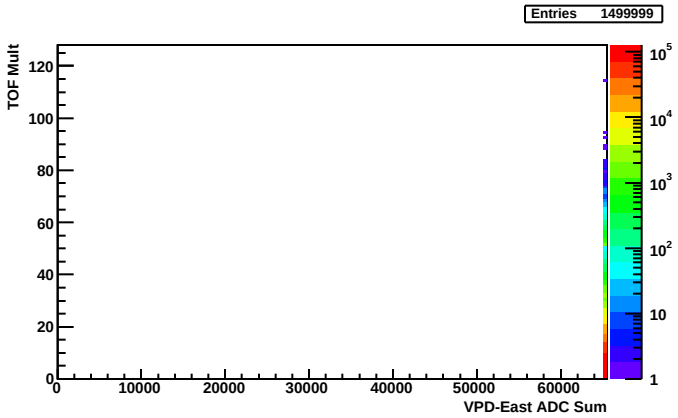

BBQ-VP004 (HI threshold)
Entries 2.399998e+07
TAC
QT Input Channel

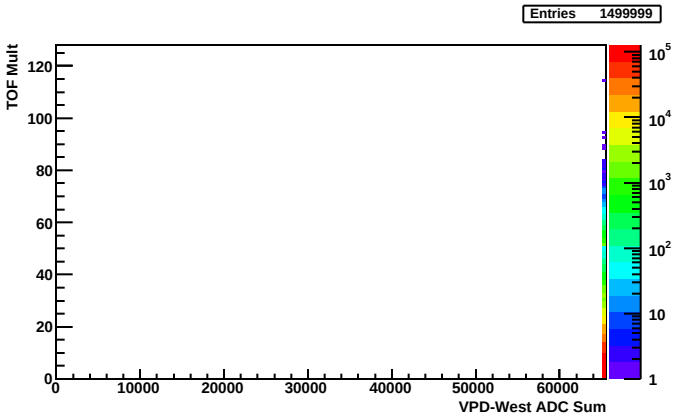

Entries 1499999
TOF Mult
BBC-L-East ADC Sum

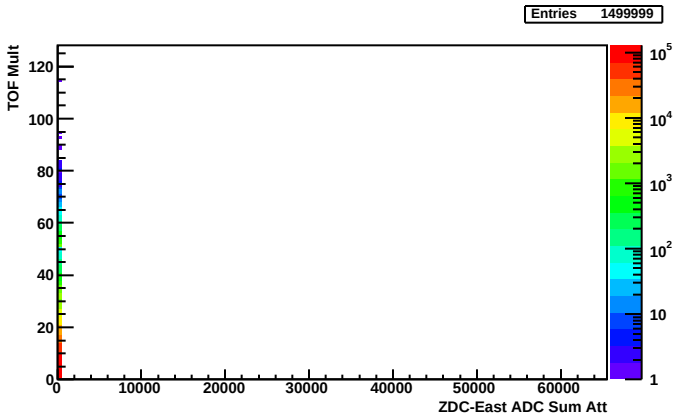

Entries 1499999
TOF Mult
BBC-L-West ADC Sum

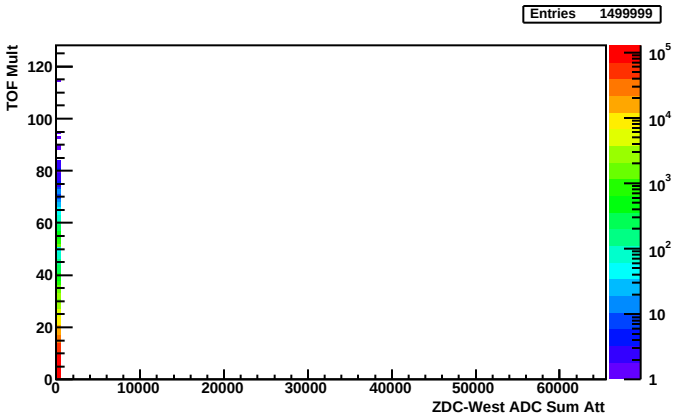

Entries 1499999
TOF Mult
BBC-S-East ADC Sum

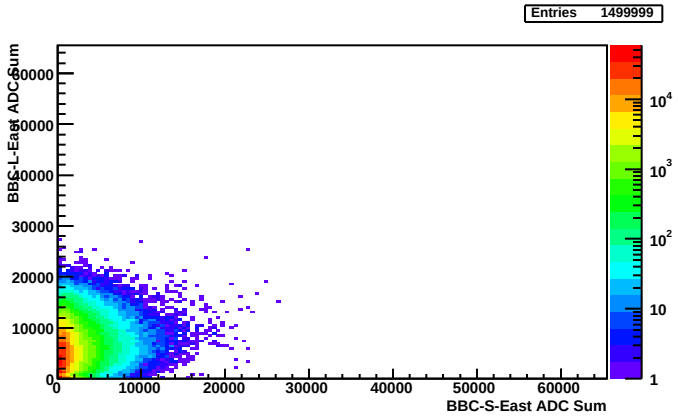

Entries 1499999
TOF Mult
BBC-S-West ADC Sum

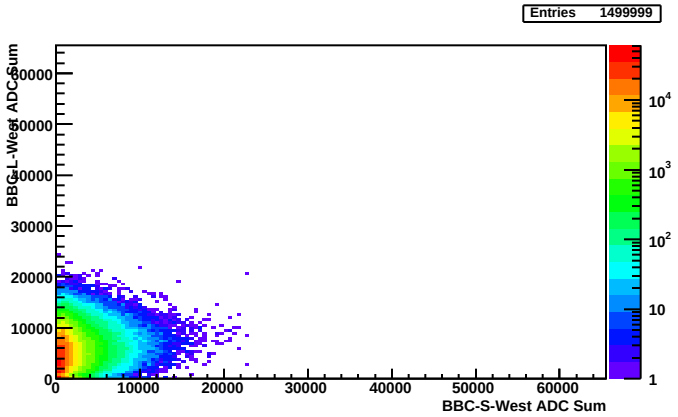

Entries 1499999
TOF Mult
VPD-East ADC Sum

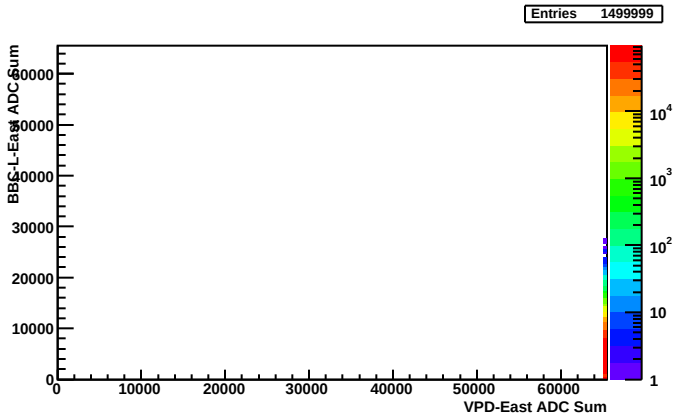

Entries 1499999
TOF Mult
VPD-West ADC Sum

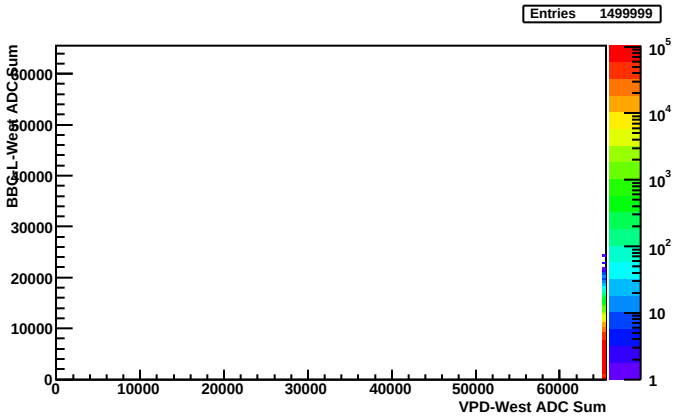

Entries 1499999
TOF Mult
ZDC-East ADC Sum Att

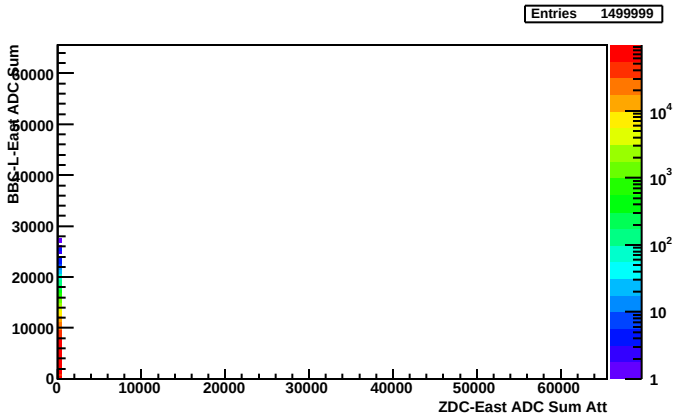

Entries 1499999
TOF Mult
ZDC-West ADC Sum Att

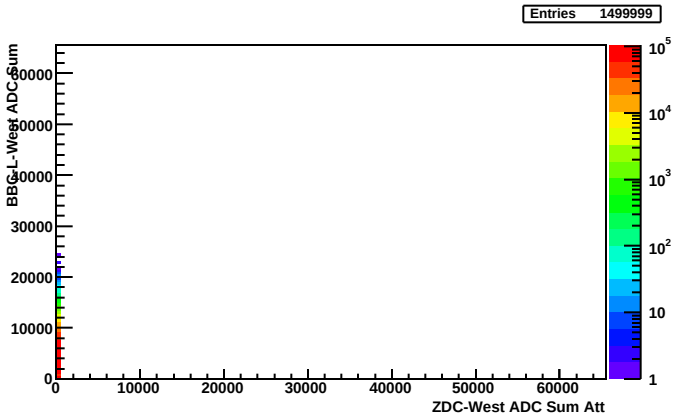

Entries 1499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

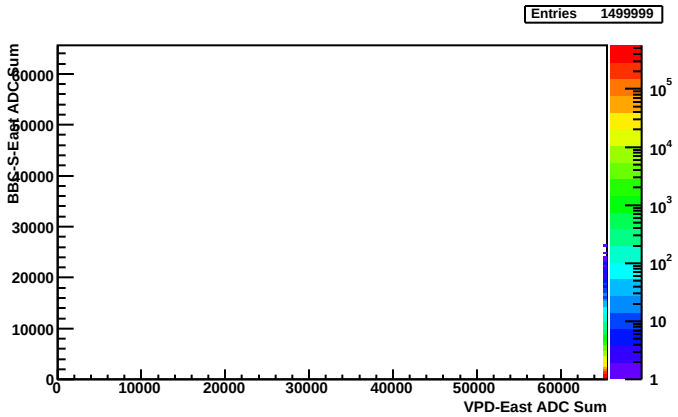

Entries 1499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

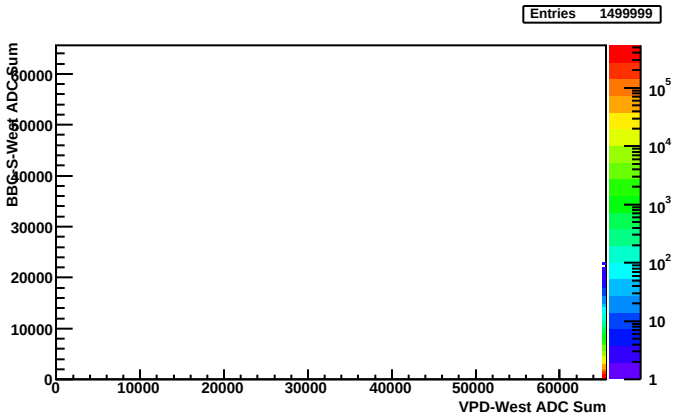

Entries 1499999
BBC-L-East ADC Sum
VPD-East ADC Sum

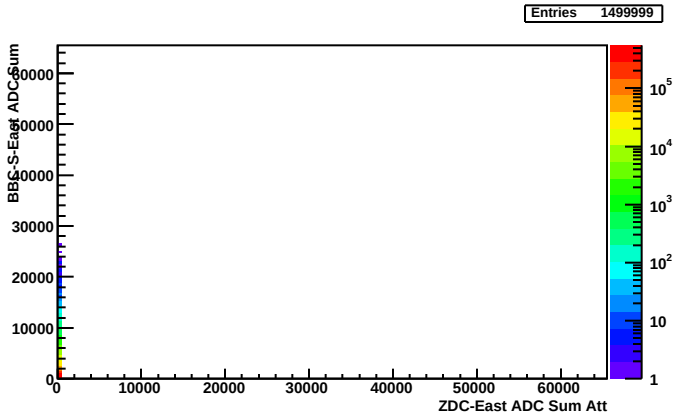

Entries 1499999
BBC-L-West ADC Sum
VPD-West ADC Sum

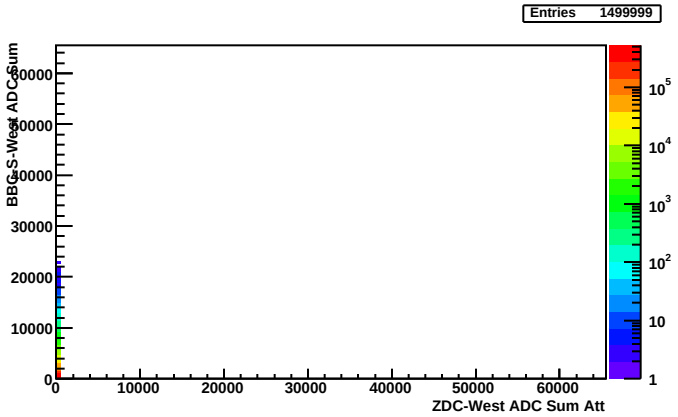

Entries 1499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

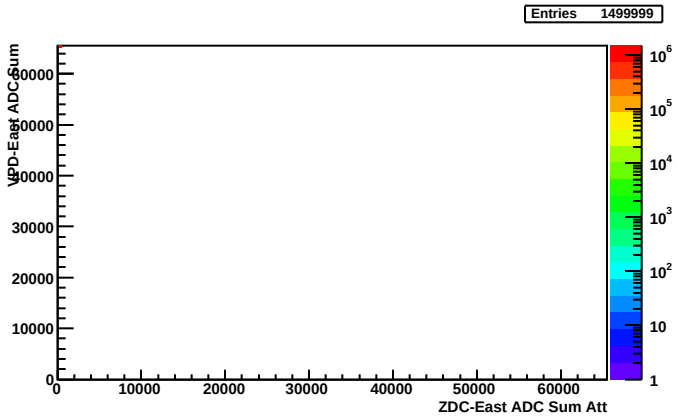

Entries 1499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

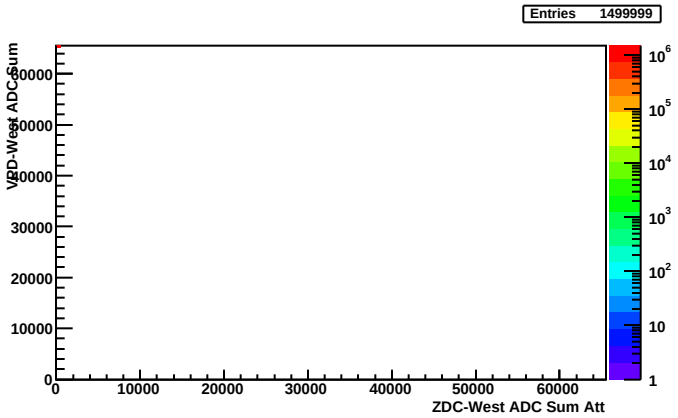

Entries 1499999
BBC-S-East ADC Sum
VPD-East ADC Sum

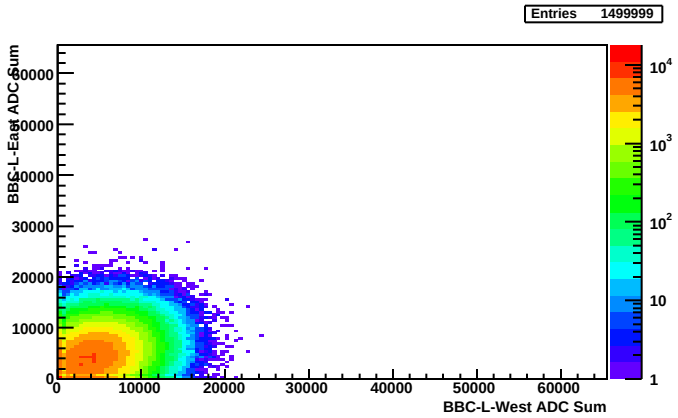

Entries 1499999
BBC-S-West ADC Sum
VPD-West ADC Sum

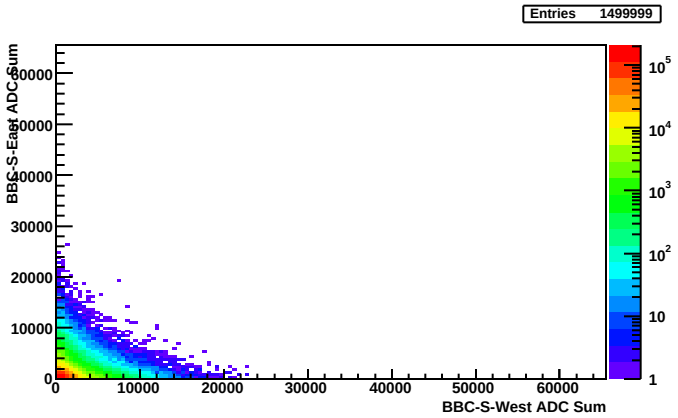

Entries 1499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

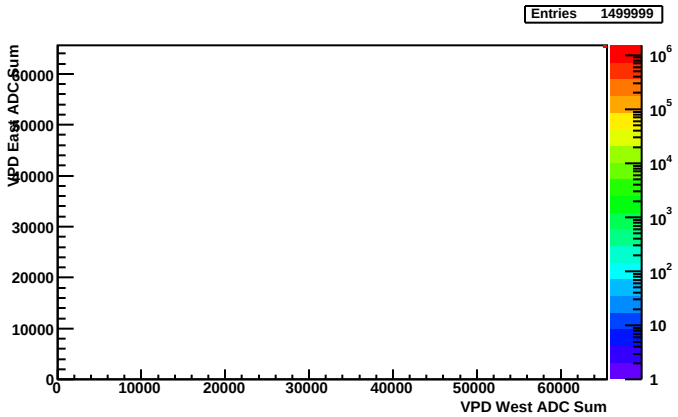

Entries 1499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

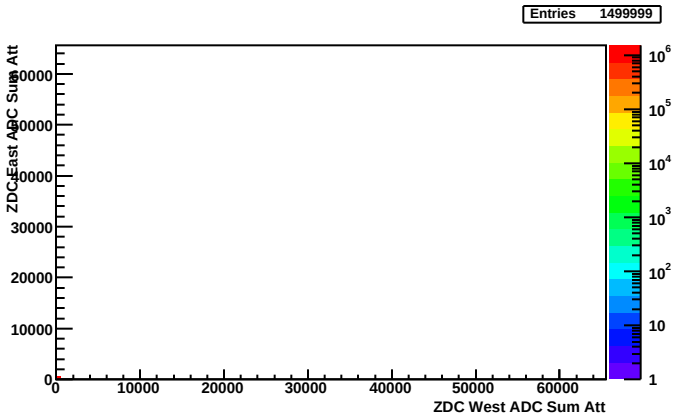

Entries 1499999
VPD-East ADC Sum
ZDC-East ADC Sum Att

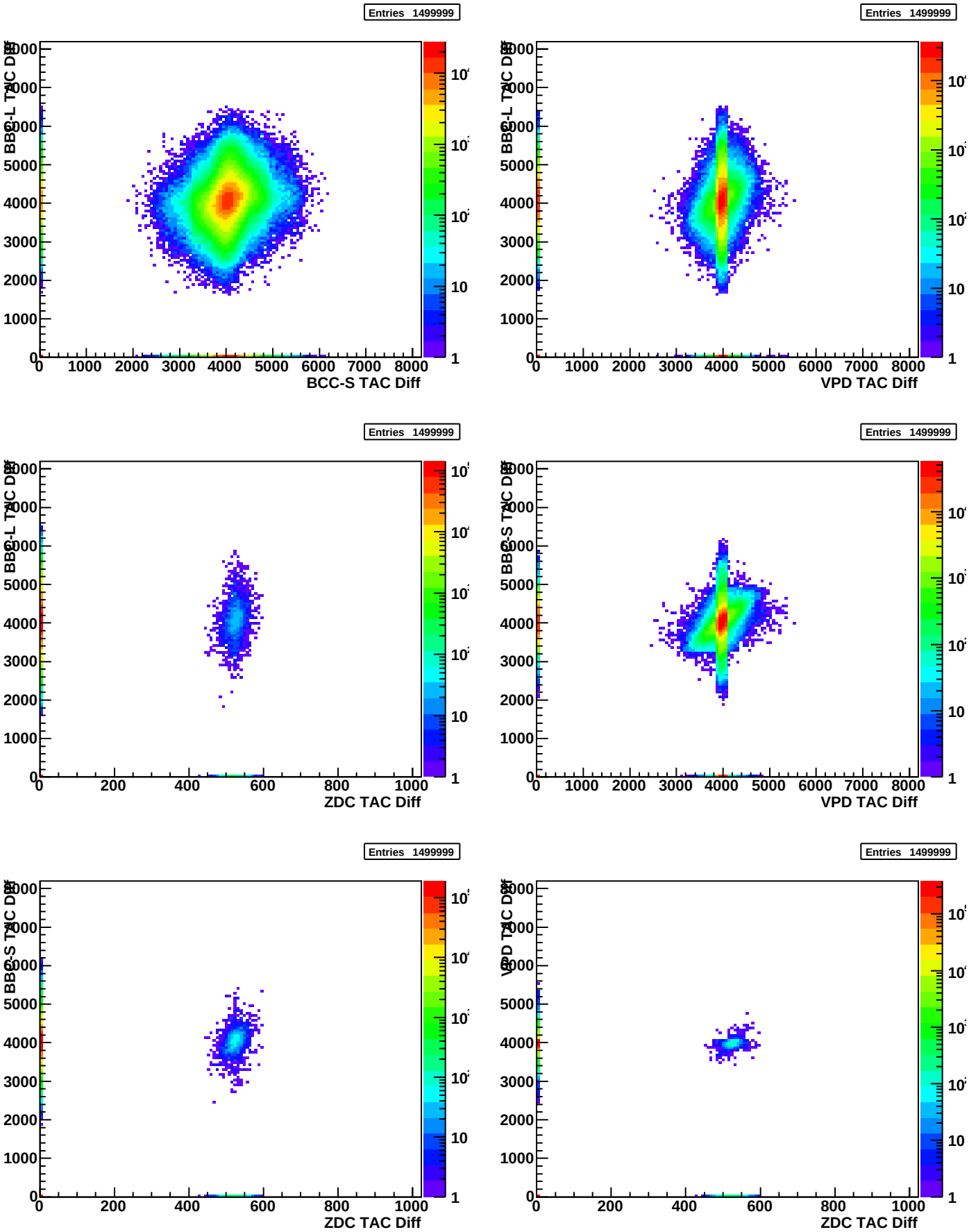

Entries 1499999
VPD-West ADC Sum
ZDC-West ADC Sum Att

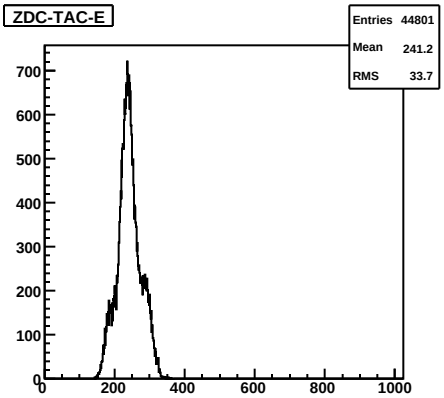

Entries 1499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

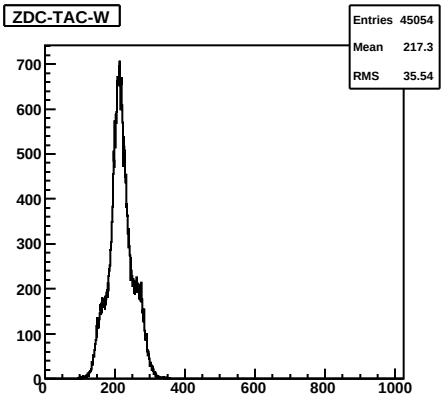

Entries 1499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

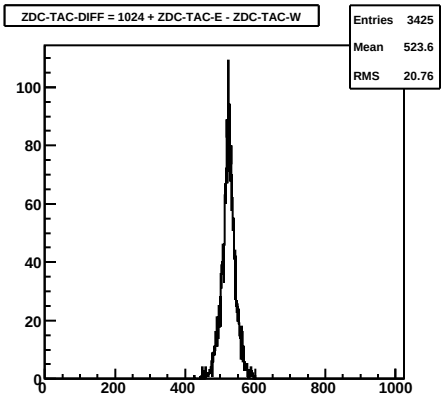

Entries 1499999
VPD East ADC Sum
VPD West ADC Sum

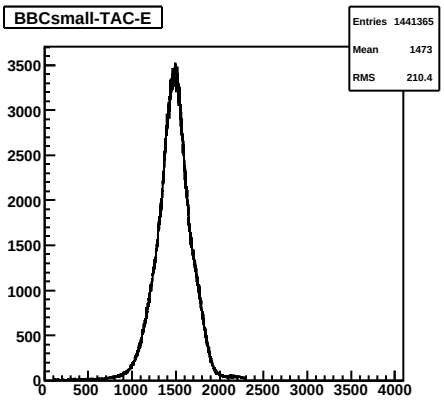

Entries 1499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

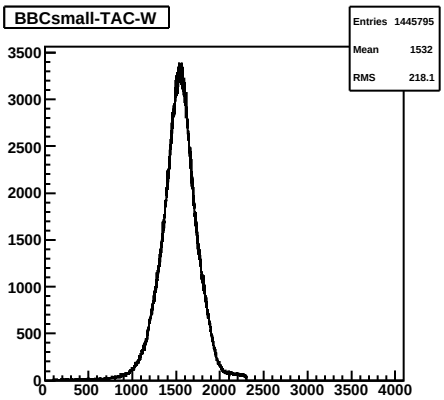

Entries 1499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1499999
BBC-L TAC Diff
VPD TAC Diff
Entries 1499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 1499999
BBC-S TAC Diff
VPD TAC Diff
Entries 1499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 1499999
VPD TAC Diff
ZDC TAC Diff

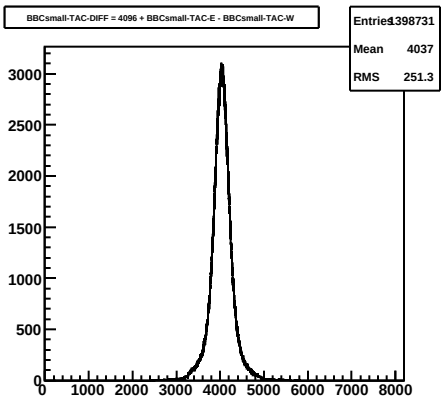

ZDC-TAC-E
Entries 44801
Mean 241.2
RMS 33.7

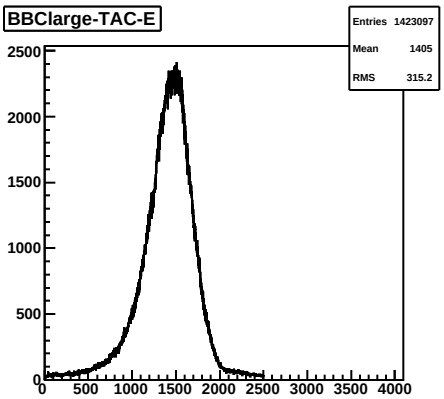

ZDC-TAC-W
Entries 45054
Mean 217.3
RMS 35.54

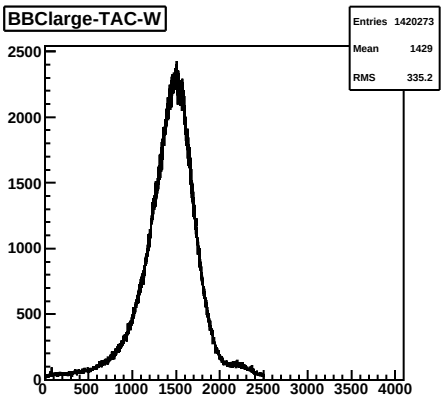

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3425
Mean 523.6
RMS 20.76

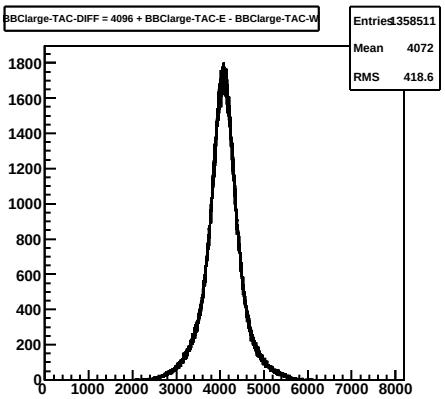

BBCsmall-TAC-E
Entries 1441365
Mean 1473
RMS 210.4

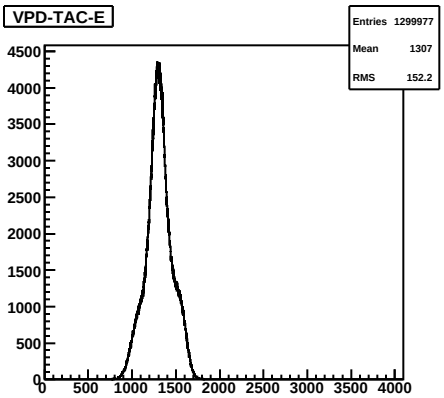

BBCsmall-TAC-W
Entries 1445795
Mean 1532
RMS 218.1

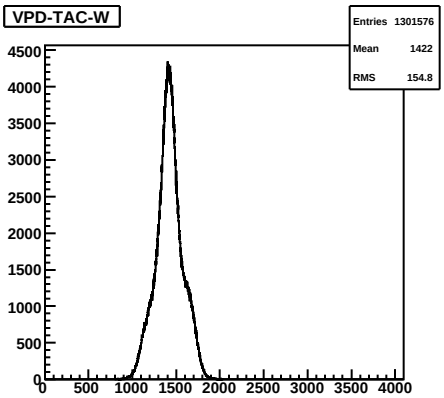

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1398731
Mean 4037
RMS 251.3

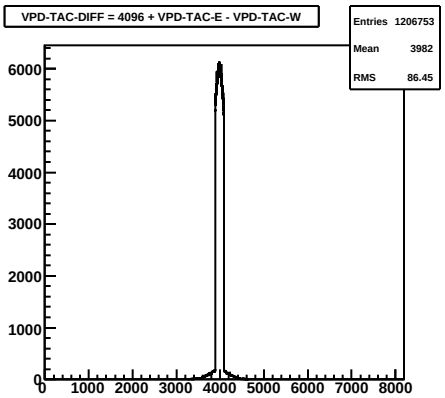

BBClarge-TAC-E
Entries 1423097
Mean 1405
RMS 315.2

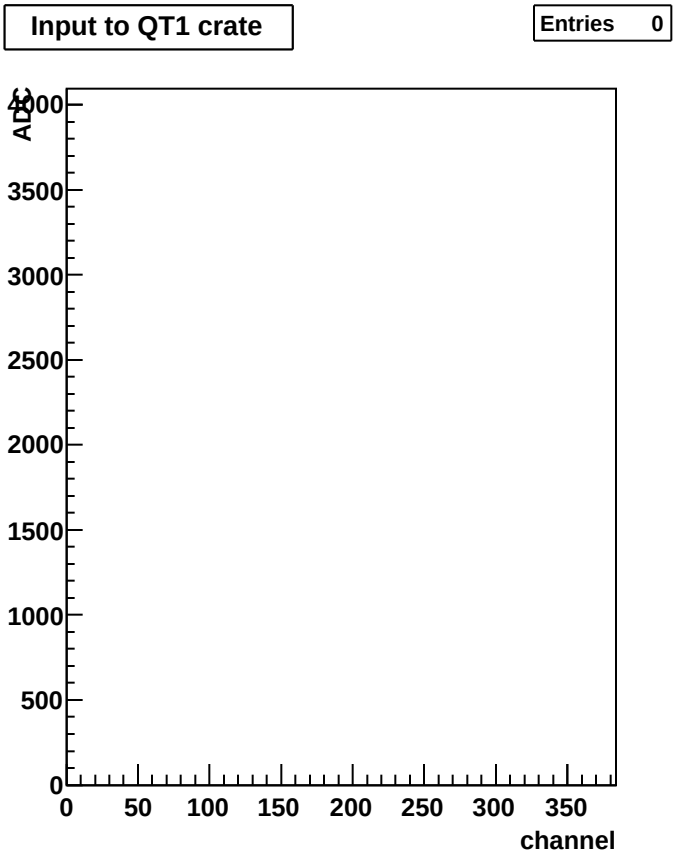

BBClarge-TAC-W
Entries 1420273
Mean 1429
RMS 335.2

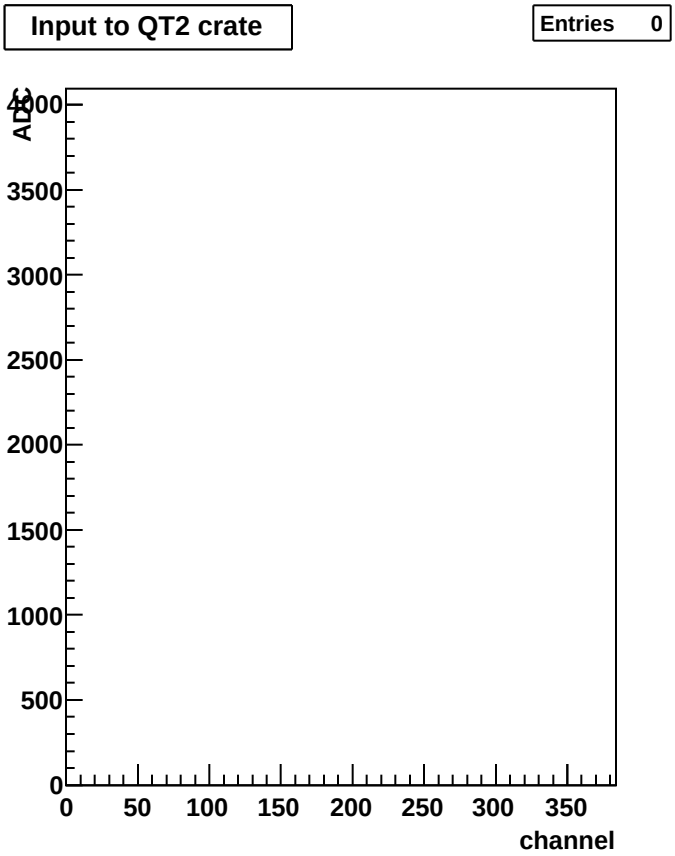

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1358511
Mean 4072
RMS 418.6

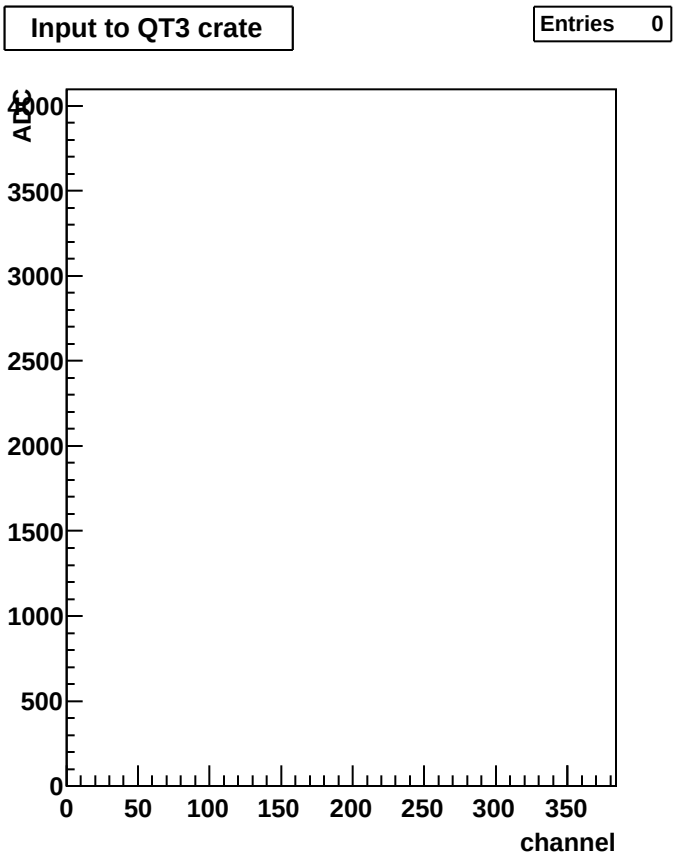

VPD-TAC-E
Entries 1299977
Mean 1307
RMS 152.2

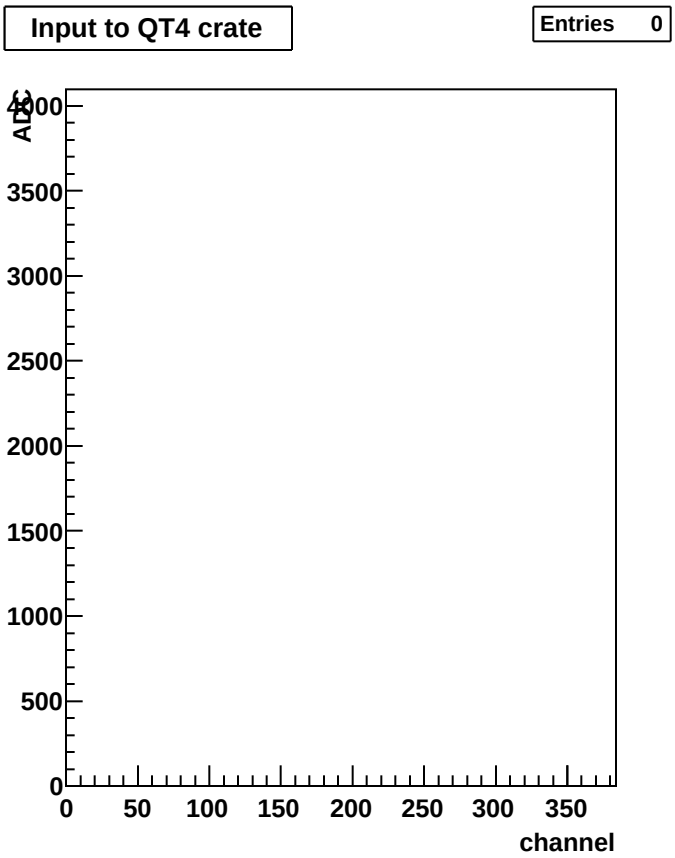

VPD-TAC-W
Entries 1301576
Mean 1422
RMS 154.8

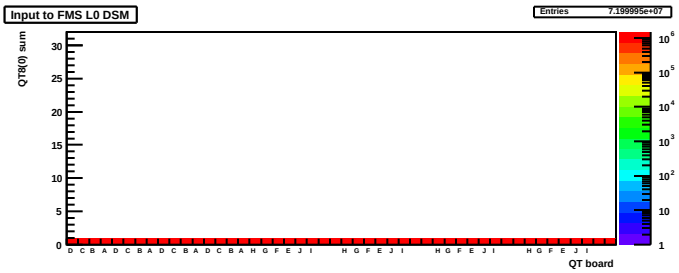

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1206753
Mean 3982
RMS 86.45

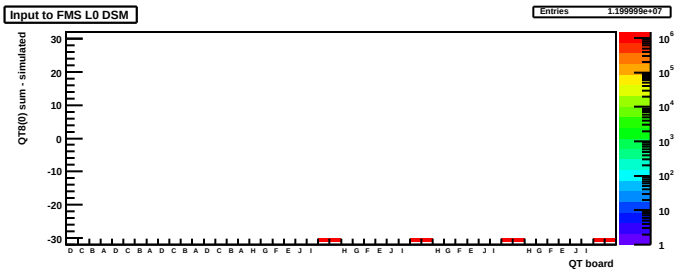

Input to QT1 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

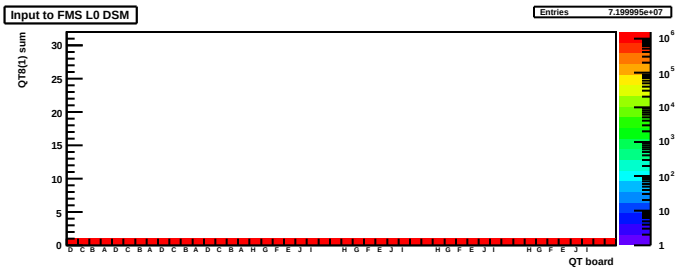

Input to QT2 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

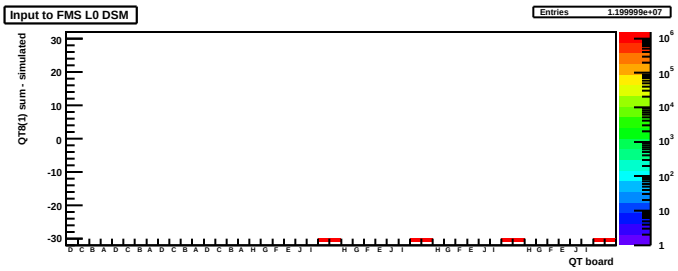

Input to QT3 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

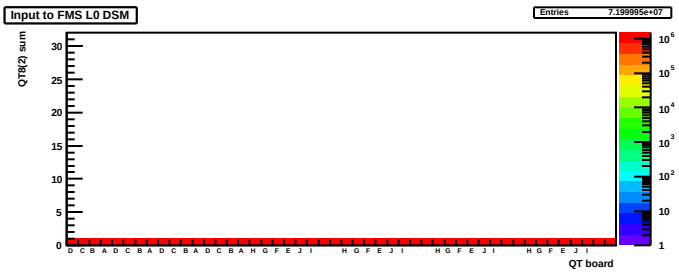

Input to QT4 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

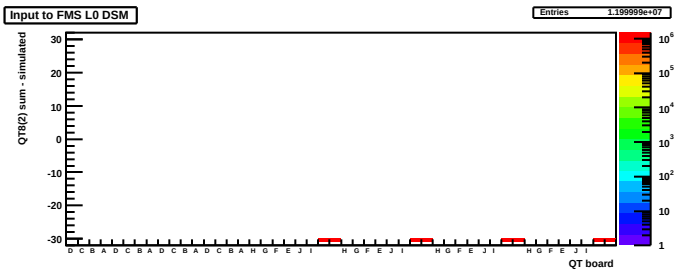

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(0) sum
QT board

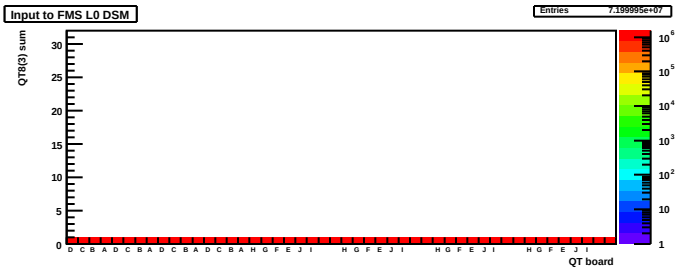

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(0) sum - simulated
QT board

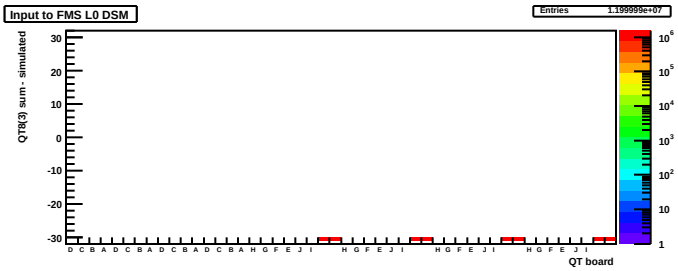

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(1) sum
QT board

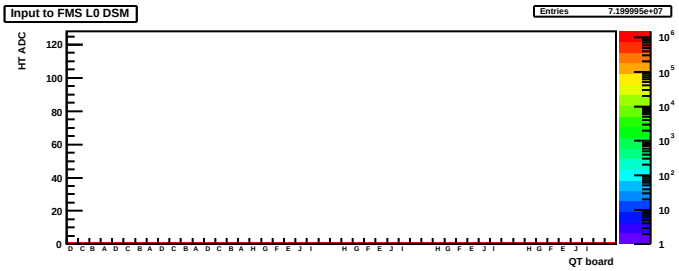

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(1) sum - simulated
QT board

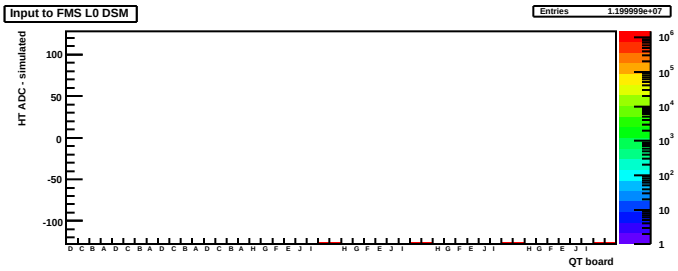

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(2) sum
QT board

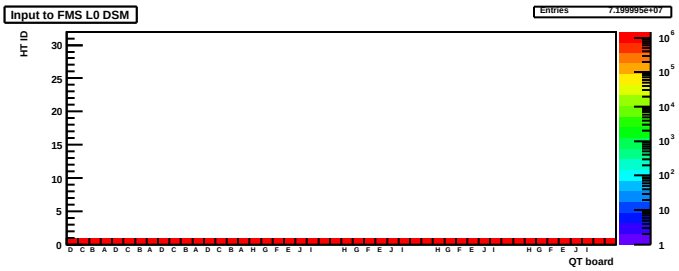

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(2) sum - simulated
QT board

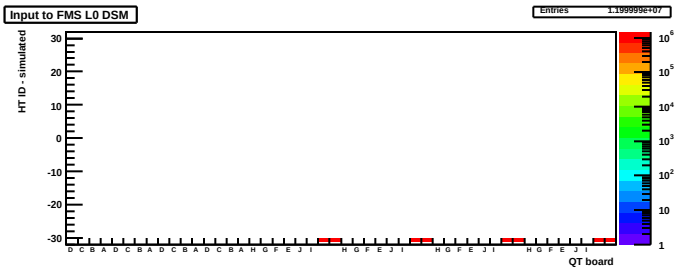

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(3) sum
QT board

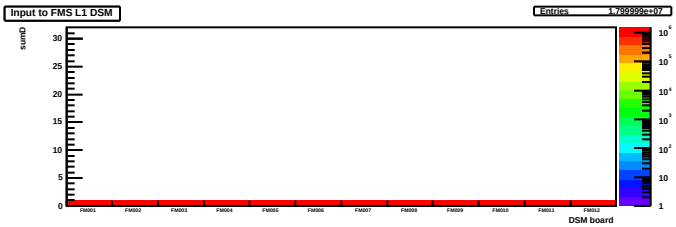

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(3) sum - simulated
QT board

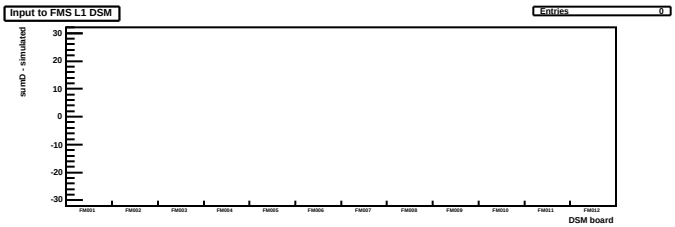

Input to FMS L0 DSM
Entries 7.199995e+07
HT ADC
QT board

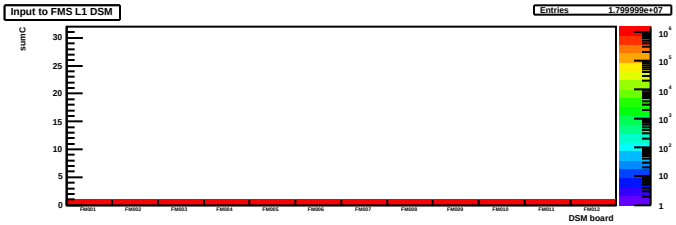

Input to FMS L0 DSM
Entries 1.199999e+07
HT ADC - simulated
QT board

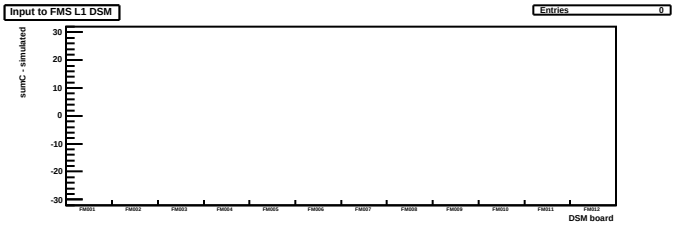

Input to FMS L0 DSM
Entries 7.199995e+07
HT ID
QT board

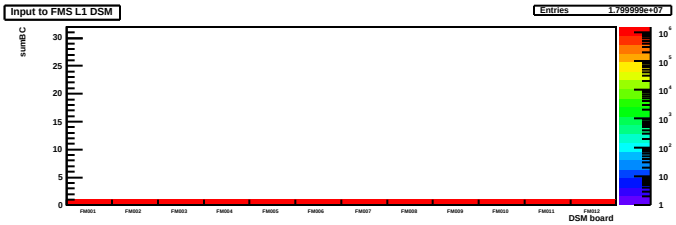

Input to FMS L0 DSM
Entries 1.199999e+07
HT ID - simulated
QT board

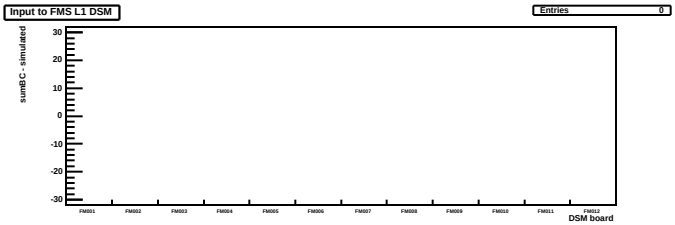

Input to FMS L1 DSM
Entries 1.799999e+07
sumD
DSM board

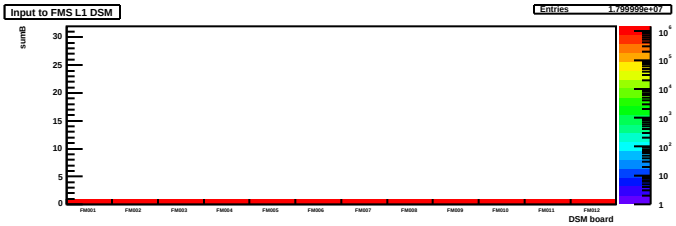

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

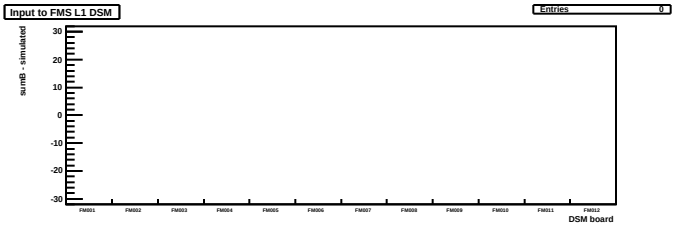

Input to FMS L1 DSM
Entries 1.799999e+07
sumC
DSM board

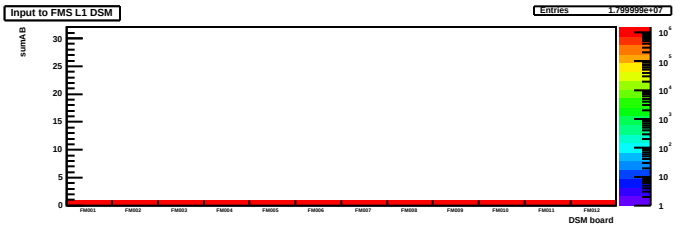

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

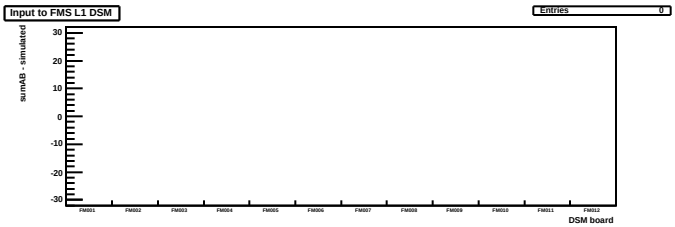

Input to FMS L1 DSM
Entries 1.799999e+07
sumBC
DSM board

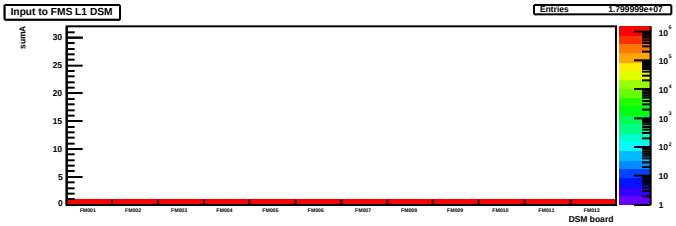

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

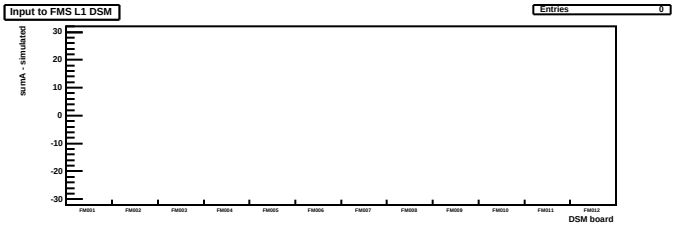

Input to FMS L1 DSM
Entries 1.799999e+07
sumB
DSM board

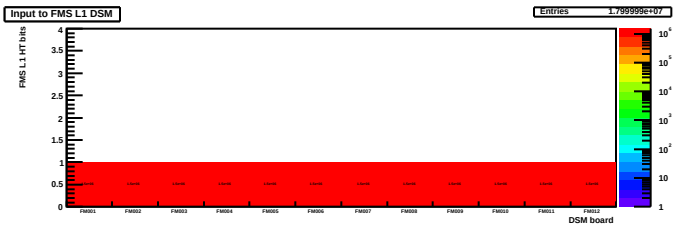

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

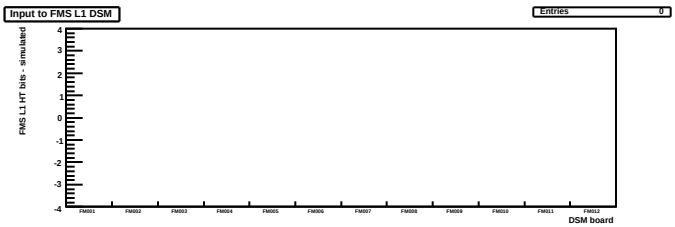

Input to FMS L1 DSM
Entries 1.799999e+07
sumAB
DSM board

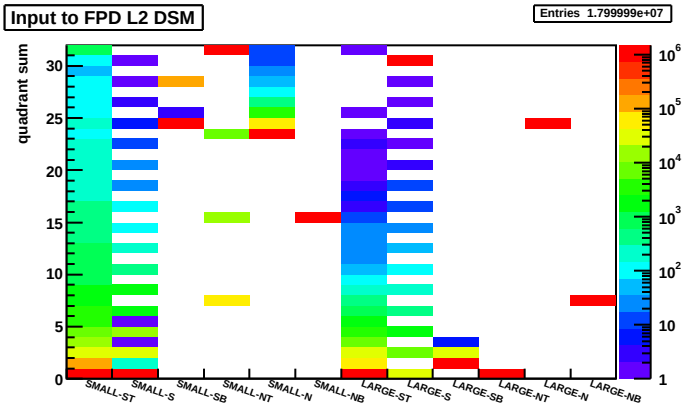

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

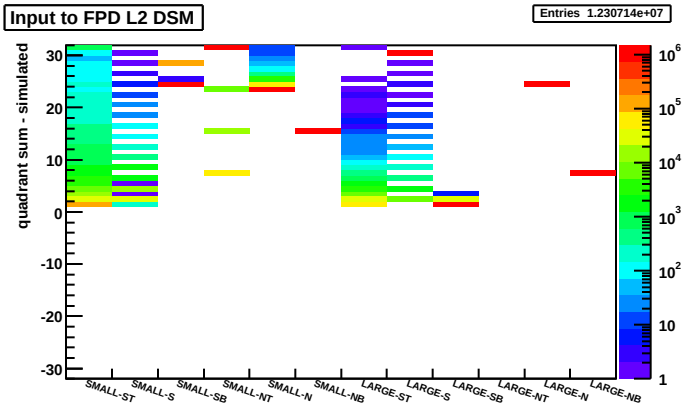

Input to FMS L1 DSM
Entries 1.799999e+07
sumA
DSM board

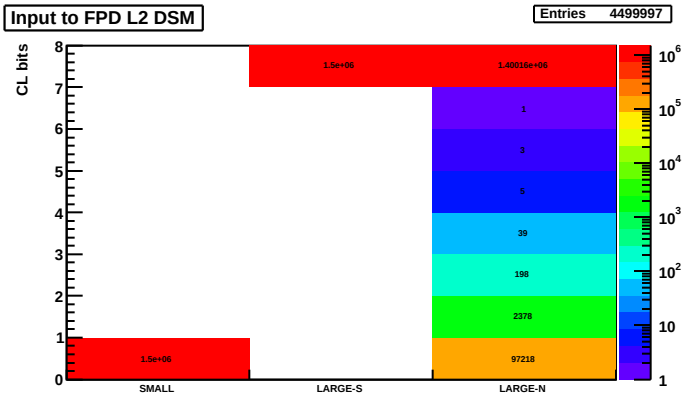

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

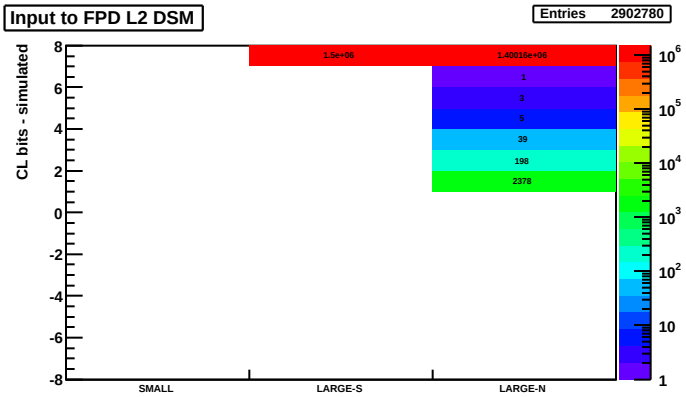

Input to FMS L1 DSM
Entries 1.799999e+07
FMS L1 HT bits
DSM board

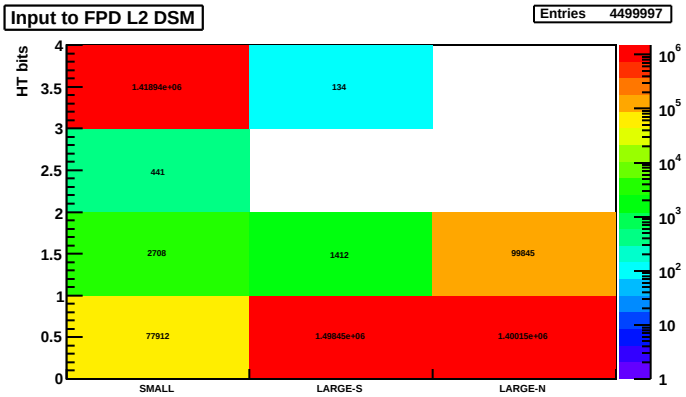

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

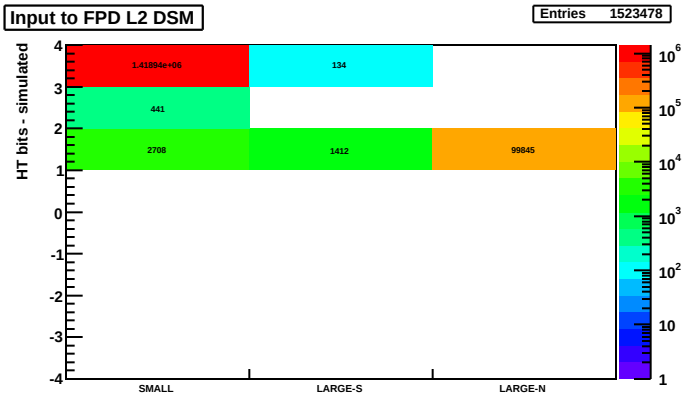

Input to FPD L2 DSM
Entries 1.799999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

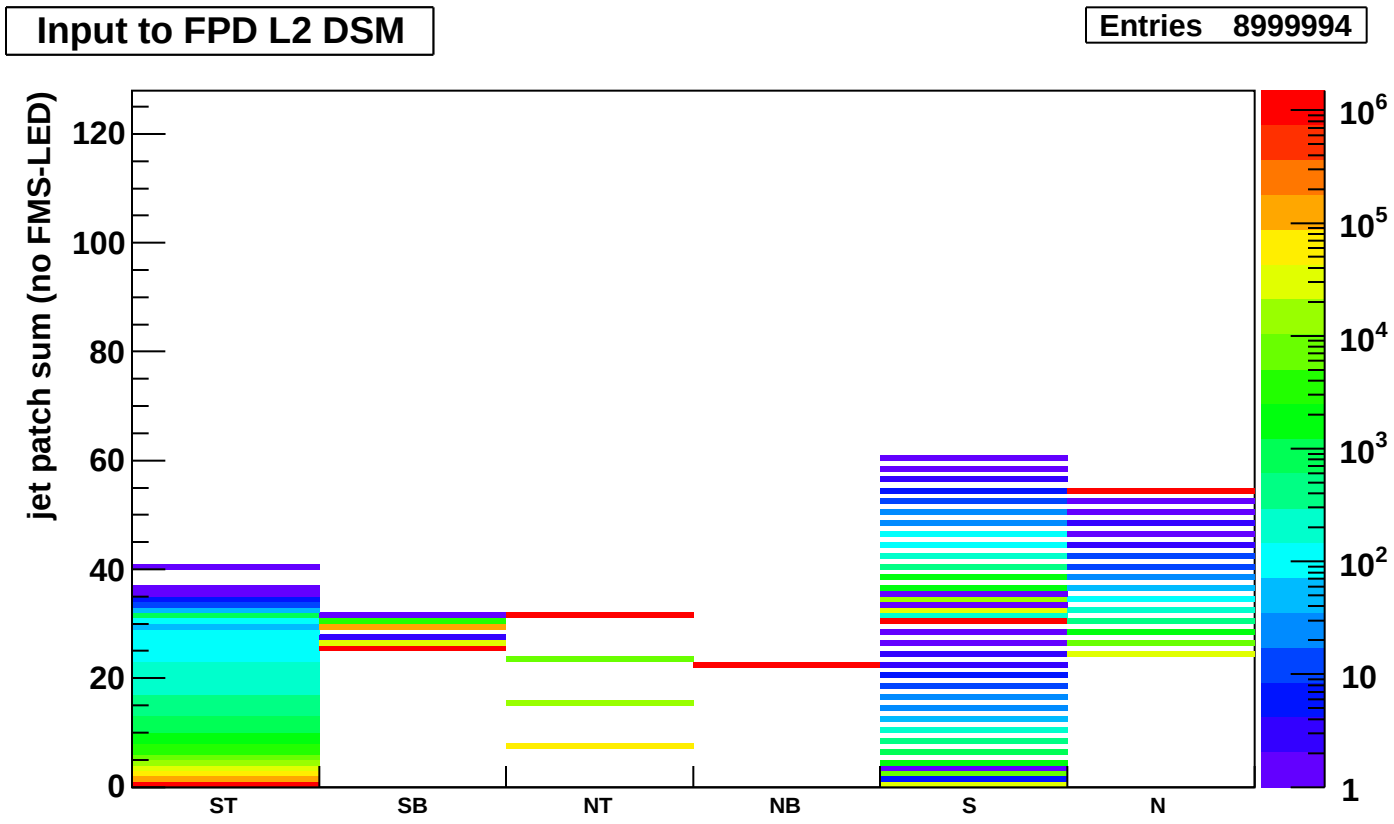

Input to FPD L2 DSM
Entries 1.230714e+07
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

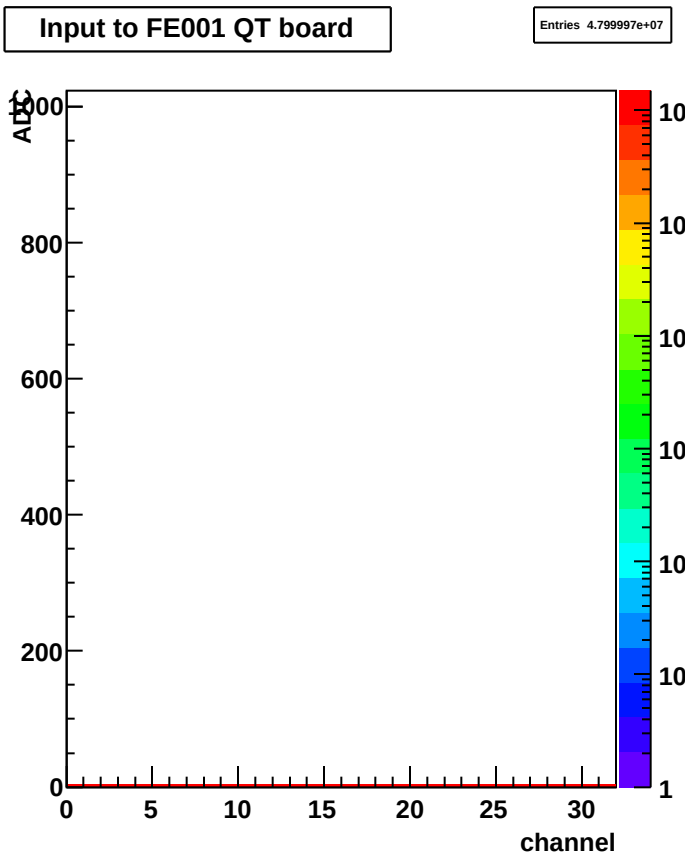

Input to FPD L2 DSM
Entries 4499997
CL bits
1.5e+06
1.40016e+06
1
3
5
39
198
2378
97218
SMALL
LARGE-S
LARGE-N

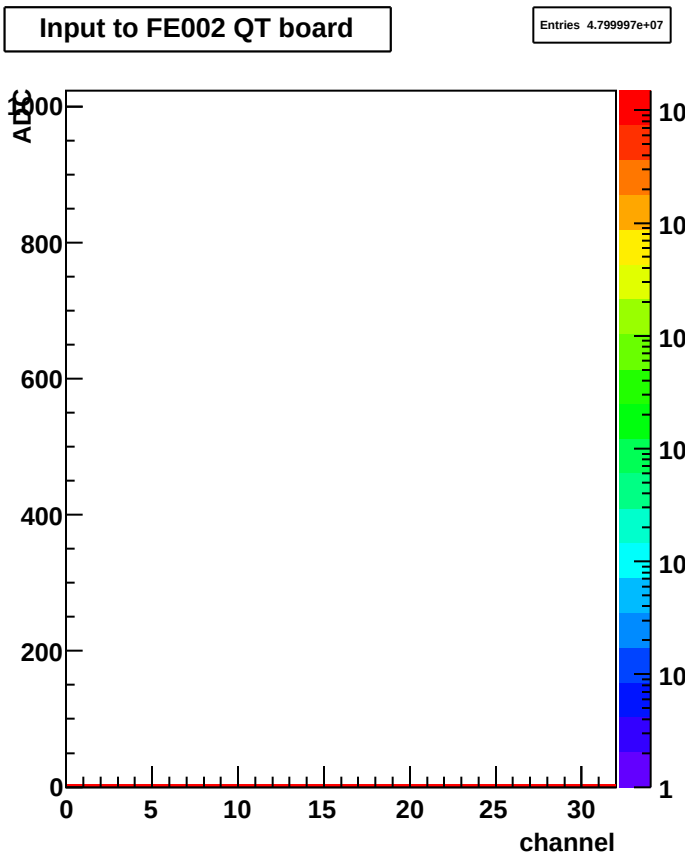

Input to FPD L2 DSM
Entries 2902780
CL bits - simulated
1.5e+06
1.40016e+06
1
3
5
39
198
2378
SMALL
LARGE-S
LARGE-N

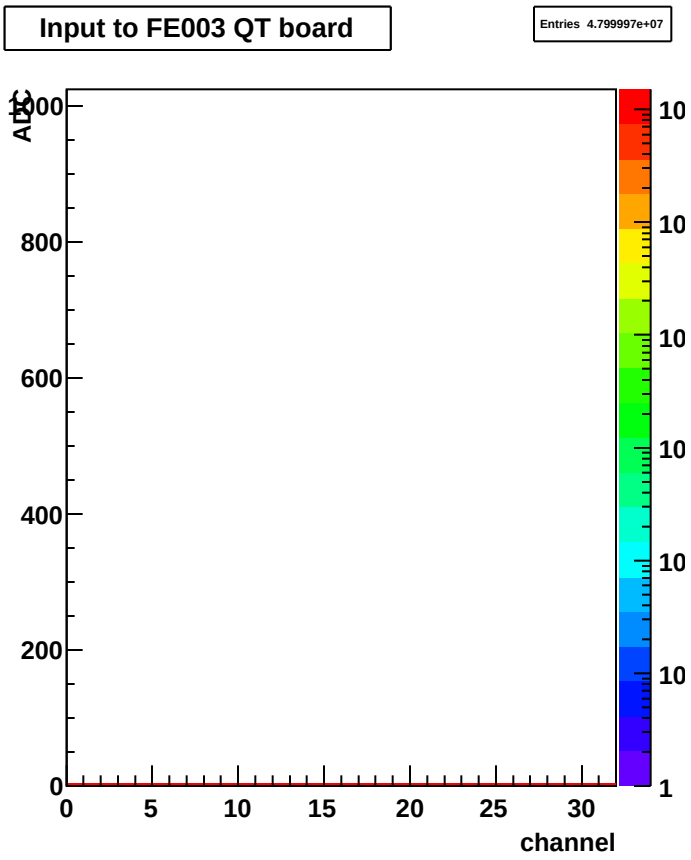

Input to FPD L2 DSM
Entries 4499997
HT bits
1.41894e+06
134
441
2708
1412
99845
77912
1.49845e+06
1.40015e+06
SMALL
LARGE-S
LARGE-N

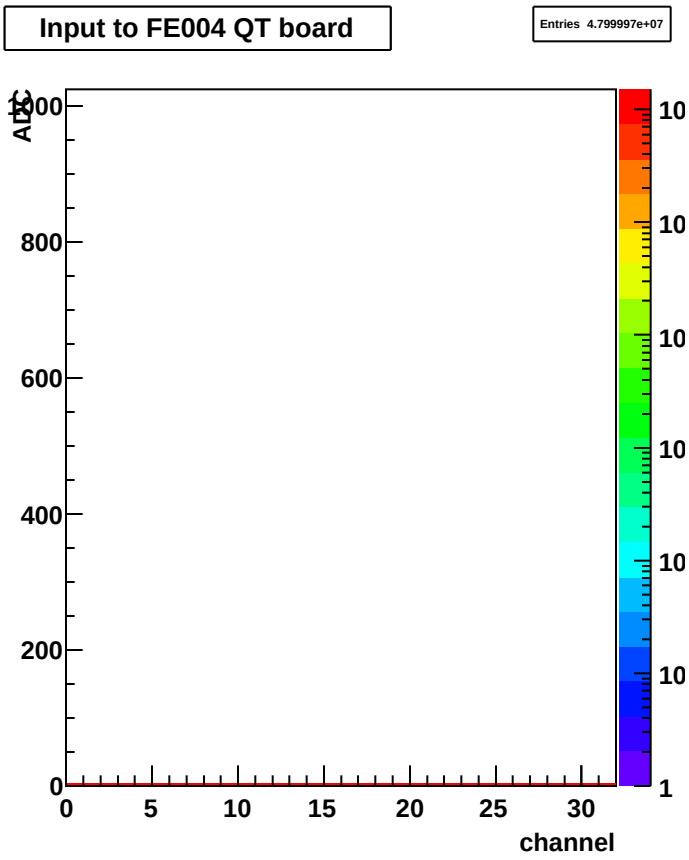

Input to FPD L2 DSM
Entries 1523478
HT bits - simulated
1.41894e+06
134
441
2708
1412
99845
SMALL
LARGE-S
LARGE-N

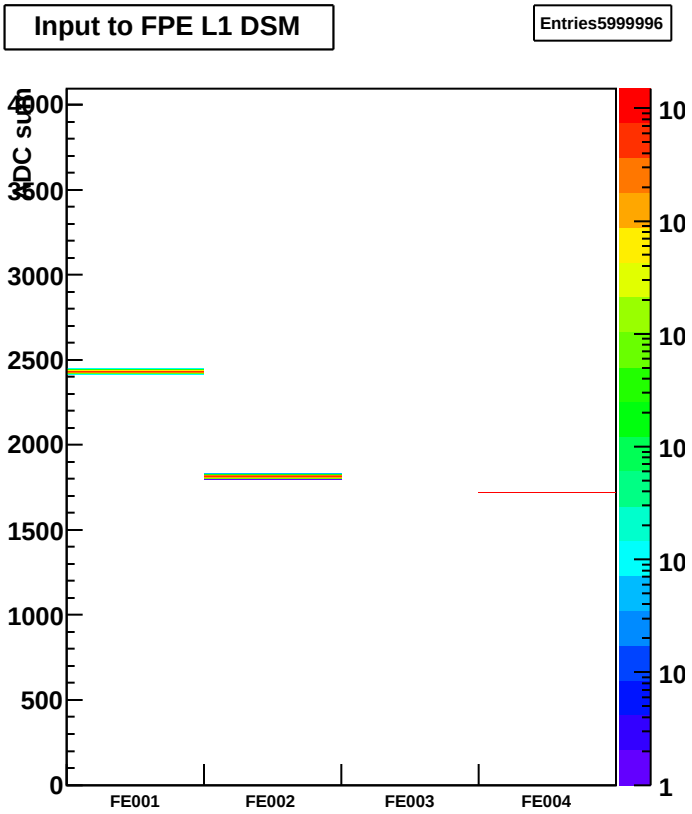

Input to FPD L2 DSM
Entries 8999994
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

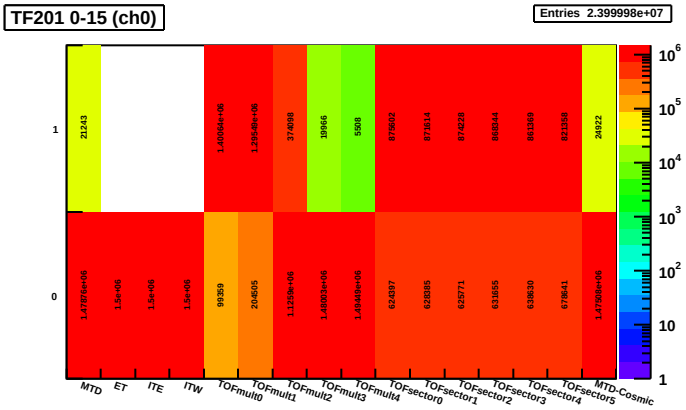

Input to FE001 QT board
Entries 4.799997e+07
ADC
channel

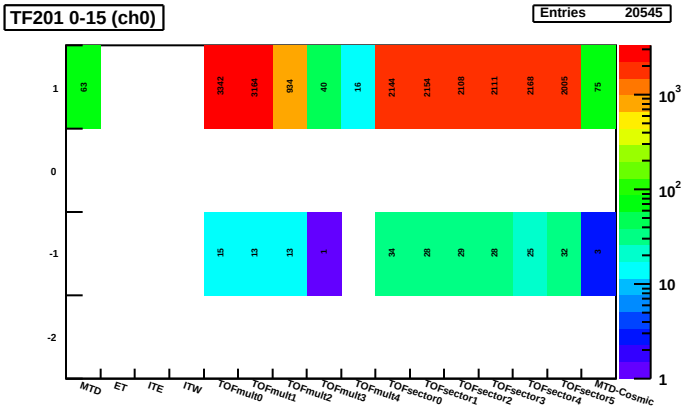

Input to FE002 QT board
Entries 4.799997e+07
ADC
channel


Input to FE003 QT board
Entries 4.799997e+07
ADC
channel


Input to FE004 QT board
Entries 4.799997e+07
ADC
channel


Input to FPE L1 DSM
Entries5999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.399998e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 20545
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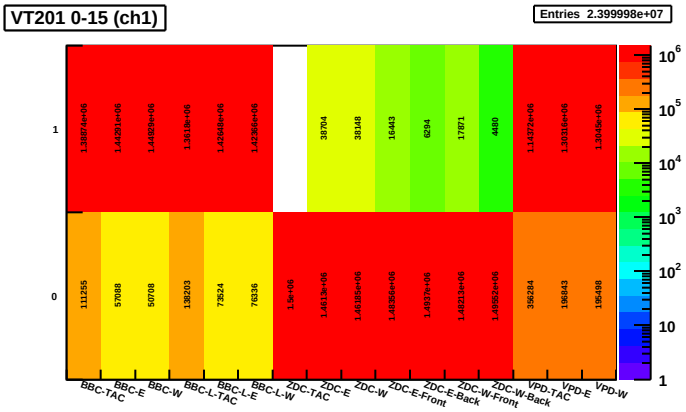
VT201 0-15 (ch1)
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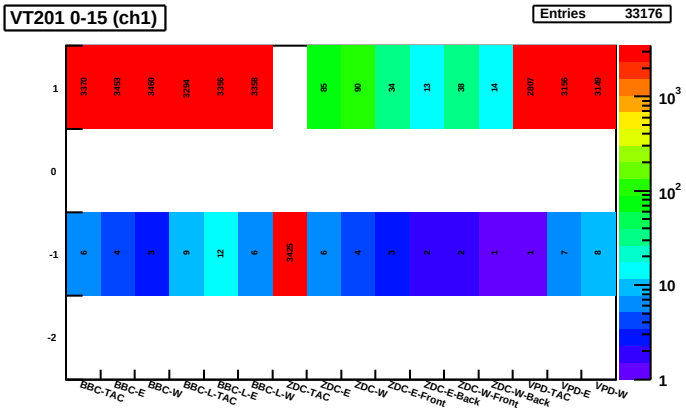
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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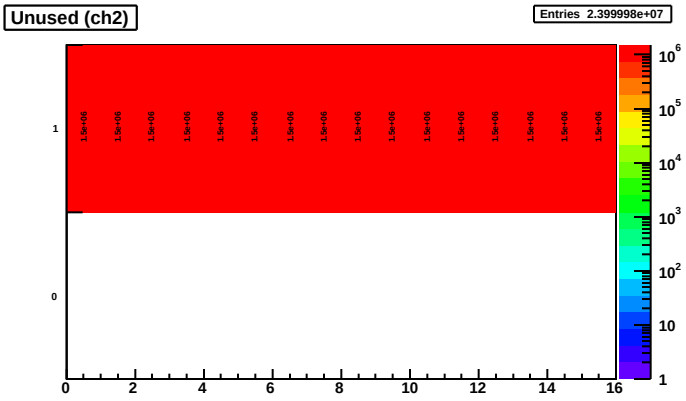
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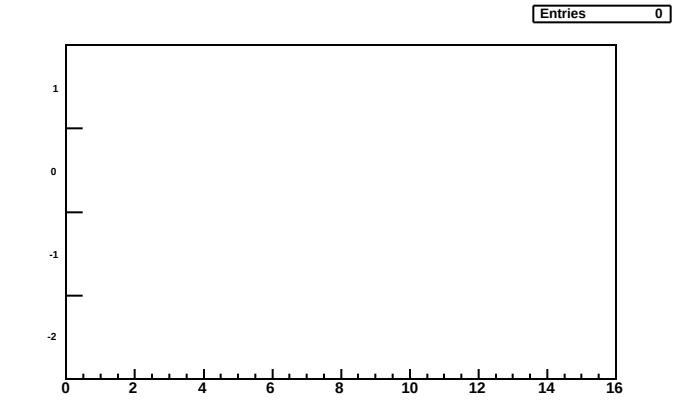
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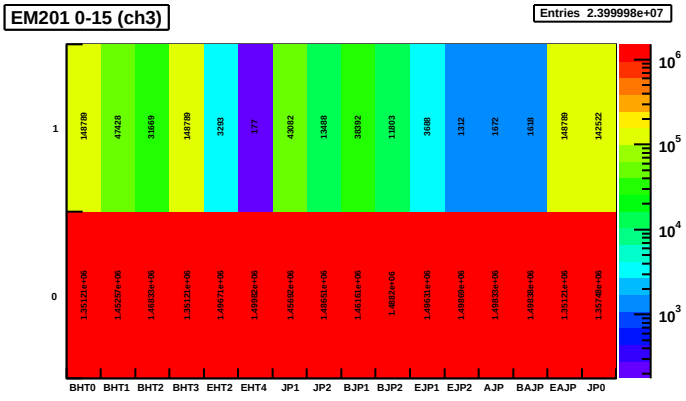
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Entries 2.399998e+07
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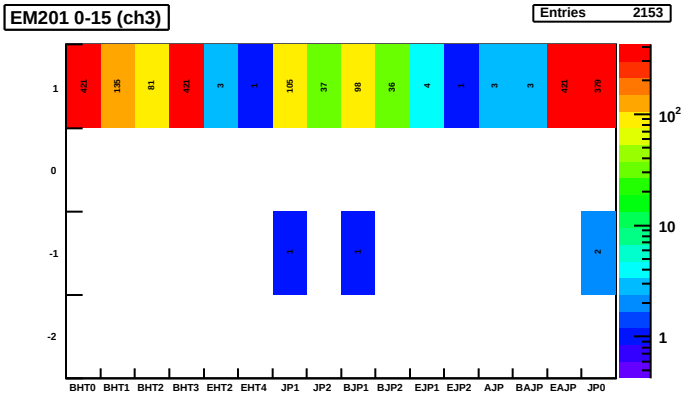
EM201 0-15 (ch3)
Entries 2153
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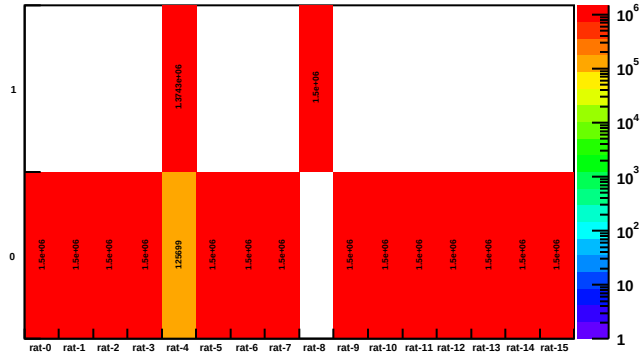
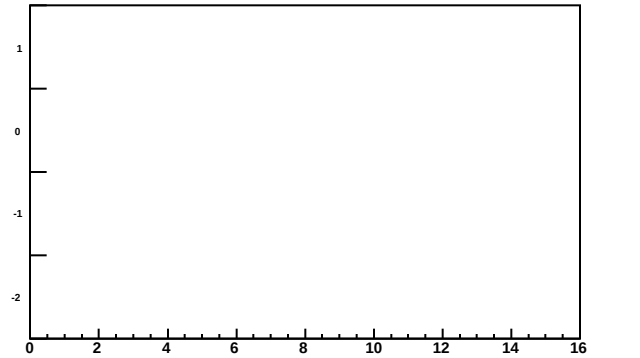
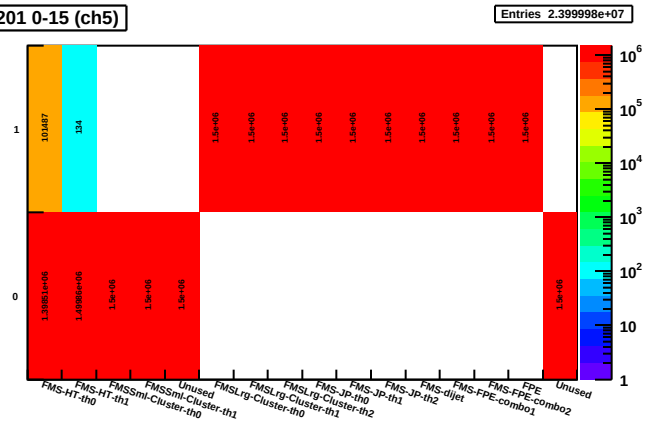
RAT board (ch4)





FP201 0-15 (ch5)



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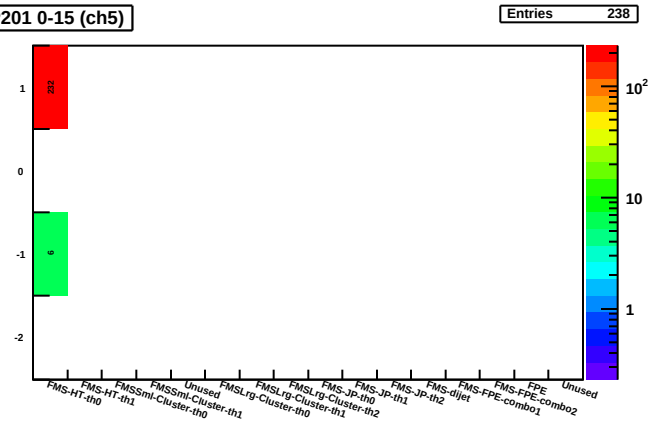
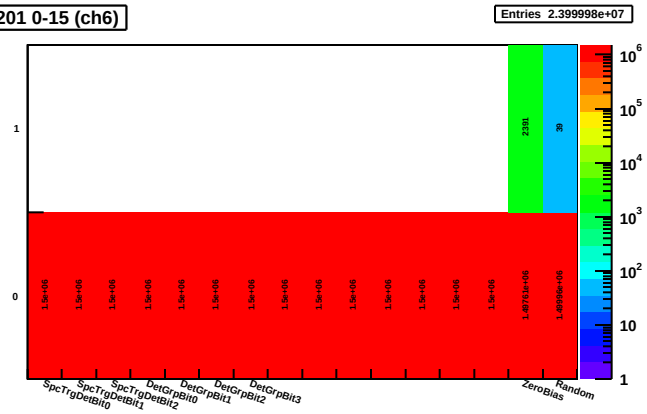
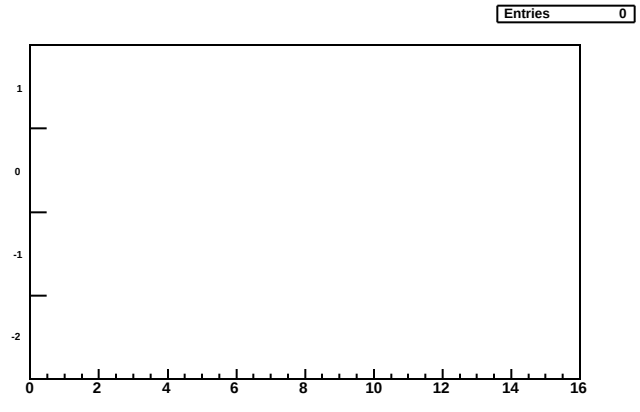
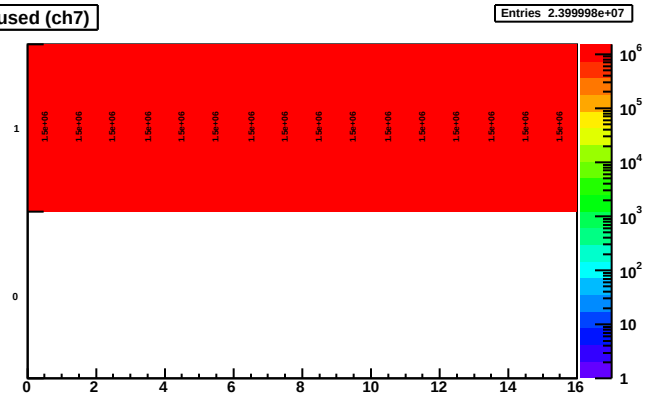
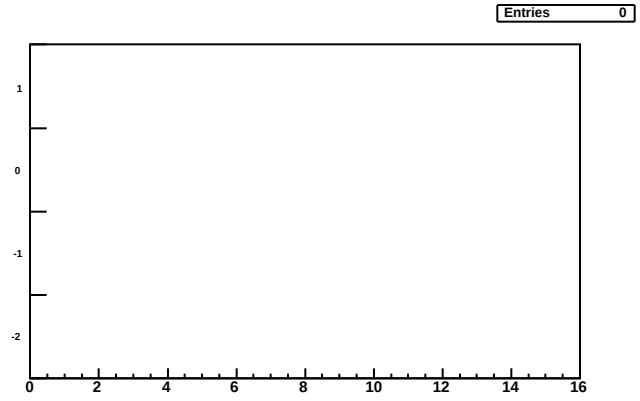


ST201 0-15 (ch6)





Unused (ch7)