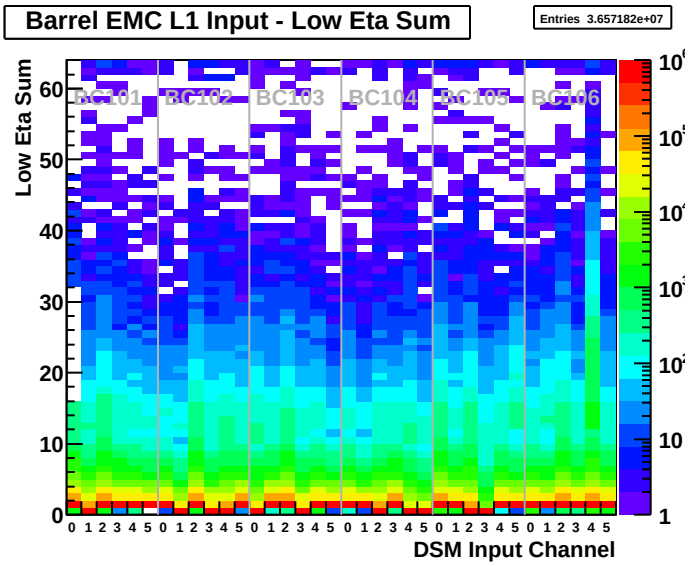
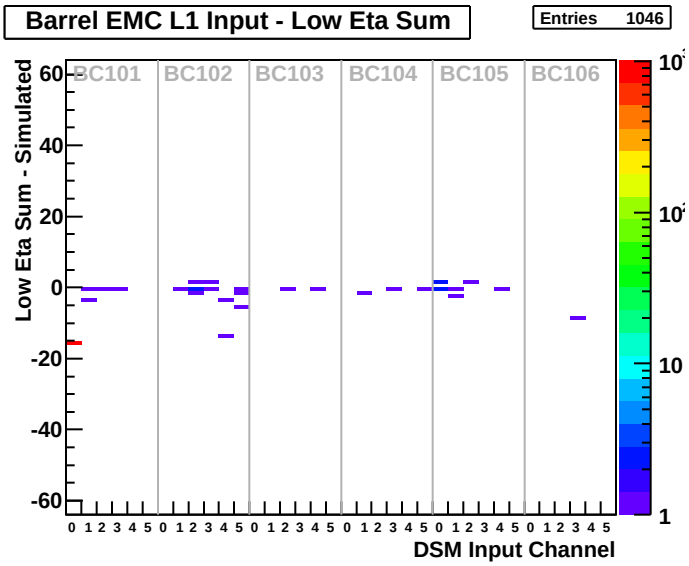
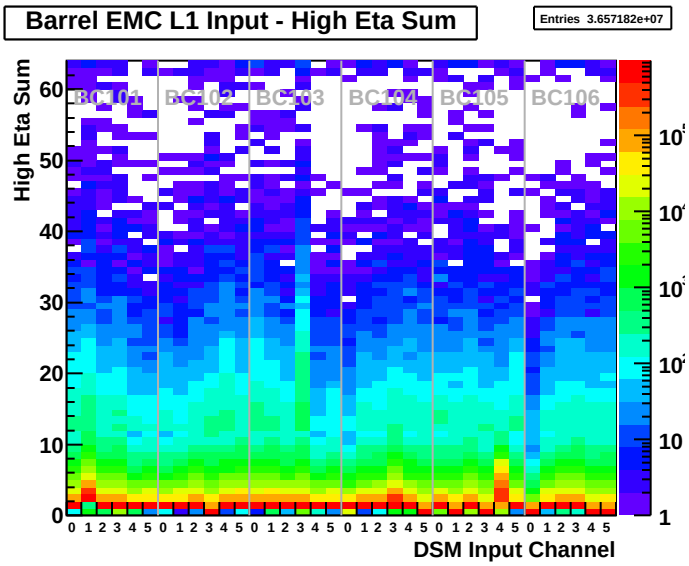
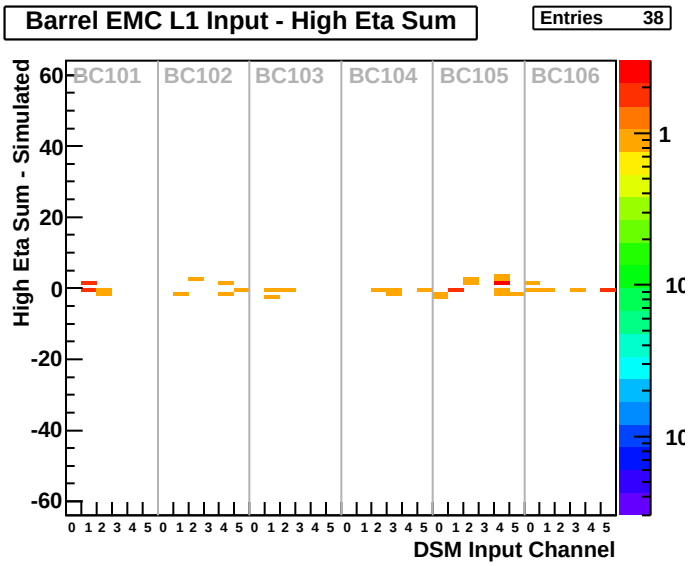
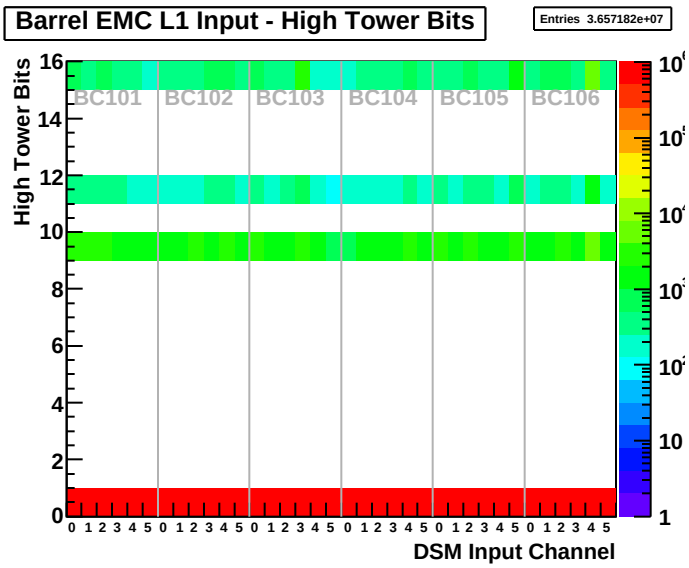
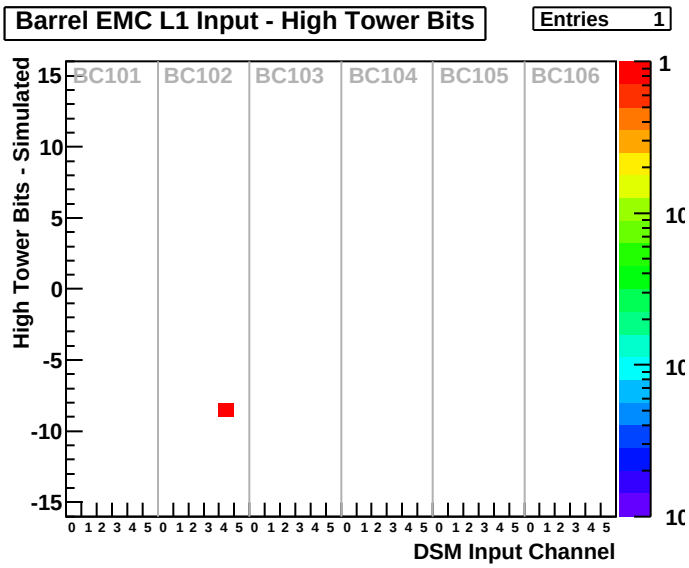

Barrel EMC L1 Input - Low Eta Sum
Entries 3.657182e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

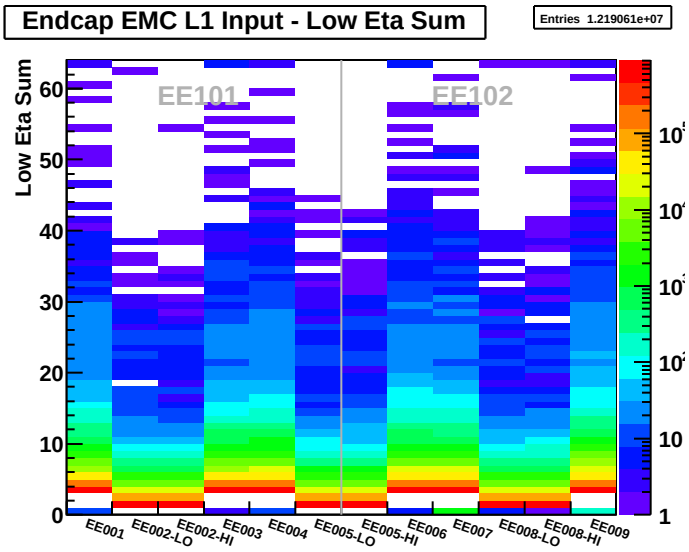

Barrel EMC L1 Input - Low Eta Sum
Entries 1046
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

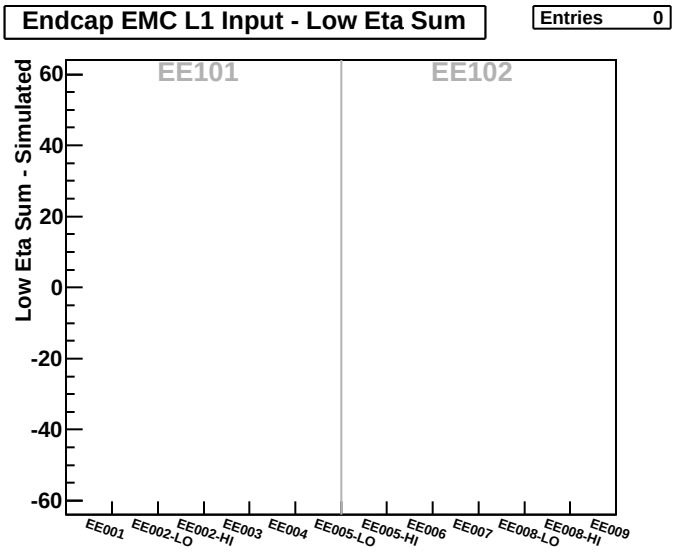

Barrel EMC L1 Input - High Eta Sum
Entries 3.657182e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

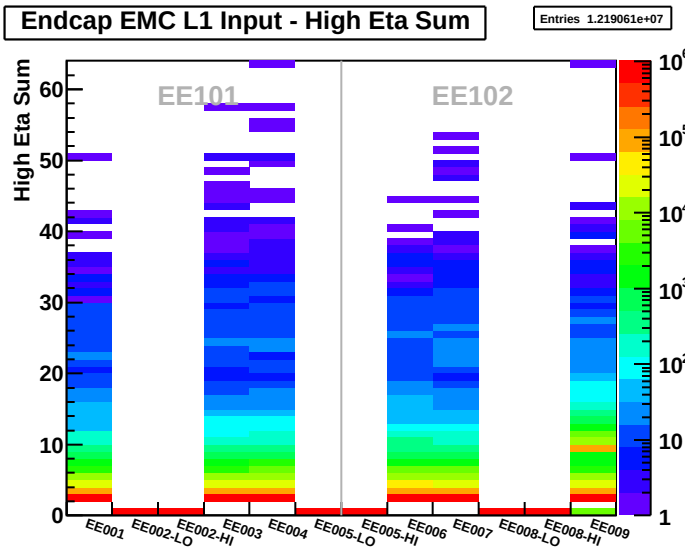

Barrel EMC L1 Input - High Eta Sum
Entries 38
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

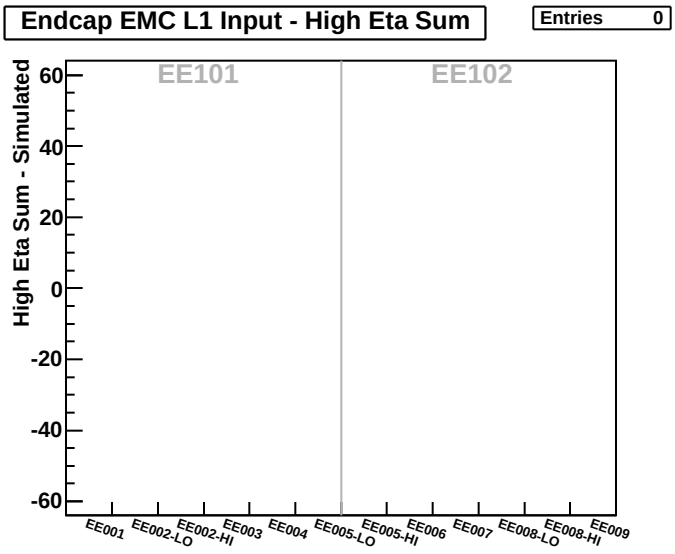

Barrel EMC L1 Input - High Tower Bits
Entries 3.657182e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

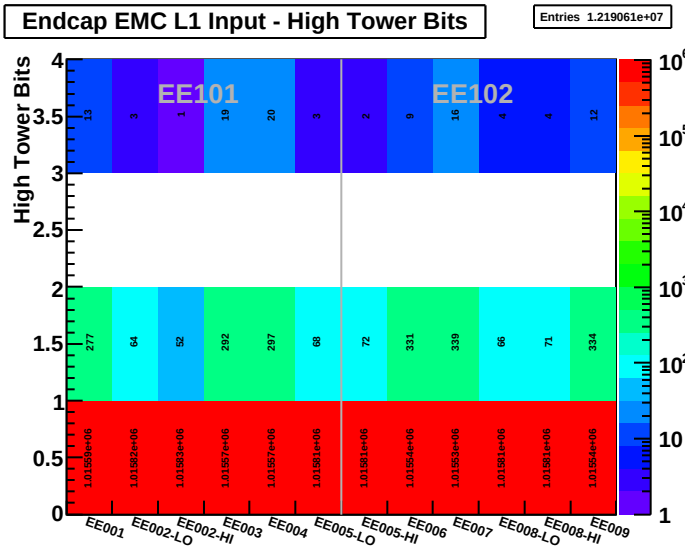

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

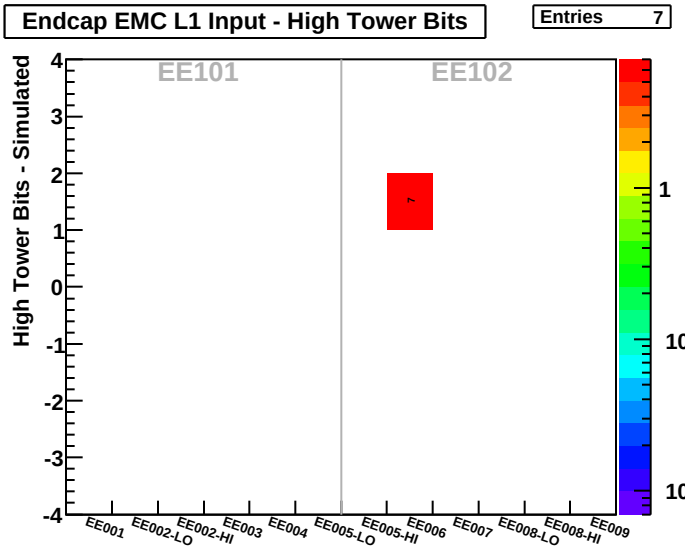

Endcap EMC L1 Input - Low Eta Sum
Entries 1.219061e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

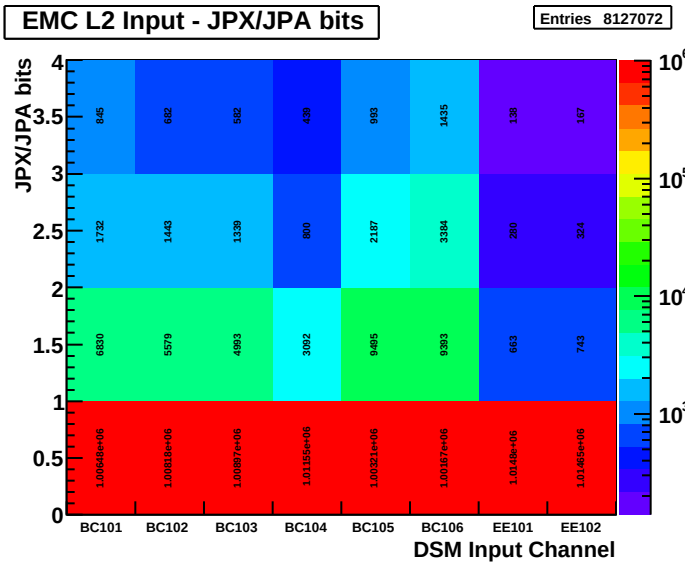

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

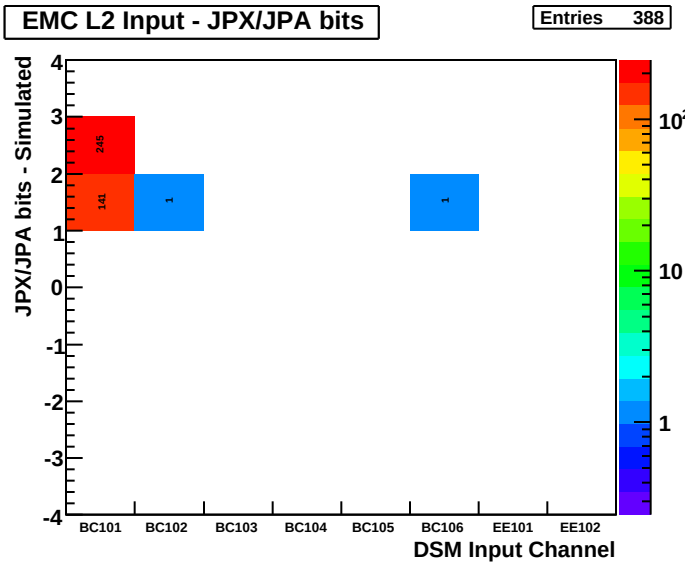

Endcap EMC L1 Input - High Eta Sum
Entries 1.219061e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

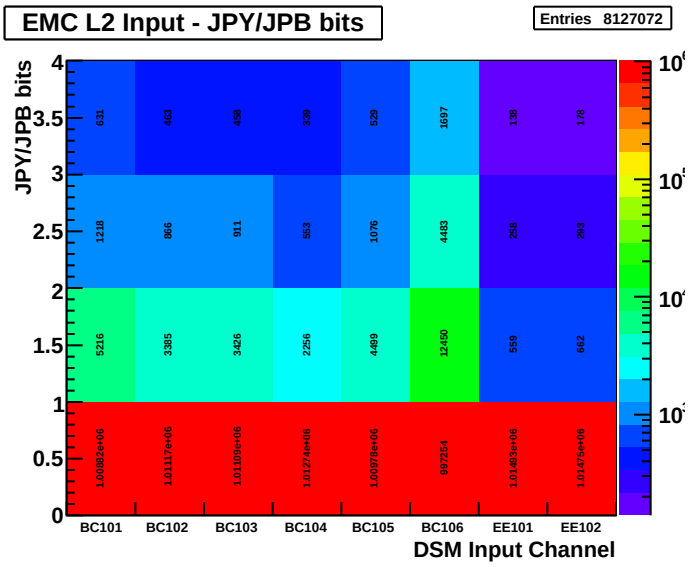

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

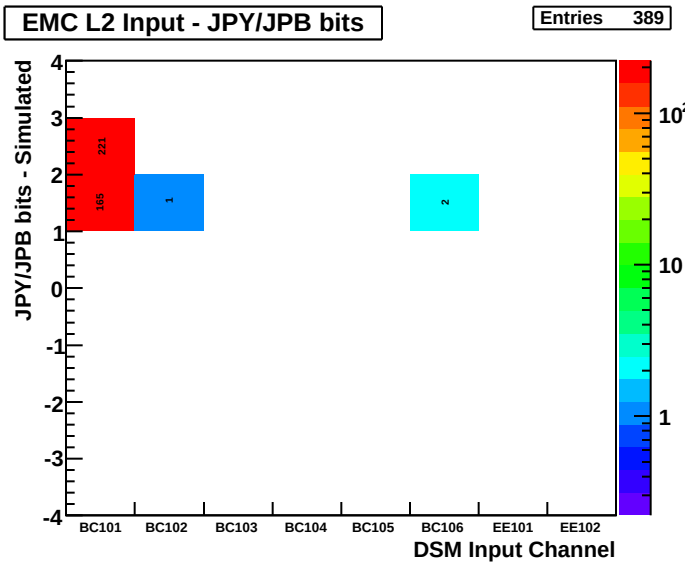

Endcap EMC L1 Input - High Tower Bits
Entries 1.219061e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

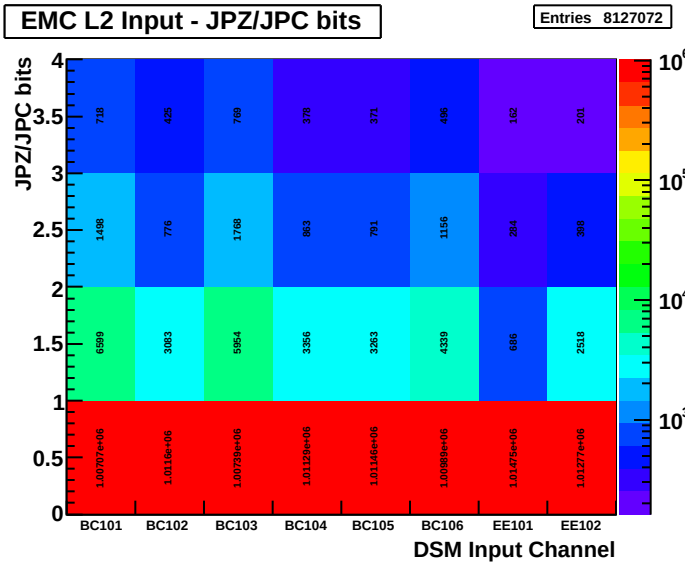

Endcap EMC L1 Input - High Tower Bits
Entries 7
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

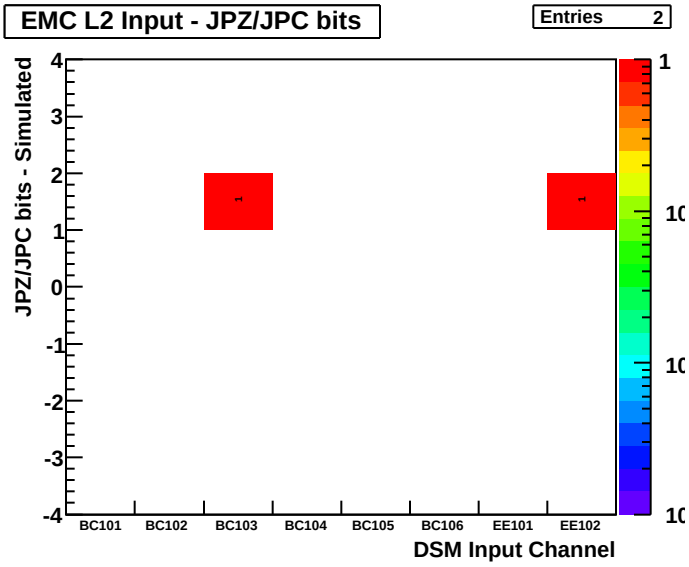

EMC L2 Input - JPX/JPA bits
Entries 8127072
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

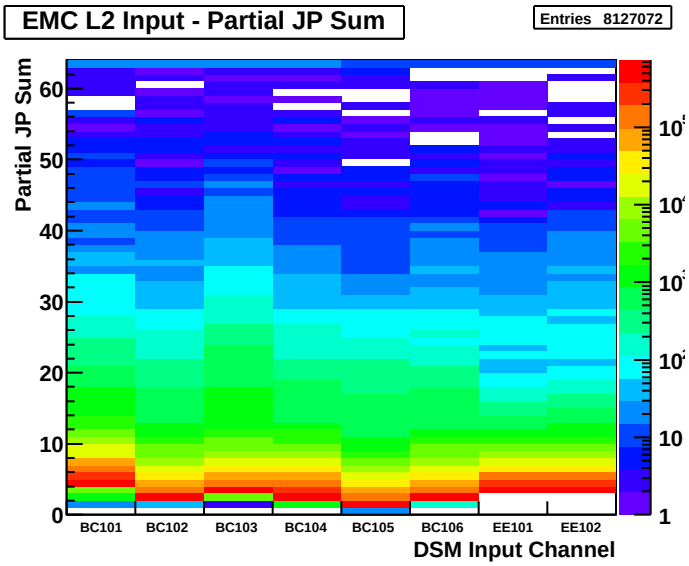

EMC L2 Input - JPX/JPA bits
Entries 388
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

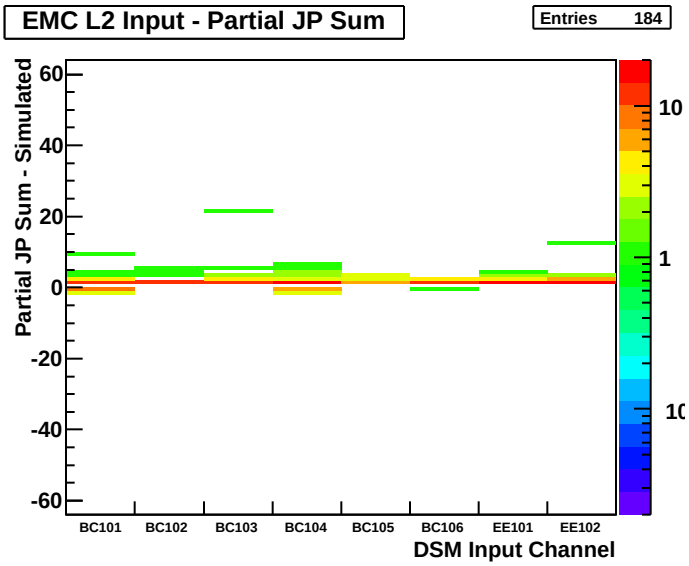

EMC L2 Input - JPY/JPB bits
Entries 8127072
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

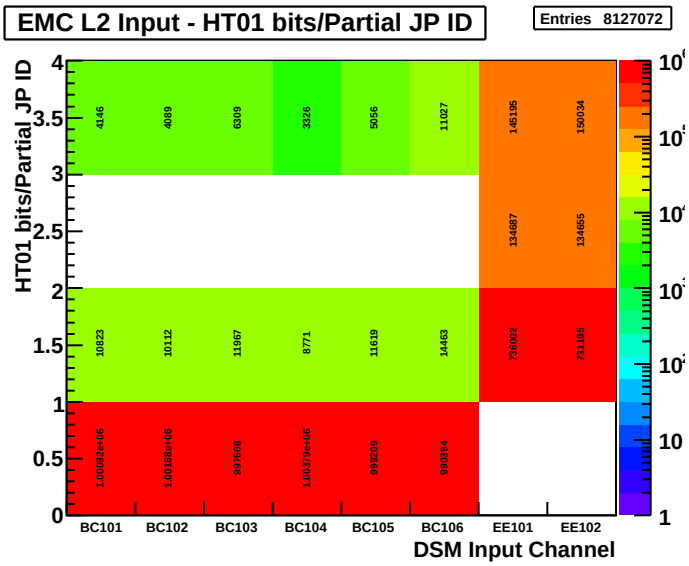

EMC L2 Input - JPY/JPB bits
Entries 389
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

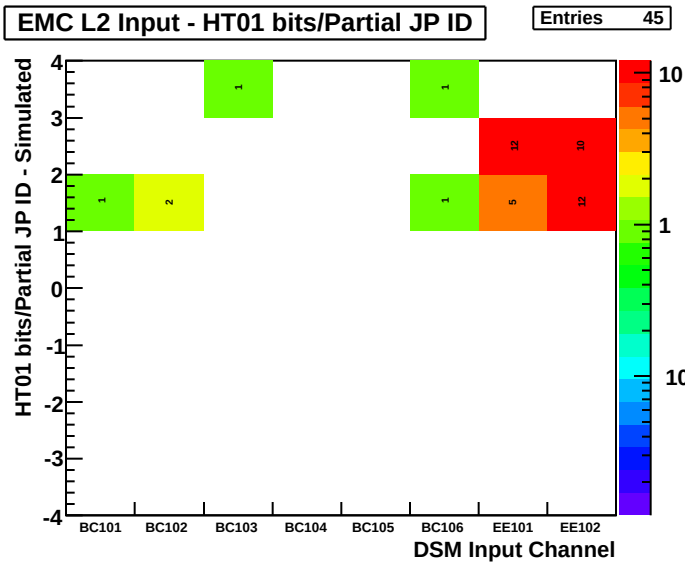

EMC L2 Input - JPZ/JPC bits
Entries 8127072
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

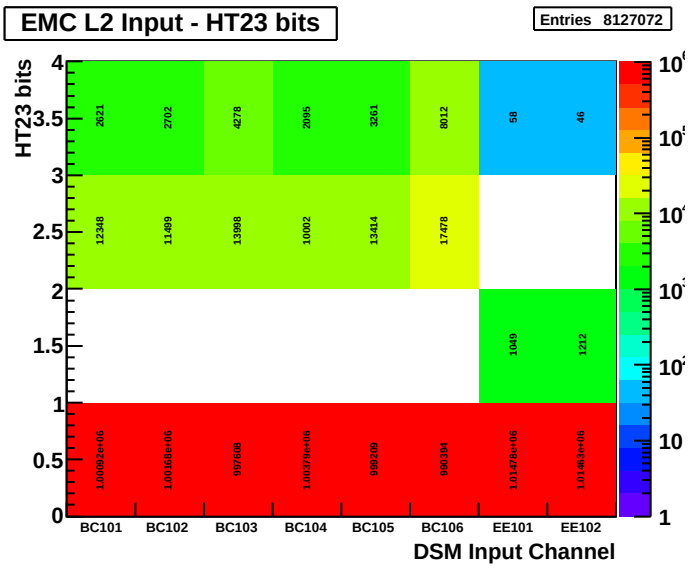

EMC L2 Input - JPZ/JPC bits
Entries 2
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

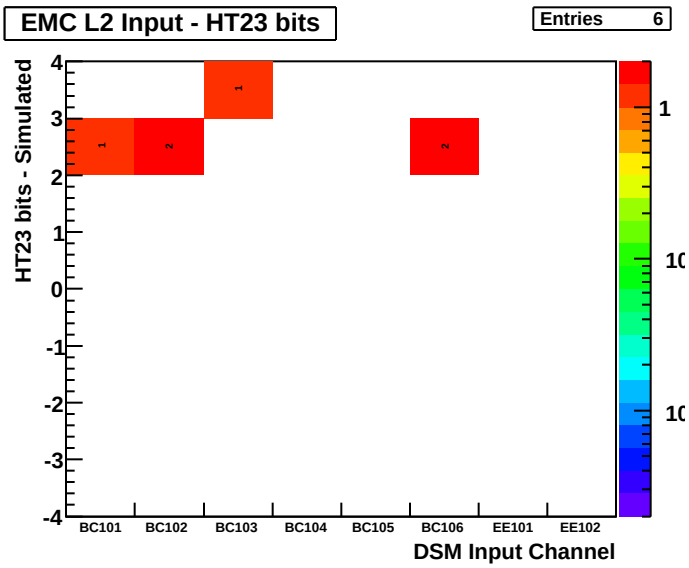

EMC L2 Input - Partial JP Sum
Entries 8127072
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

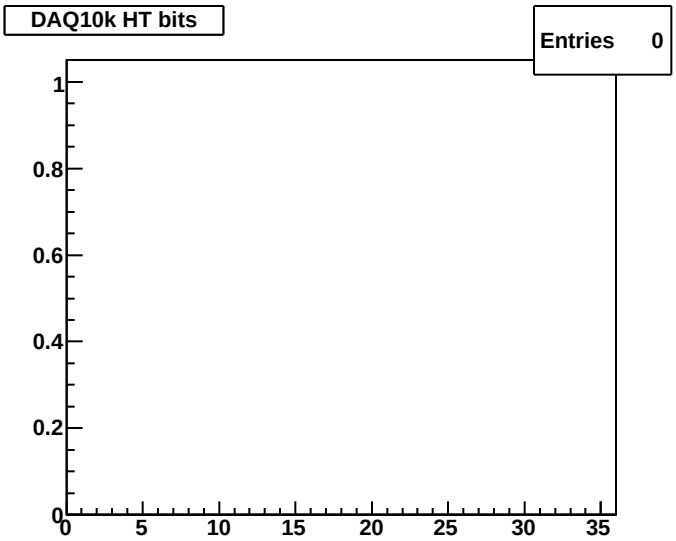

EMC L2 Input - Partial JP Sum
Entries 184
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

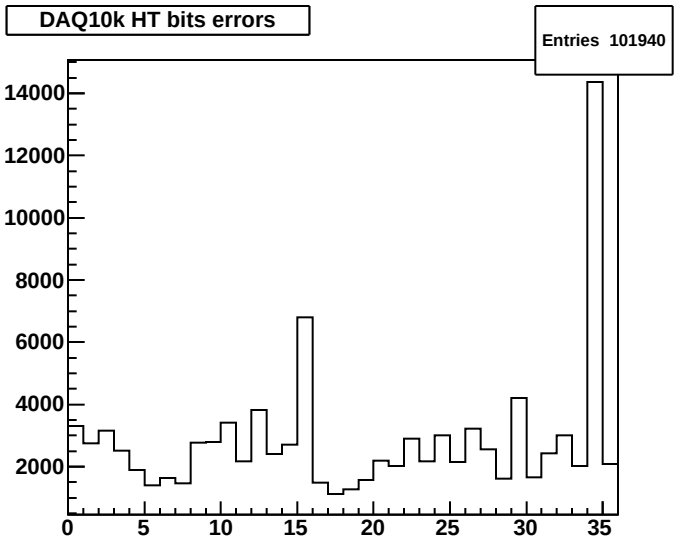

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8127072
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

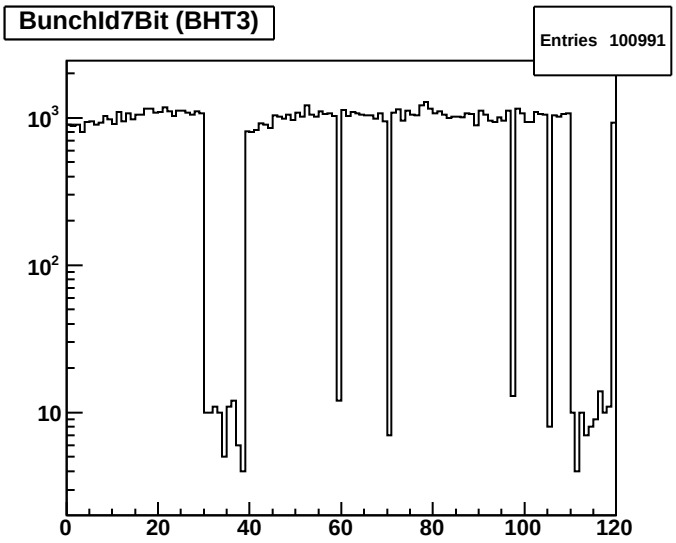

EMC L2 Input - HT01 bits/Partial JP ID
Entries 45
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

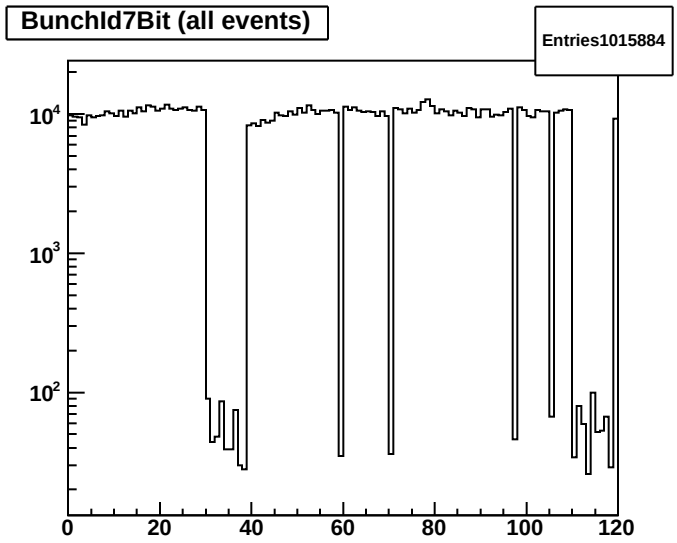

EMC L2 Input - HT23 bits
Entries 8127072
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

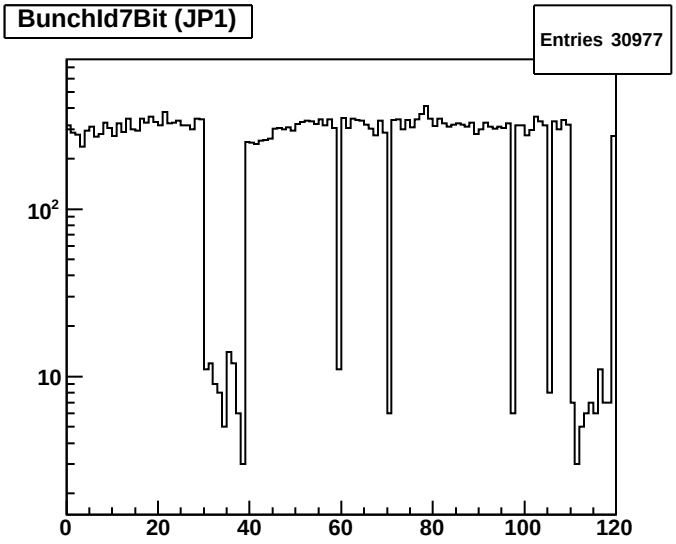

EMC L2 Input - HT23 bits
Entries 6
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

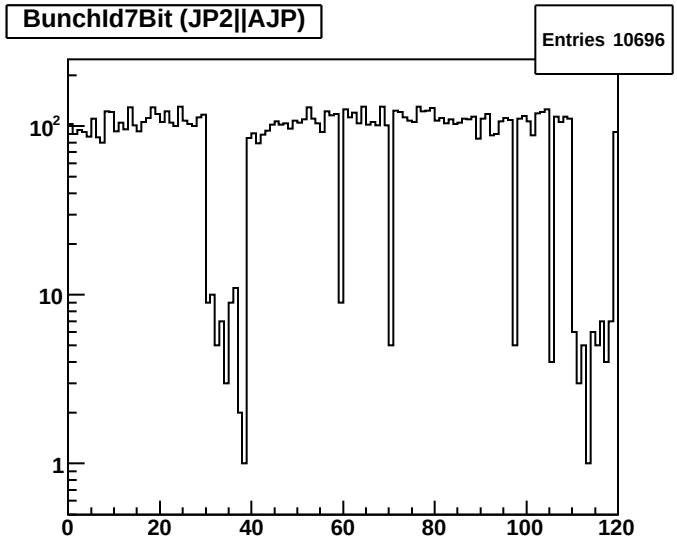

DAQ10k HT bits
Entries 0

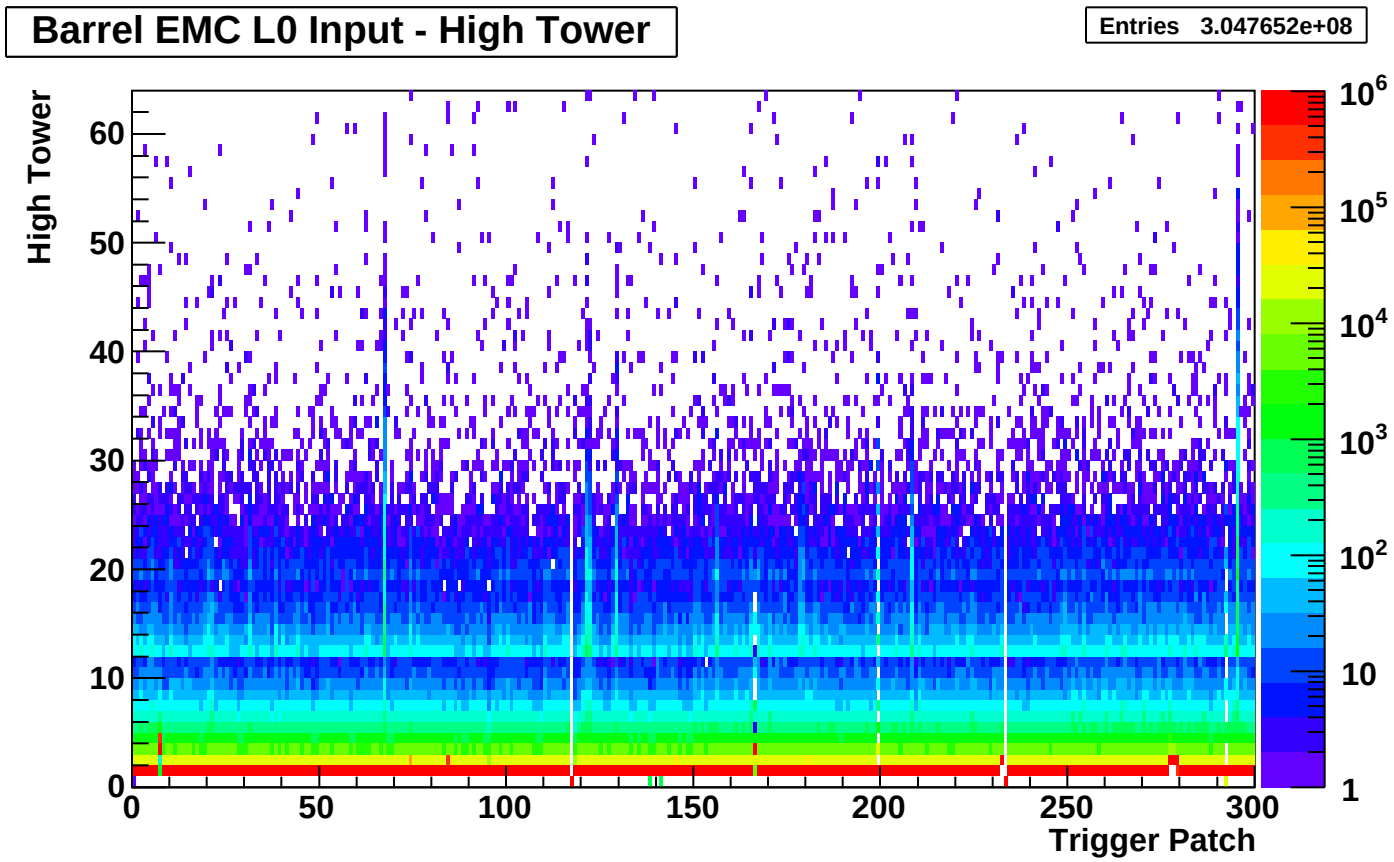

DAQ10k HT bits errors
Entries 101940

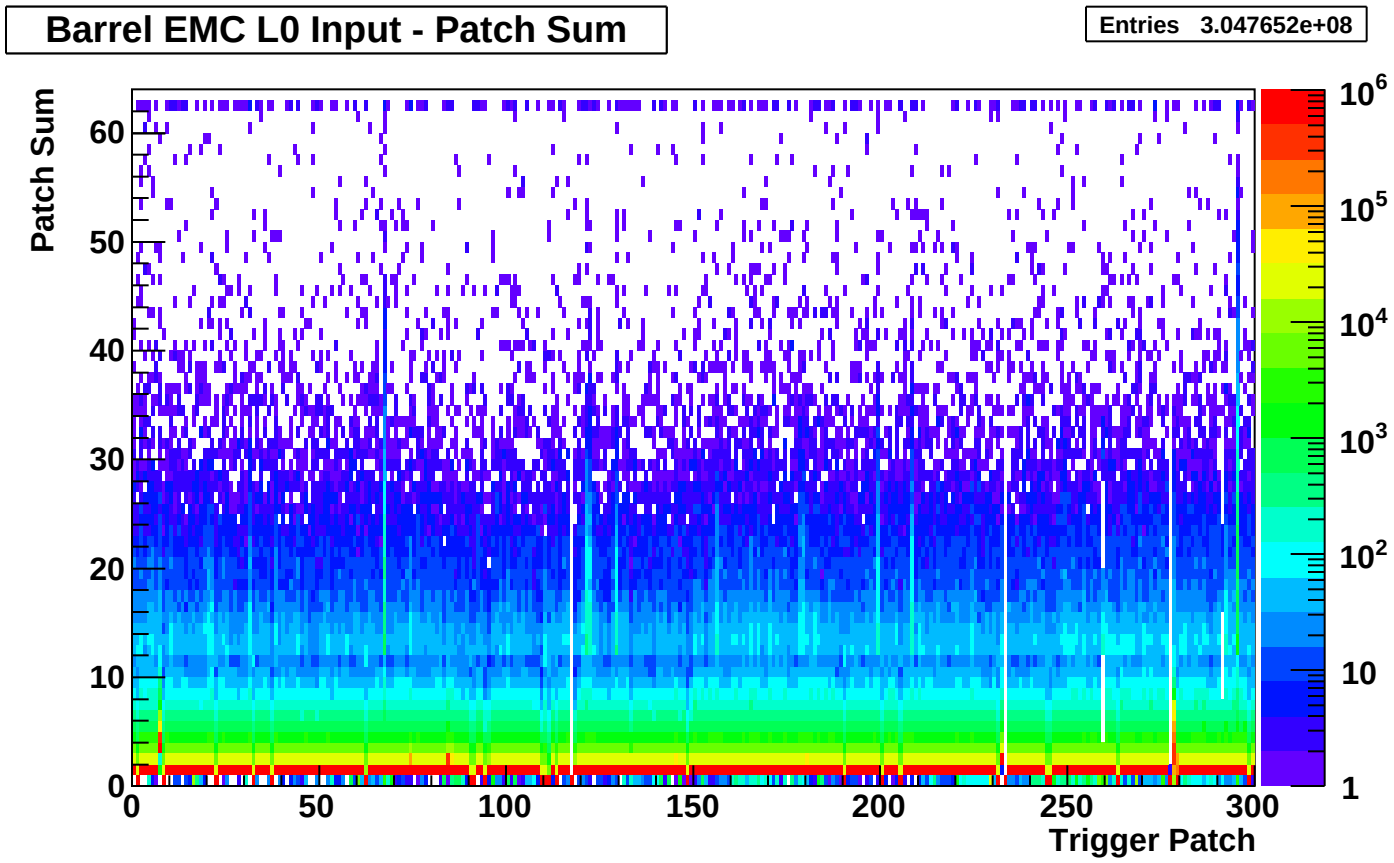

BunchId7Bit (BHT3)
Entries 100991

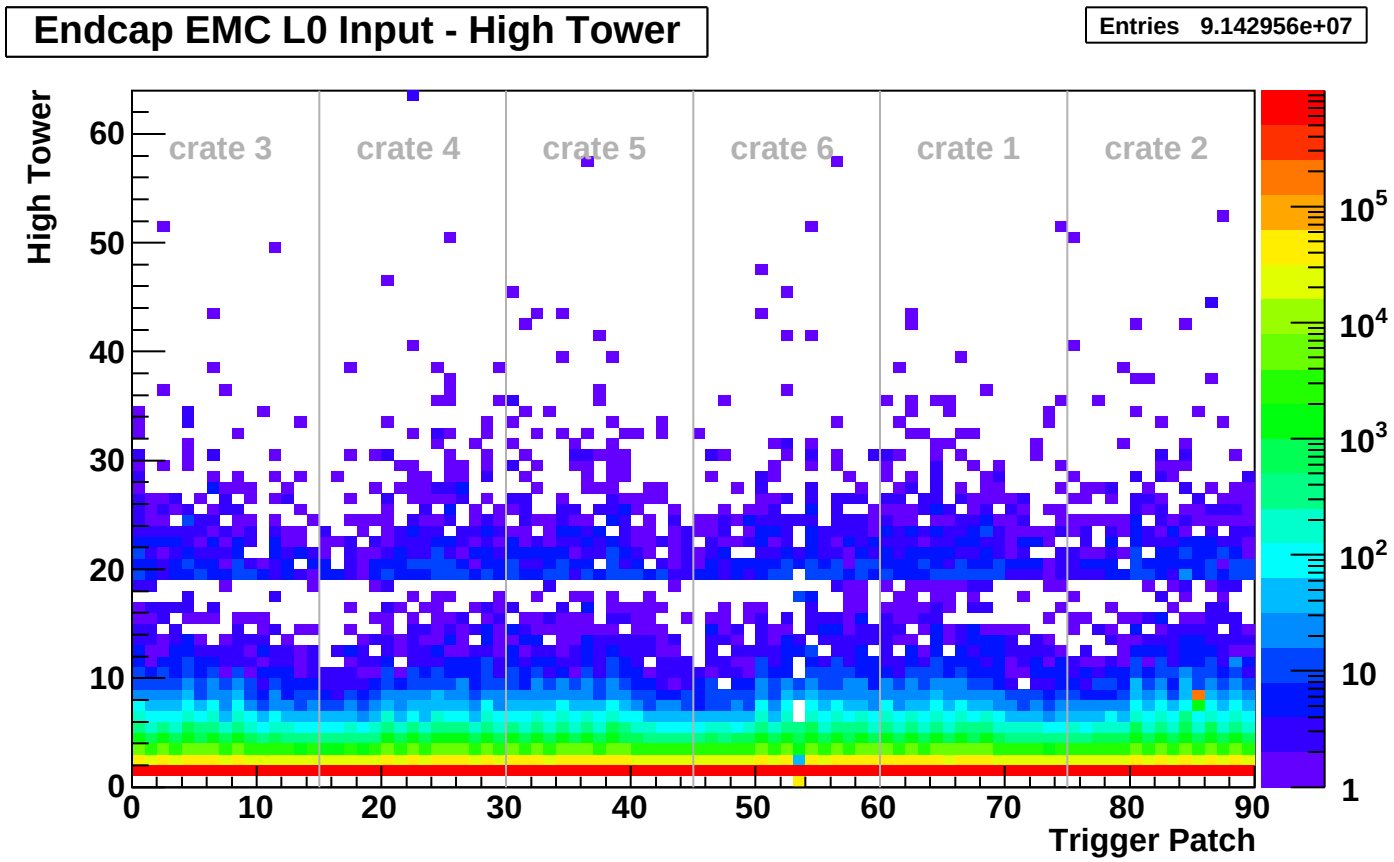

BunchId7Bit (all events)
Entries1015884

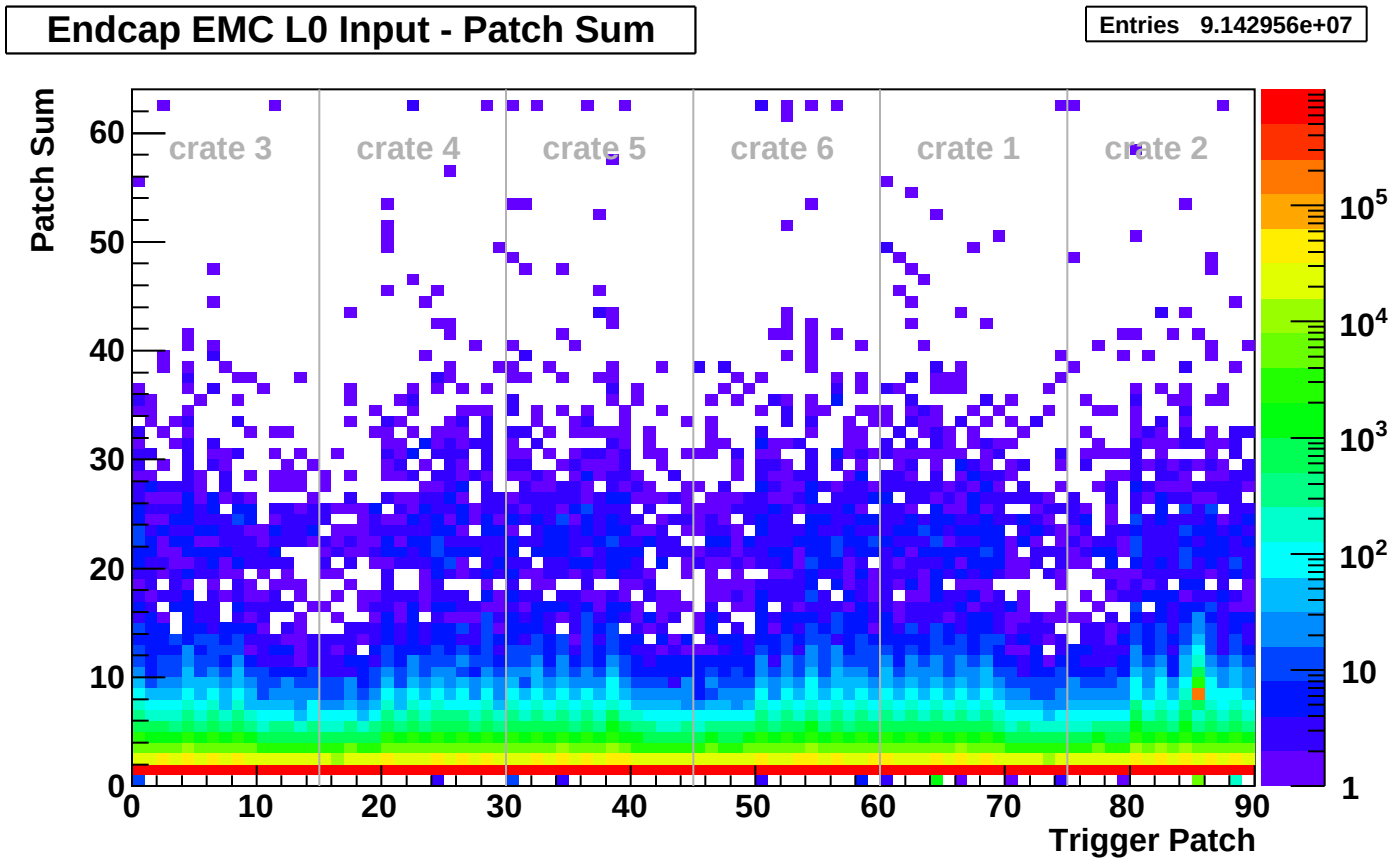

BunchId7Bit (JP1)
Entries 30977

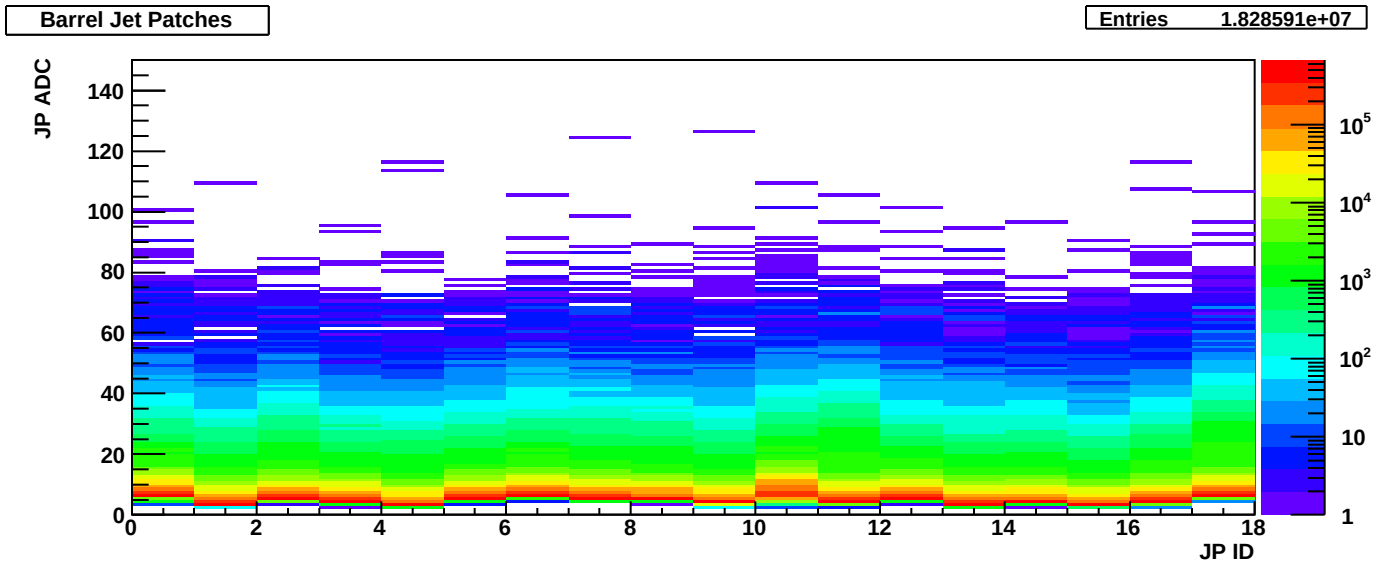

BunchId7Bit (JP2||AJP)
Entries 10696

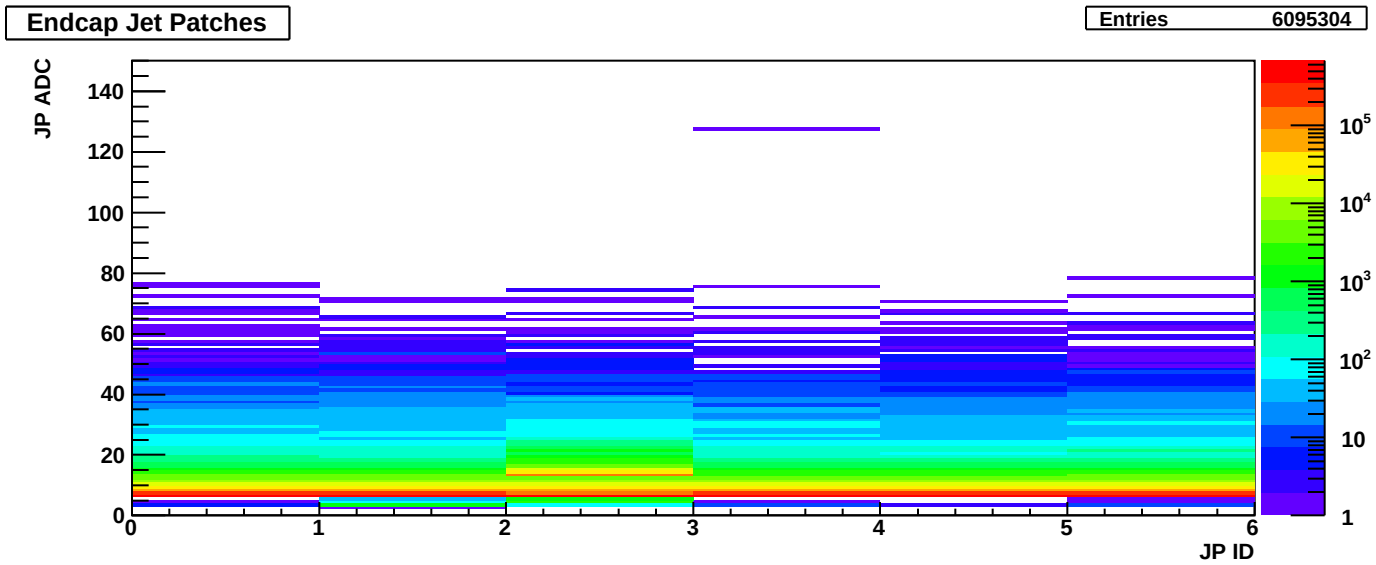

Barrel EMC L0 Input - High Tower
Entries 3.047652e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

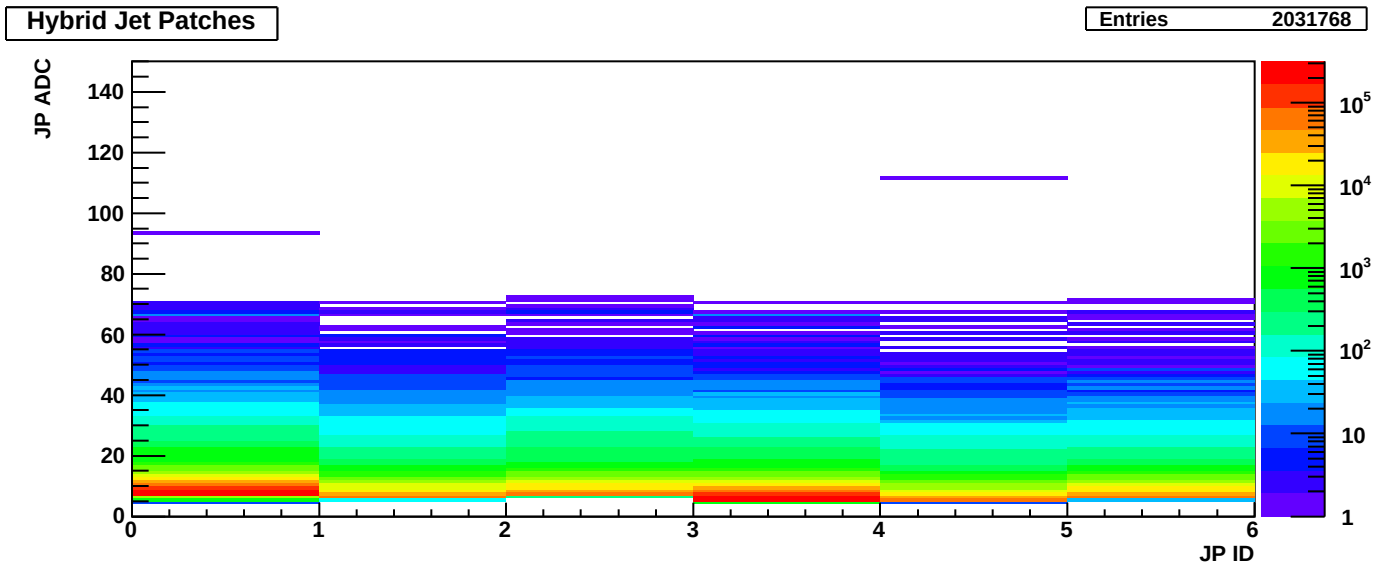

Barrel EMC L0 Input - Patch Sum
Entries 3.047652e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

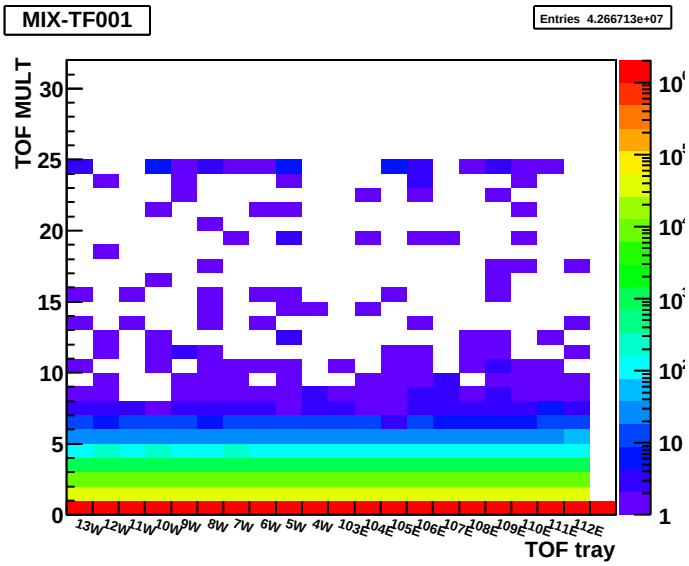

Endcap EMC L0 Input - High Tower
Entries 9.142956e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

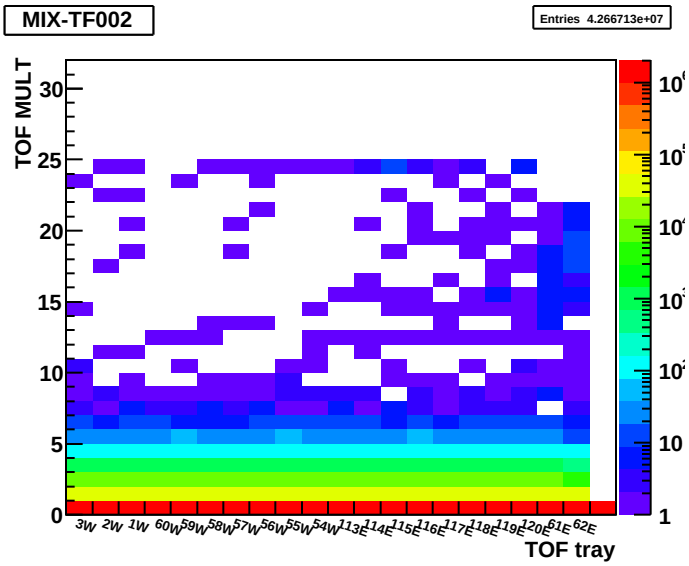

Endcap EMC L0 Input - Patch Sum
Entries 9.142956e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

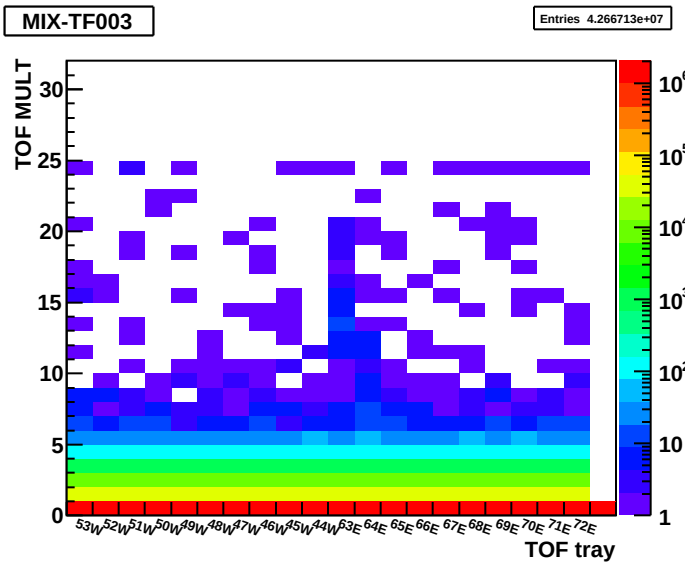

Barrel Jet Patches
Entries 1.828591e+07
JP ADC
JP ID

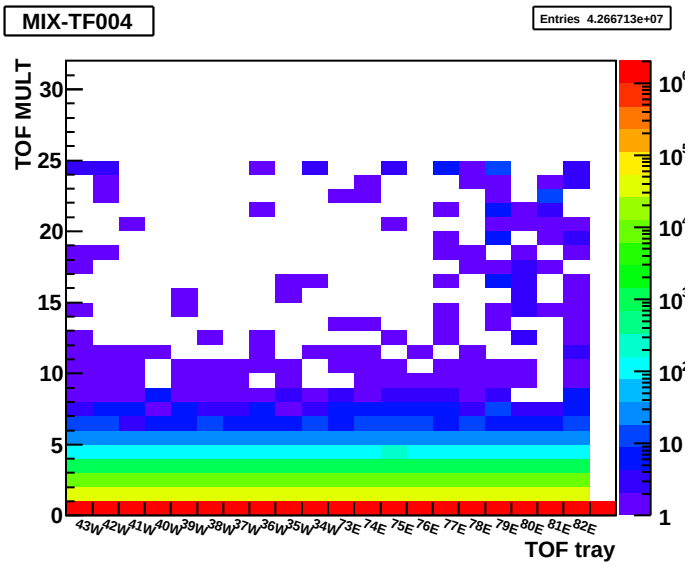

Endcap Jet Patches
Entries 6095304
JP ADC
JP ID

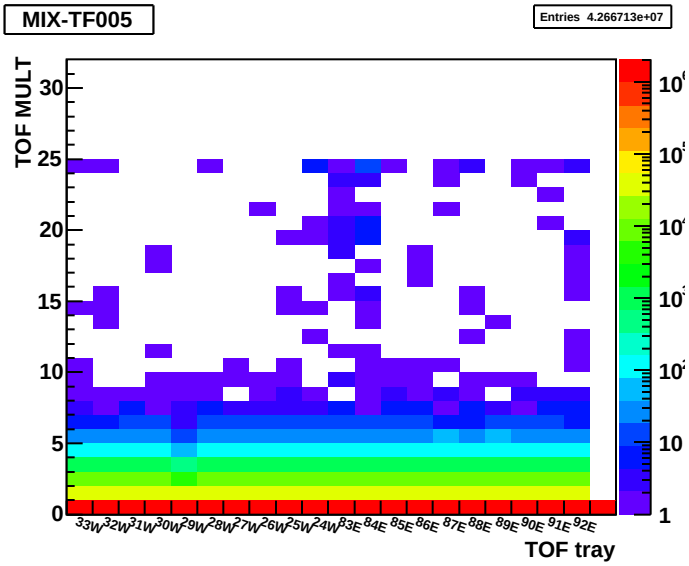

Hybrid Jet Patches
Entries 2031768
JP ADC
JP ID

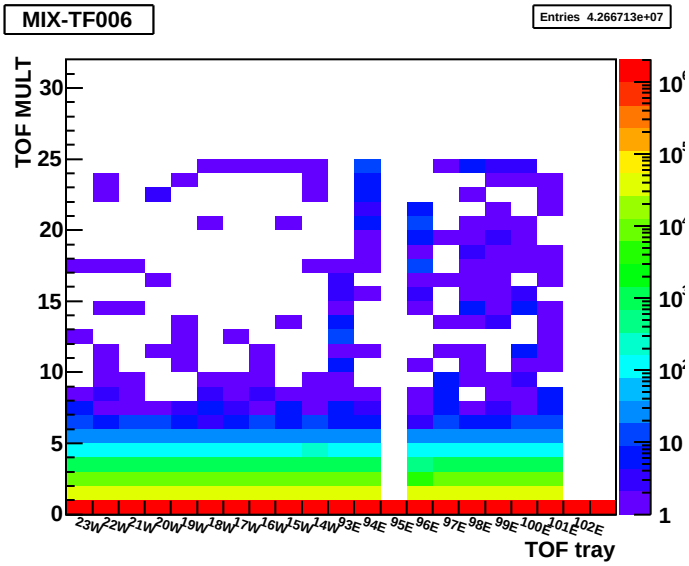

MIX-TF001
Entries 4.266713e+07
TOF MULT
TOF tray

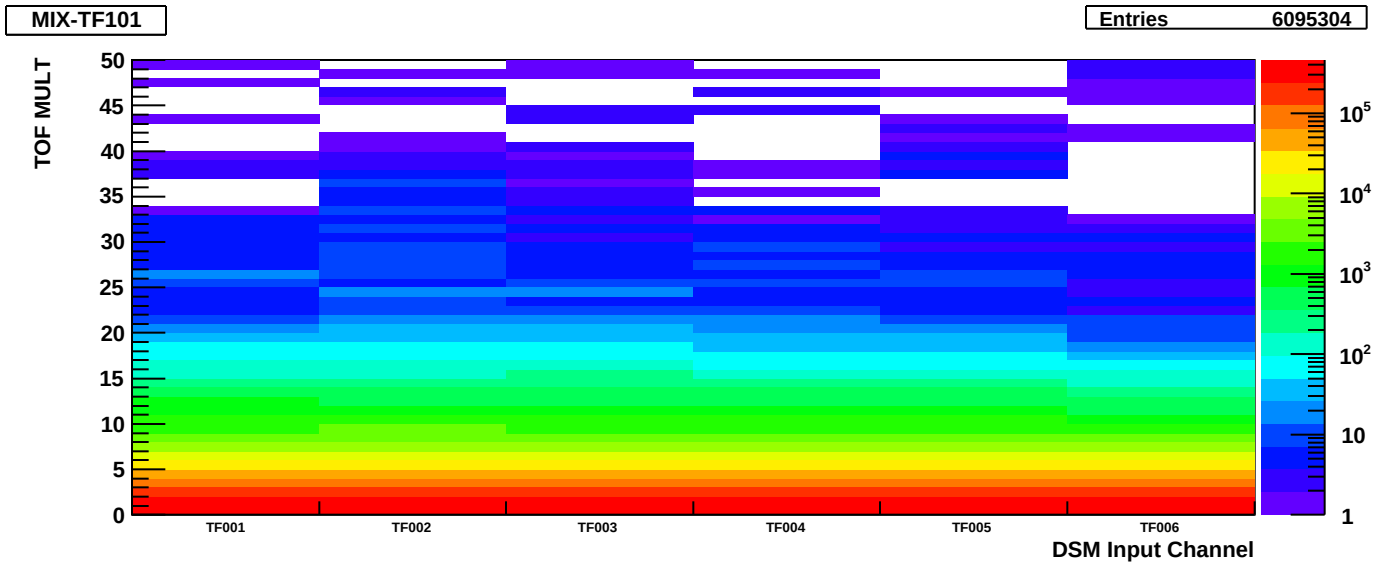

MIX-TF002
Entries 4.266713e+07
TOF MULT
TOF tray

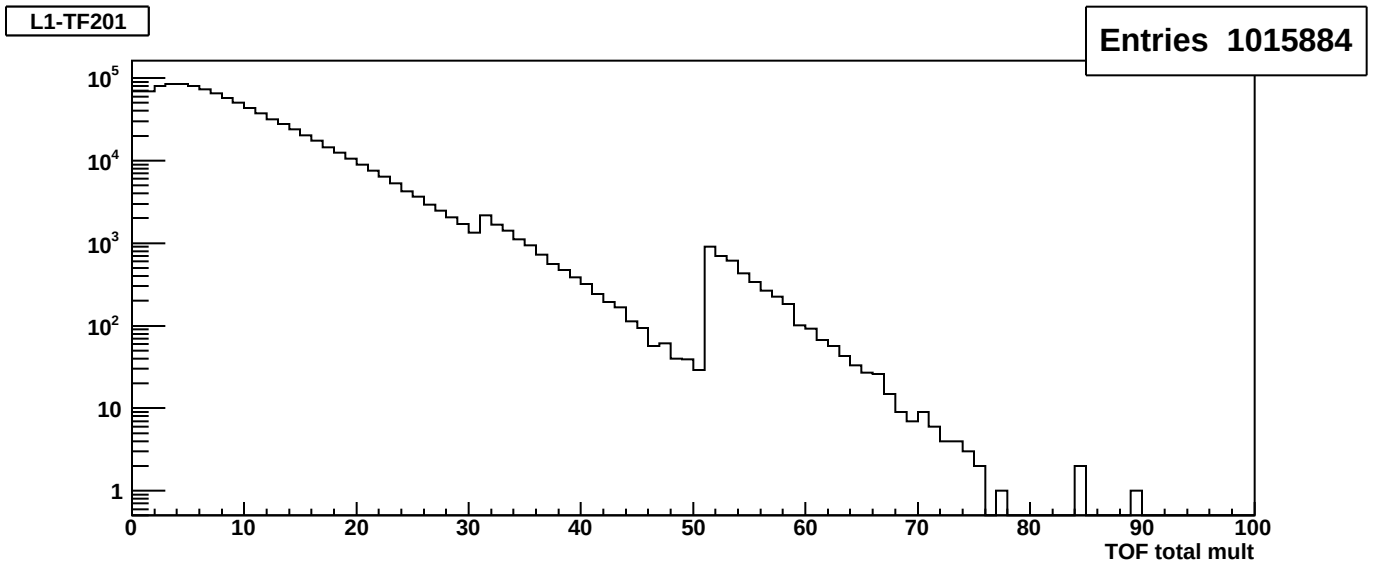

MIX-TF003
Entries 4.266713e+07
TOF MULT
TOF tray

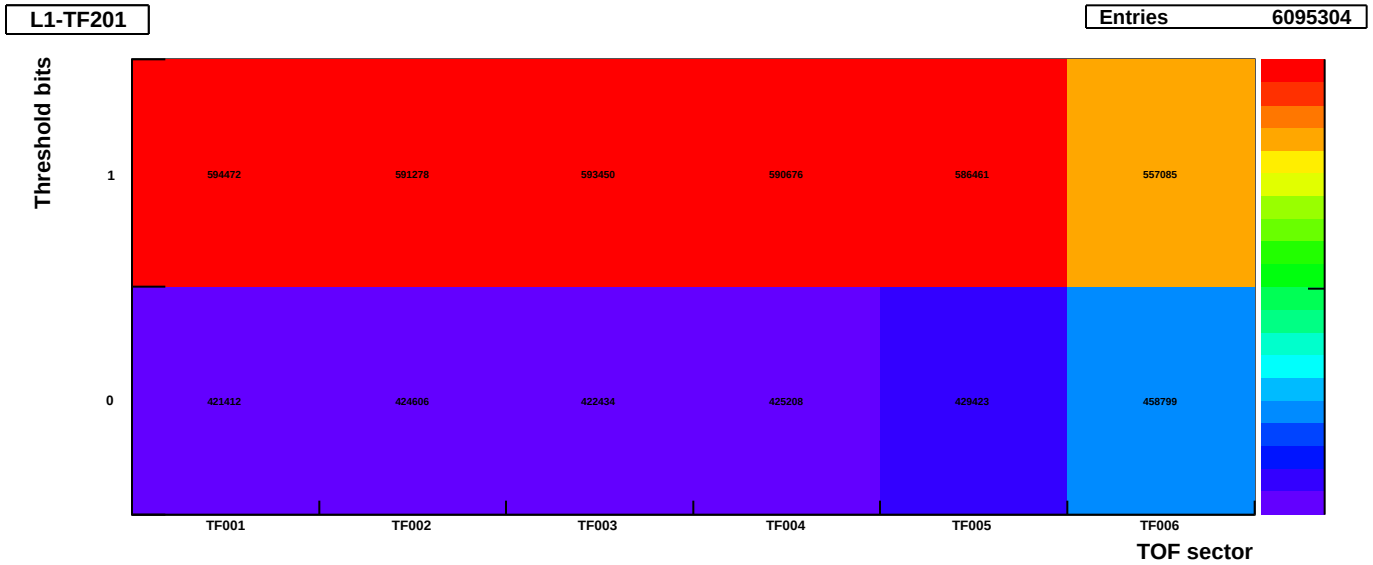

MIX-TF004
Entries 4.266713e+07
TOF MULT
TOF tray

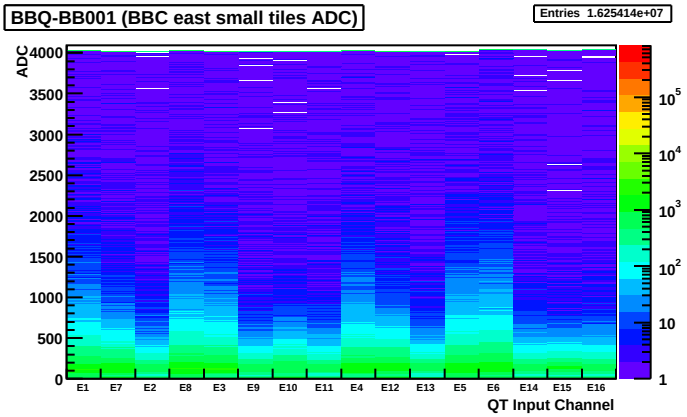

MIX-TF005
Entries 4.266713e+07
TOF MULT
TOF tray

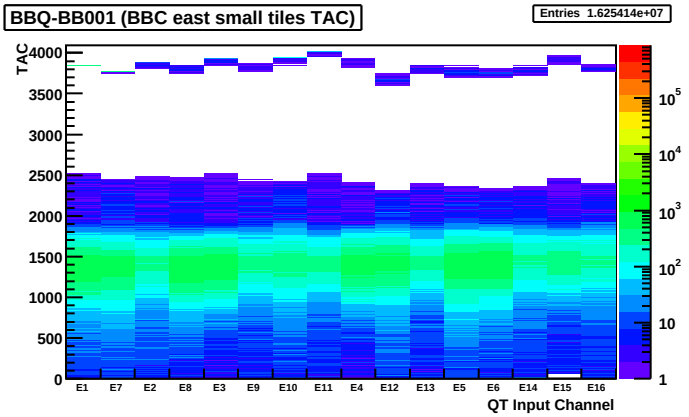

MIX-TF006
Entries 4.266713e+07
TOF MULT
TOF tray

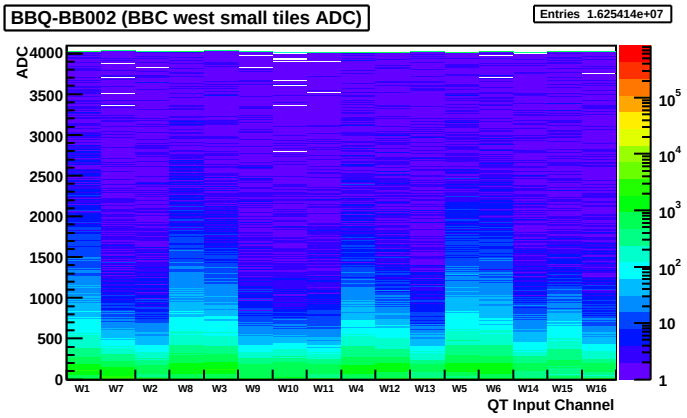

MIX-TF101
Entries 6095304
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

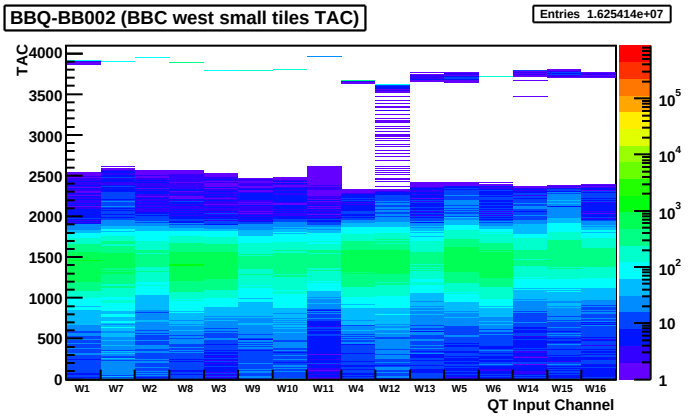

L1-TF201
Entries 1015884
TOF total mult

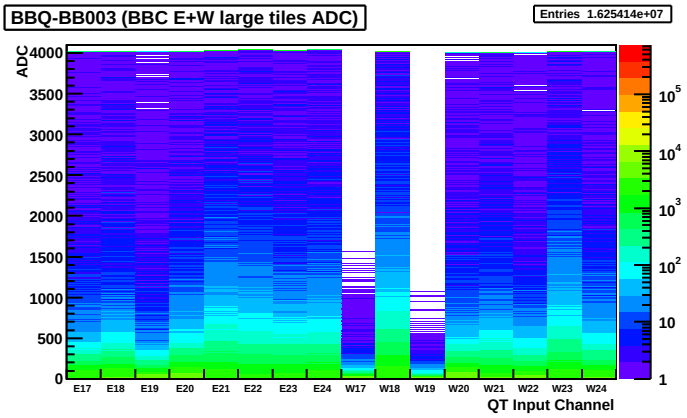

L1-TF201
Entries 6095304
Threshold bits
594472
591278
593450
590676
586461
557085
421412
424606
422434
425208
429423
458799
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

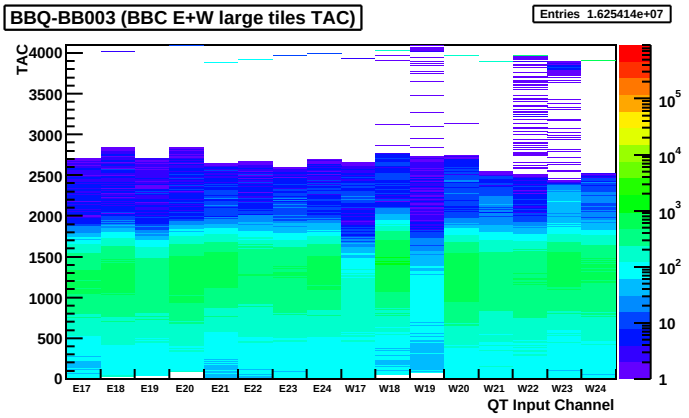

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.625414e+07
ADC
QT Input Channel

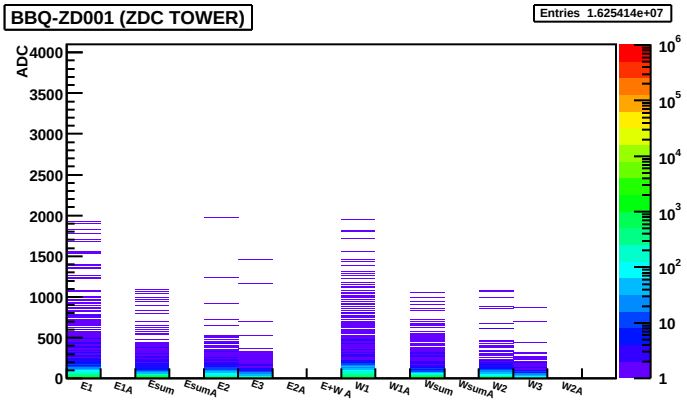

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.625414e+07
TAC
QT Input Channel

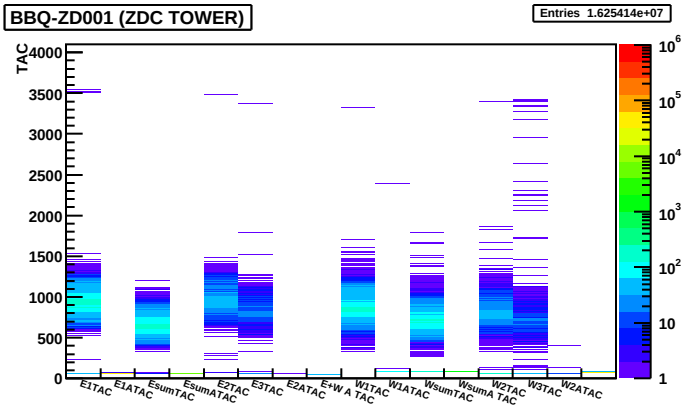

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.625414e+07
ADC
QT Input Channel

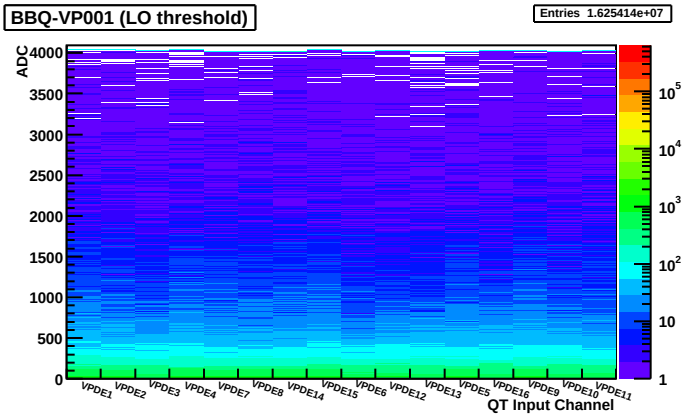

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.625414e+07
TAC
QT Input Channel

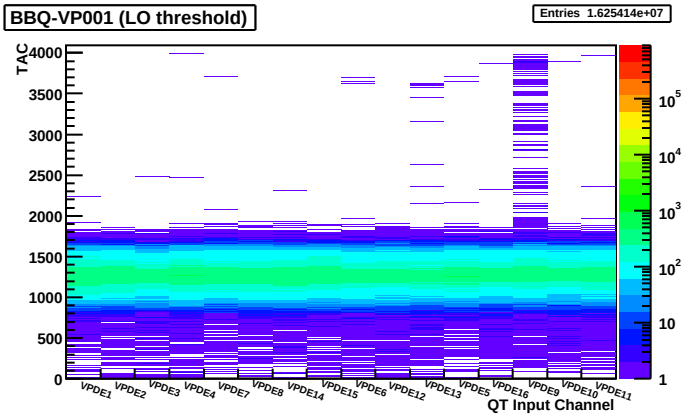

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.625414e+07
ADC
QT Input Channel

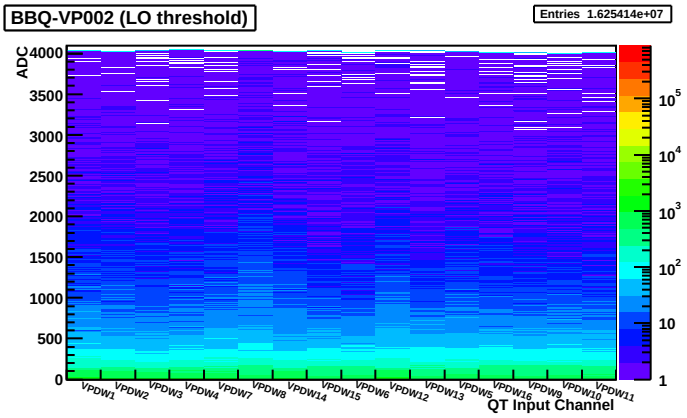

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.625414e+07
TAC
QT Input Channel

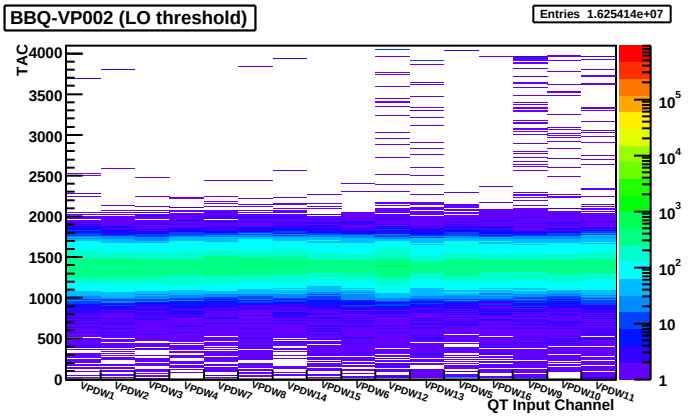

BBQ-ZD001 (ZDC TOWER)
Entries 1.625414e+07
ADC

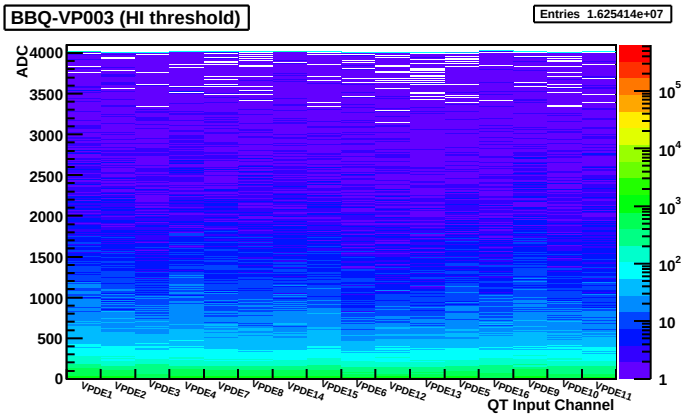

BBQ-ZD001 (ZDC TOWER)
Entries 1.625414e+07
TAC

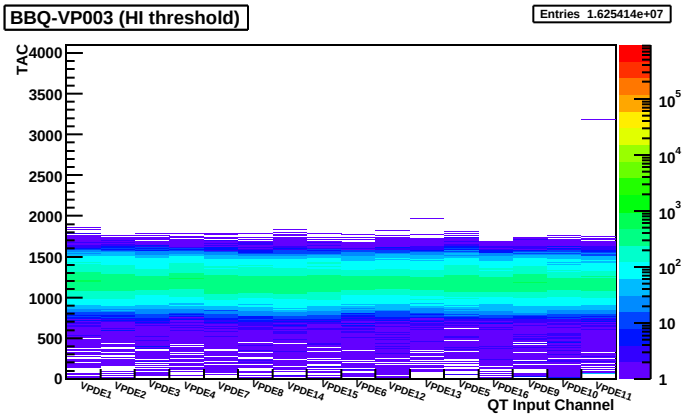

BBQ-VP001 (LO threshold)
Entries 1.625414e+07
ADC
QT Input Channel

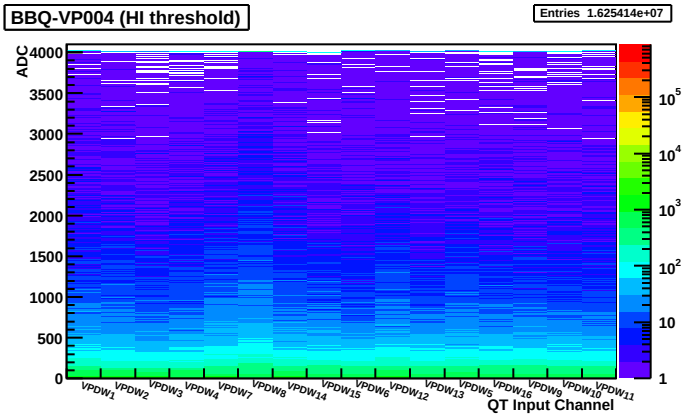

BBQ-VP001 (LO threshold)
Entries 1.625414e+07
TAC
QT Input Channel

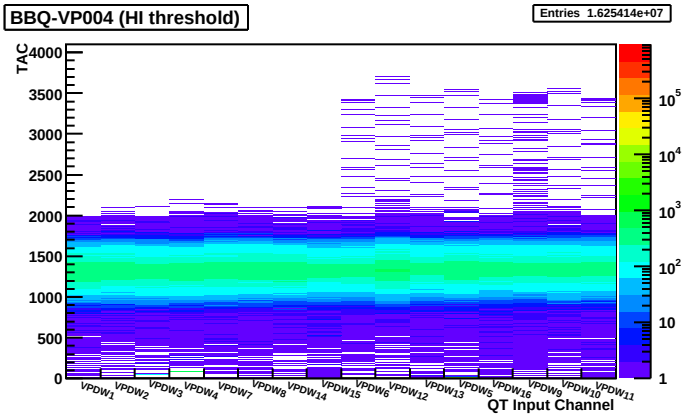

BBQ-VP002 (LO threshold)
Entries 1.625414e+07
ADC
QT Input Channel

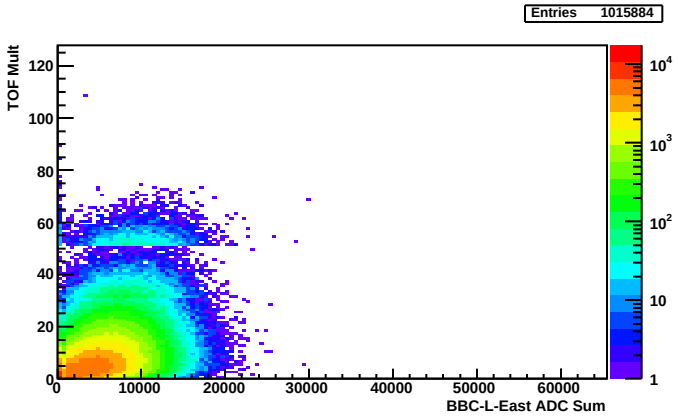

BBQ-VP002 (LO threshold)
Entries 1.625414e+07
TAC
QT Input Channel

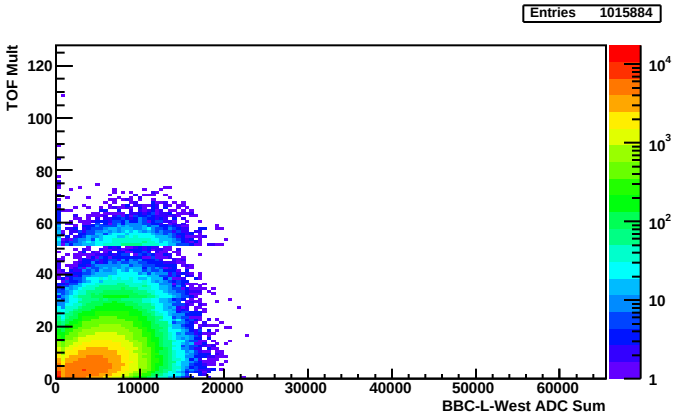

BBQ-VP003 (HI threshold)
Entries 1.625414e+07
ADC
QT Input Channel

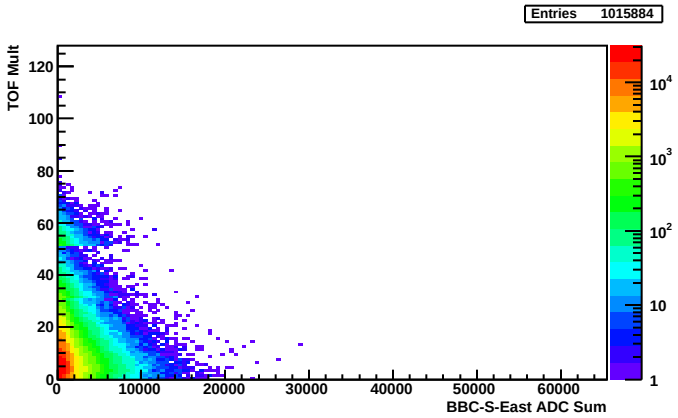

BBQ-VP003 (HI threshold)
Entries 1.625414e+07
TAC
QT Input Channel

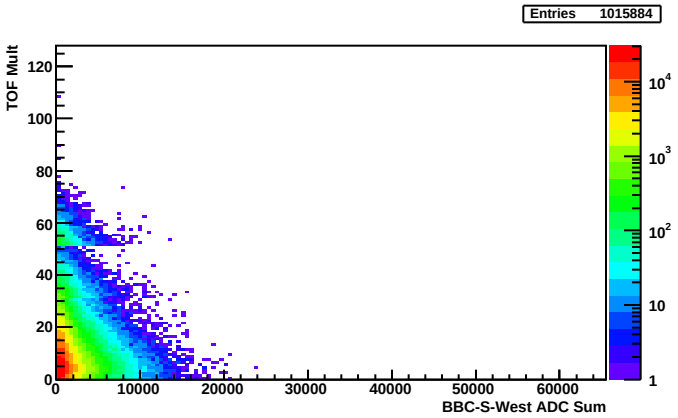

BBQ-VP004 (HI threshold)
Entries 1.625414e+07
ADC
QT Input Channel

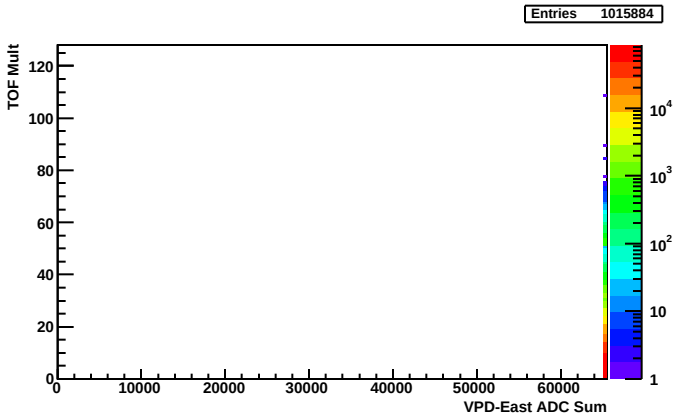

BBQ-VP004 (HI threshold)
Entries 1.625414e+07
TAC
QT Input Channel

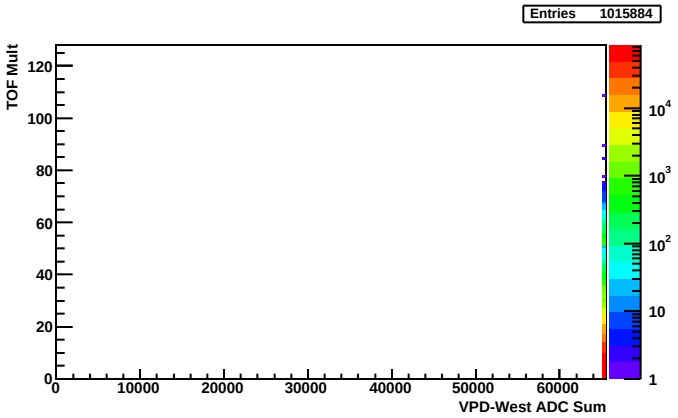

Entries 1015884
TOF Mult
BBC-L-East ADC Sum

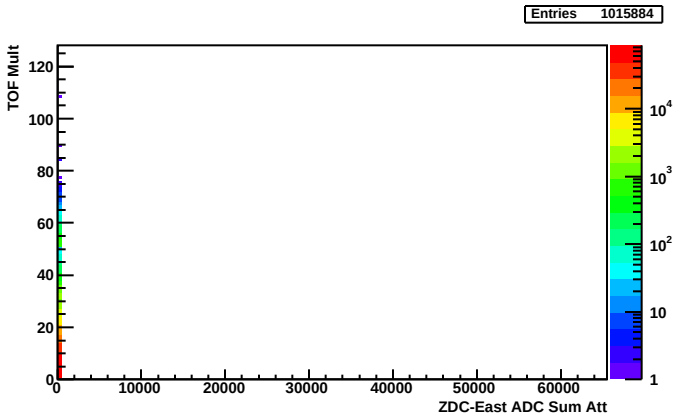

Entries 1015884
TOF Mult
BBC-L-West ADC Sum

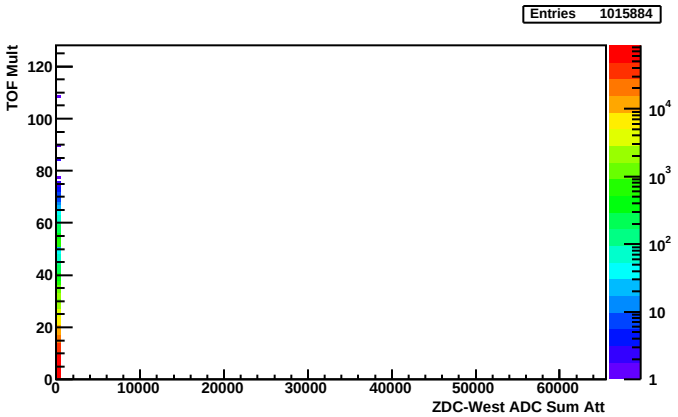

Entries 1015884
TOF Mult
BBC-S-East ADC Sum

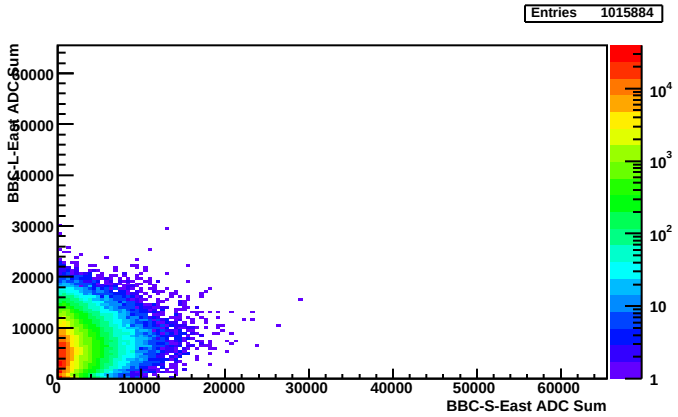

Entries 1015884
TOF Mult
BBC-S-West ADC Sum

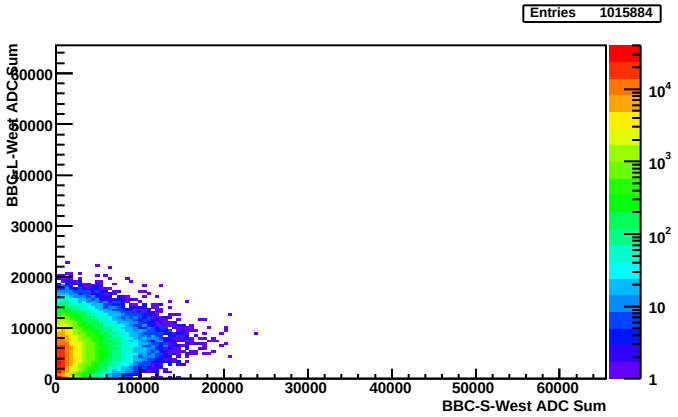

Entries 1015884
TOF Mult
VPD-East ADC Sum

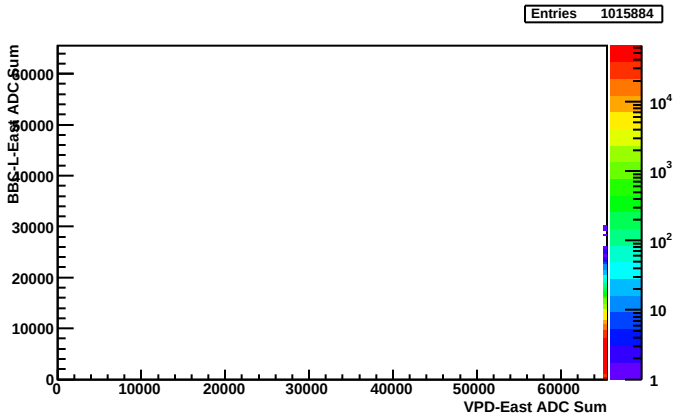

Entries 1015884
TOF Mult
VPD-West ADC Sum

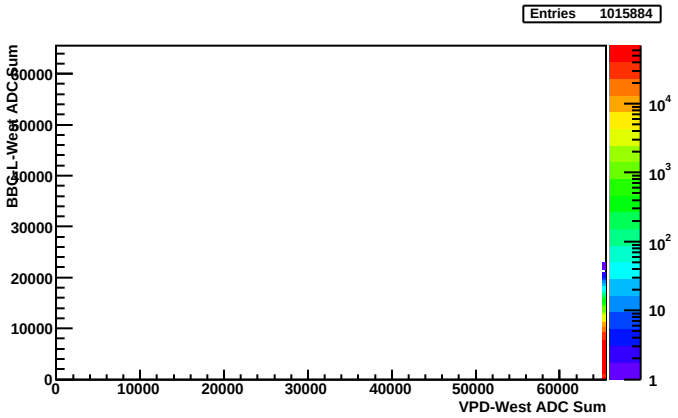

Entries 1015884
TOF Mult
ZDC-East ADC Sum Att

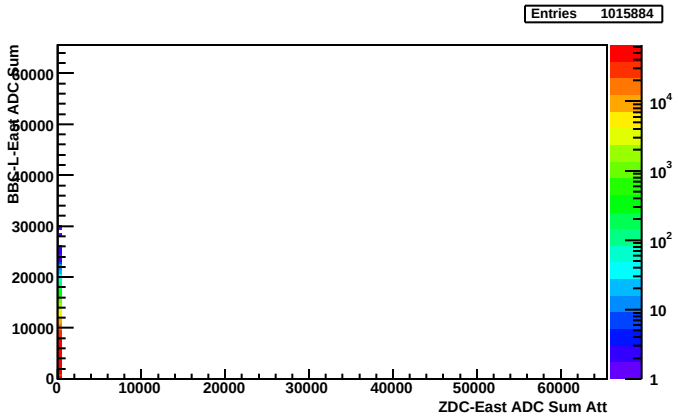

Entries 1015884
TOF Mult
ZDC-West ADC Sum Att

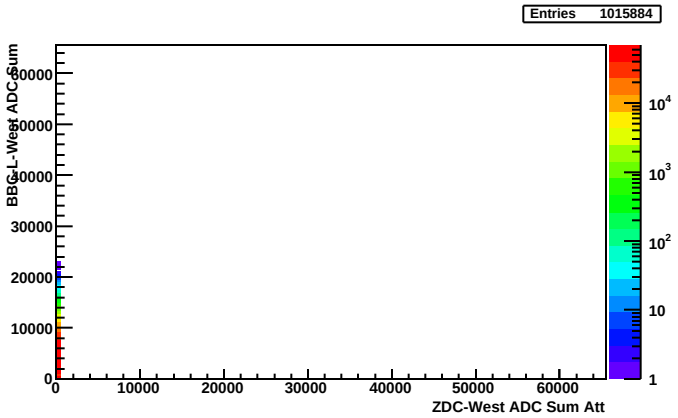

Entries 1015884
BBC-L-East ADC Sum
BBC-S-East ADC Sum

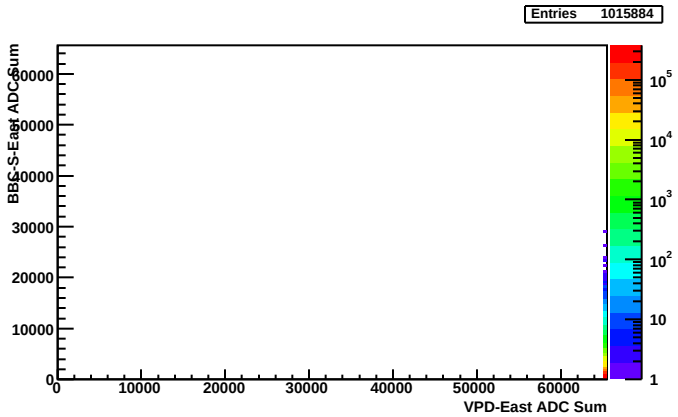

Entries 1015884
BBC-L-West ADC Sum
BBC-S-West ADC Sum

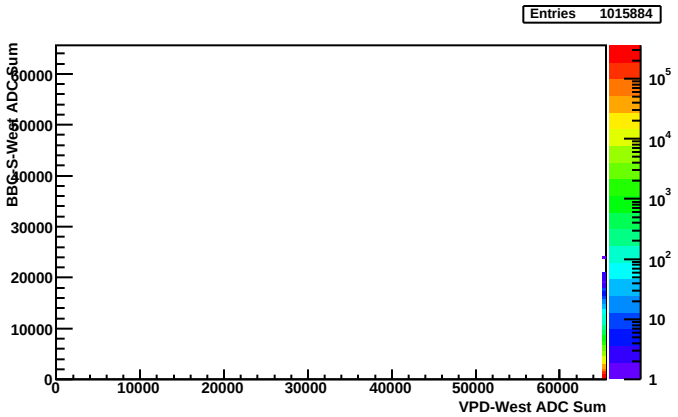

Entries 1015884
BBC-L-East ADC Sum
VPD-East ADC Sum

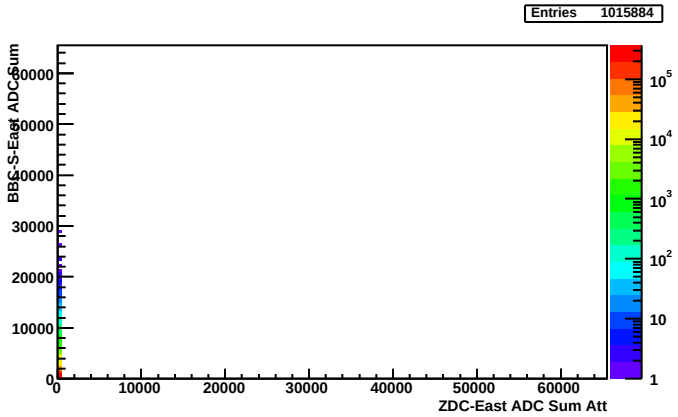

Entries 1015884
BBC-L-West ADC Sum
VPD-West ADC Sum

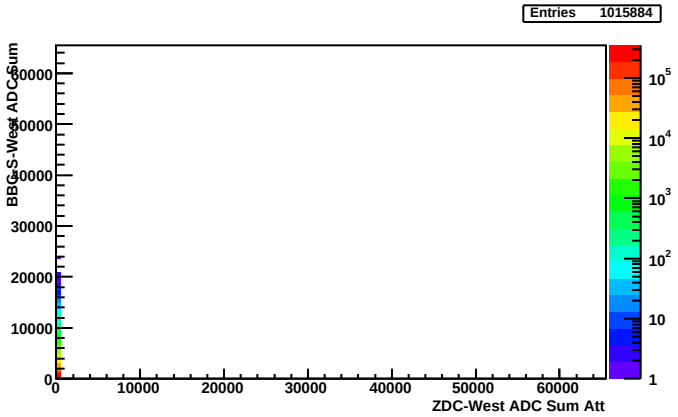

Entries 1015884
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

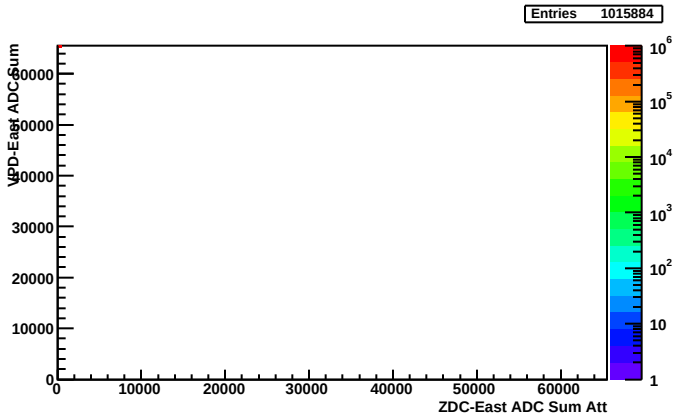

Entries 1015884
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

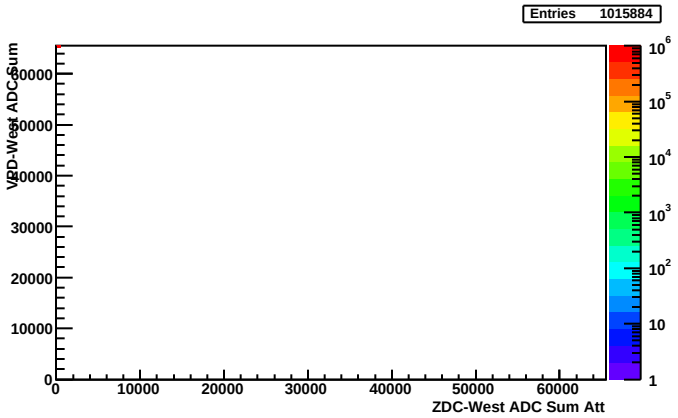

Entries 1015884
BBC-S-East ADC Sum
VPD-East ADC Sum

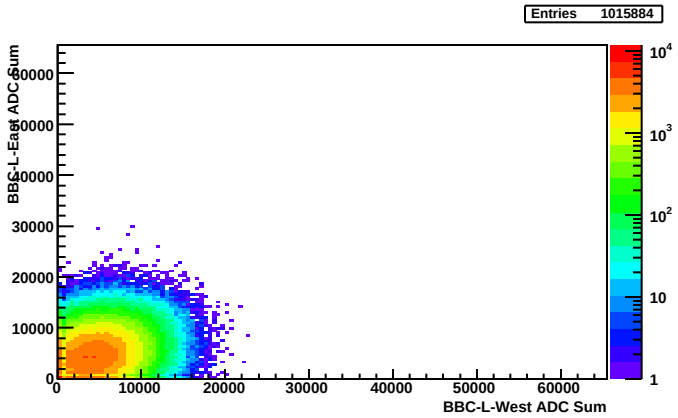

Entries 1015884
BBC-S-West ADC Sum
VPD-West ADC Sum

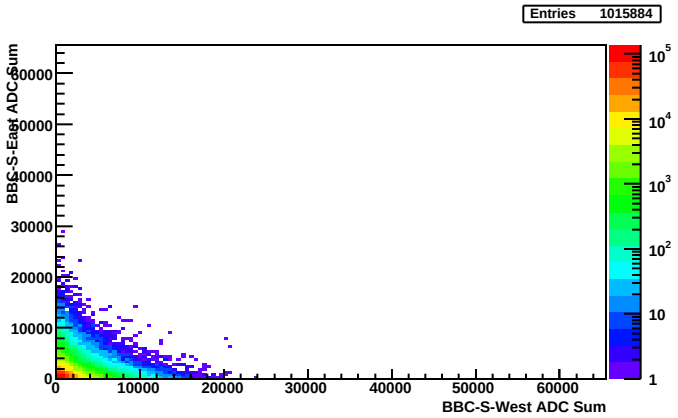

Entries 1015884
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

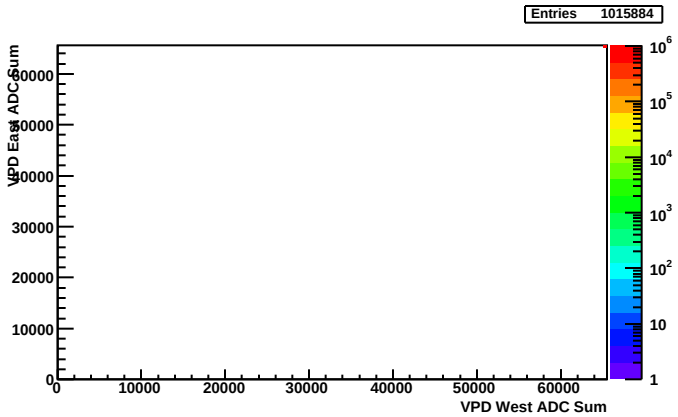

Entries 1015884
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

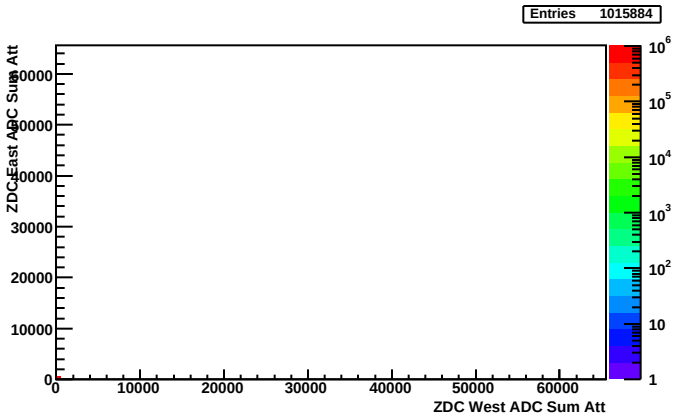

Entries 1015884
VPD-East ADC Sum
ZDC-East ADC Sum Att

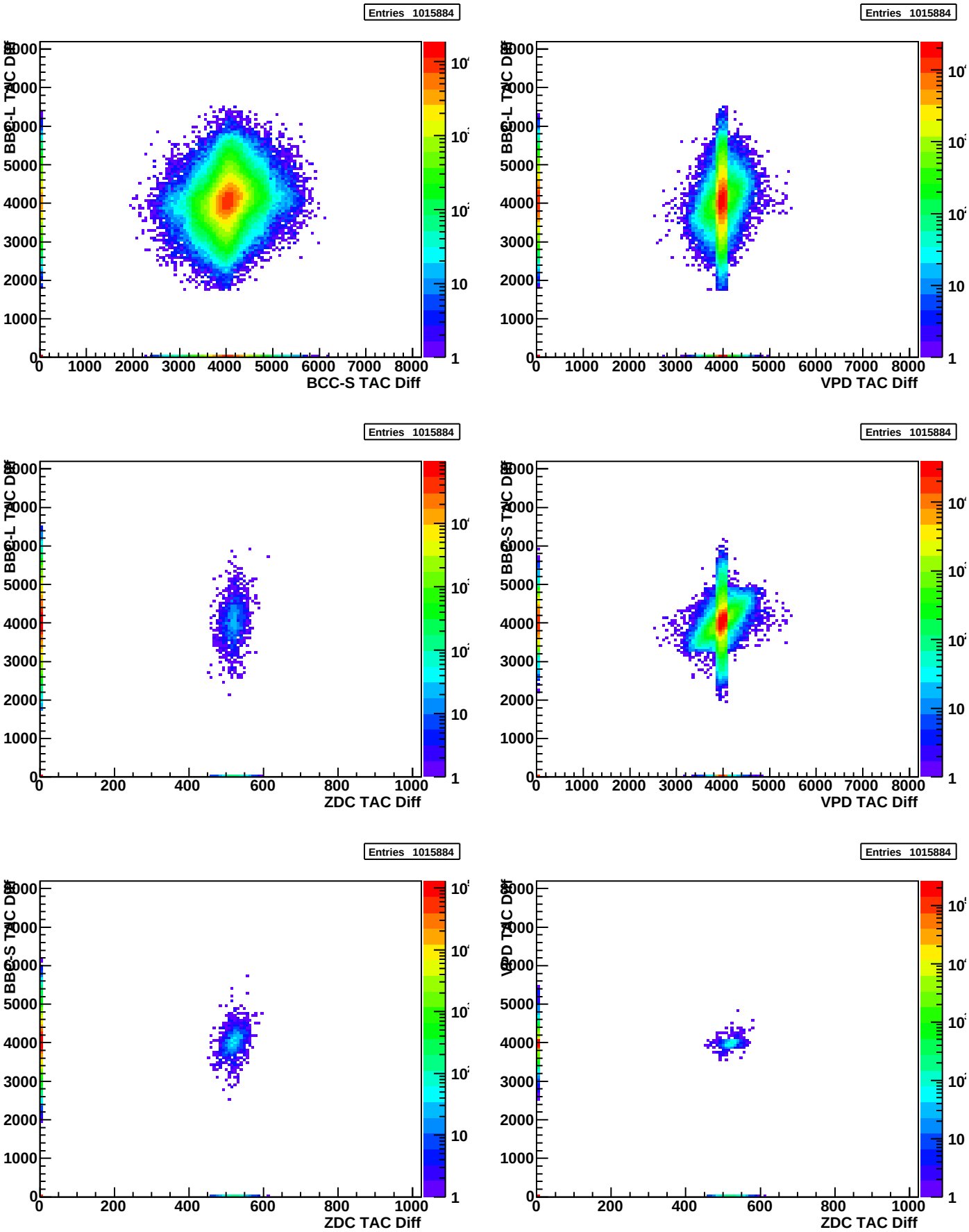

Entries 1015884
VPD-West ADC Sum
ZDC-West ADC Sum Att

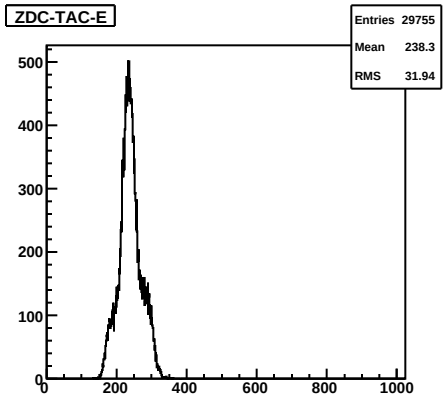

Entries 1015884
BBC-L-East ADC Sum
BBC-L-West ADC Sum

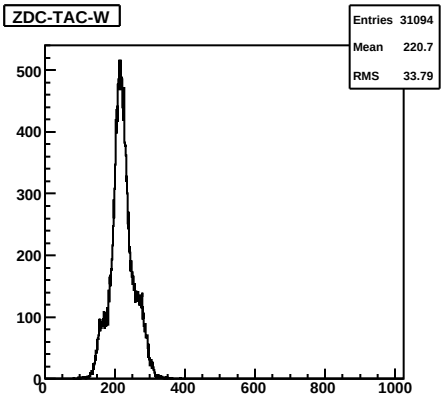

Entries 1015884
BBC-S-East ADC Sum
BBC-S-West ADC Sum

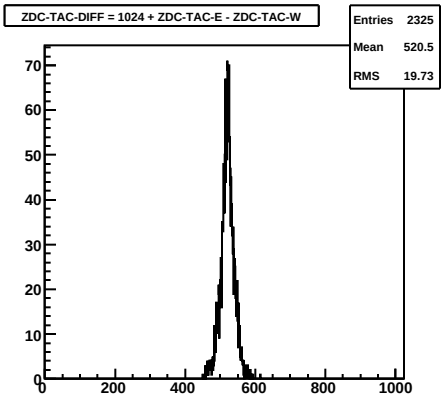

Entries 1015884
VPD East ADC Sum
VPD West ADC Sum

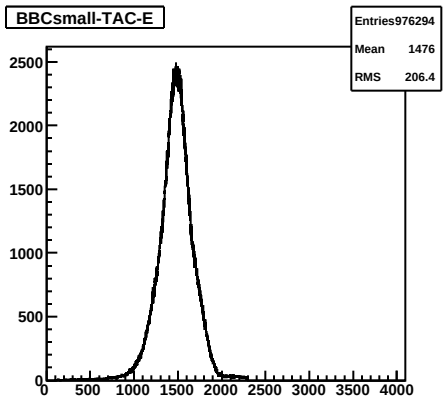

Entries 1015884
ZDC East ADC Sum Att
ZDC West ADC Sum Att

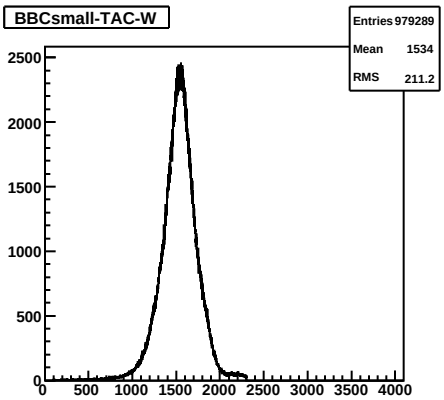

Entries 1015884
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1015884
BBC-L TAC Diff
VPD TAC Diff
Entries 1015884
BBC-L TAC Diff
ZDC TAC Diff
Entries 1015884
BBC-S TAC Diff
VPD TAC Diff
Entries 1015884
BBC-S TAC Diff
ZDC TAC Diff
Entries 1015884
VPD TAC Diff
ZDC TAC Diff

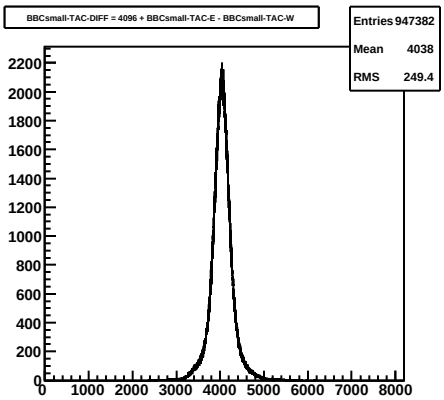

ZDC-TAC-E
Entries 29755
Mean 238.3
RMS 31.94

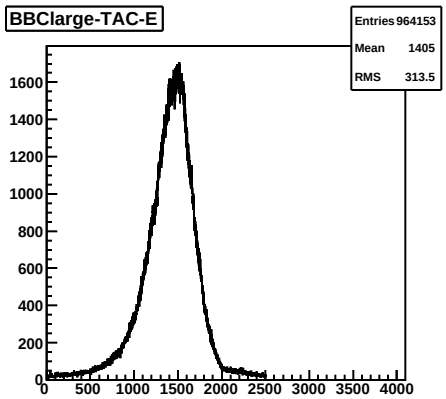

ZDC-TAC-W
Entries 31094
Mean 220.7
RMS 33.79

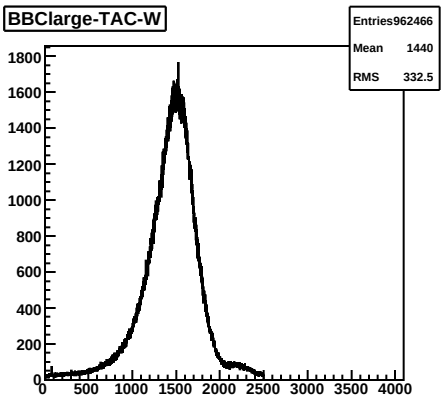

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2325
Mean 520.5
RMS 19.73

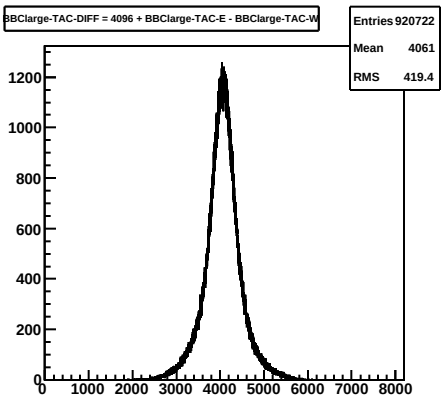

BBCsmall-TAC-E
Entries 976294
Mean 1476
RMS 206.4

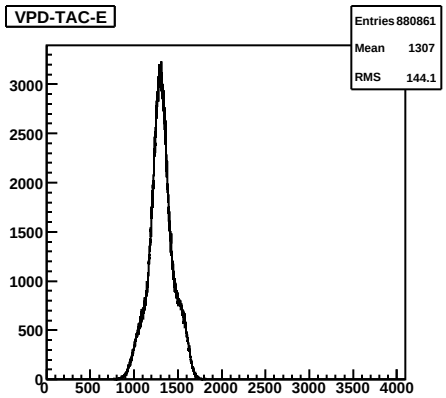

BBCsmall-TAC-W
Entries 979289
Mean 1534
RMS 211.2

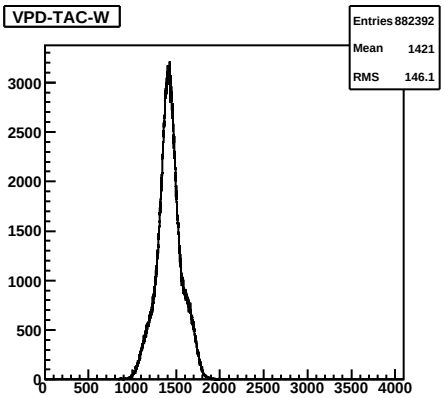

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 947382
Mean 4038
RMS 249.4

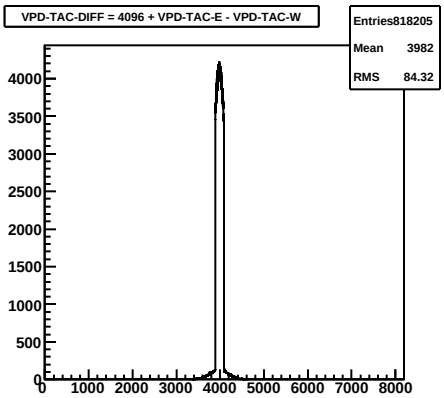

BBClarge-TAC-E
Entries 964153
Mean 1405
RMS 313.5

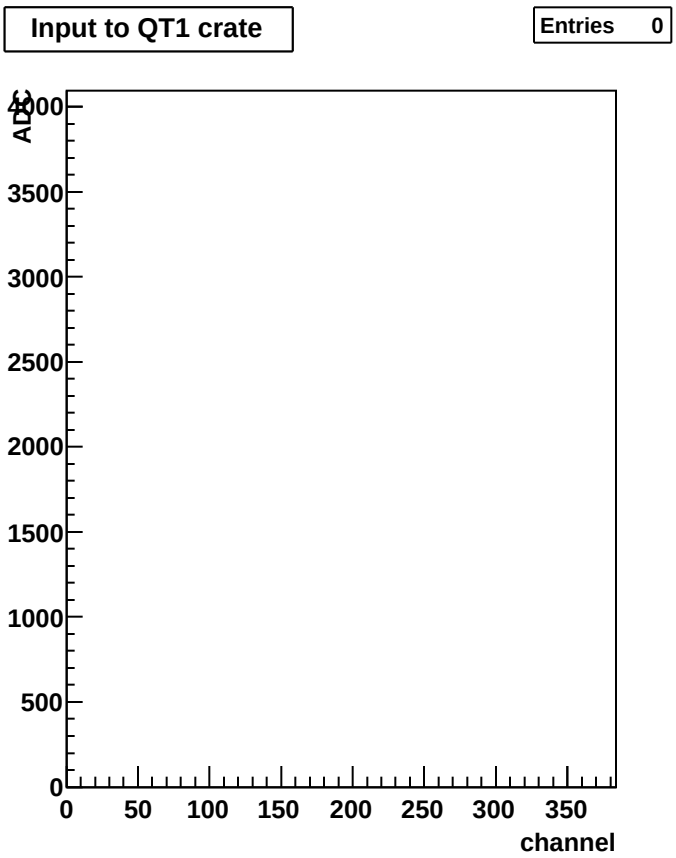

BBClarge-TAC-W
Entries 962466
Mean 1440
RMS 332.5

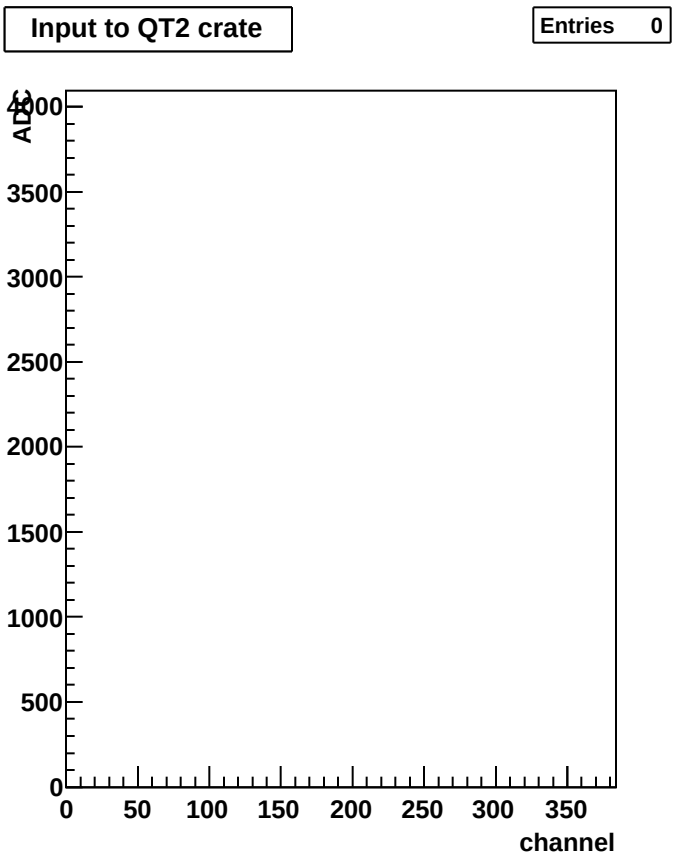

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 920722
Mean 4061
RMS 419.4

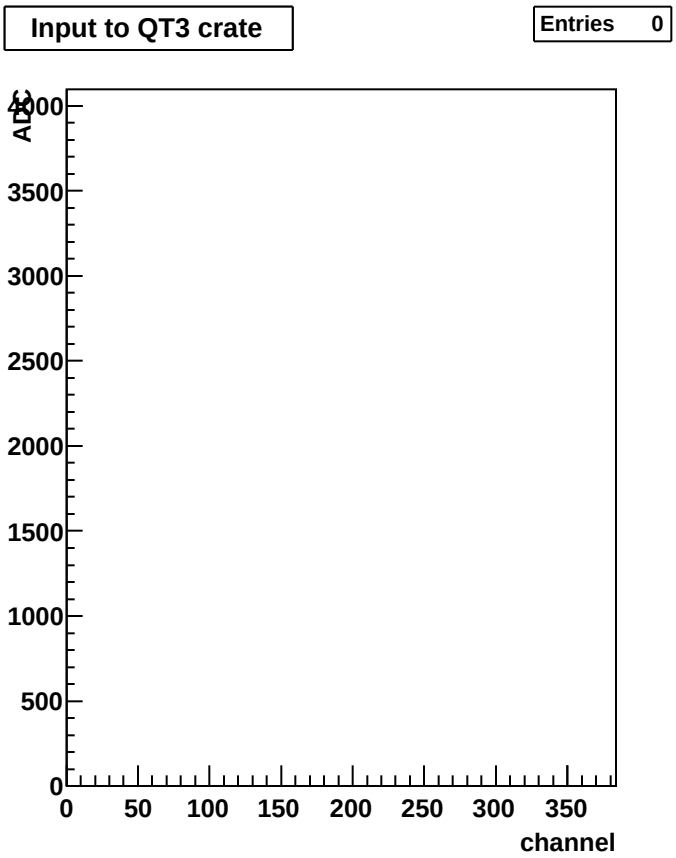

VPD-TAC-E
Entries 880861
Mean 1307
RMS 144.1

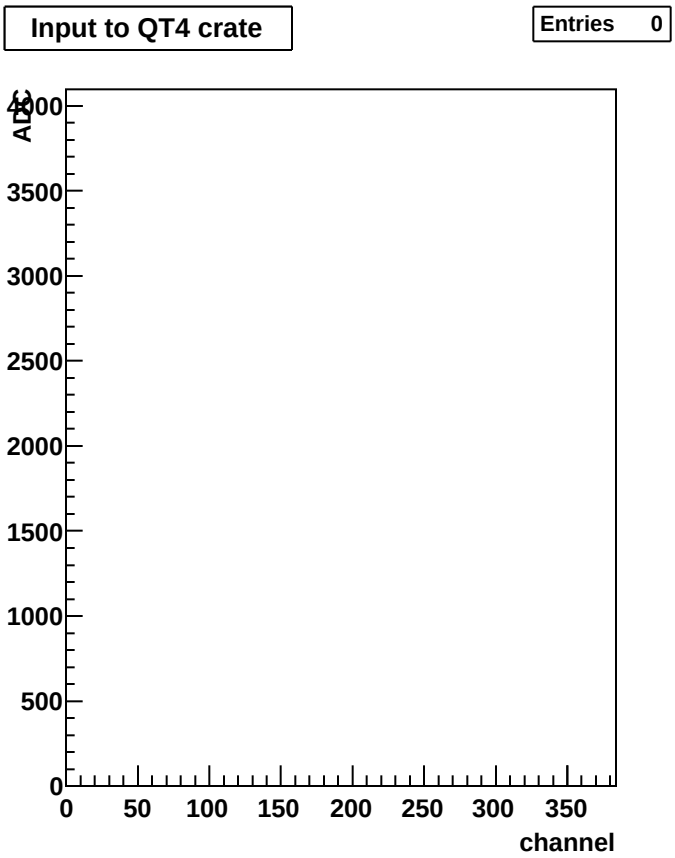

VPD-TAC-W
Entries 882392
Mean 1421
RMS 146.1

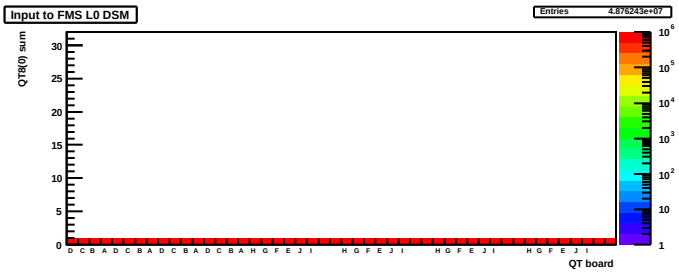

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 818205
Mean 3982
RMS 84.32

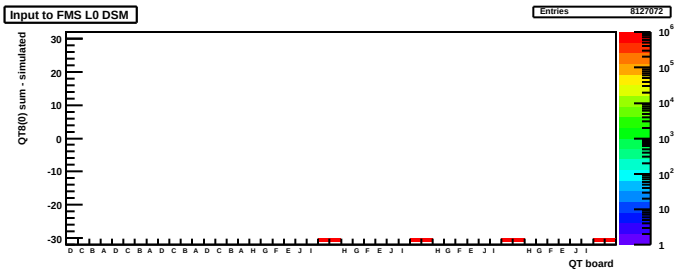

Input to QT1 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

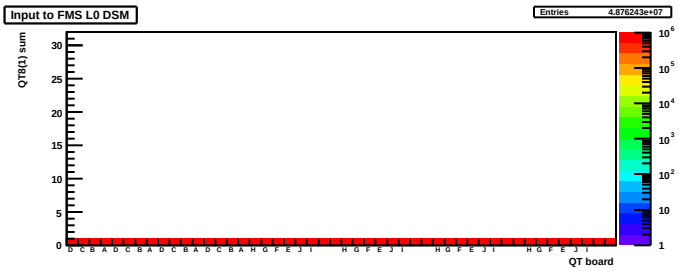

Input to QT2 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

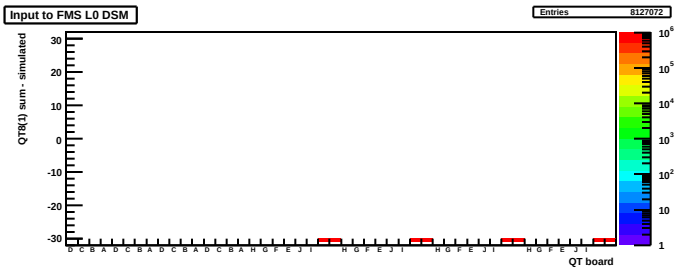

Input to QT3 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

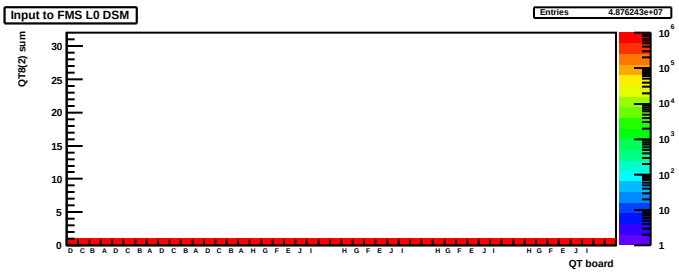

Input to QT4 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

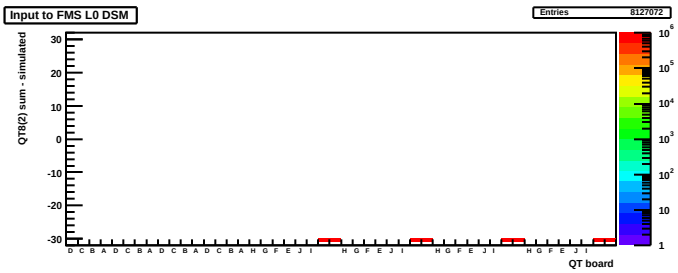

Input to FMS L0 DSM
Entries 4.876243e+07
QT8(0) sum
QT board

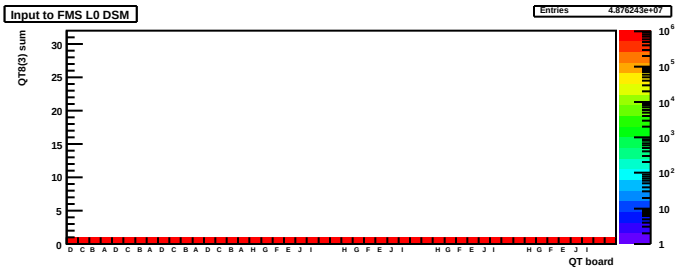

Input to FMS L0 DSM
Entries 8127072
QT8(0) sum - simulated
QT board

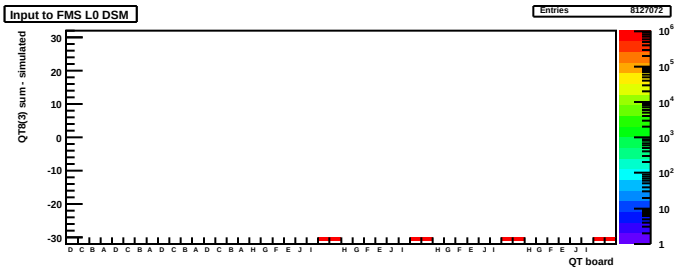

Input to FMS L0 DSM
Entries 4.876243e+07
QT8(1) sum
QT board

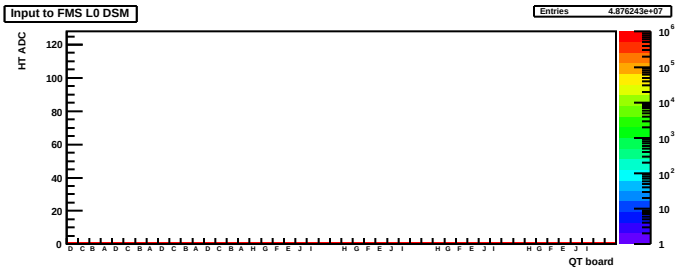

Input to FMS L0 DSM
Entries 8127072
QT8(1) sum - simulated
QT board

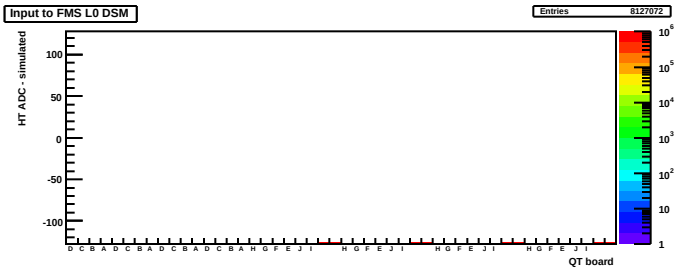

Input to FMS L0 DSM
Entries 4.876243e+07
QT8(2) sum
QT board

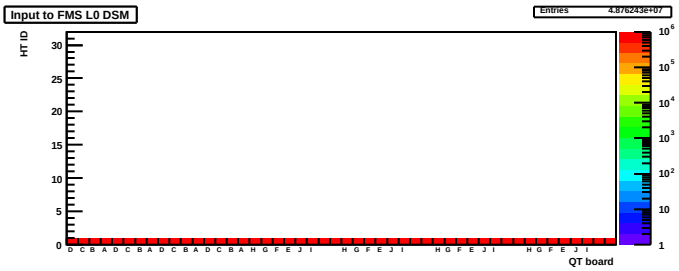

Input to FMS L0 DSM
Entries 8127072
QT8(2) sum - simulated
QT board

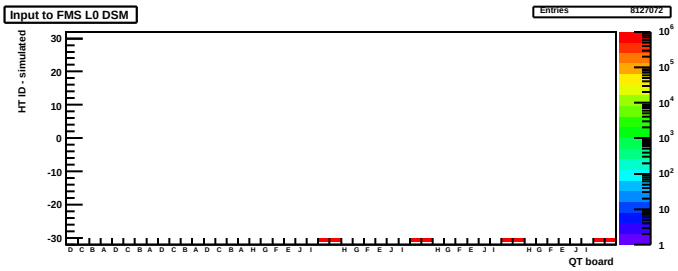

Input to FMS L0 DSM
Entries 4.876243e+07
QT8(3) sum
QT board

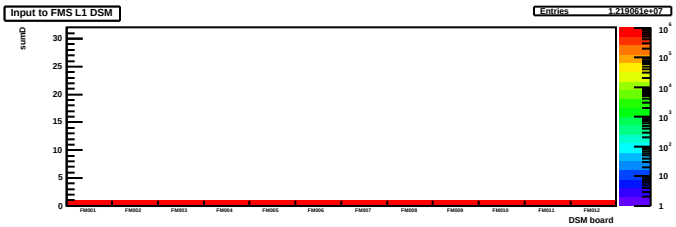

Input to FMS L0 DSM
Entries 8127072
QT8(3) sum - simulated
QT board

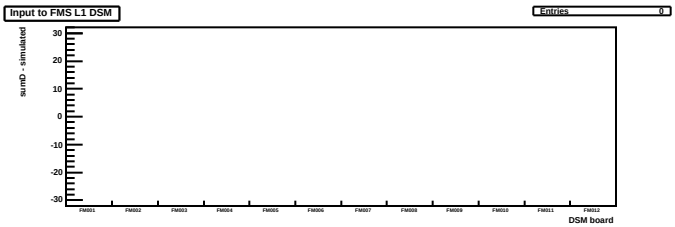

Input to FMS L0 DSM
Entries 4.876243e+07
HT ADC
QT board

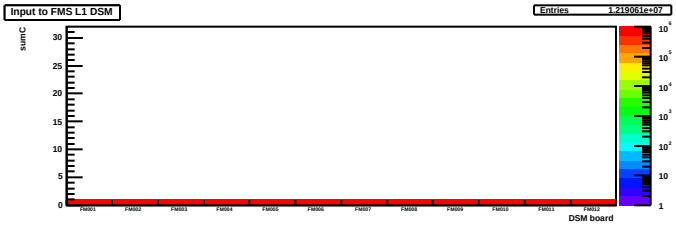

Input to FMS L0 DSM
Entries 8127072
HT ADC - simulated
QT board

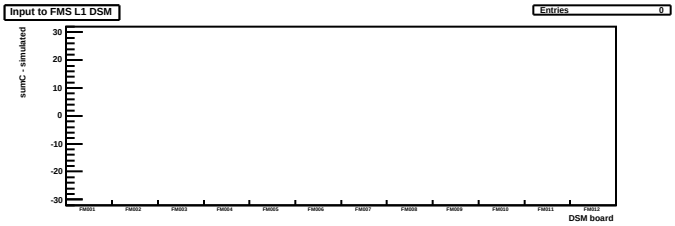

Input to FMS L0 DSM
Entries 4.876243e+07
HT ID
QT board

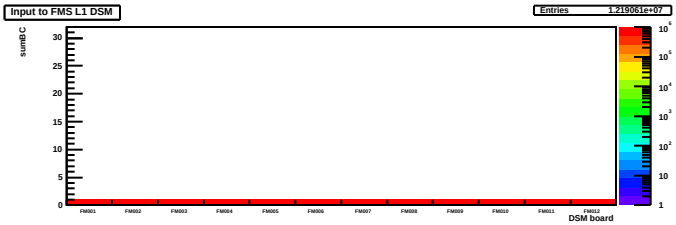

Input to FMS L0 DSM
Entries 8127072
HT ID - simulated
QT board

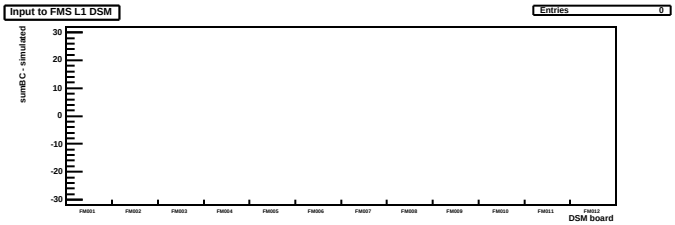

Input to FMS L1 DSM
Entries 1.219061e+07
sumD
DSM board

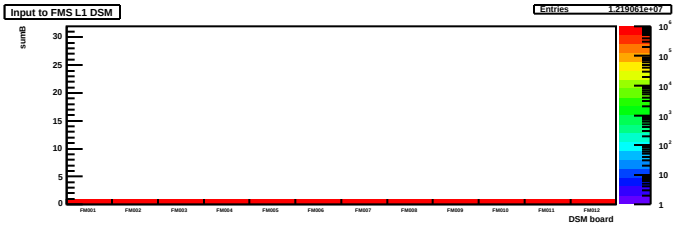

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

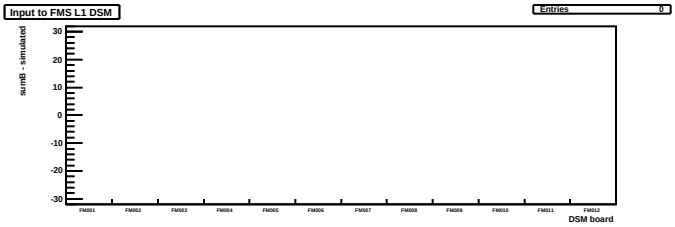

Input to FMS L1 DSM
Entries 1.219061e+07
sumC
DSM board

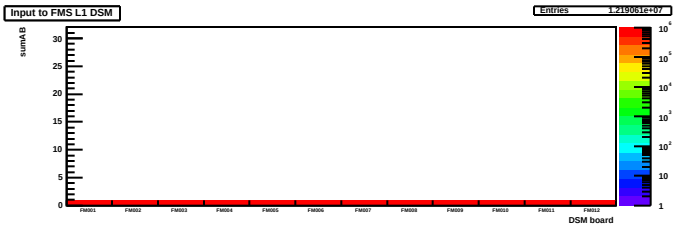

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

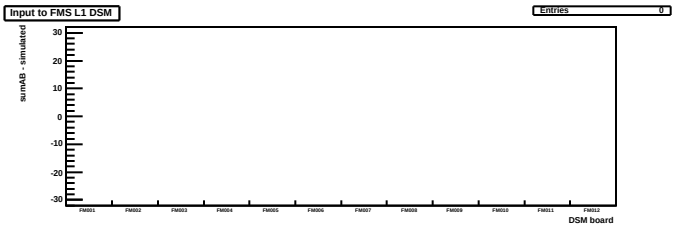

Input to FMS L1 DSM
Entries 1.219061e+07
sumBC
DSM board

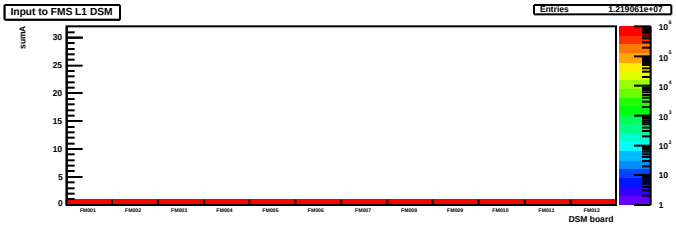

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

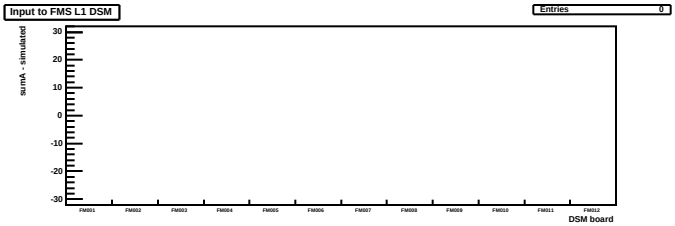

Input to FMS L1 DSM
Entries 1.219061e+07
sumB
DSM board

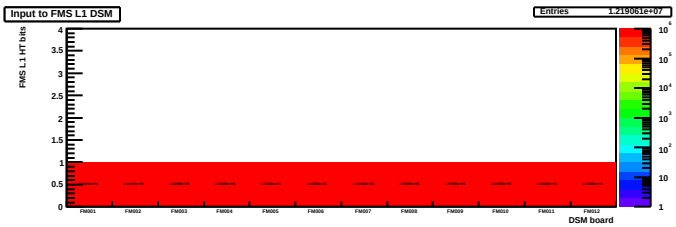

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

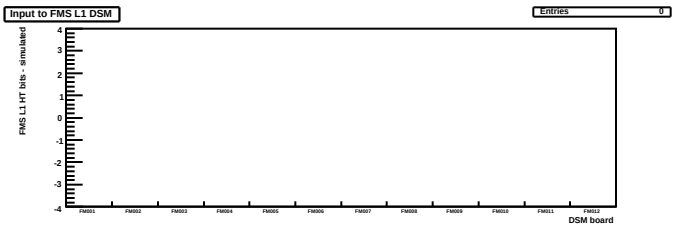

Input to FMS L1 DSM
Entries 1.219061e+07
sumAB
DSM board

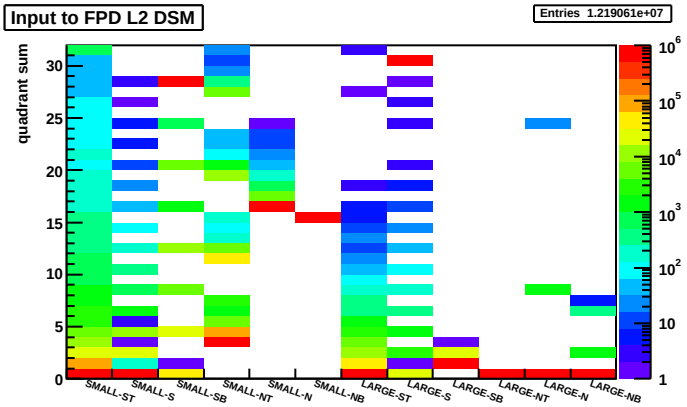

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

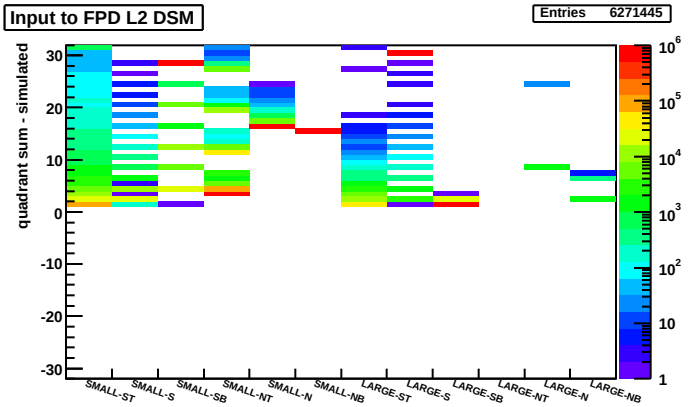

Input to FMS L1 DSM
Entries 1.219061e+07
sumA
DSM board

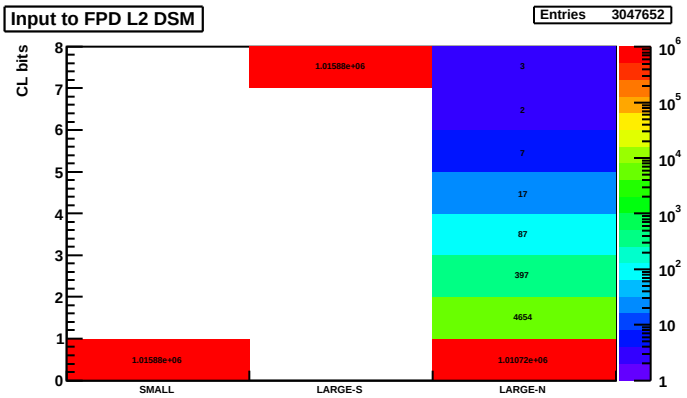

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

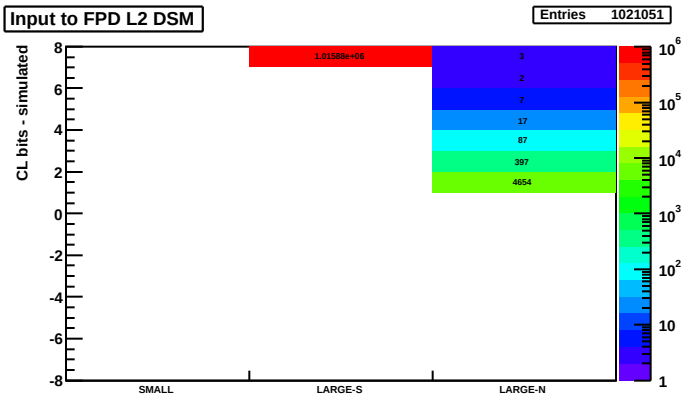

Input to FMS L1 DSM
Entries 1.219061e+07
FMS L1 HT bits
DSM board

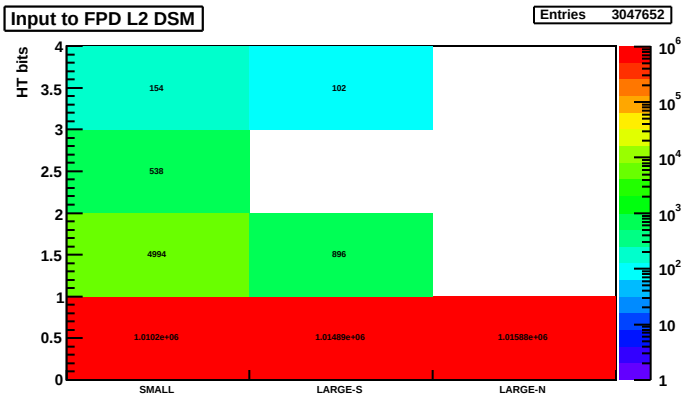

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

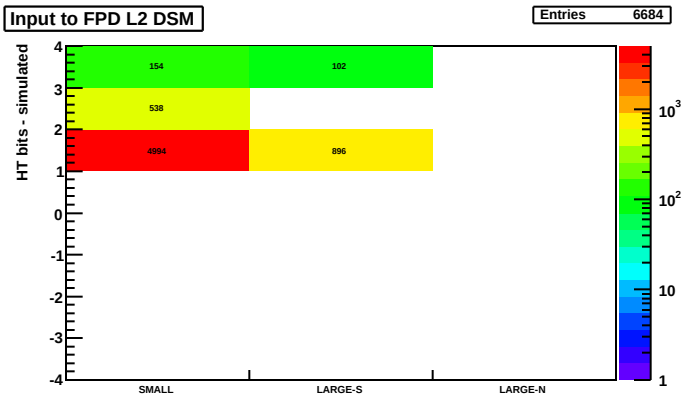

Input to FPD L2 DSM
Entries 1.219061e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

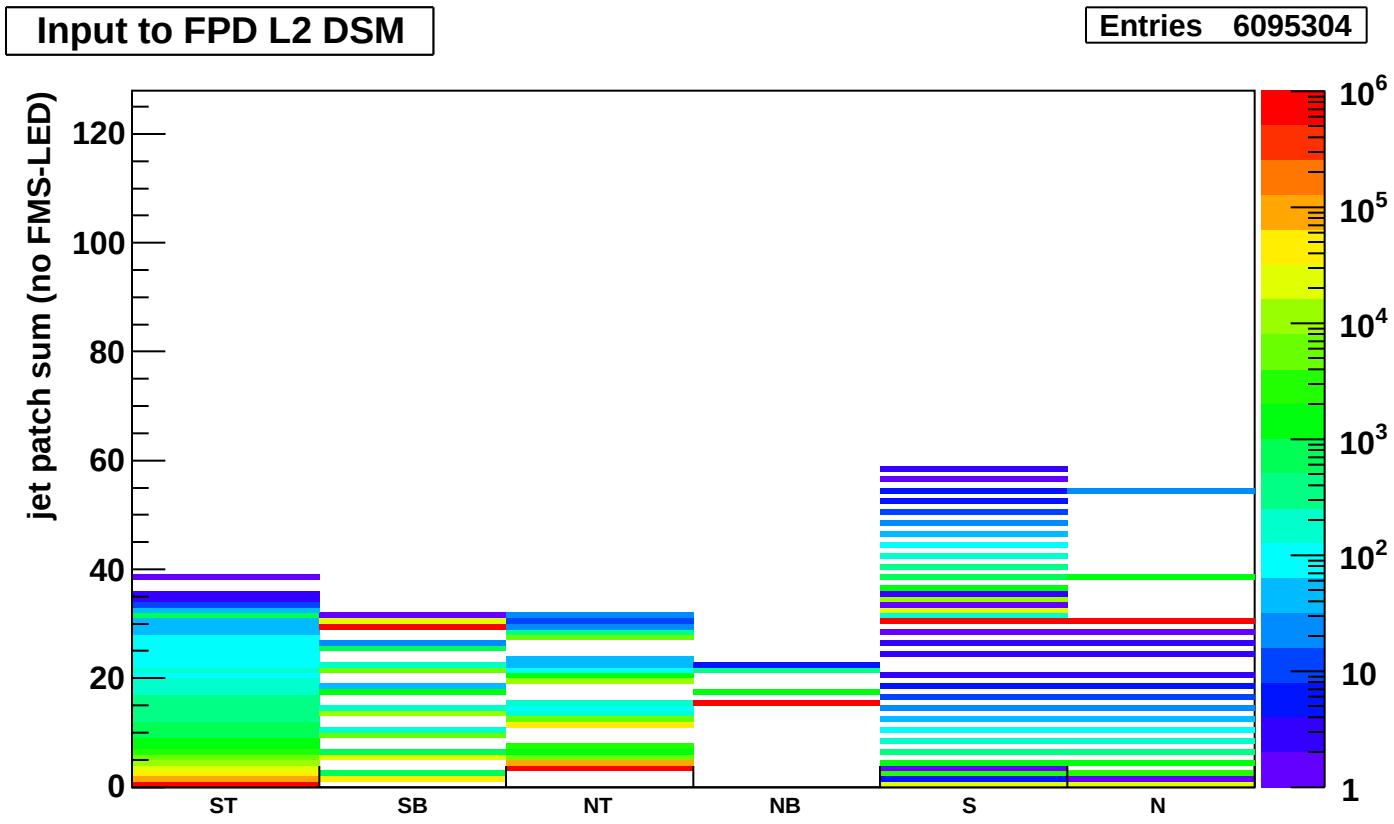

Input to FPD L2 DSM
Entries 6271445
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

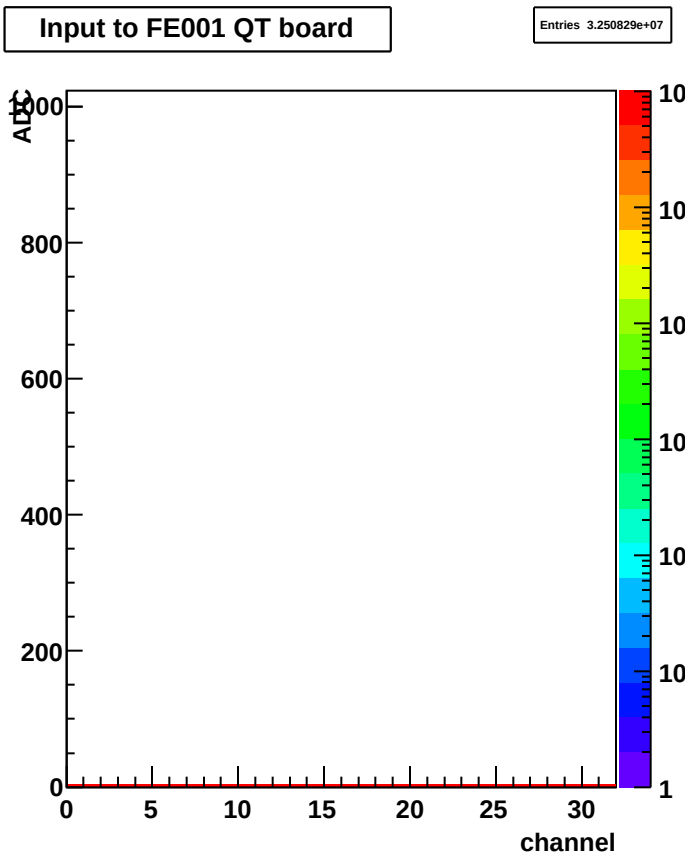

Input to FPD L2 DSM
Entries 3047652
CL bits
1.01588e+06
3
2
7
17
87
397
4654
1.01588e+06
1.01072e+06
SMALL
LARGE-S
LARGE-N

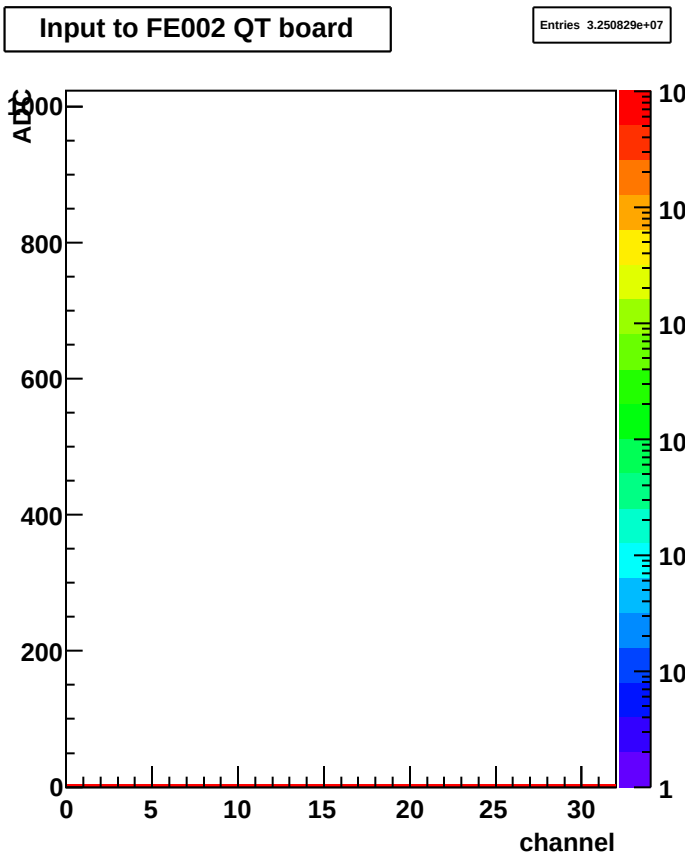

Input to FPD L2 DSM
Entries 1021051
CL bits - simulated
1.01588e+06
3
2
7
17
87
397
4654
SMALL
LARGE-S
LARGE-N

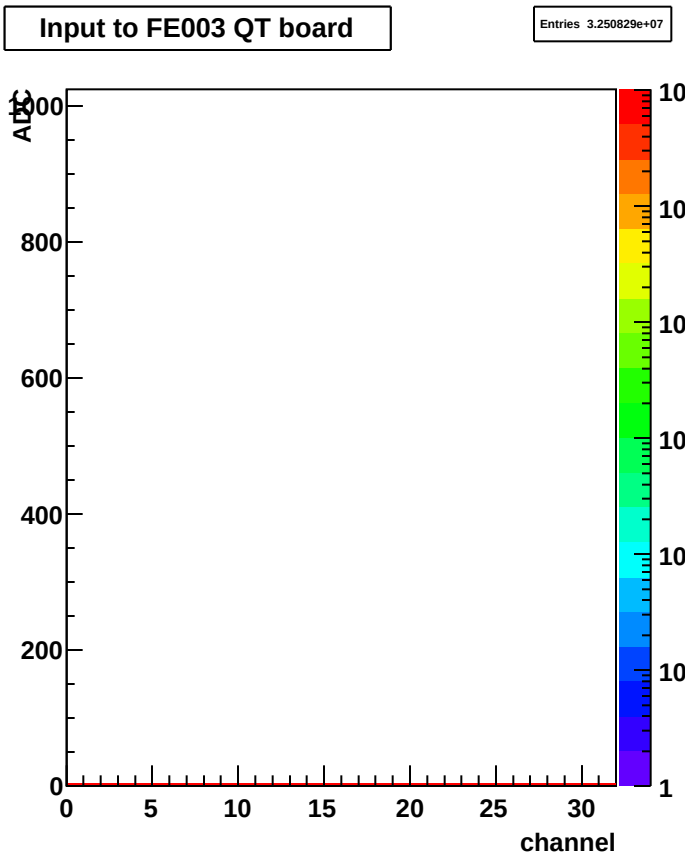

Input to FPD L2 DSM
Entries 3047652
HT bits
154
102
538
4994
896
1.0102e+06
1.01489e+06
1.01588e+06
SMALL
LARGE-S
LARGE-N

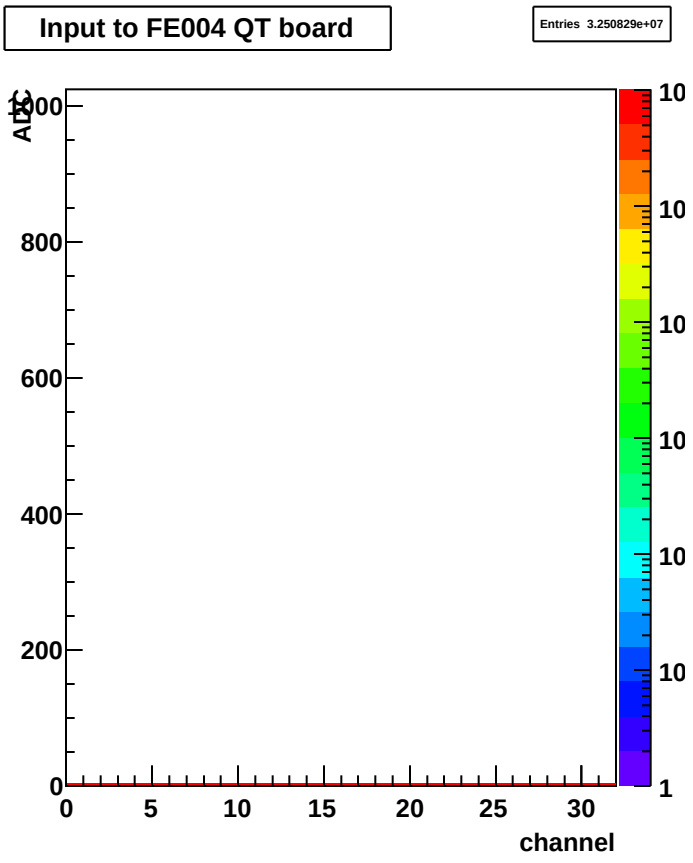

Input to FPD L2 DSM
Entries 6684
HT bits - simulated
154
102
538
4994
896
SMALL
LARGE-S
LARGE-N

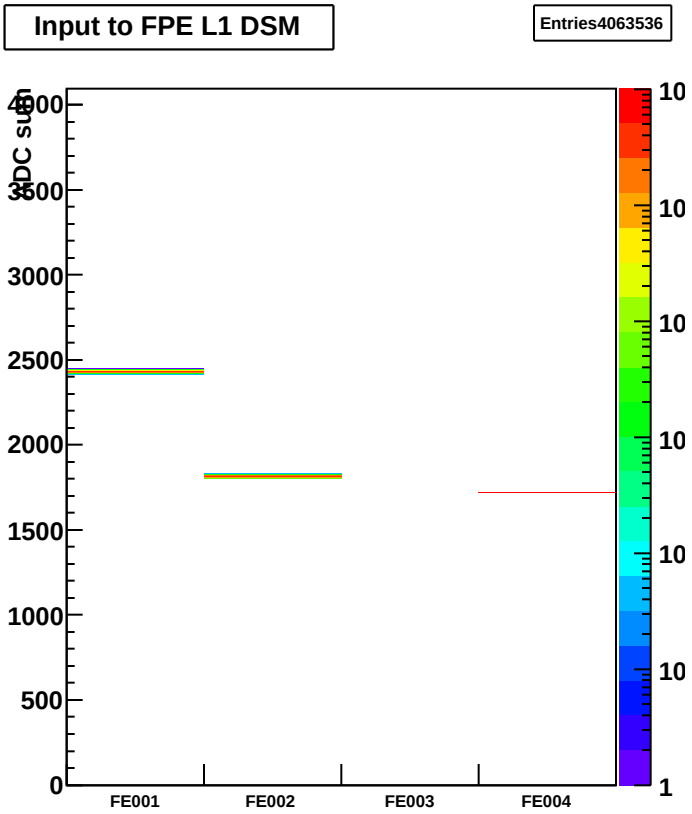

Input to FPD L2 DSM
Entries 6095304
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 3.250829e+07
ADC
channel

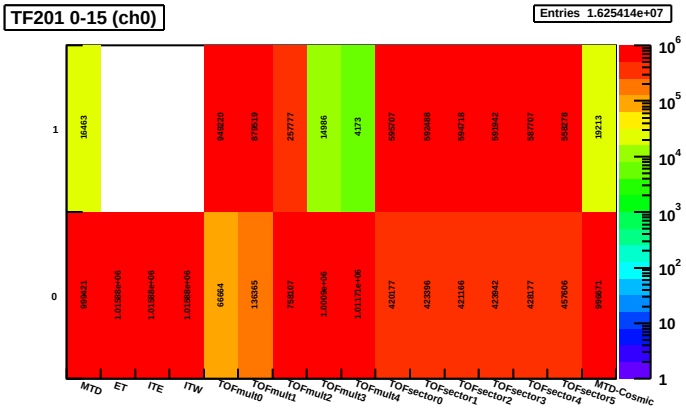

Input to FE002 QT board
Entries 3.250829e+07
ADC
channel


Input to FE003 QT board
Entries 3.250829e+07
ADC
channel

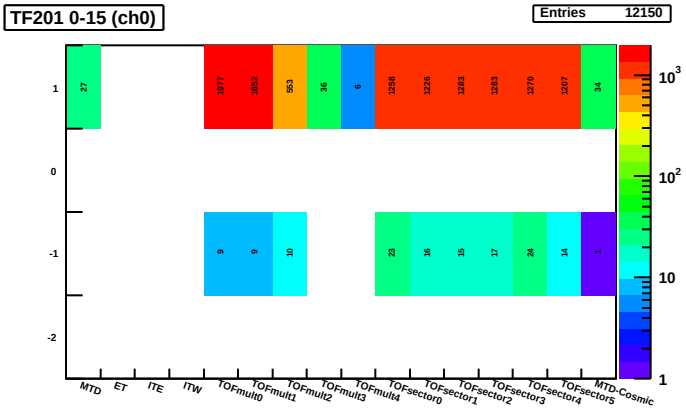

Input to FE004 QT board
Entries 3.250829e+07
ADC
channel


Input to FPE L1 DSM
Entries4063536
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

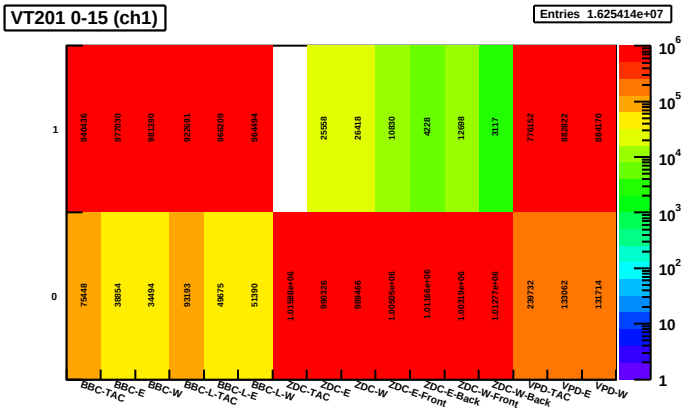
TF201 0-15 (ch0)



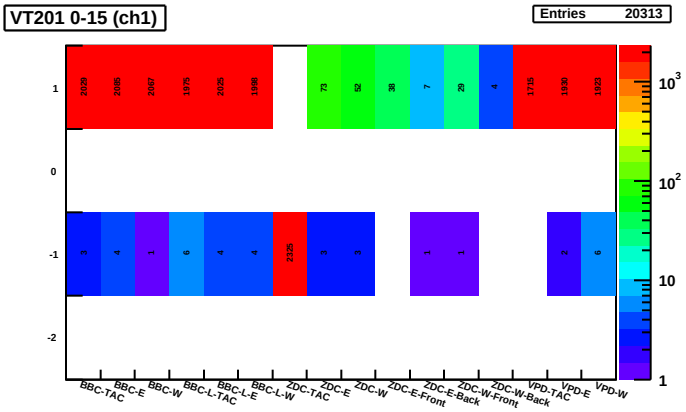
TF201 0-15 (ch0)



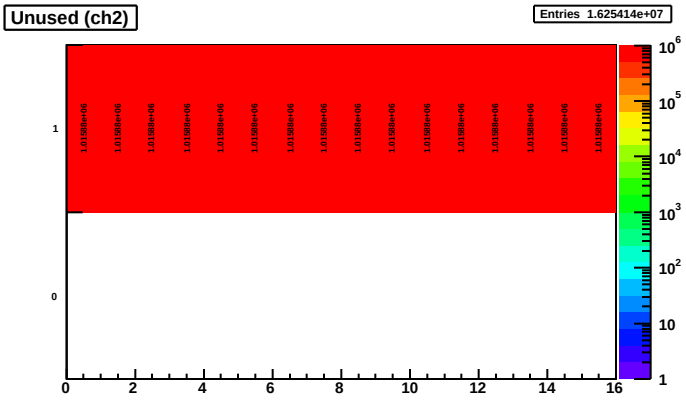
VT201 0-15 (ch1)

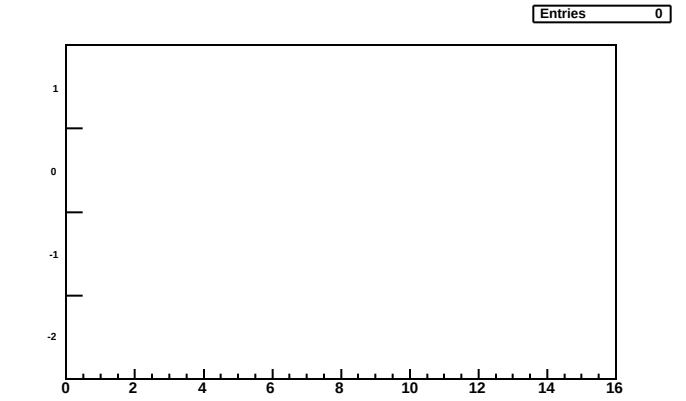


VT201 0-15 (ch1)

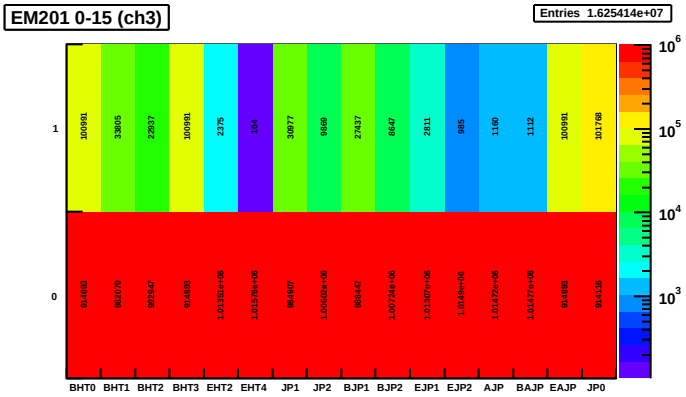


Unused (ch2)

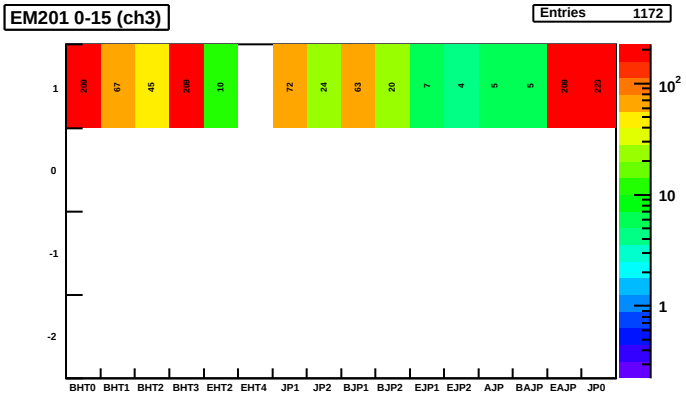




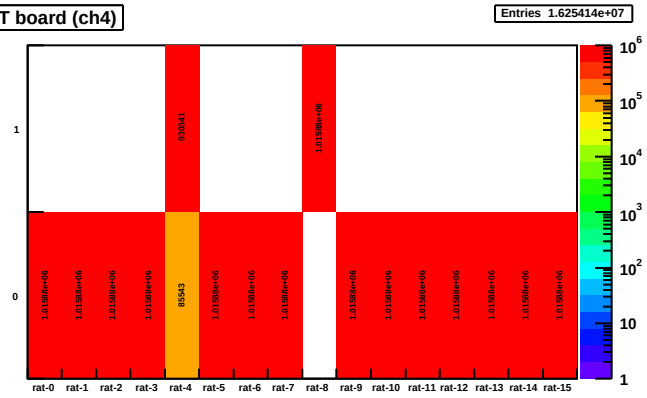
EM201 0-15 (ch3)

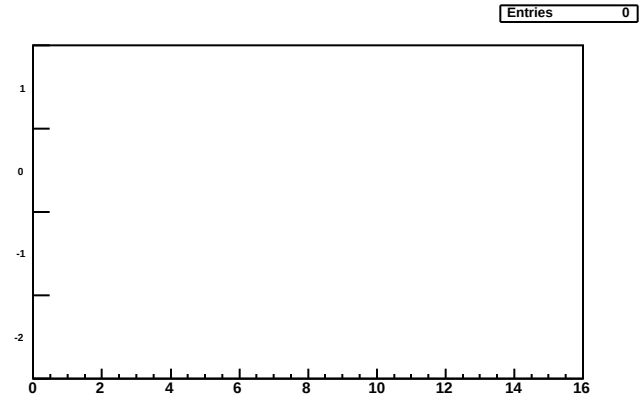


EM201 0-15 (ch3)

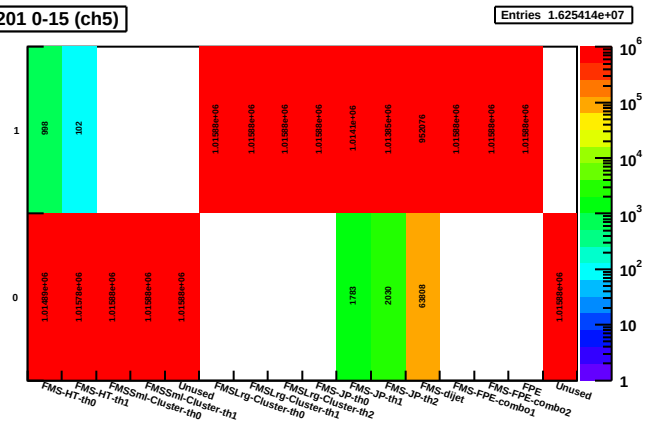


RAT board (ch4)





FP201 0-15 (ch5)


Entries 1.625414e+07
898
102
1.01589e+06
1.01589e+06
1.01589e+06