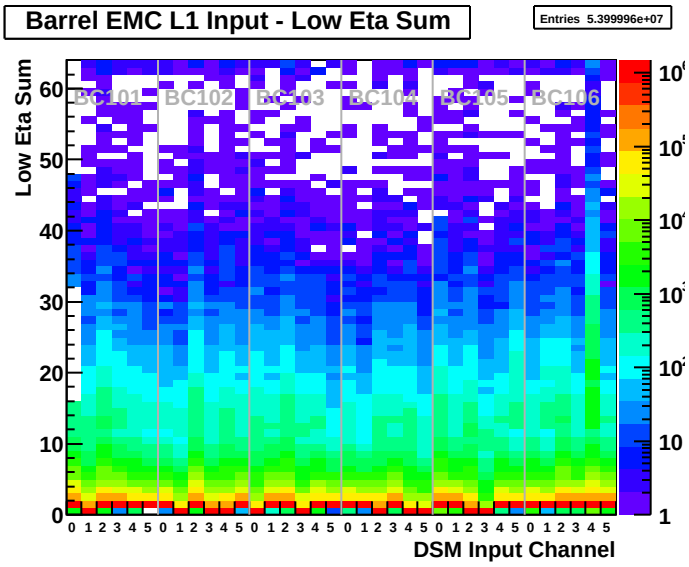
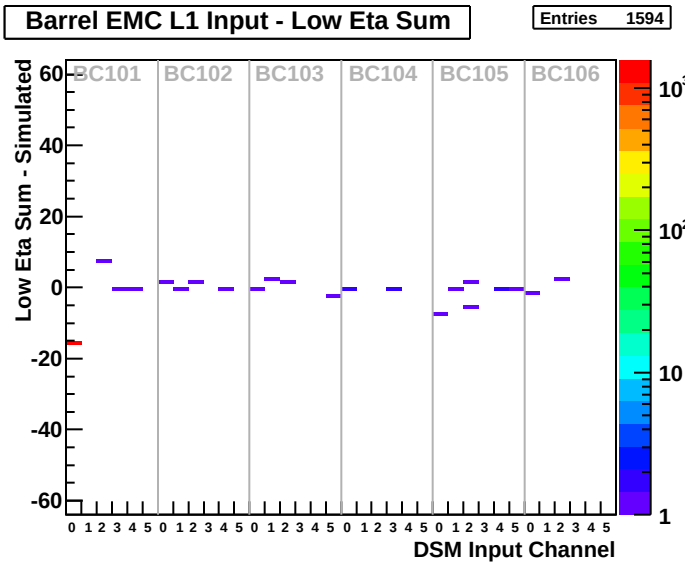
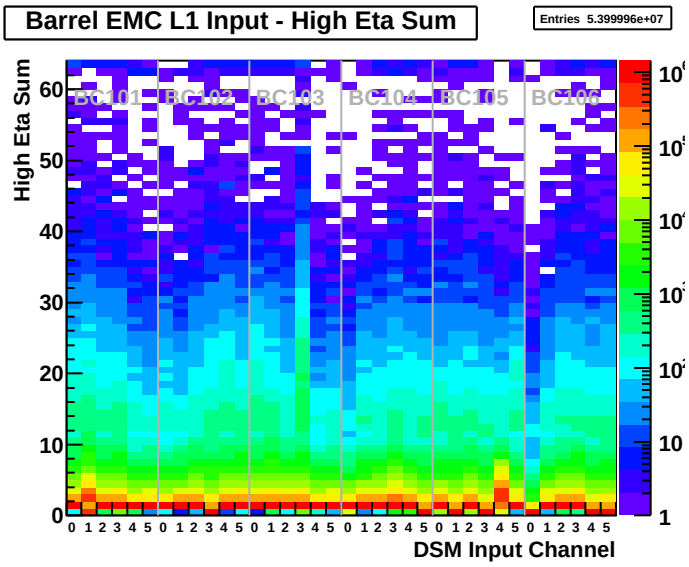
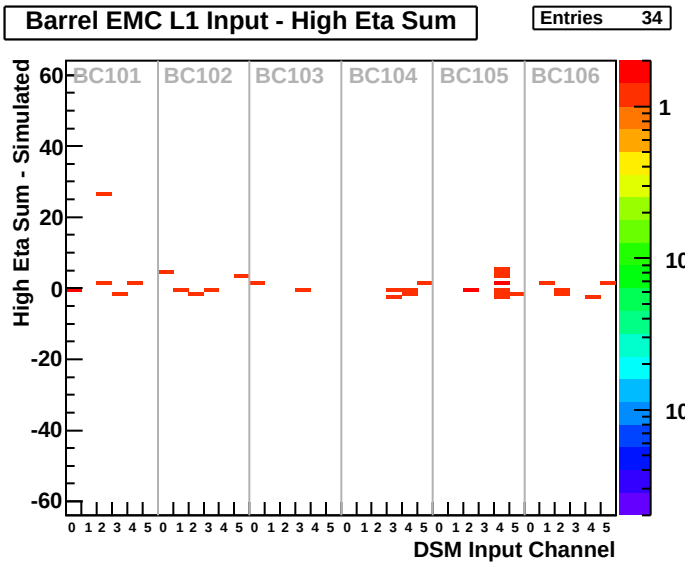
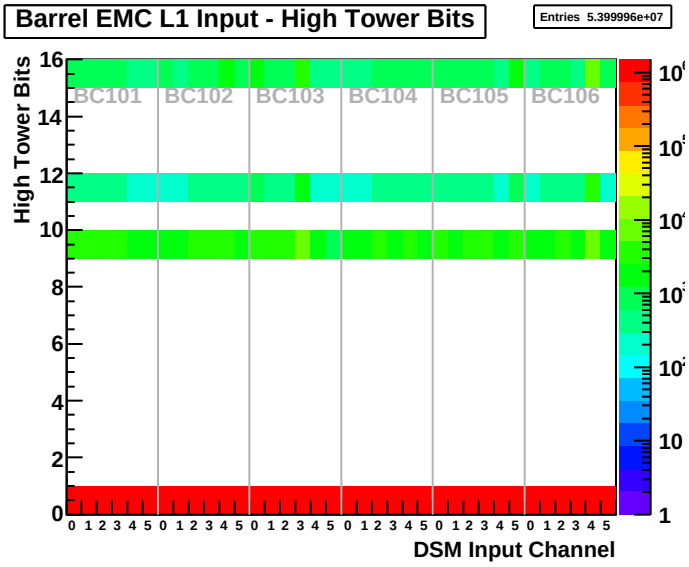
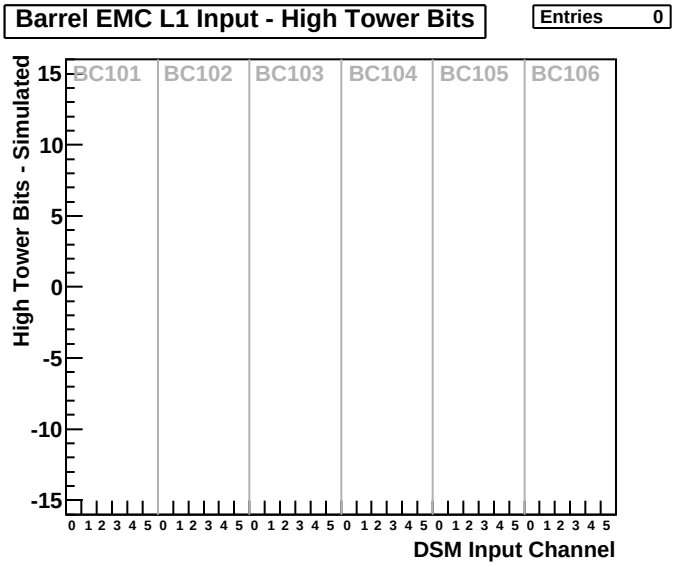

Barrel EMC L1 Input - Low Eta Sum
Entries 5.399996e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

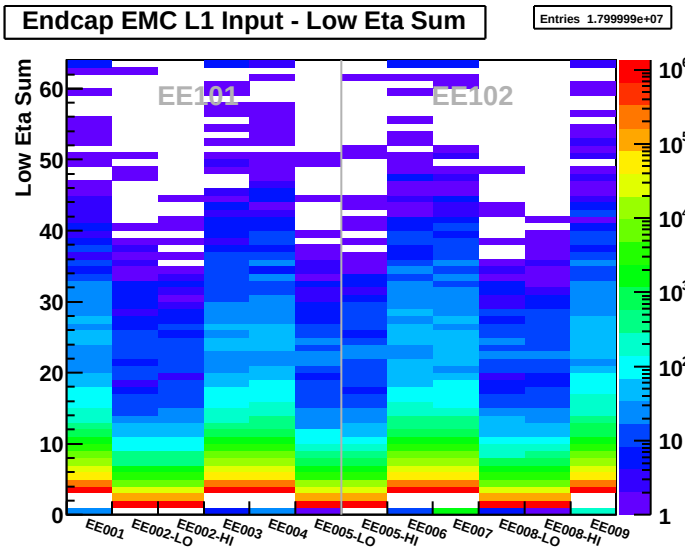

Barrel EMC L1 Input - Low Eta Sum
Entries 1594
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

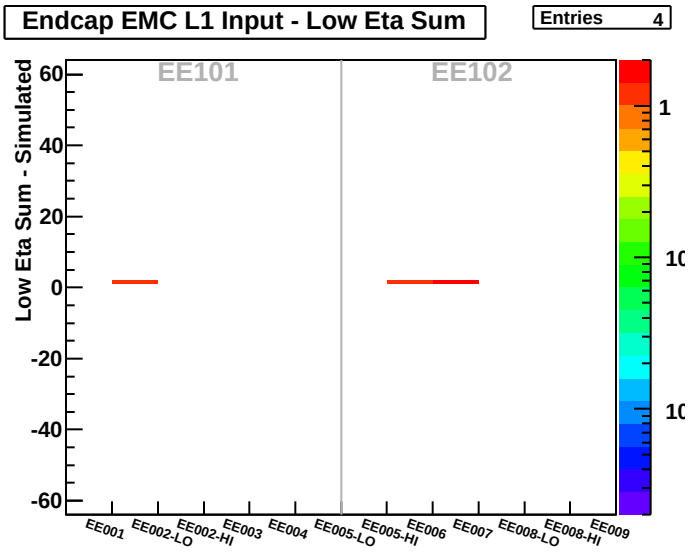

Barrel EMC L1 Input - High Eta Sum
Entries 5.399996e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

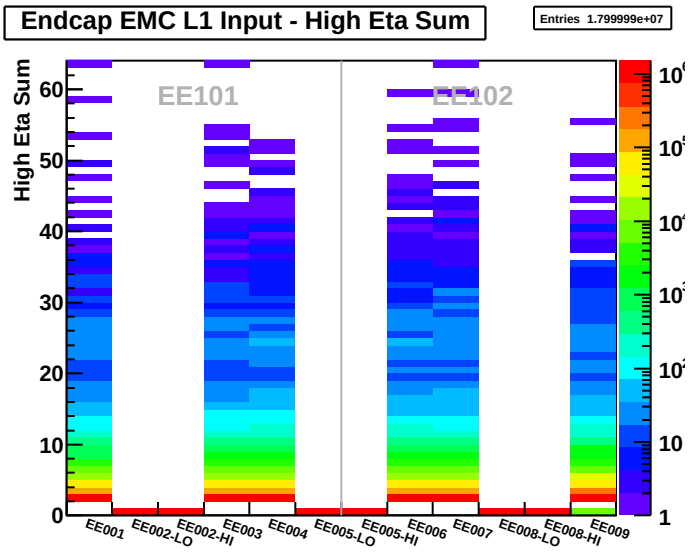

Barrel EMC L1 Input - High Eta Sum
Entries 34
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

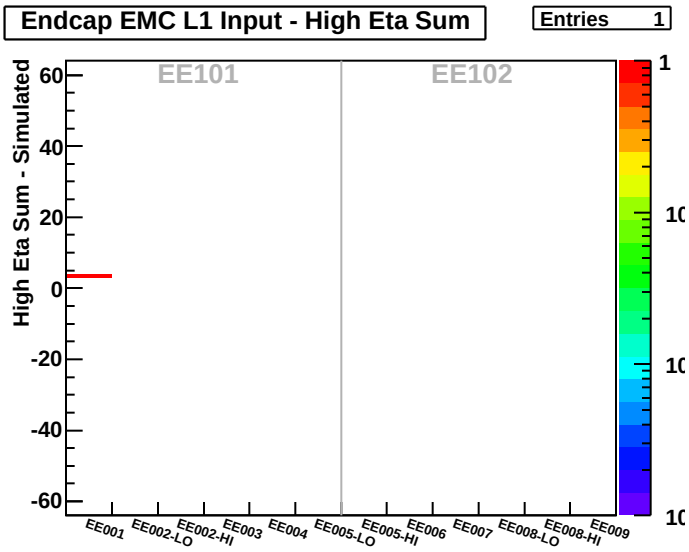

Barrel EMC L1 Input - High Tower Bits
Entries 5.399996e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

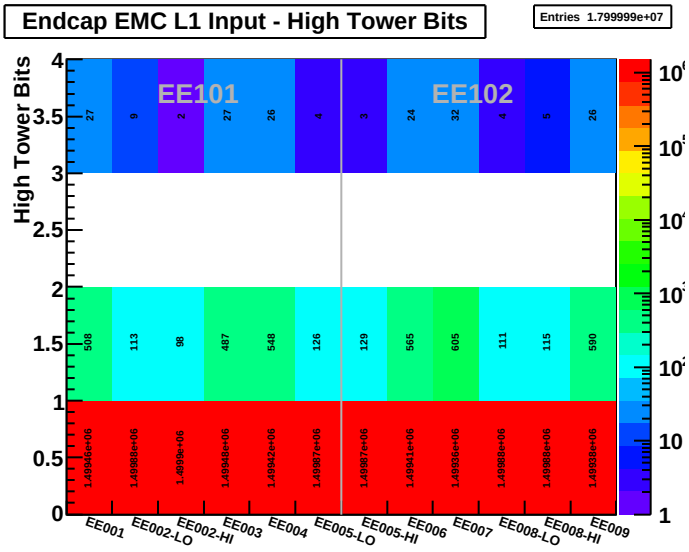

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

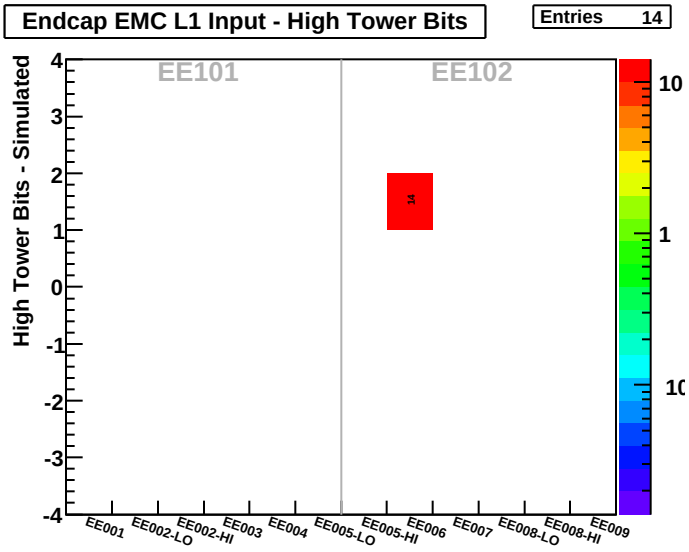

Endcap EMC L1 Input - Low Eta Sum
Entries 1.799999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

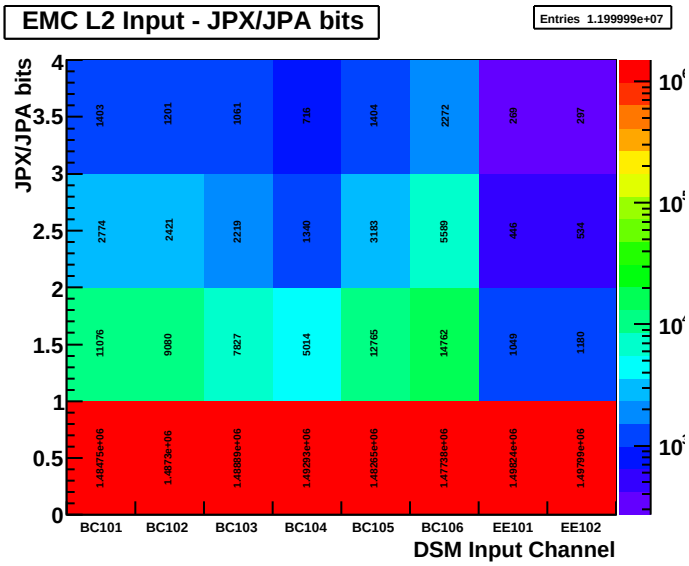

Endcap EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

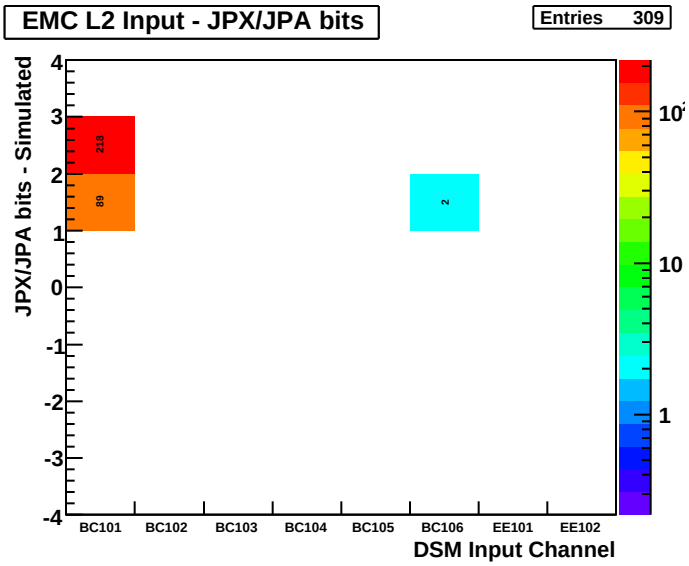

Endcap EMC L1 Input - High Eta Sum
Entries 1.799999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

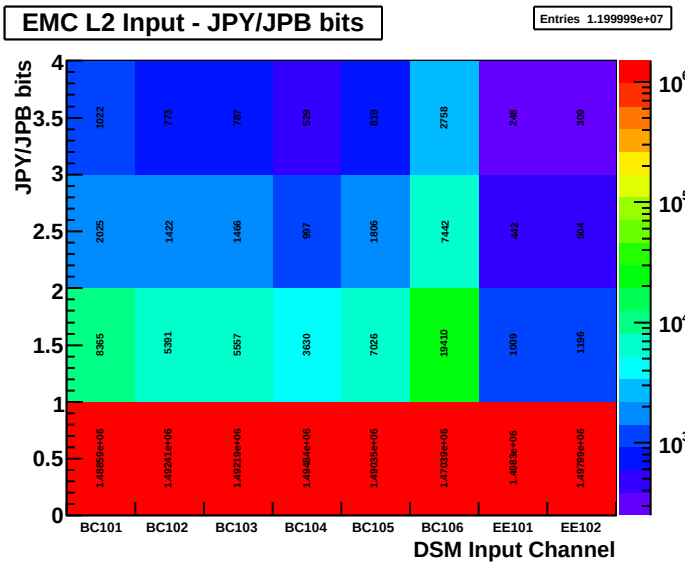

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

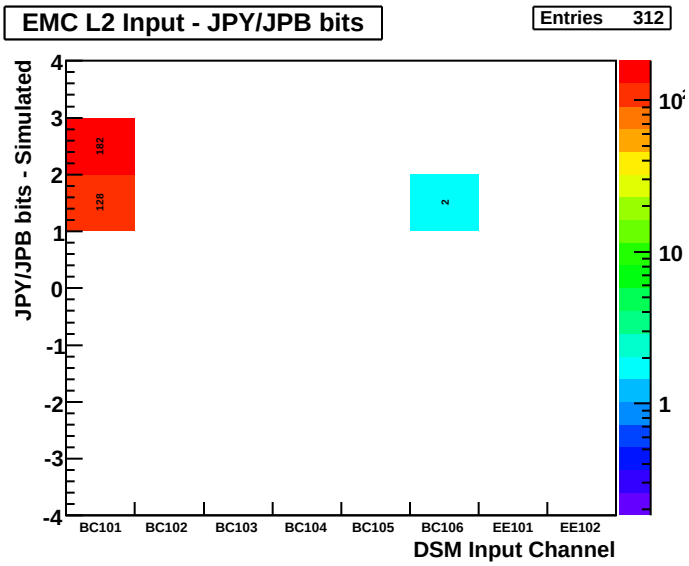

Endcap EMC L1 Input - High Tower Bits
Entries 1.799999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

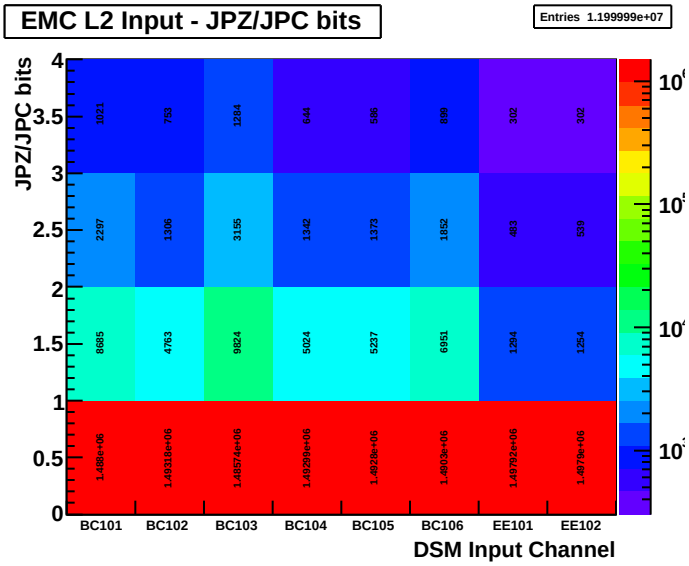

Endcap EMC L1 Input - High Tower Bits
Entries 14
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

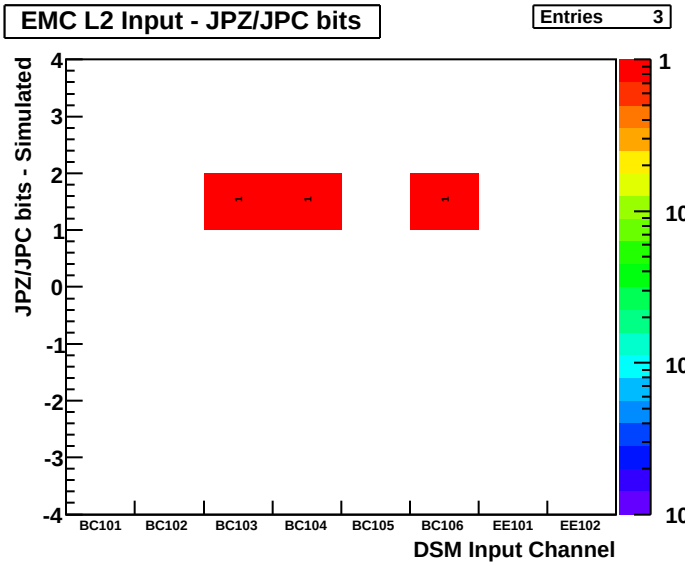

EMC L2 Input - JPX/JPA bits
Entries 1.199999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1403 1201 1061 716 1404 2272 269 297
2774 2421 2219 1340 3183 5589 446 534
11076 9080 7827 5014 12765 14762 1049 1180
1.48475e+06 1.4873e+06 1.48839e+06 1.49233e+06 1.49265e+06 1.47738e+06 1.49824e+06 1.49799e+06

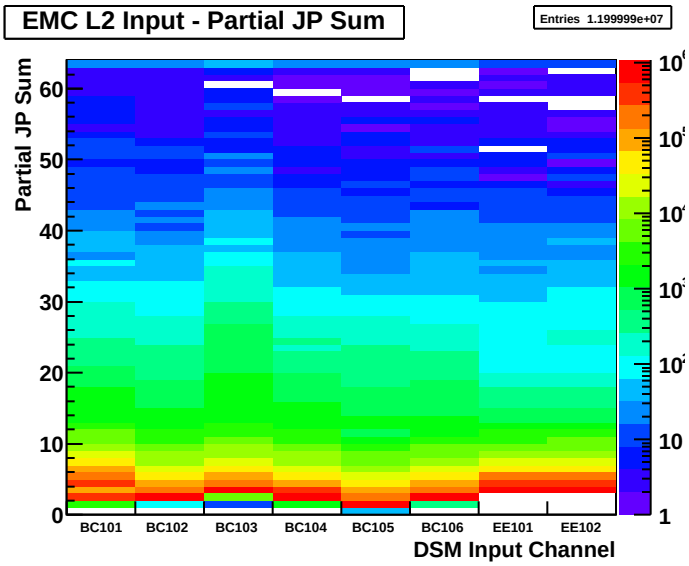

EMC L2 Input - JPX/JPA bits
Entries 309
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
218
89
2

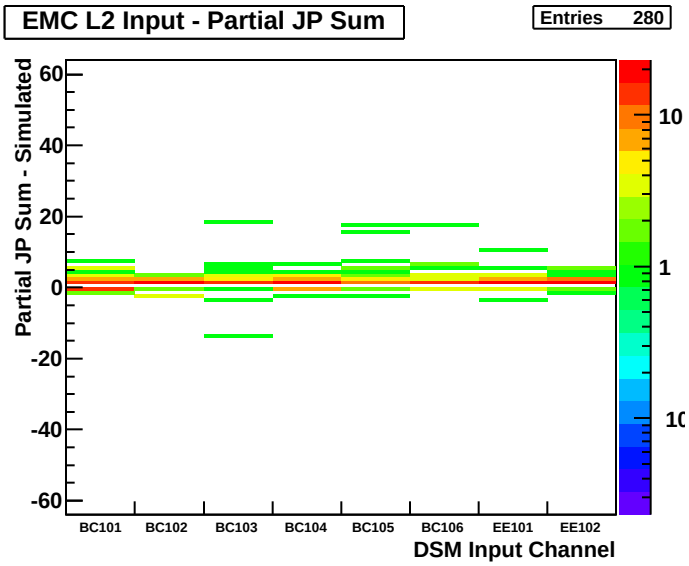

EMC L2 Input - JPY/JPB bits
Entries 1.199999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1022 773 797 529 819 2758 248 309
2025 1422 1466 997 1806 7442 442 504
8365 5391 5557 3630 7026 19410 1009 1196
1.48839e+06 1.49231e+06 1.49219e+06 1.49484e+06 1.49035e+06 1.47039e+06 1.4983e+06 1.49799e+06

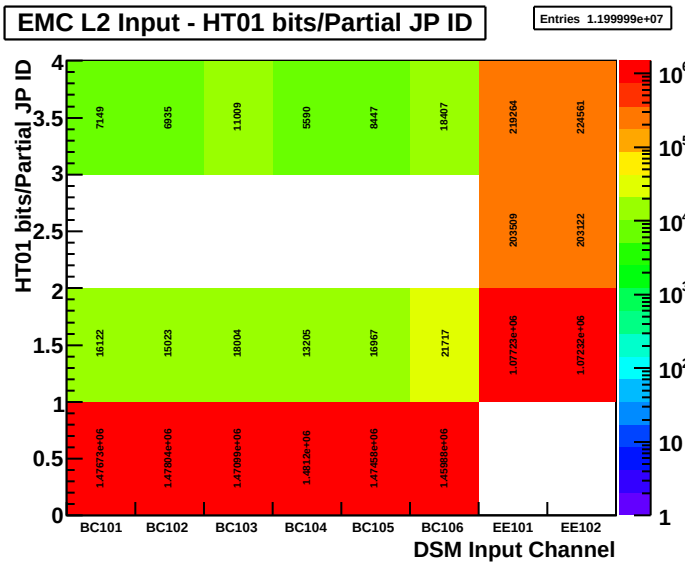

EMC L2 Input - JPY/JPB bits
Entries 312
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
182
128
2

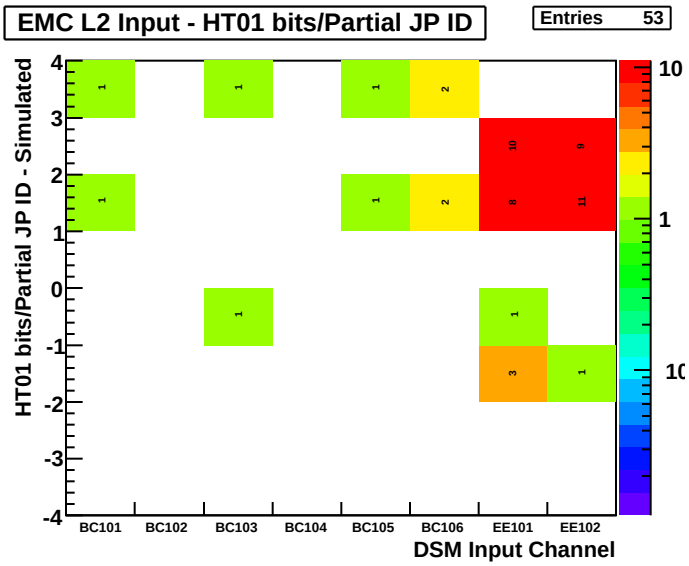

EMC L2 Input - JPZ/JPC bits
Entries 1.199999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1021 753 1284 644 586 899 302 302
2297 1306 3155 1342 1373 1852 433 539
8685 4763 9824 5024 5237 6951 1294 1254
1.488e+06 1.49318e+06 1.48574e+06 1.49293e+06 1.4928e+06 1.4903e+06 1.49792e+06 1.4979e+06

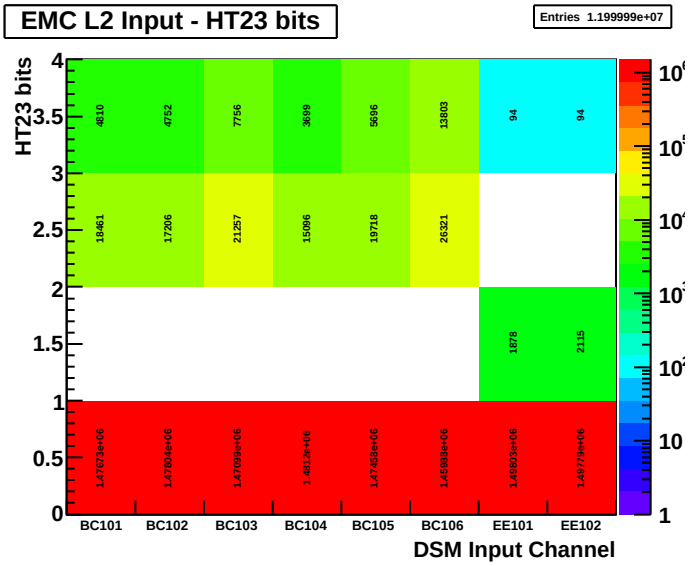

EMC L2 Input - JPZ/JPC bits
Entries 3
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1
1

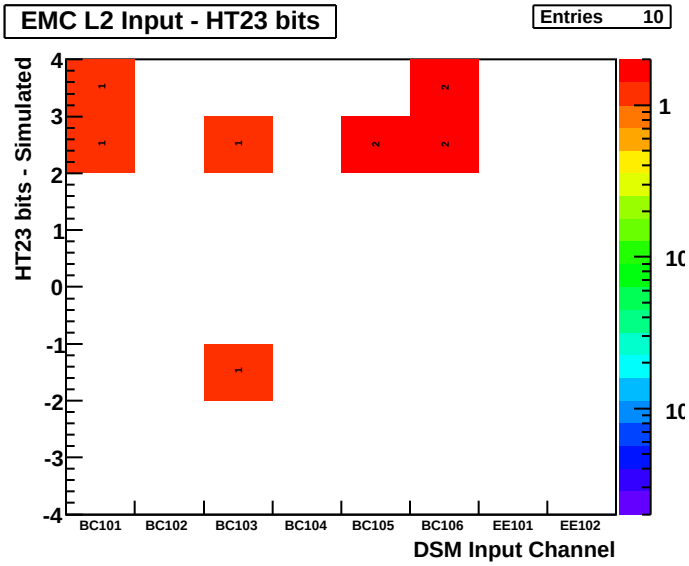

EMC L2 Input - Partial JP Sum
Entries 1.199999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

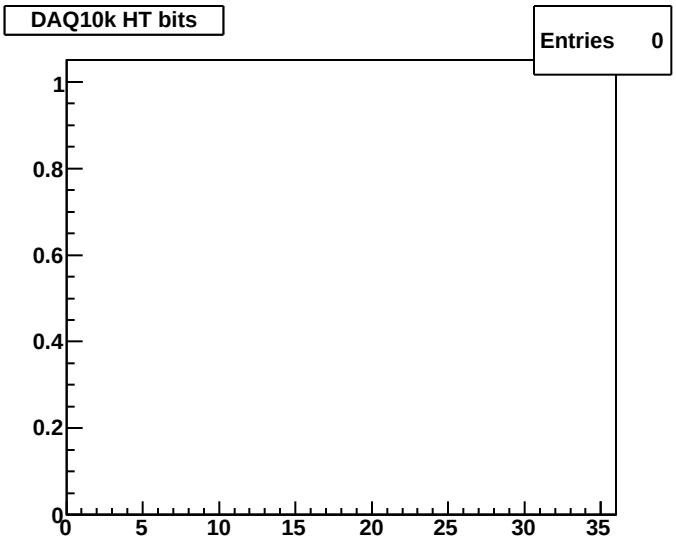

EMC L2 Input - Partial JP Sum
Entries 280
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

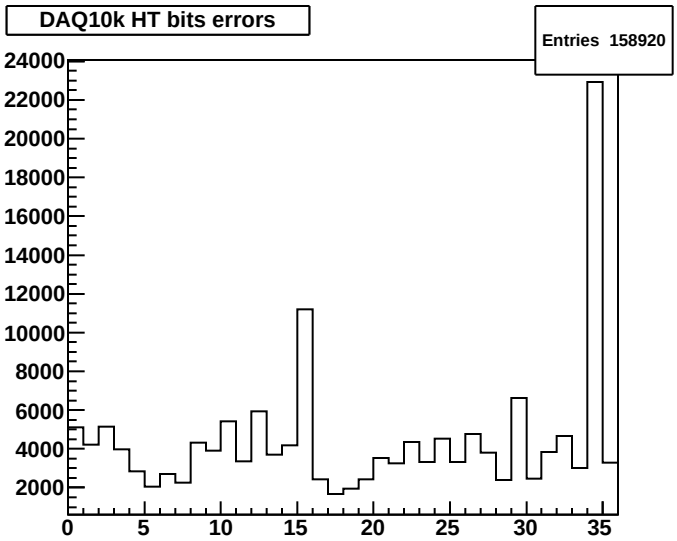

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.199999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

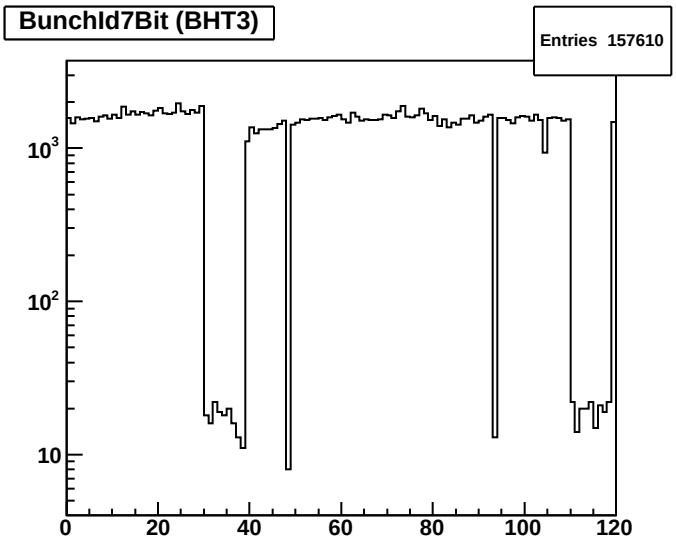

EMC L2 Input - HT01 bits/Partial JP ID
Entries 53
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

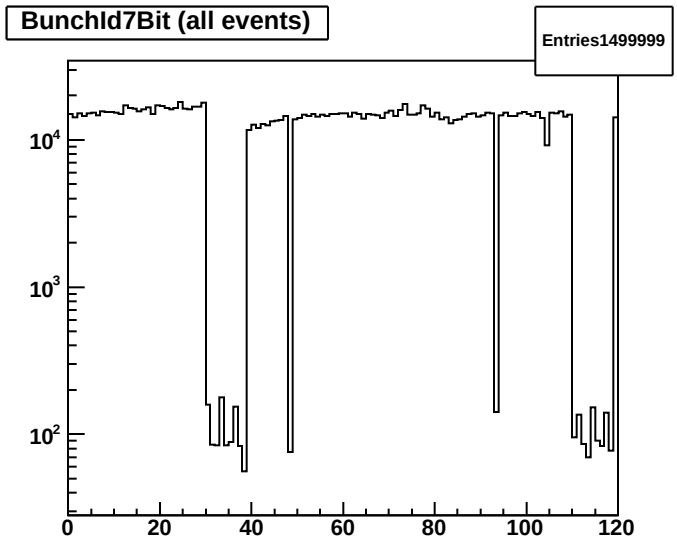

EMC L2 Input - HT23 bits
Entries 1.199999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

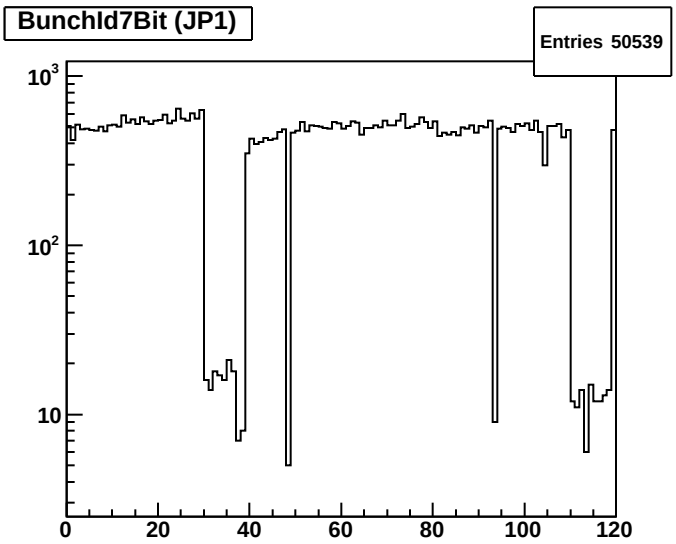

EMC L2 Input - HT23 bits
Entries 10
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

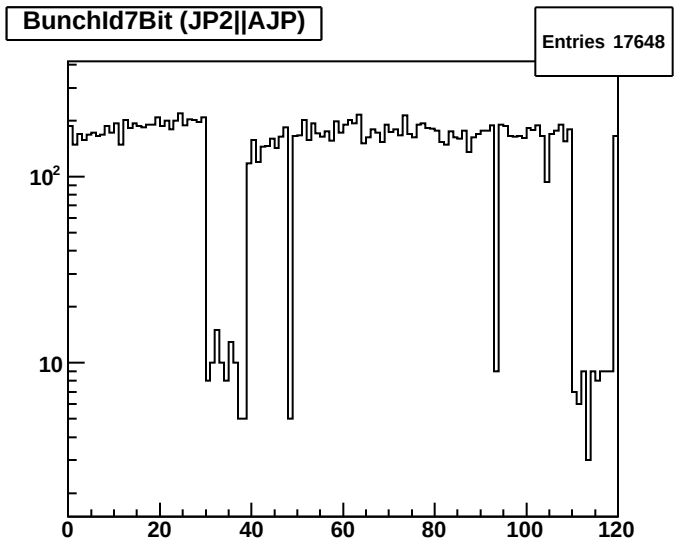

DAQ10k HT bits
Entries 0

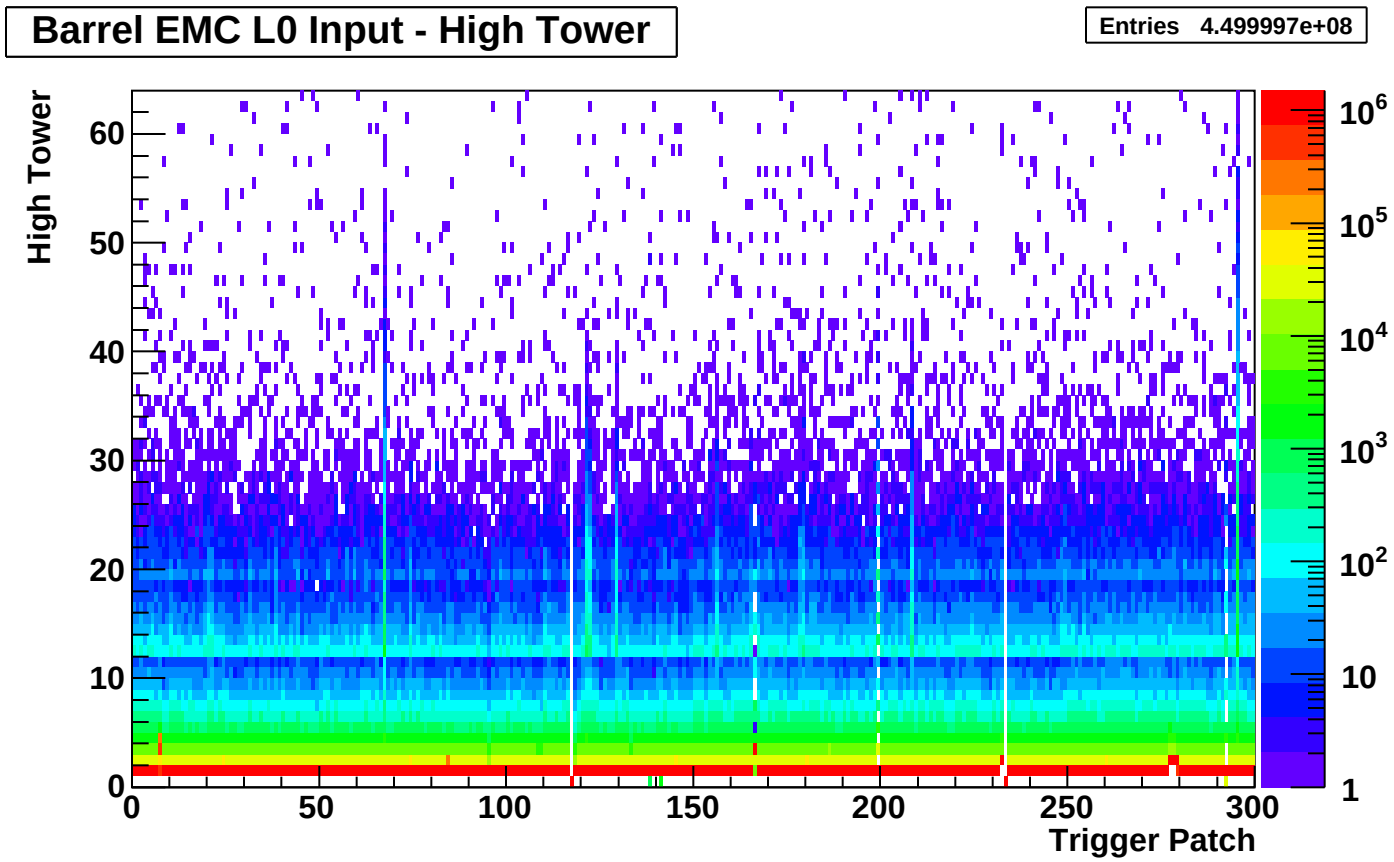

DAQ10k HT bits errors
Entries 158920

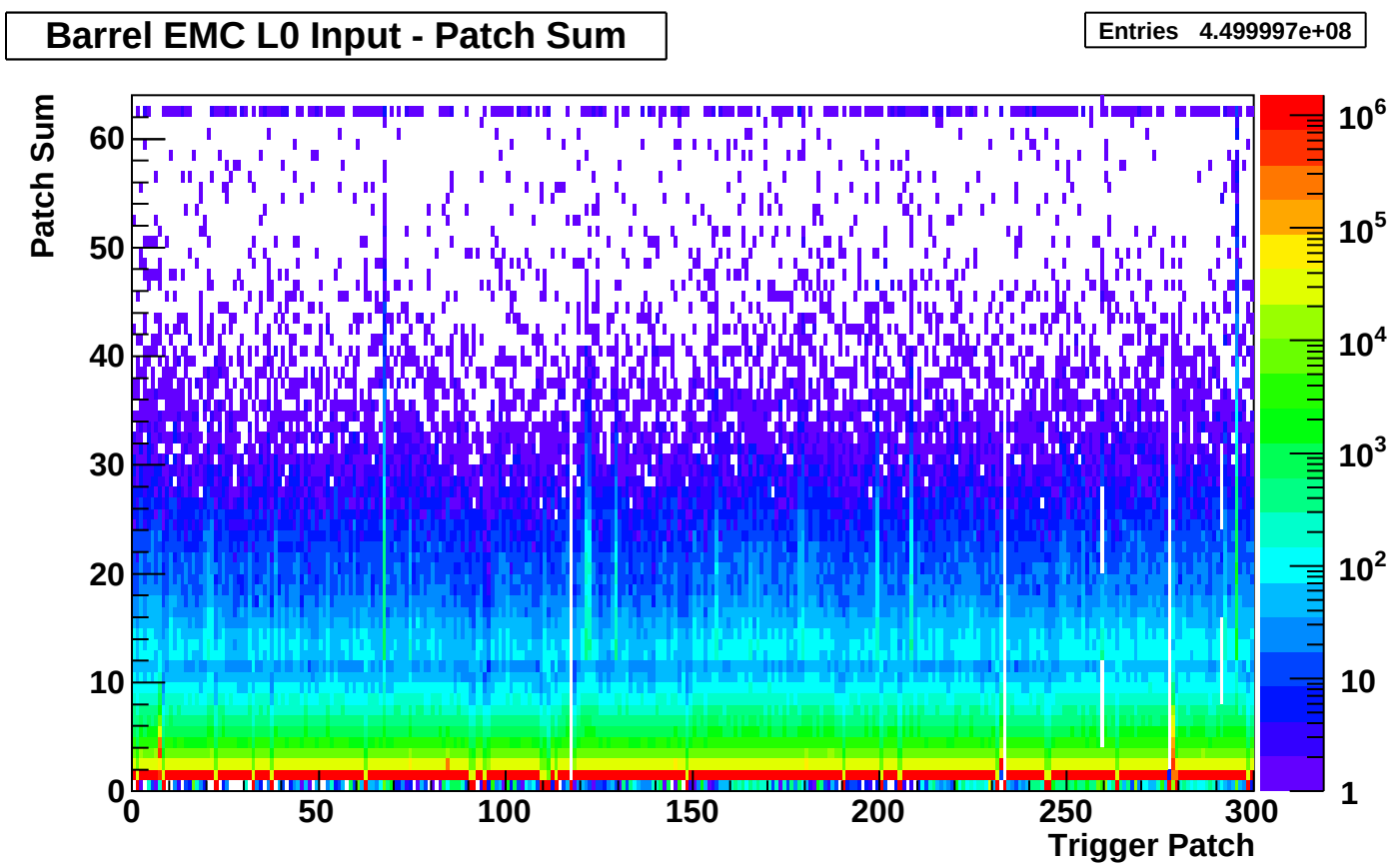

BunchId7Bit (BHT3)
Entries 157610

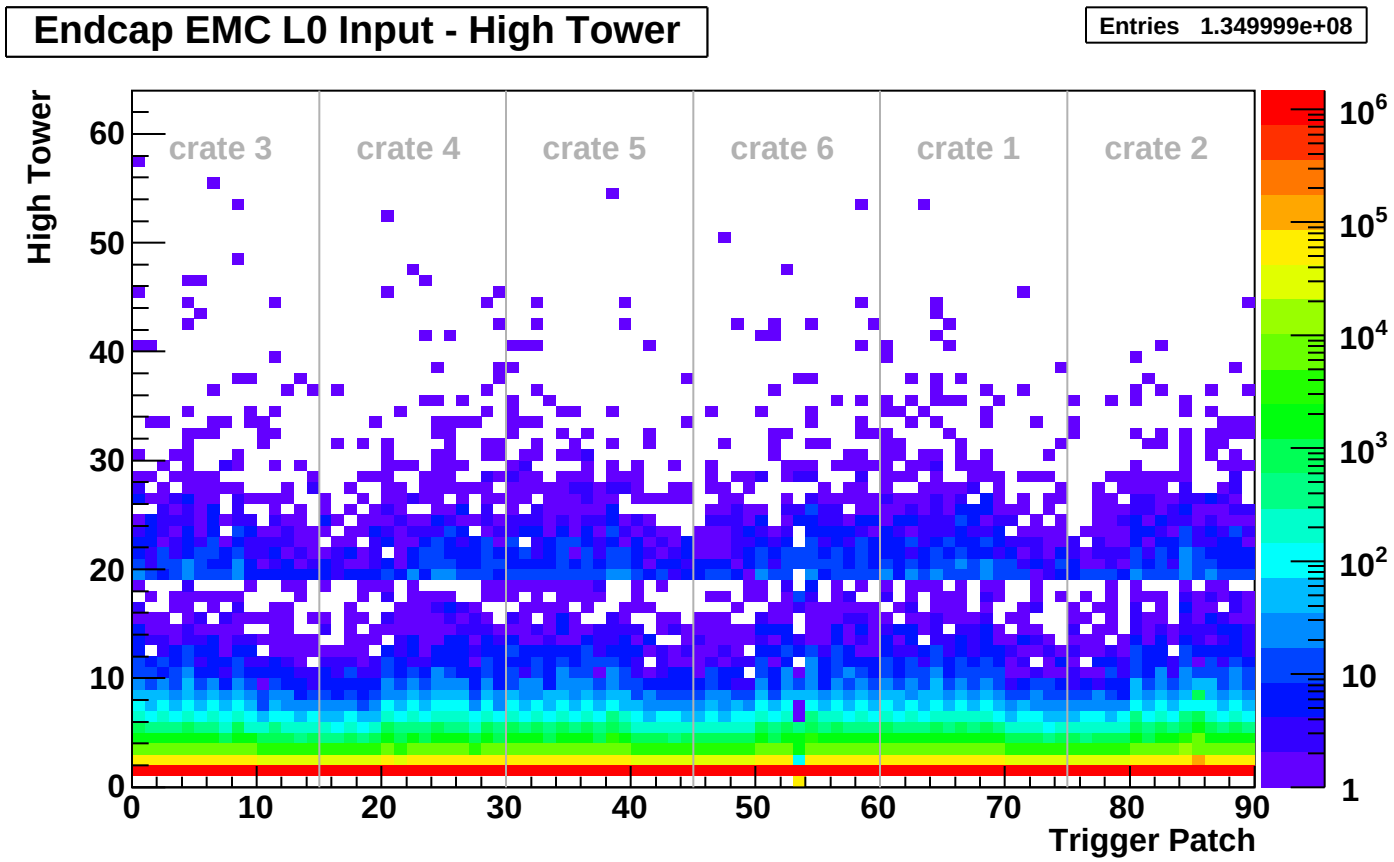

BunchId7Bit (all events)
Entries1499999

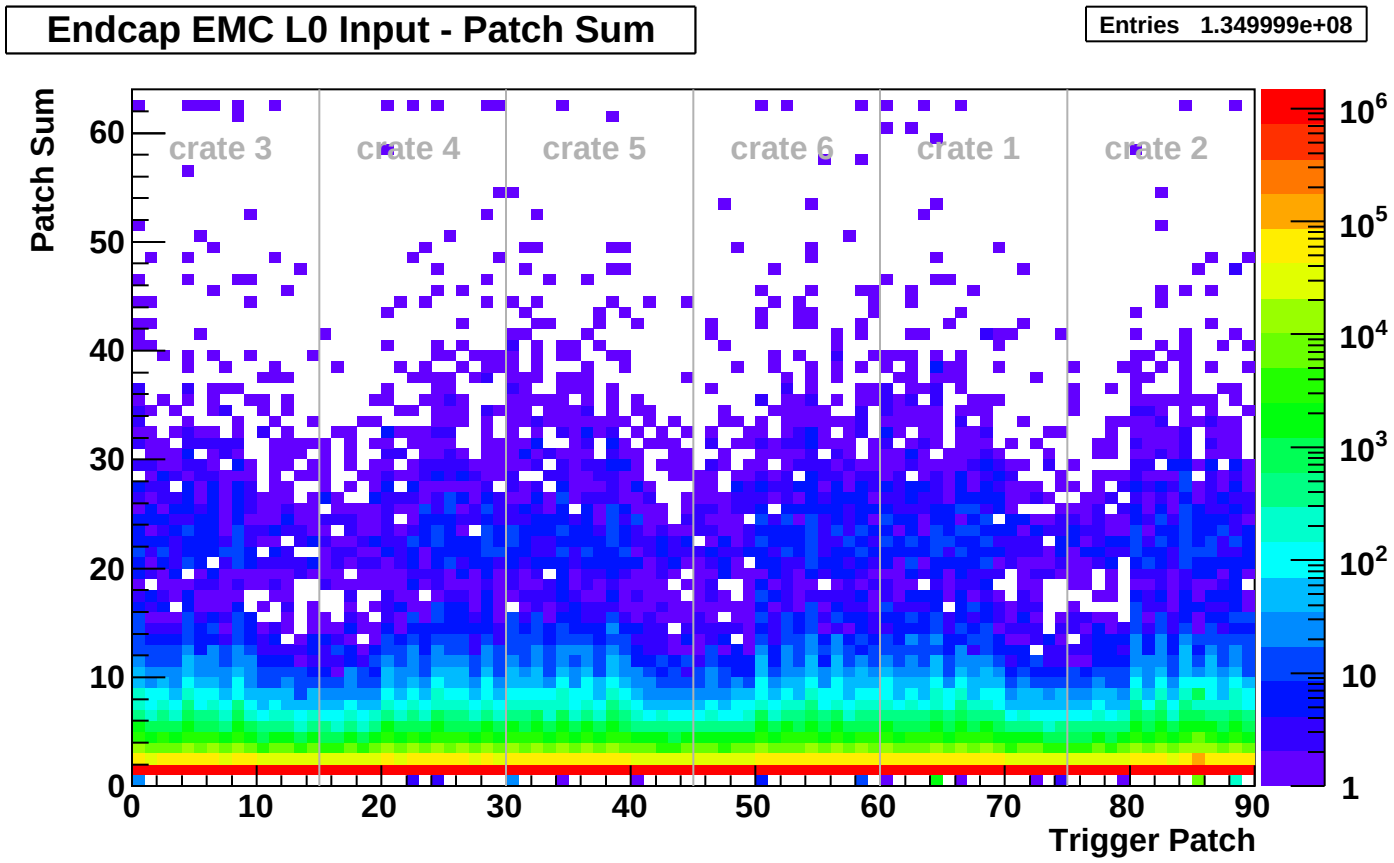

BunchId7Bit (JP1)
Entries 50539

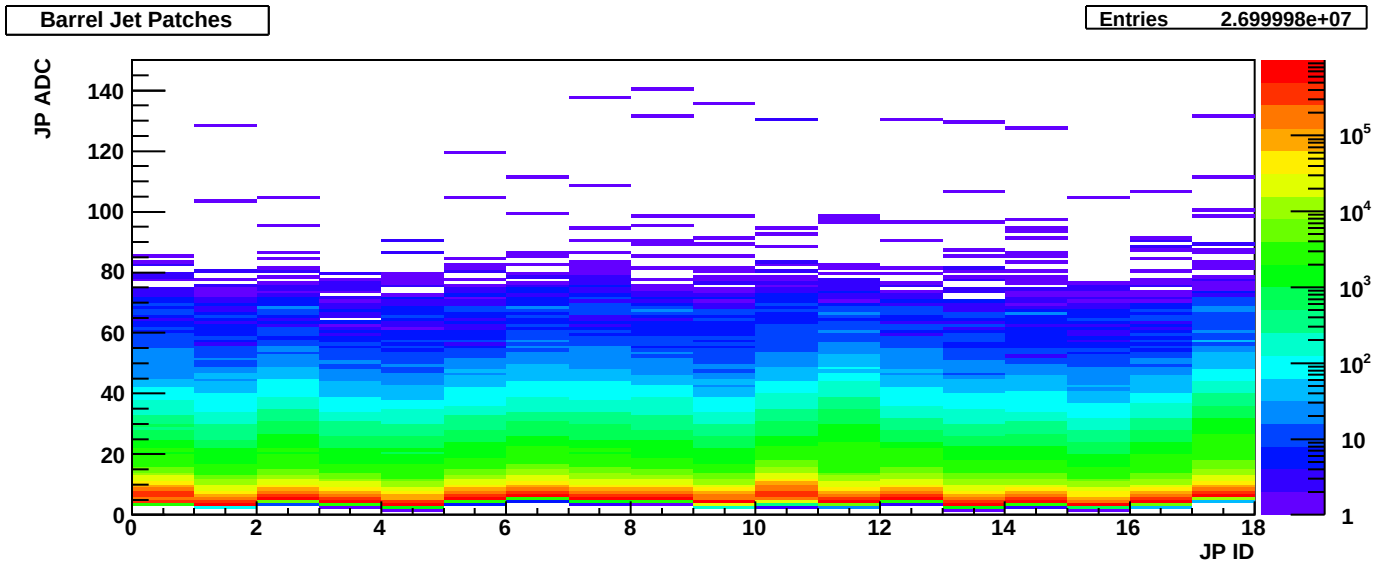

BunchId7Bit (JP2||AJP)
Entries 17648

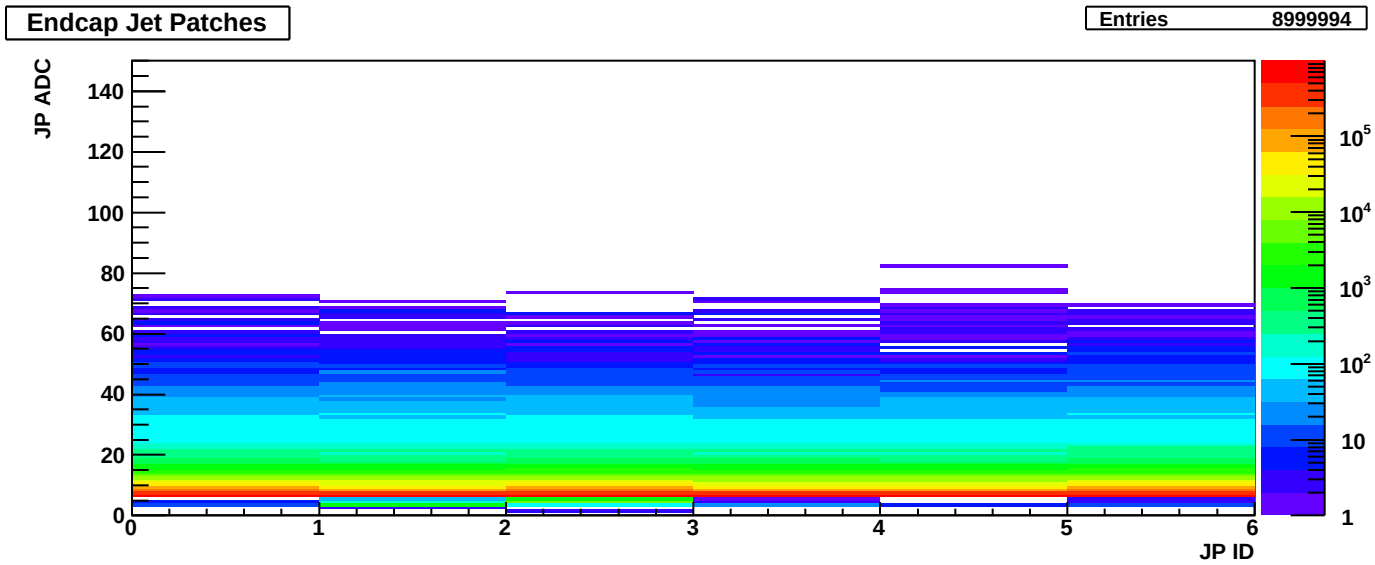

Barrel EMC L0 Input - High Tower
Entries 4.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

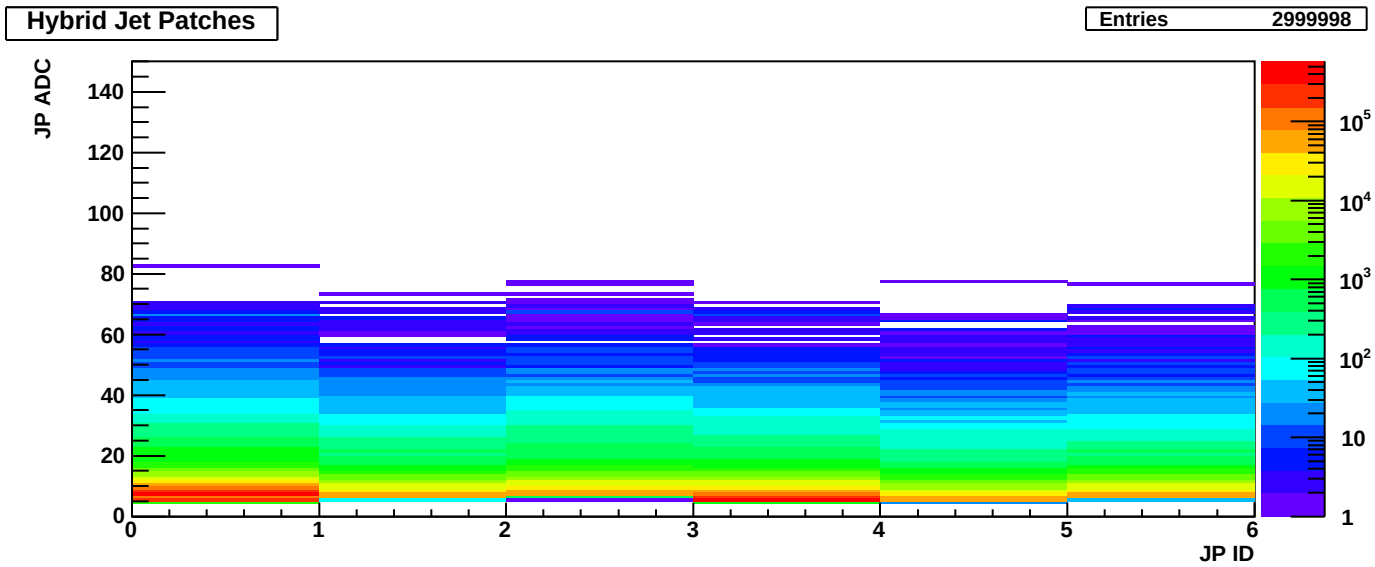

Barrel EMC L0 Input - Patch Sum
Entries 4.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

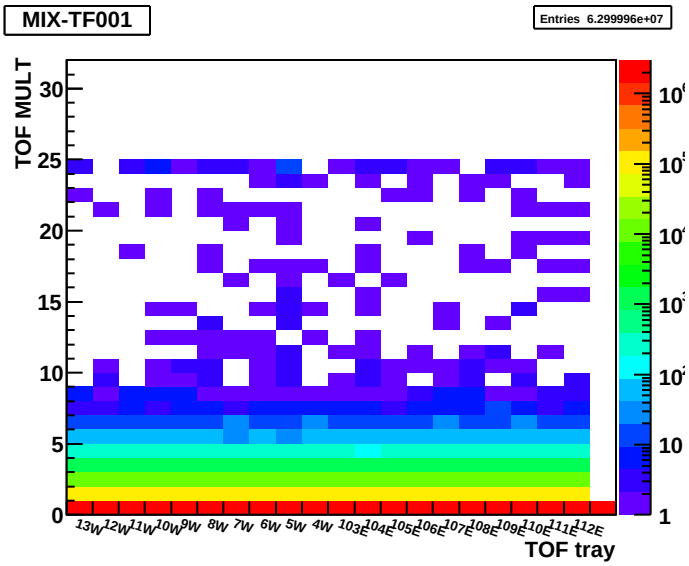

Endcap EMC L0 Input - High Tower
Entries 1.349999e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

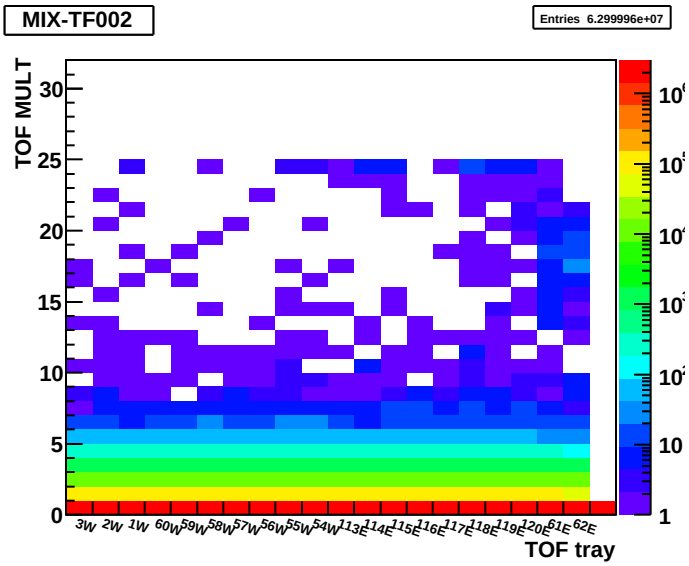

Endcap EMC L0 Input - Patch Sum
Entries 1.349999e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

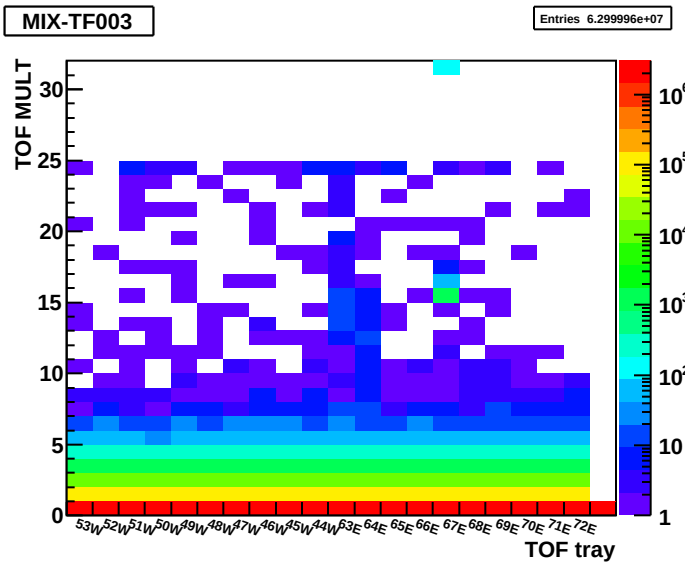

Barrel Jet Patches
Entries 2.699998e+07
JP ADC
JP ID

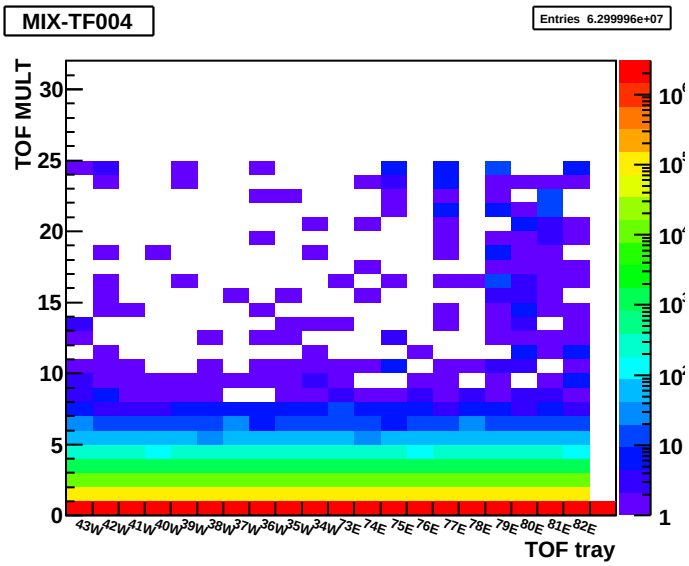

Endcap Jet Patches
Entries 8999994
JP ADC
JP ID

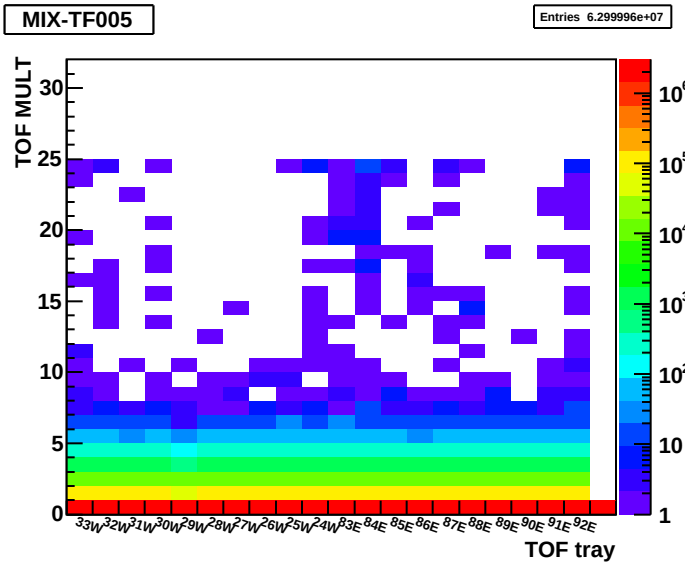

Hybrid Jet Patches
Entries 2999998
JP ADC
JP ID

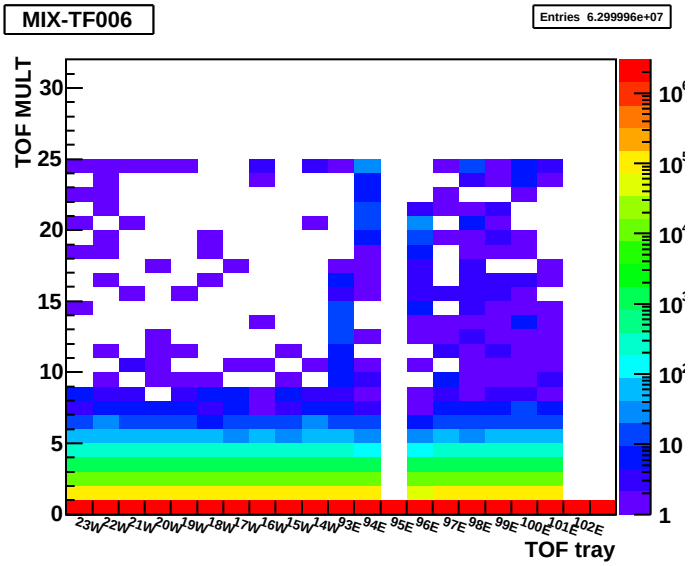

MIX-TF001
Entries 6.299996e+07
TOF MULT
TOF tray

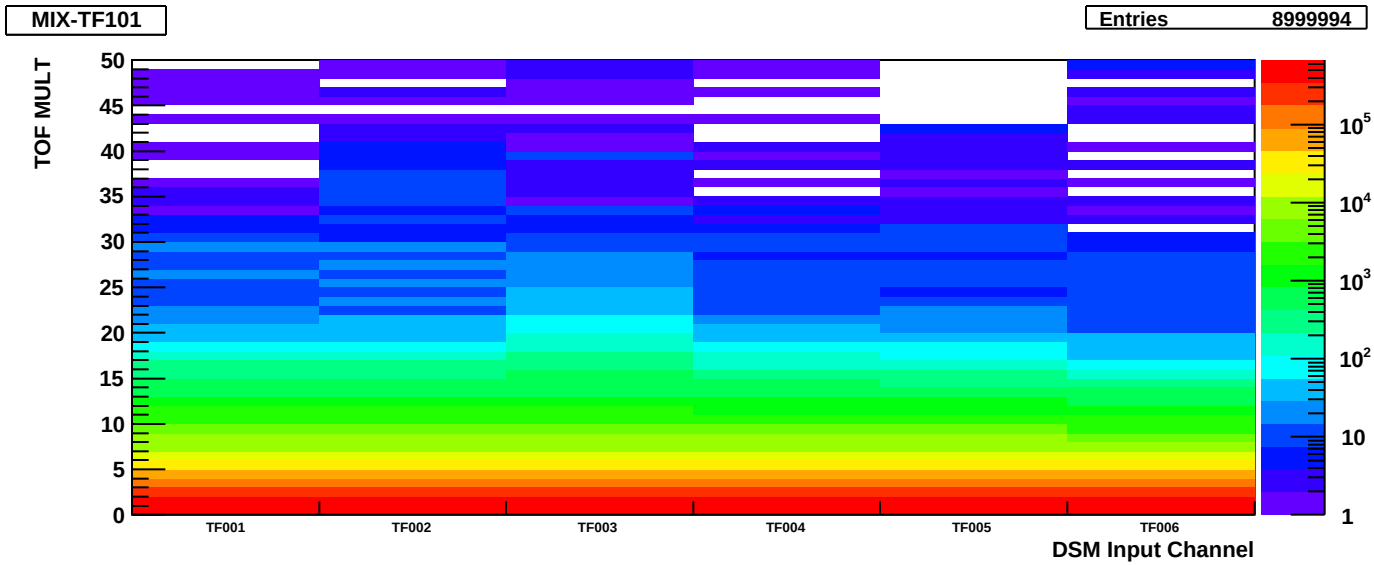

MIX-TF002
Entries 6.299996e+07
TOF MULT
TOF tray

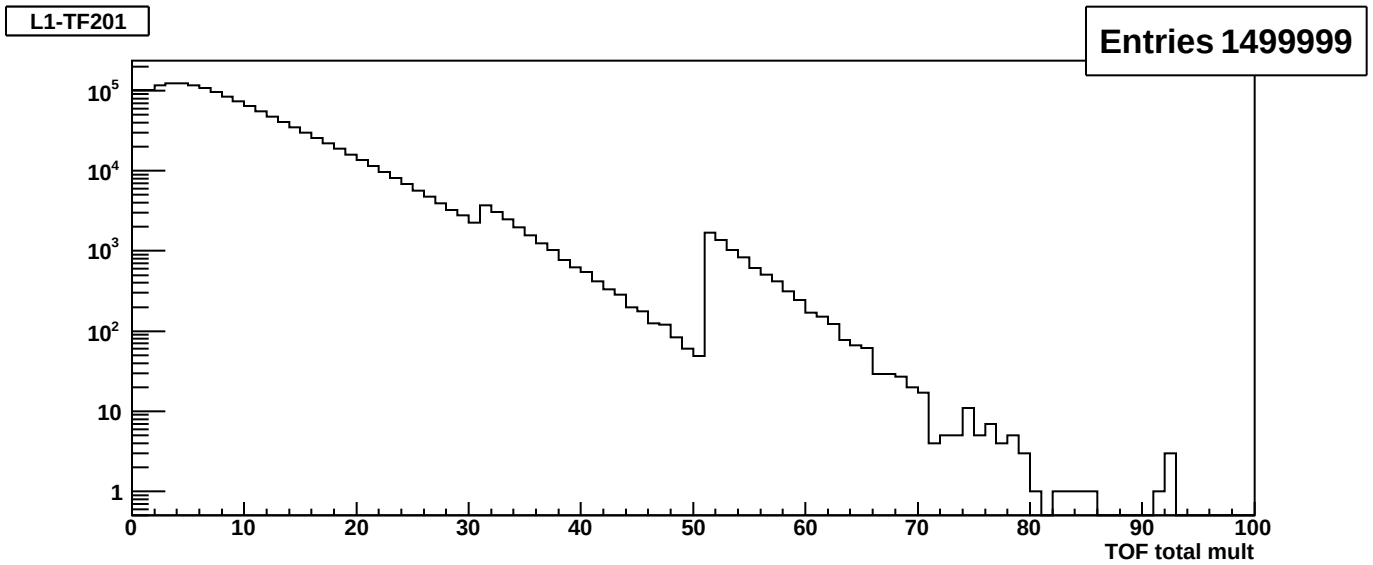

MIX-TF003
Entries 6.299996e+07
TOF MULT
TOF tray

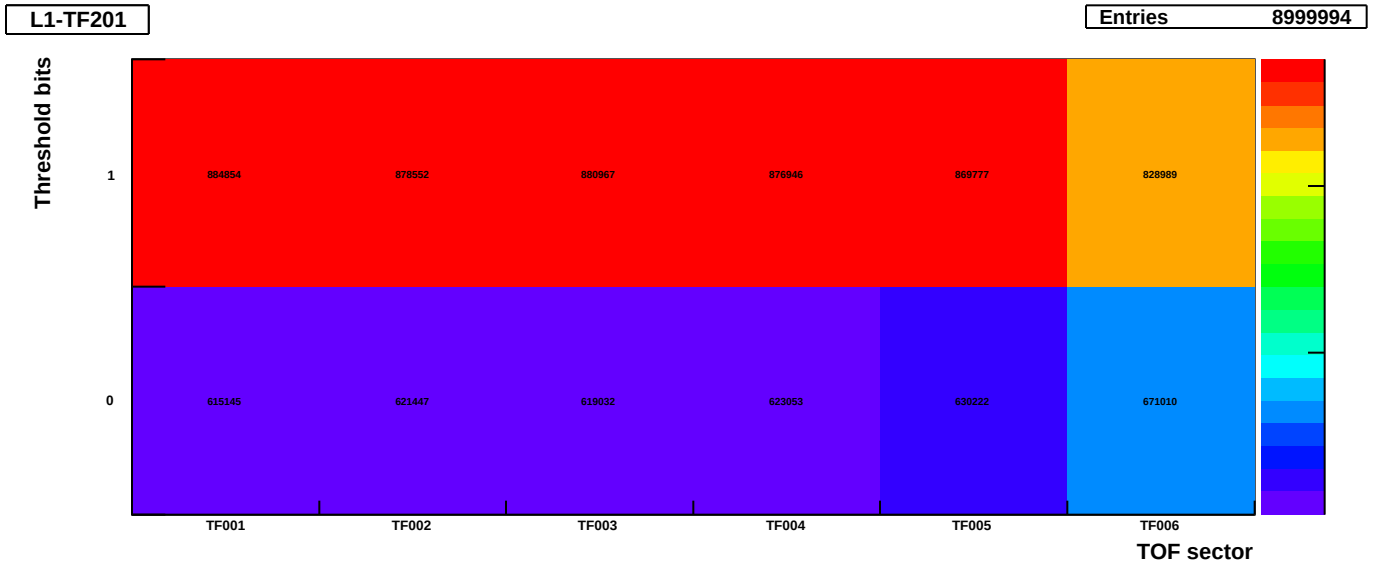

MIX-TF004
Entries 6.299996e+07
TOF MULT
TOF tray

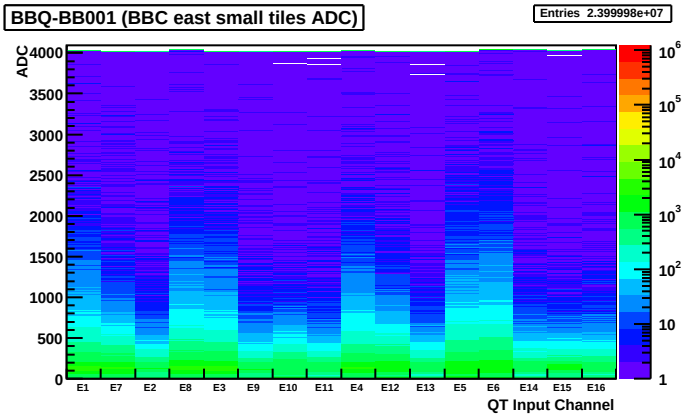

MIX-TF005
Entries 6.299996e+07
TOF MULT
TOF tray

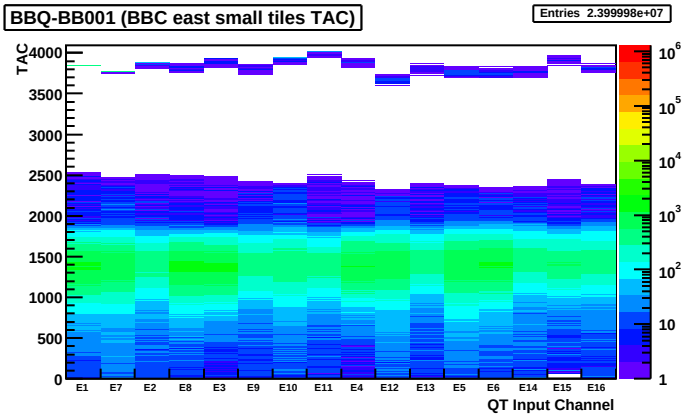

MIX-TF006
Entries 6.299996e+07
TOF MULT
TOF tray

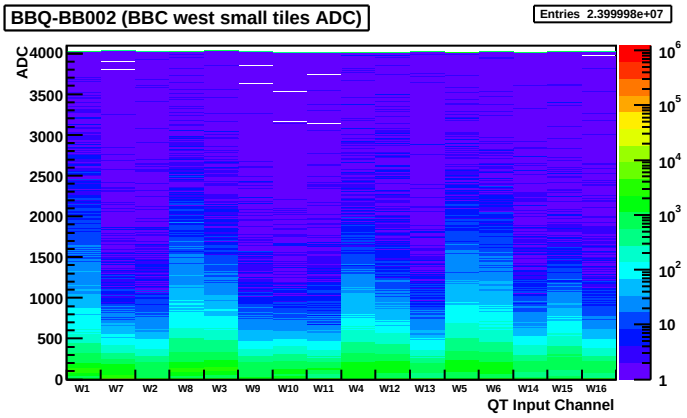

MIX-TF101
Entries 8999994
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

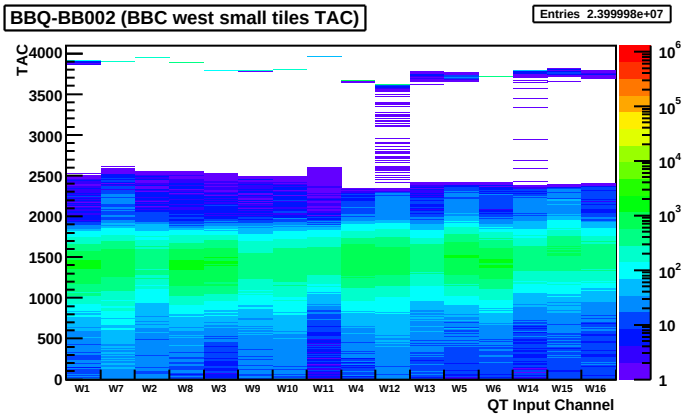

L1-TF201
Entries 1499999
TOF total mult

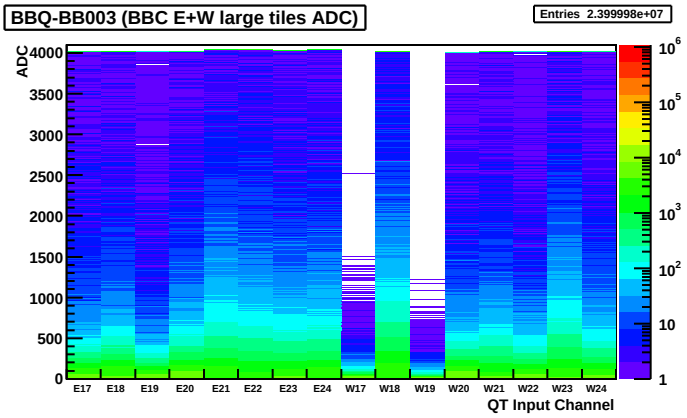

L1-TF201
Entries 8999994
Threshold bits
884854
878552
880967
876946
869777
828989
615145
621447
619032
623053
630222
671010
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

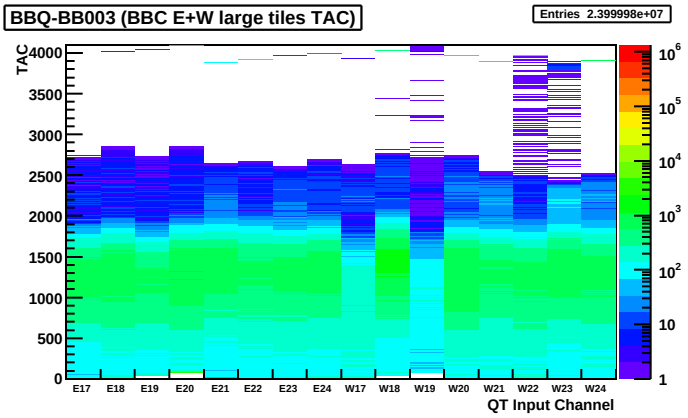

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.399998e+07
ADC
QT Input Channel

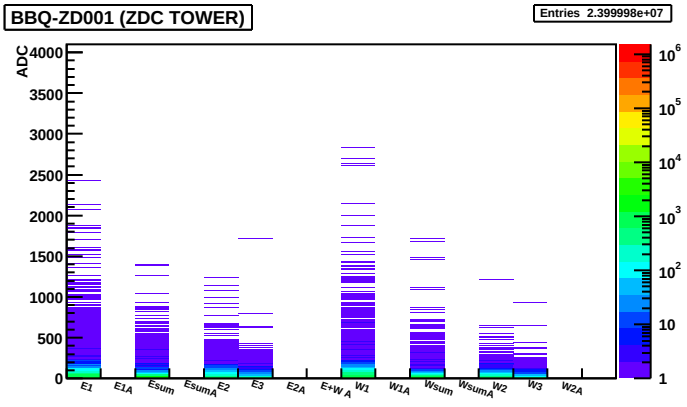

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.399998e+07
TAC
QT Input Channel

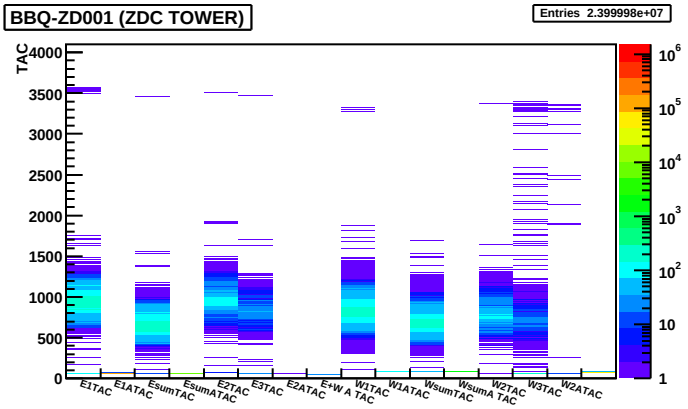

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.399998e+07
ADC
QT Input Channel

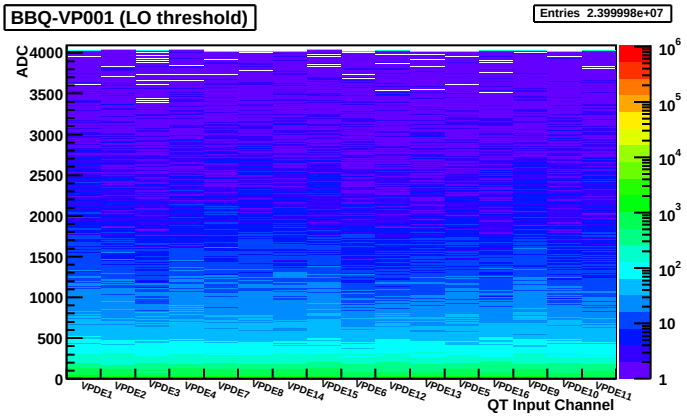

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.399998e+07
TAC
QT Input Channel

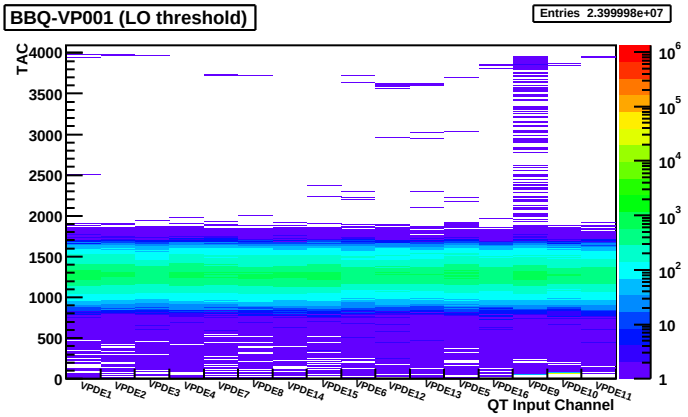

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.399998e+07
ADC
QT Input Channel

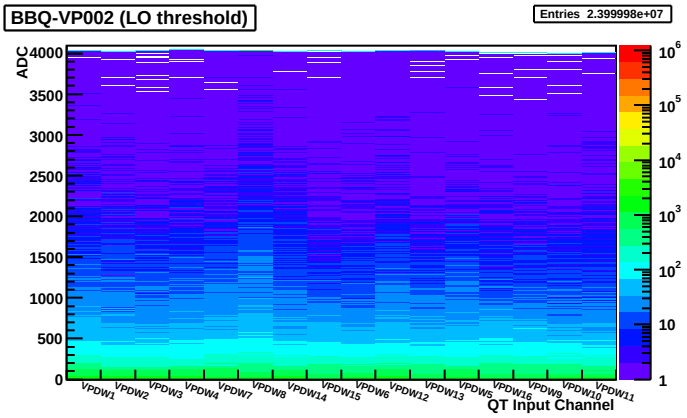

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.399998e+07
TAC
QT Input Channel

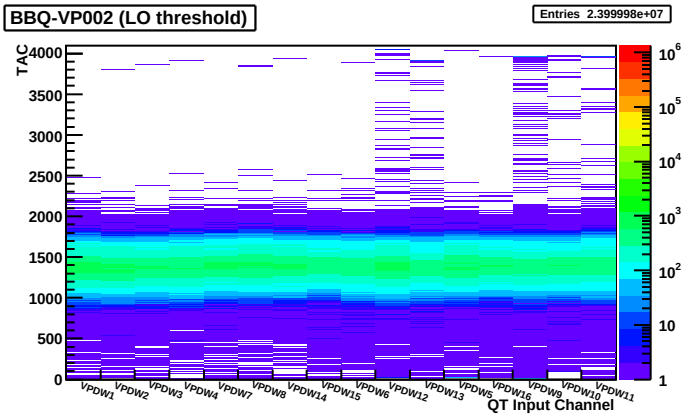

BBQ-ZD001 (ZDC TOWER)
Entries 2.399998e+07
ADC

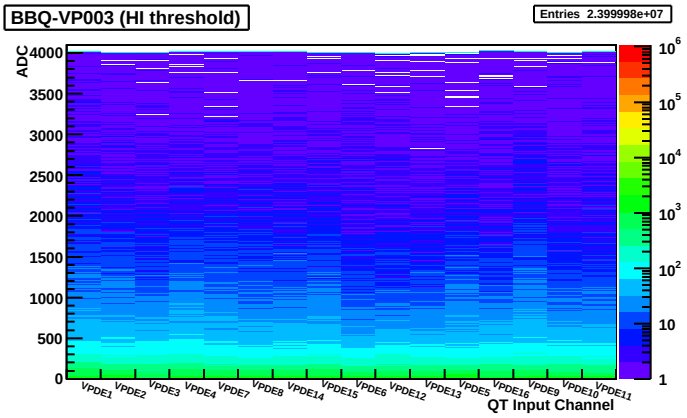

BBQ-ZD001 (ZDC TOWER)
Entries 2.399998e+07
TAC

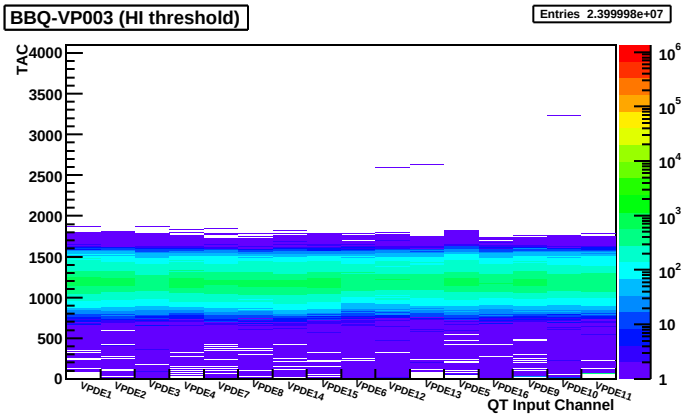

BBQ-VP001 (LO threshold)
Entries 2.399998e+07
ADC
QT Input Channel

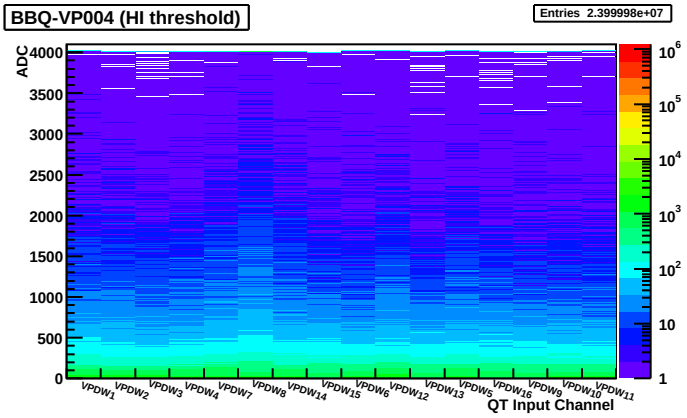

BBQ-VP001 (LO threshold)
Entries 2.399998e+07
TAC
QT Input Channel

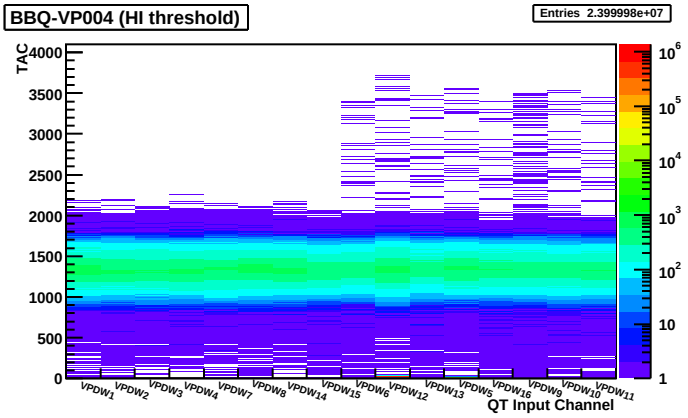

BBQ-VP002 (LO threshold)
Entries 2.399998e+07
ADC
QT Input Channel

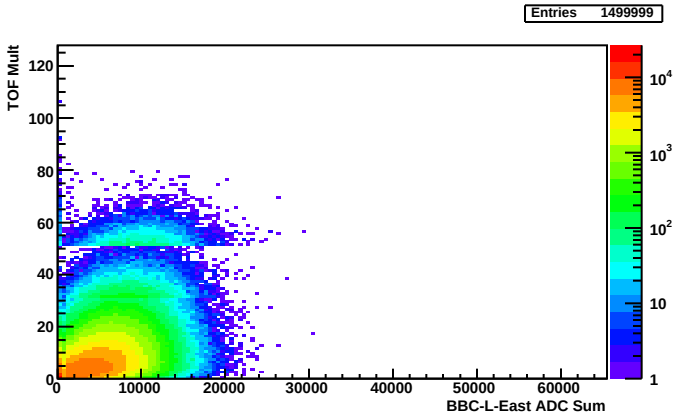

BBQ-VP002 (LO threshold)
Entries 2.399998e+07
TAC
QT Input Channel

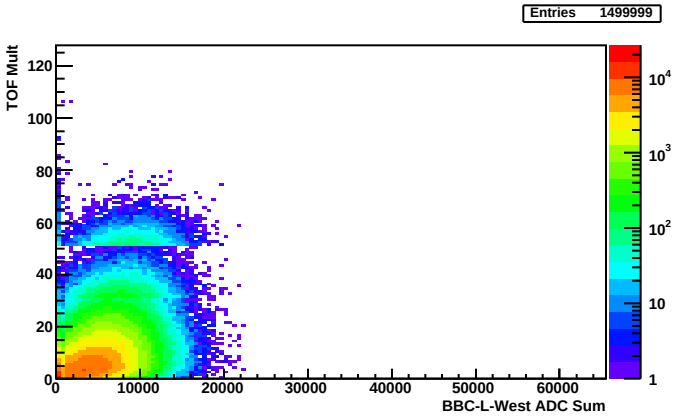

BBQ-VP003 (HI threshold)
Entries 2.399998e+07
ADC
QT Input Channel

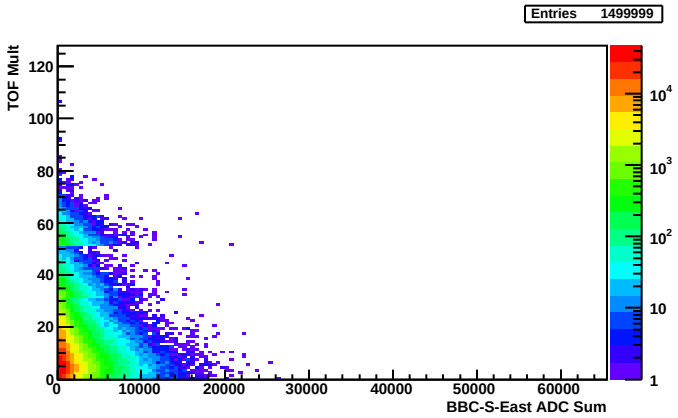

BBQ-VP003 (HI threshold)
Entries 2.399998e+07
TAC
QT Input Channel

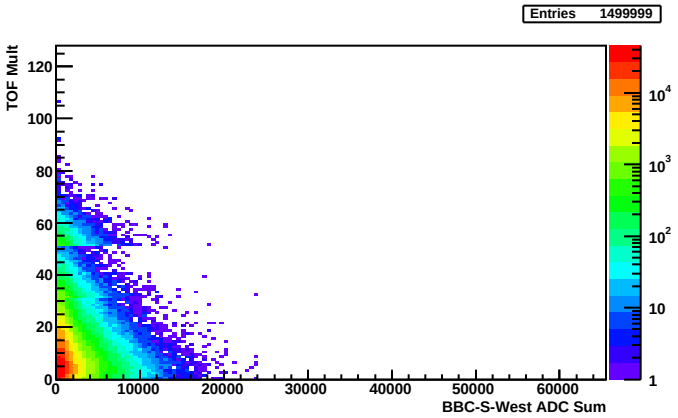

BBQ-VP004 (HI threshold)
Entries 2.399998e+07
ADC
QT Input Channel

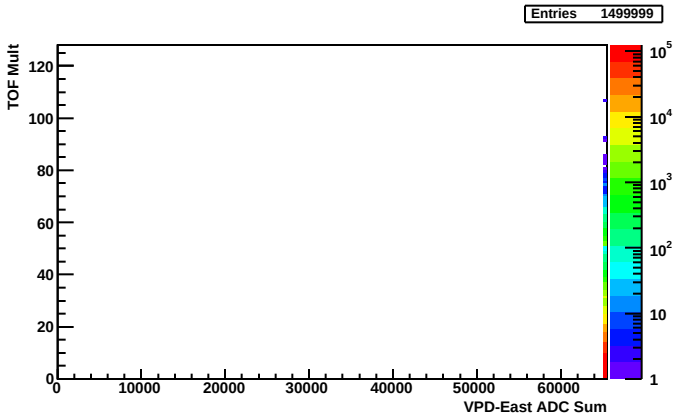

BBQ-VP004 (HI threshold)
Entries 2.399998e+07
TAC
QT Input Channel

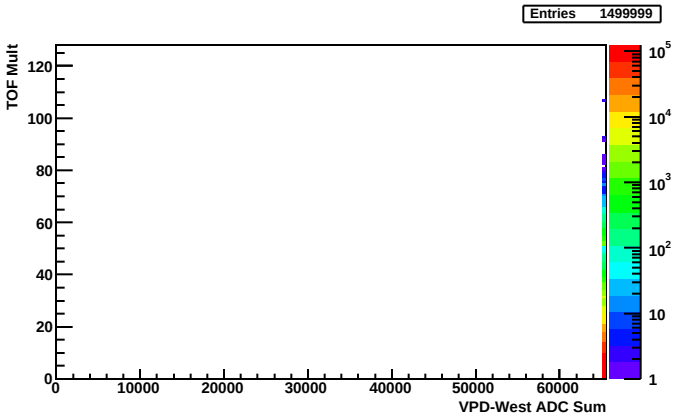

Entries 1499999
TOF Mult
BBC-L-East ADC Sum

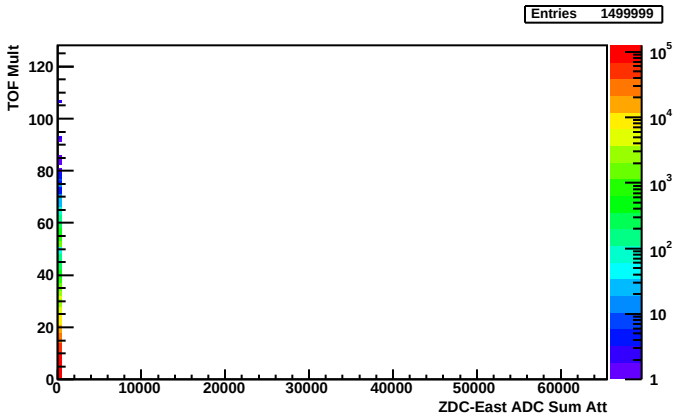

Entries 1499999
TOF Mult
BBC-L-West ADC Sum

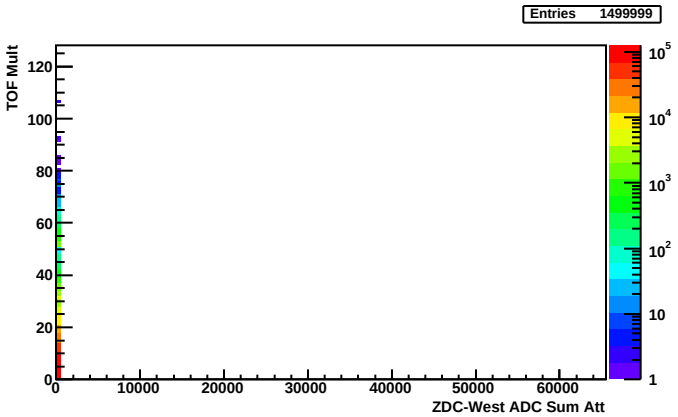

Entries 1499999
TOF Mult
BBC-S-East ADC Sum

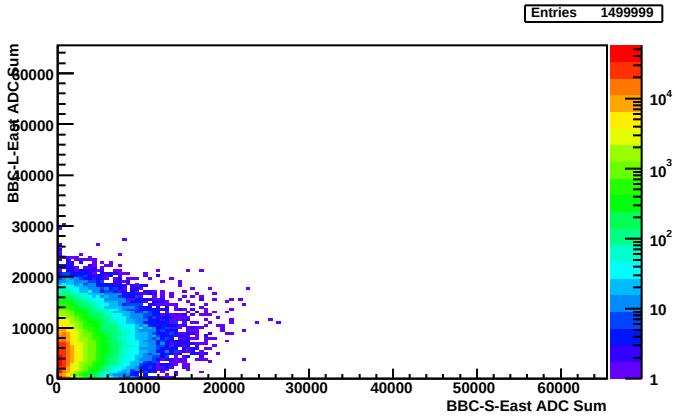

Entries 1499999
TOF Mult
BBC-S-West ADC Sum

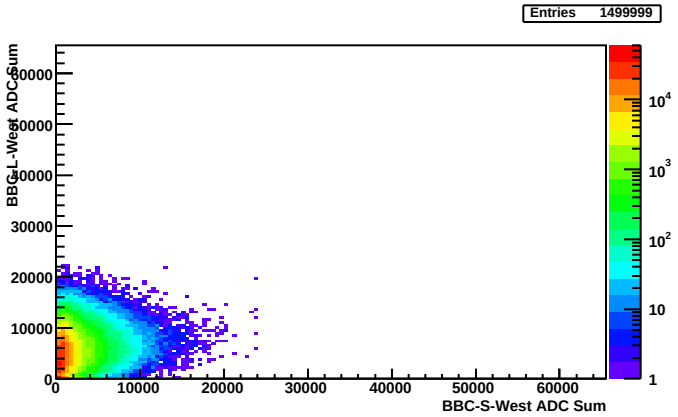

Entries 1499999
TOF Mult
VPD-East ADC Sum

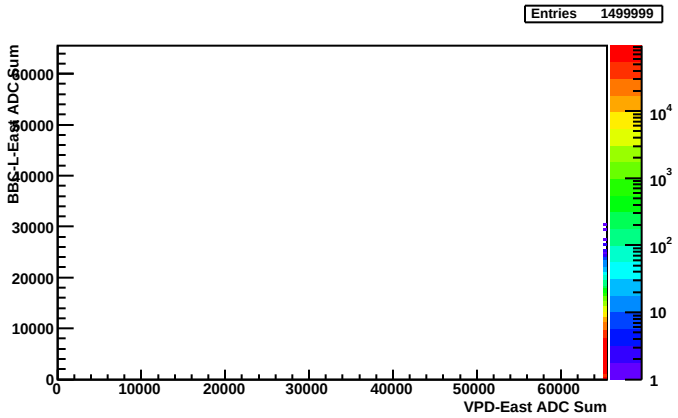

Entries 1499999
TOF Mult
VPD-West ADC Sum

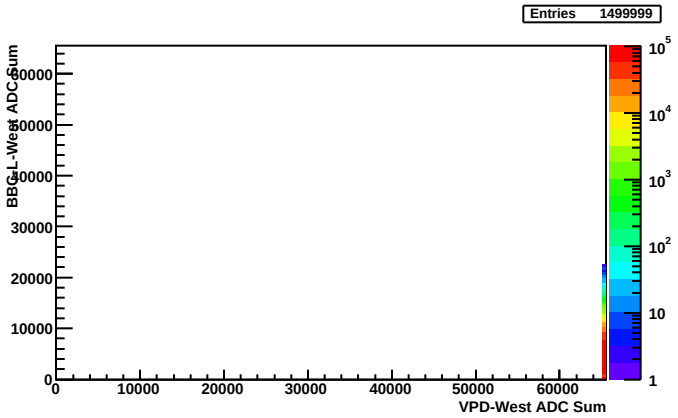

Entries 1499999
TOF Mult
ZDC-East ADC Sum Att

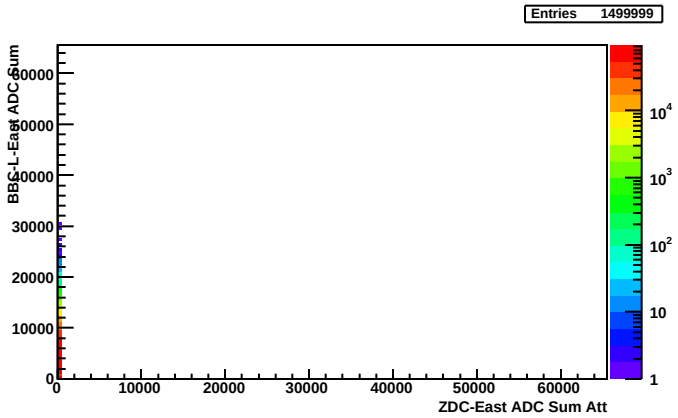

Entries 1499999
TOF Mult
ZDC-West ADC Sum Att

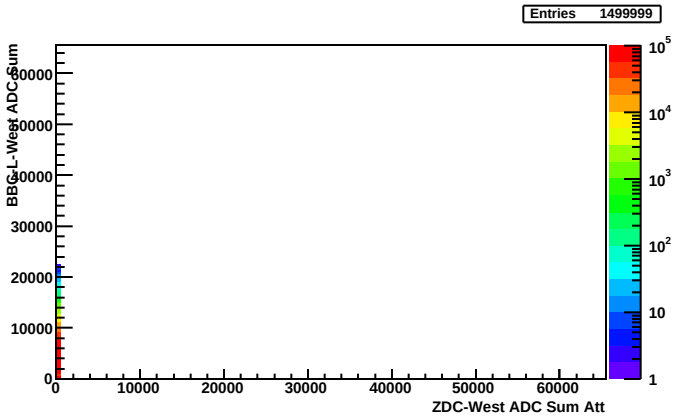

Entries 1499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

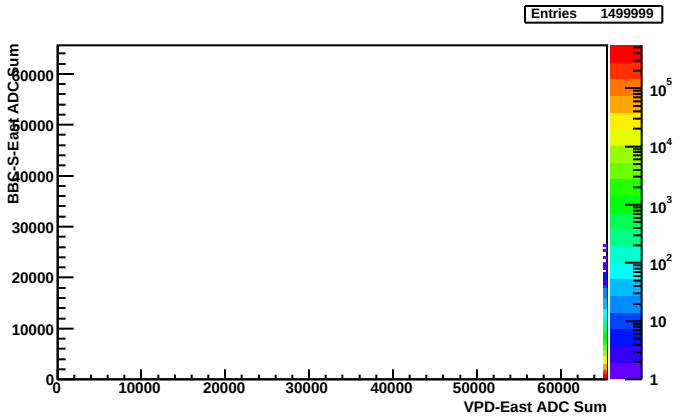

Entries 1499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

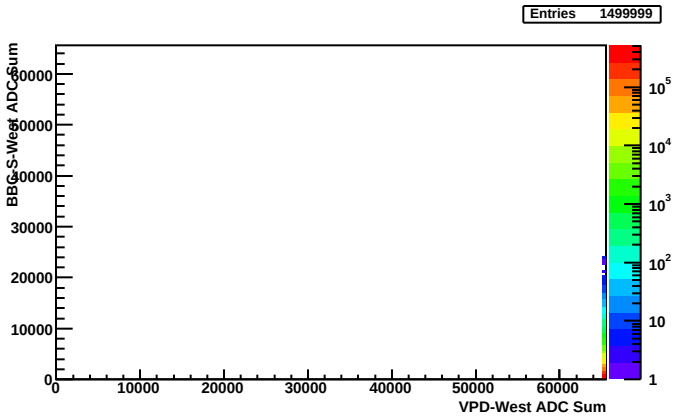

Entries 1499999
BBC-L-East ADC Sum
VPD-East ADC Sum

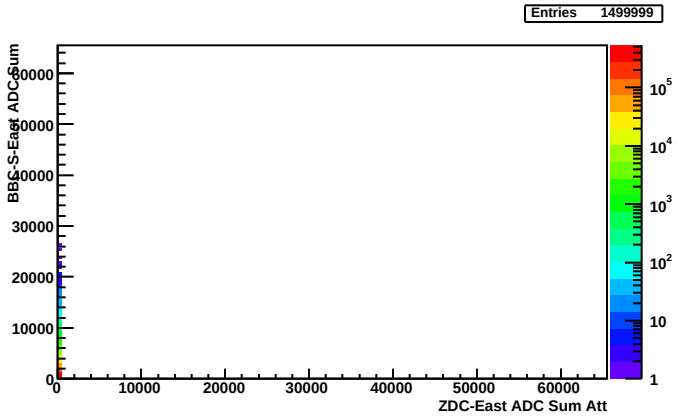

Entries 1499999
BBC-L-West ADC Sum
VPD-West ADC Sum

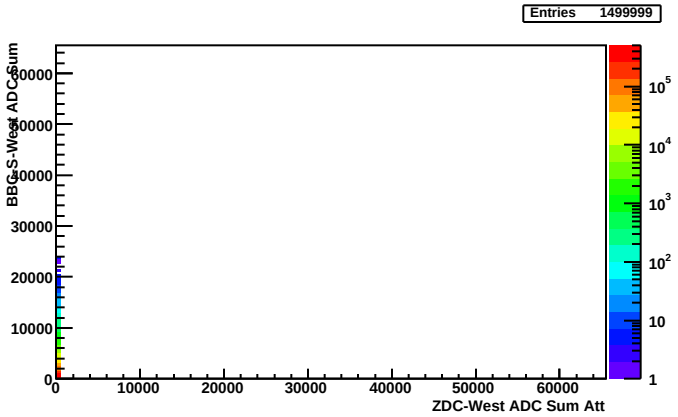

Entries 1499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

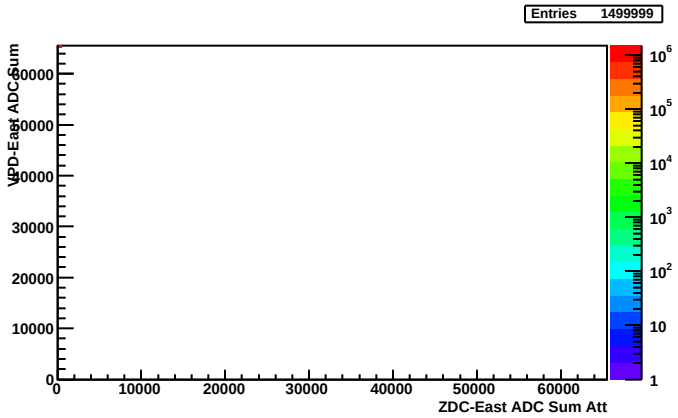

Entries 1499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

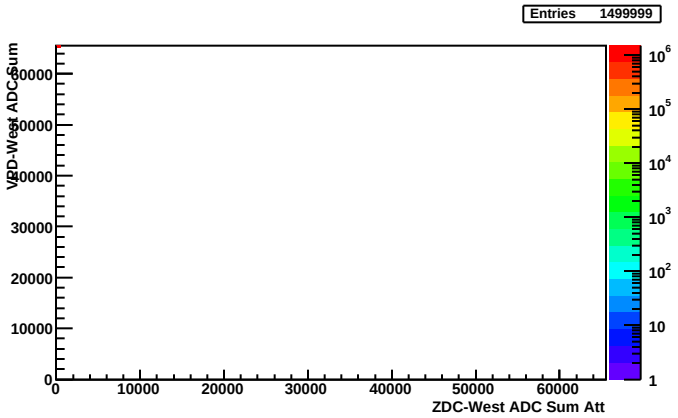

Entries 1499999
BBC-S-East ADC Sum
VPD-East ADC Sum

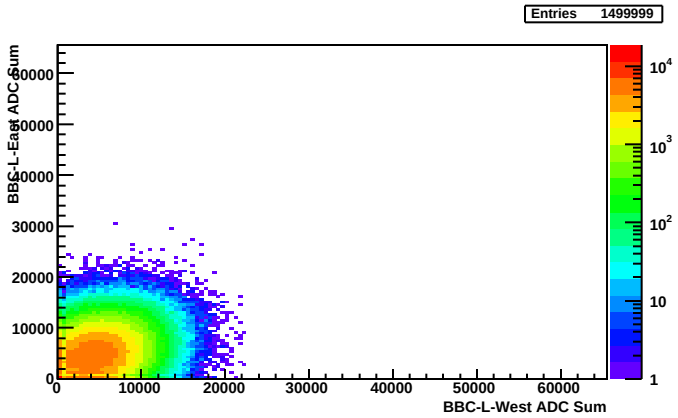

Entries 1499999
BBC-S-West ADC Sum
VPD-West ADC Sum

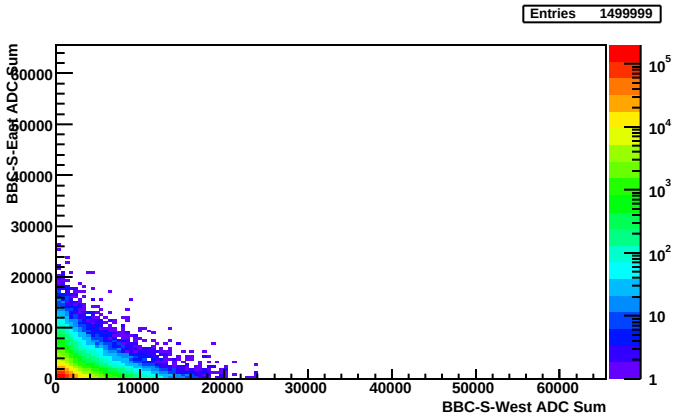

Entries 1499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

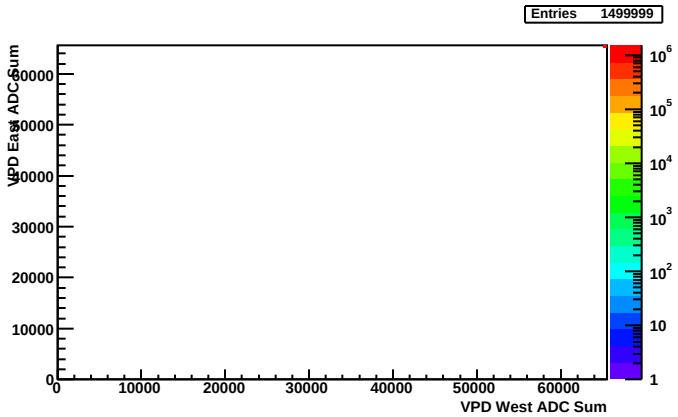

Entries 1499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

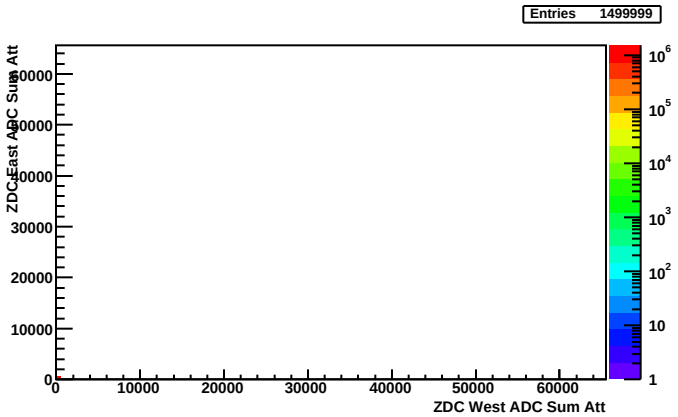

Entries 1499999
VPD-East ADC Sum
ZDC-East ADC Sum Att

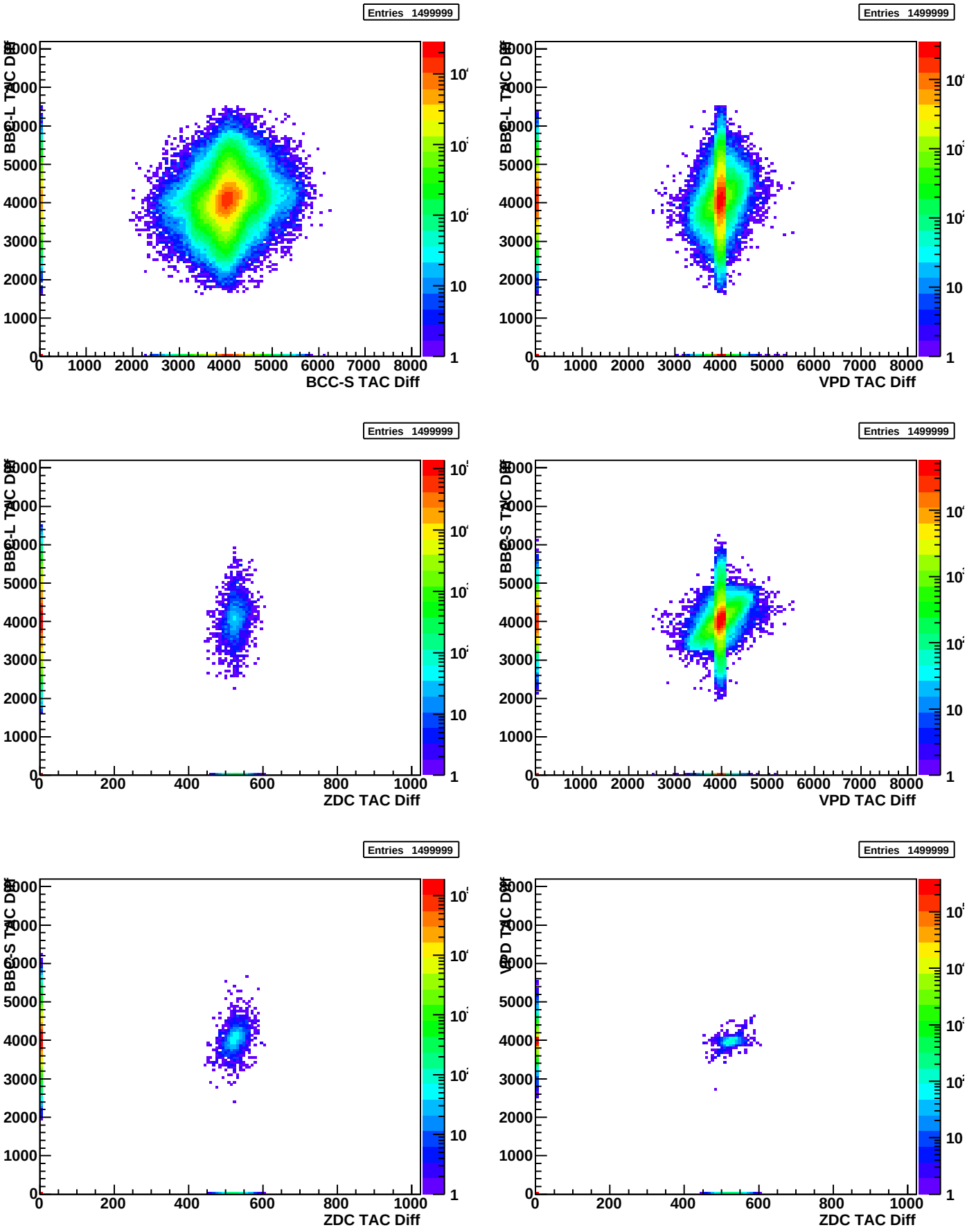

Entries 1499999
VPD-West ADC Sum
ZDC-West ADC Sum Att

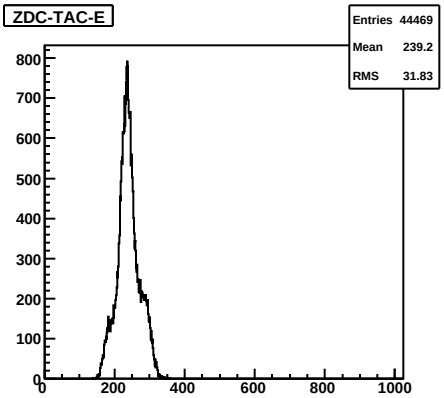

Entries 1499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

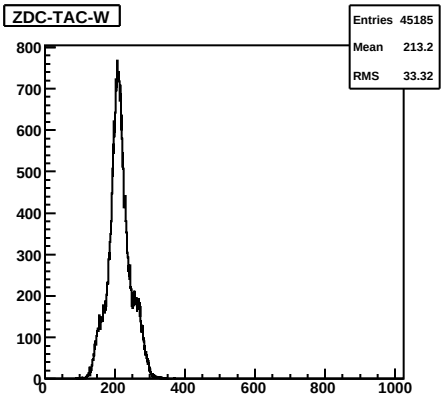

Entries 1499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

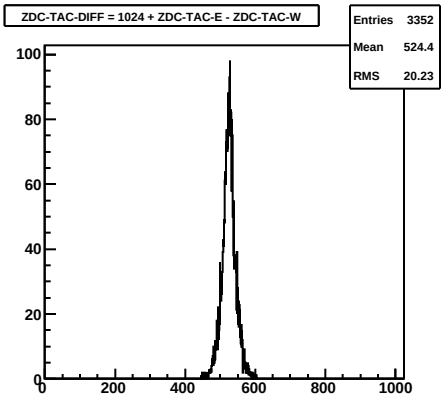

Entries 1499999
VPD East ADC Sum
VPD West ADC Sum

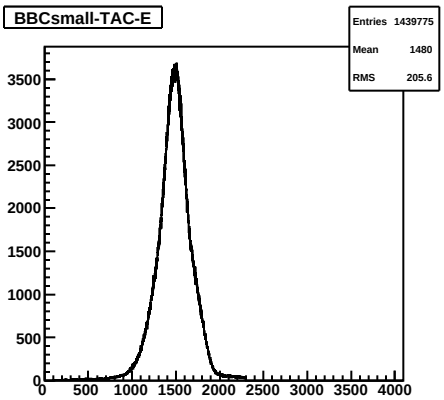

Entries 1499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

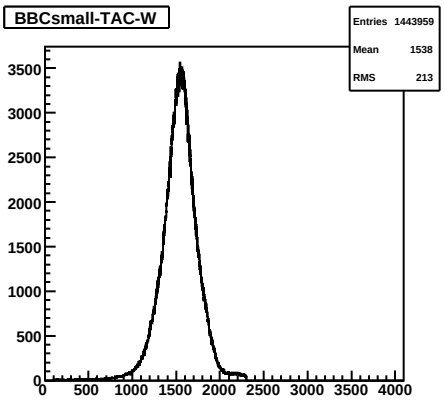

Entries 1499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1499999
BBC-L TAC Diff
VPD TAC Diff
Entries 1499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 1499999
BBC-S TAC Diff
VPD TAC Diff
Entries 1499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 1499999
VPD TAC Diff
ZDC TAC Diff

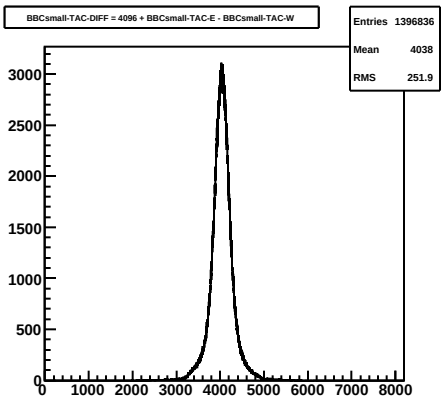

ZDC-TAC-E
Entries 44469
Mean 239.2
RMS 31.83

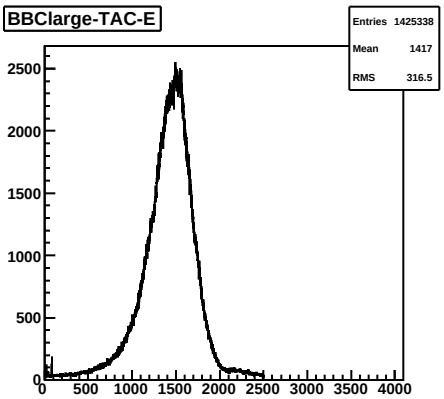

ZDC-TAC-W
Entries 45185
Mean 213.2
RMS 33.32

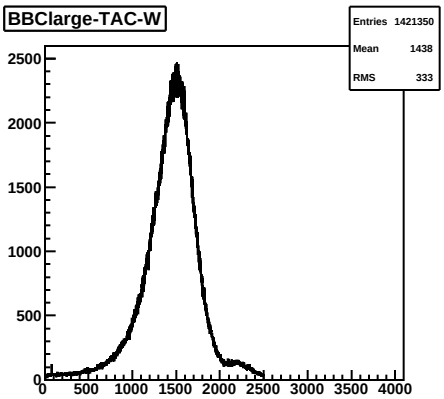

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3352
Mean 524.4
RMS 20.23

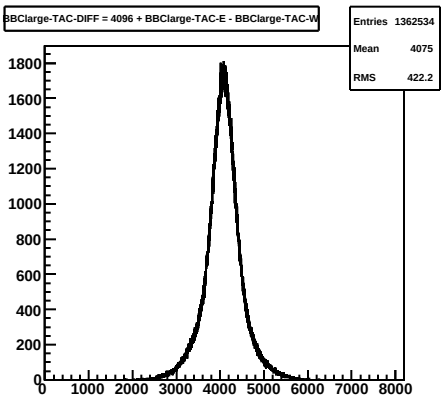

BBCsmall-TAC-E
Entries 1439775
Mean 1480
RMS 205.6

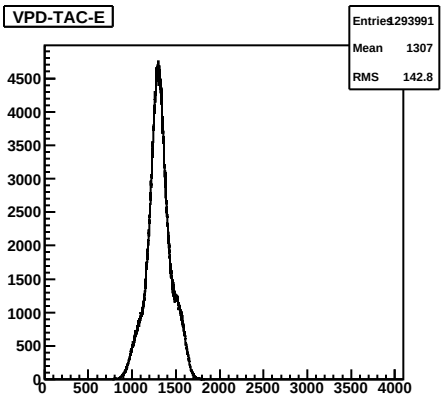

BBCsmall-TAC-W
Entries 1443959
Mean 1538
RMS 213

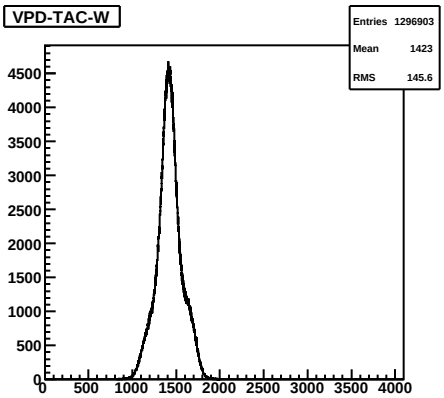

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1396836
Mean 4038
RMS 251.9

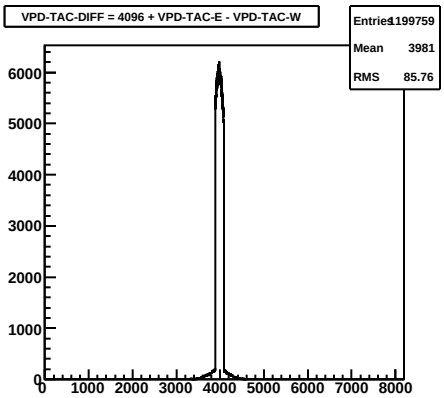

BBClarge-TAC-E
Entries 1425338
Mean 1417
RMS 316.5

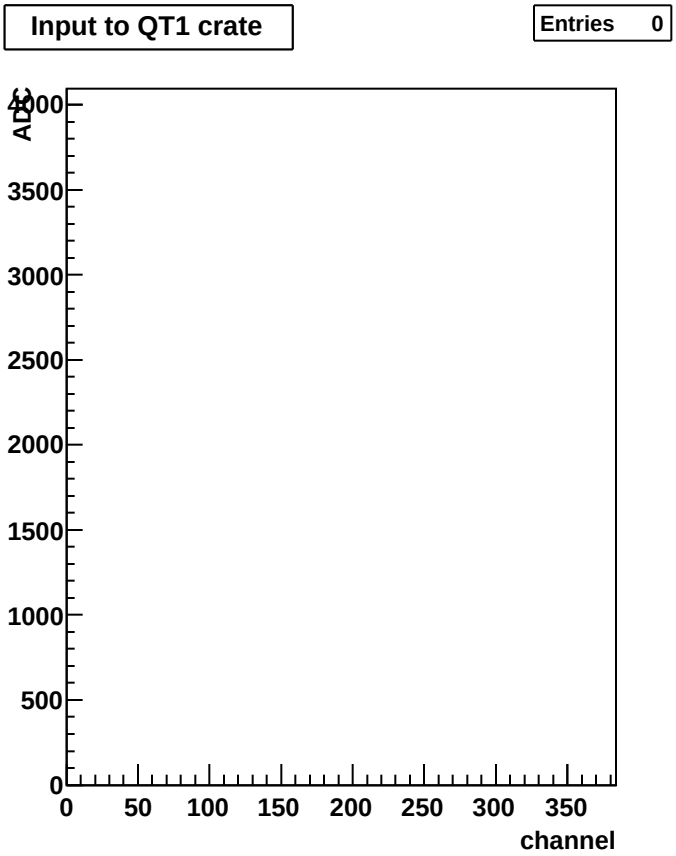

BBClarge-TAC-W
Entries 1421350
Mean 1438
RMS 333

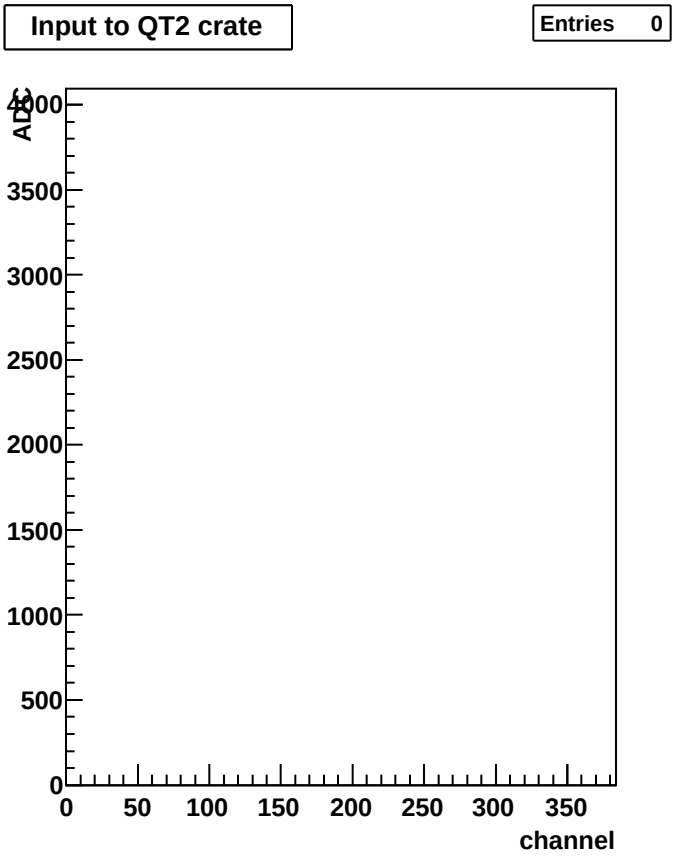

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1362534
Mean 4075
RMS 422.2

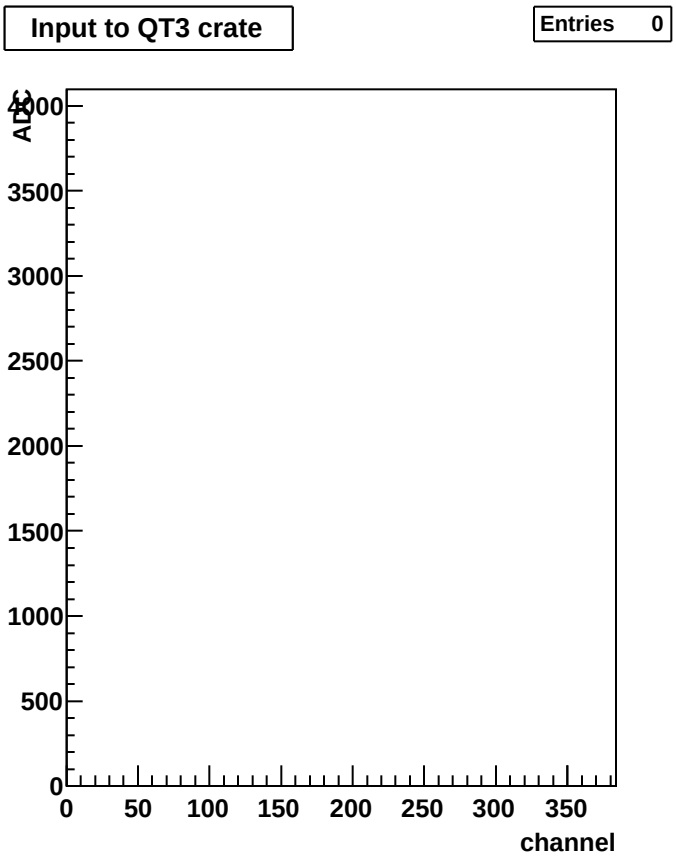

VPD-TAC-E
Entries 1293991
Mean 1307
RMS 142.8

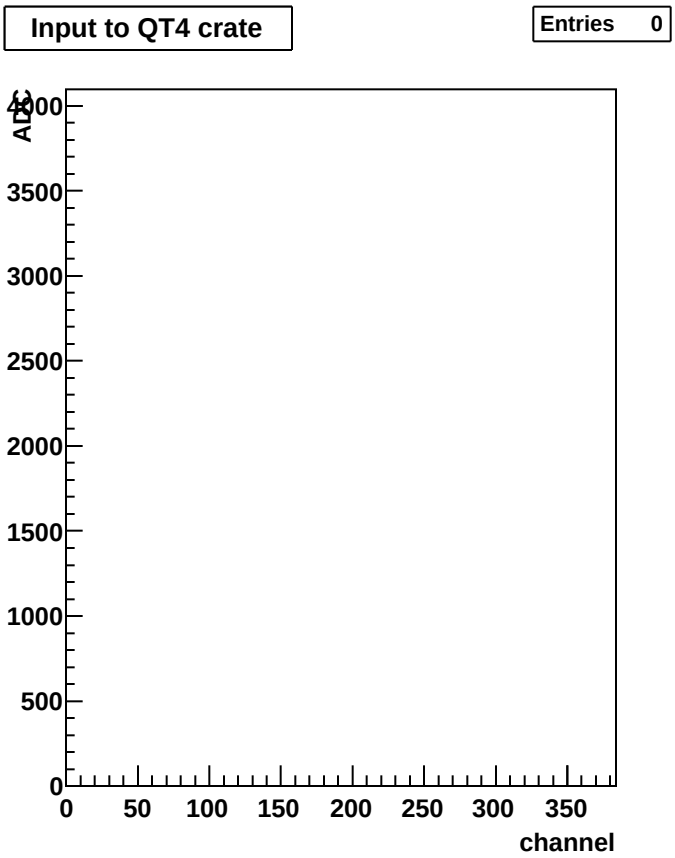

VPD-TAC-W
Entries 1296903
Mean 1423
RMS 145.6

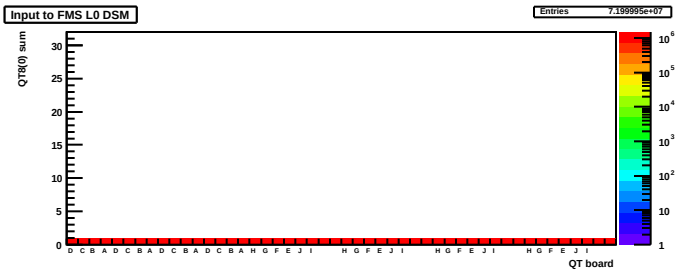

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1199759
Mean 3981
RMS 85.76

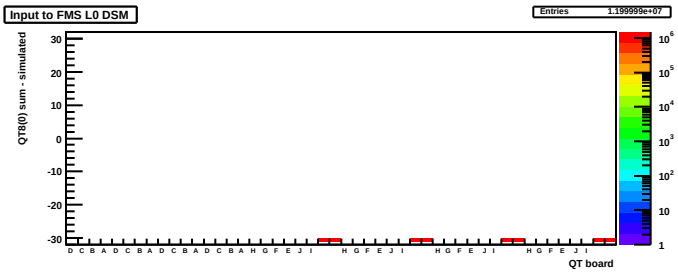

Input to QT1 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

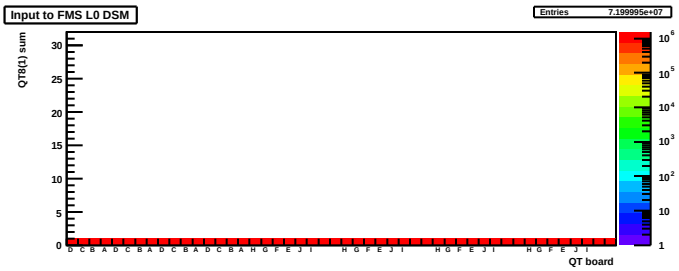

Input to QT2 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

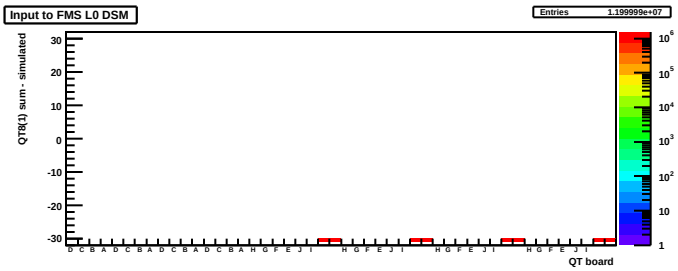

Input to QT3 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

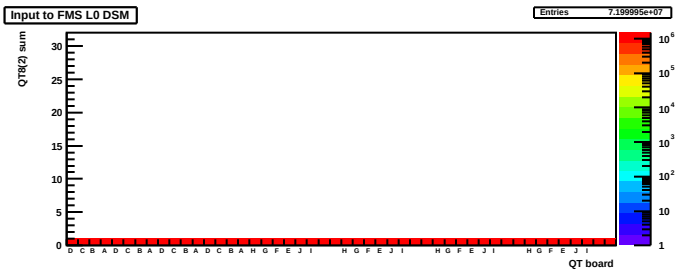

Input to QT4 crate
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

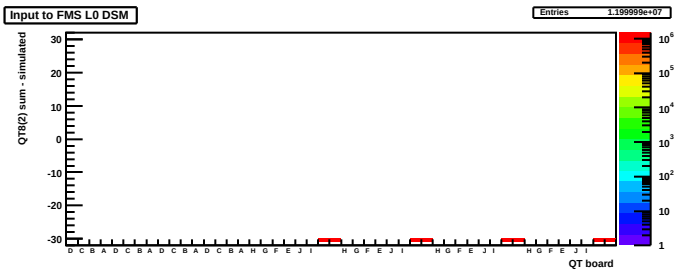

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(0) sum
QT board

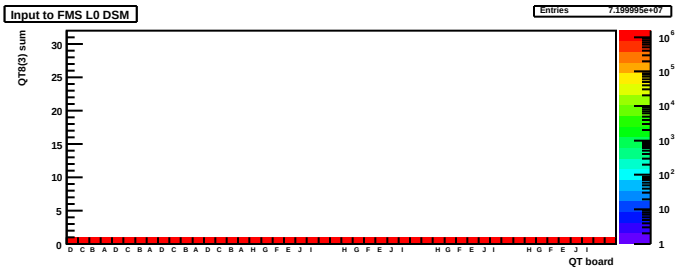

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(0) sum - simulated
QT board

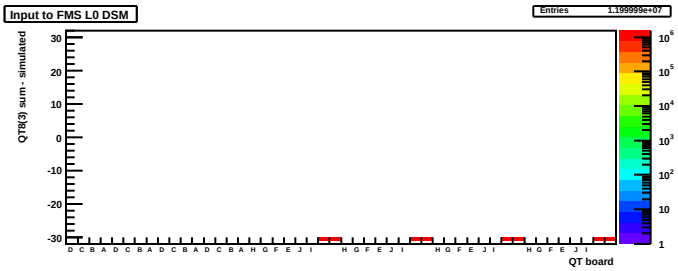

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(1) sum
QT board

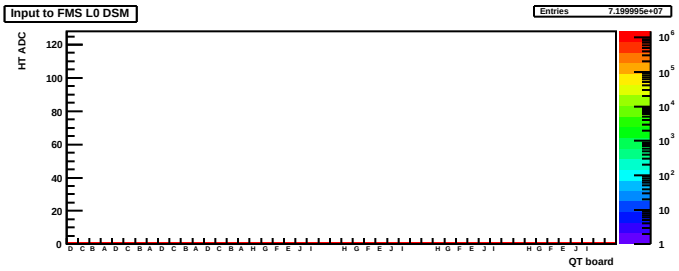

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(1) sum - simulated
QT board

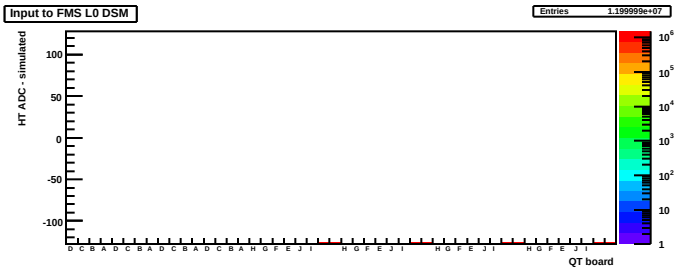

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(2) sum
QT board

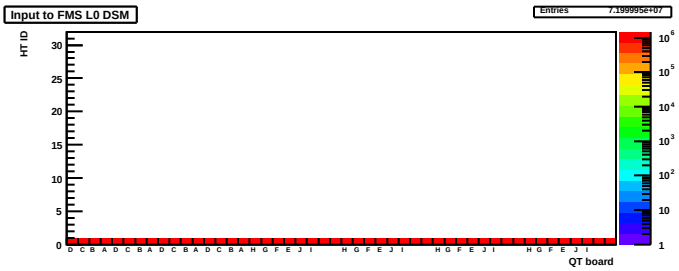

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(2) sum - simulated
QT board

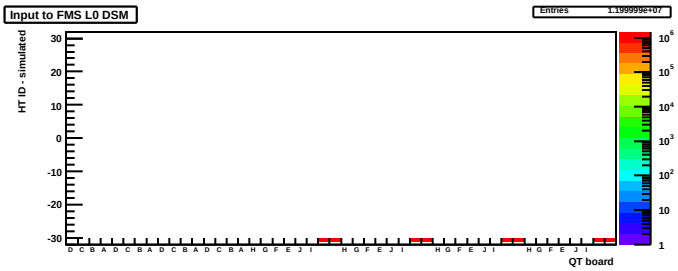

Input to FMS L0 DSM
Entries 7.199995e+07
QT8(3) sum
QT board

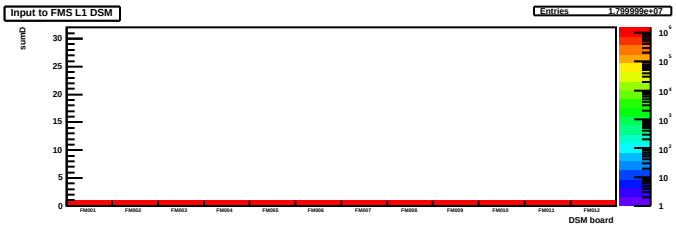

Input to FMS L0 DSM
Entries 1.199999e+07
QT8(3) sum - simulated
QT board

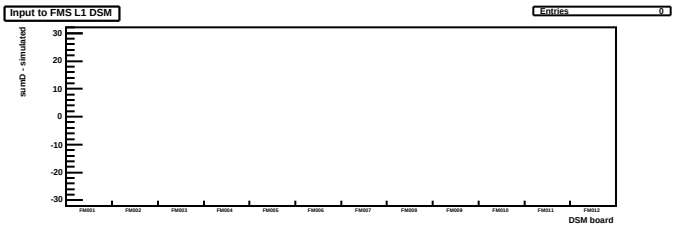

Input to FMS L0 DSM
Entries 7.199995e+07
HT ADC
QT board

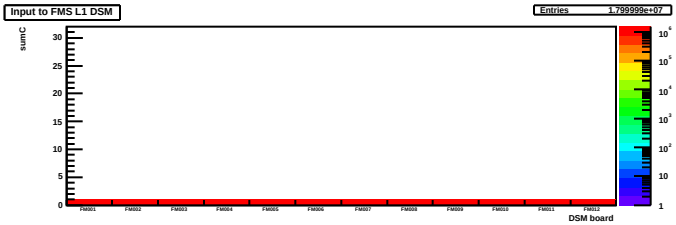

Input to FMS L0 DSM
Entries 1.199999e+07
HT ADC - simulated
QT board

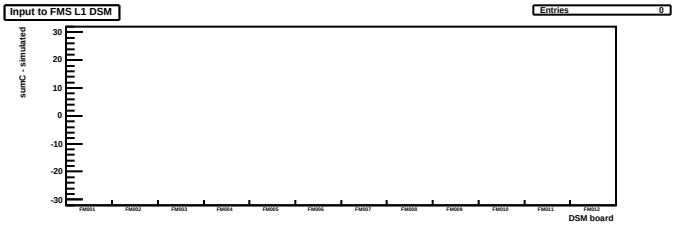

Input to FMS L0 DSM
Entries 7.199995e+07
HT ID
QT board

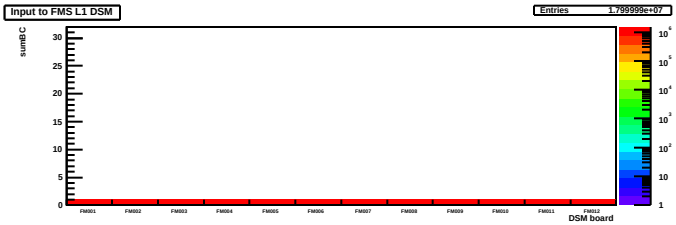

Input to FMS L0 DSM
Entries 1.199999e+07
HT ID - simulated
QT board

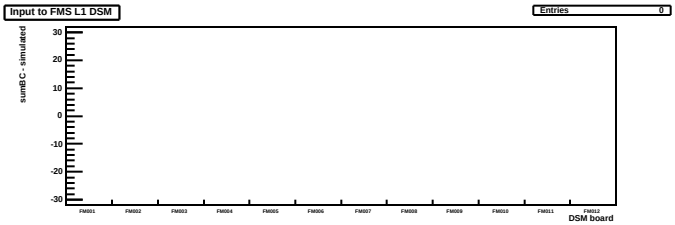

Input to FMS L1 DSM
Entries 1.799999e+07
sumD
DSM board

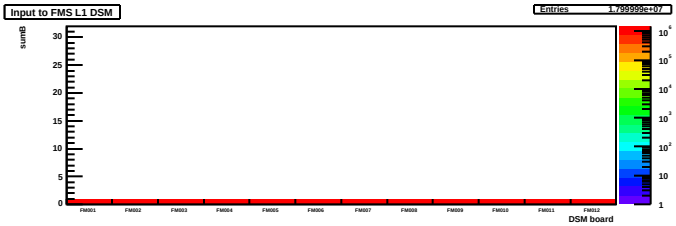

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

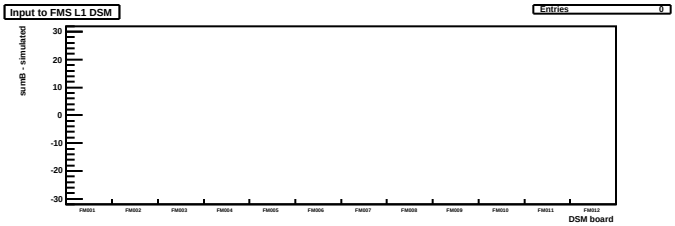

Input to FMS L1 DSM
Entries 1.799999e+07
sumC
DSM board

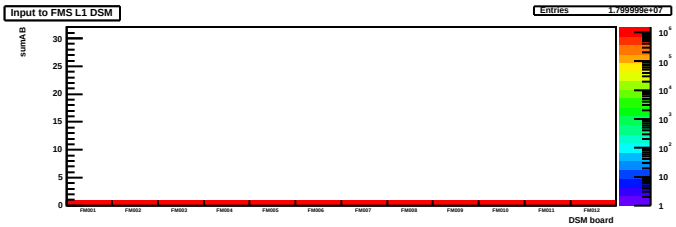

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

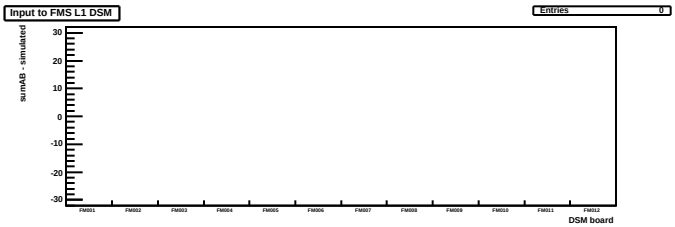

Input to FMS L1 DSM
Entries 1.799999e+07
sumBC
DSM board

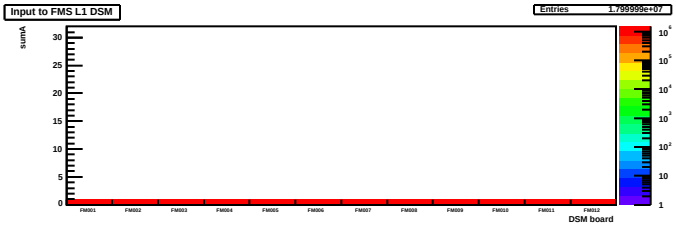

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

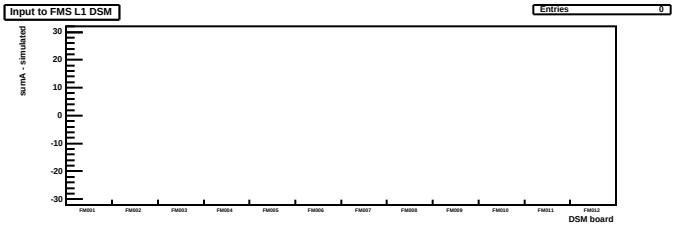

Input to FMS L1 DSM
Entries 1.799999e+07
sumB
DSM board

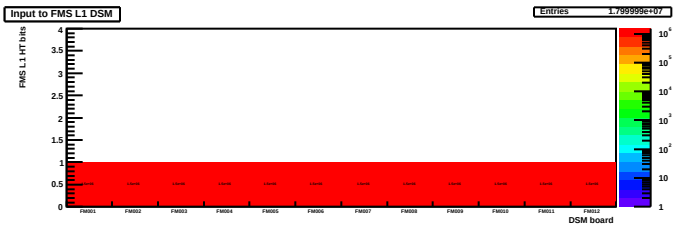

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

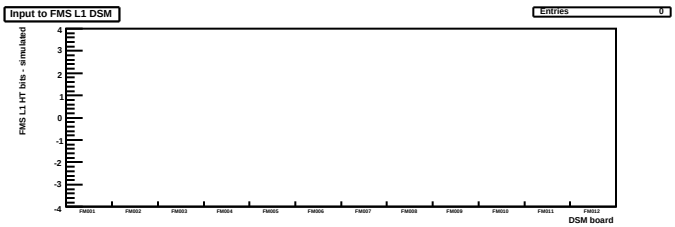

Input to FMS L1 DSM
Entries 1.799999e+07
sumAB
DSM board

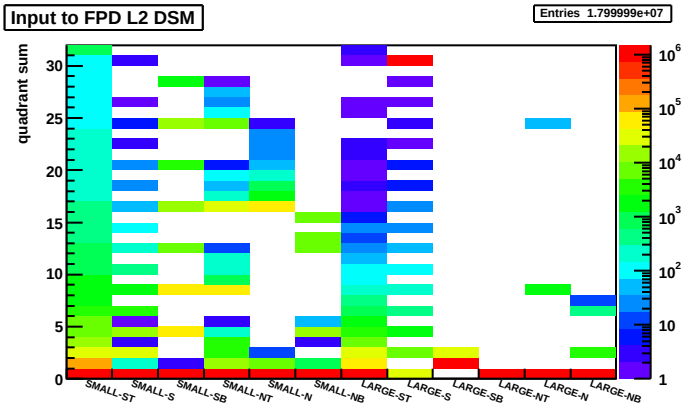

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

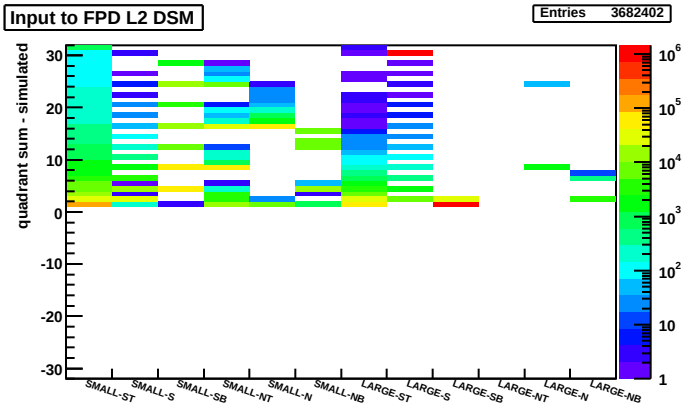

Input to FMS L1 DSM
Entries 1.799999e+07
sumA
DSM board

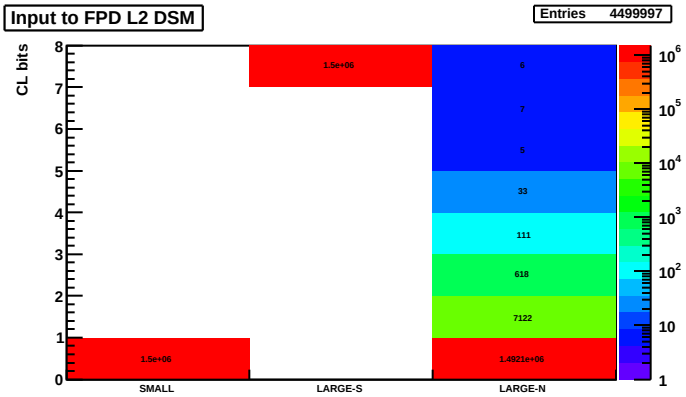

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

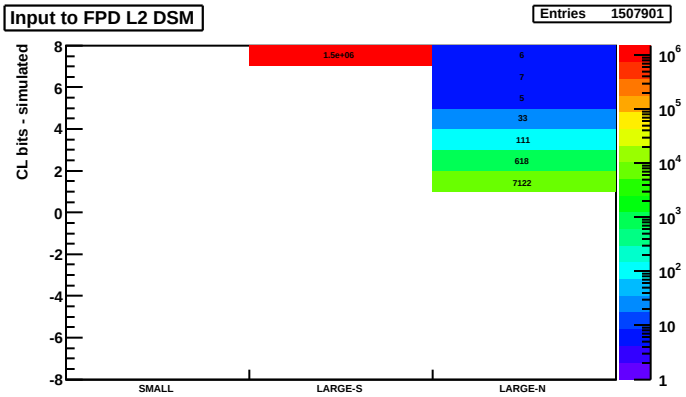

Input to FMS L1 DSM
Entries 1.799999e+07
FMS L1 HT bits
DSM board

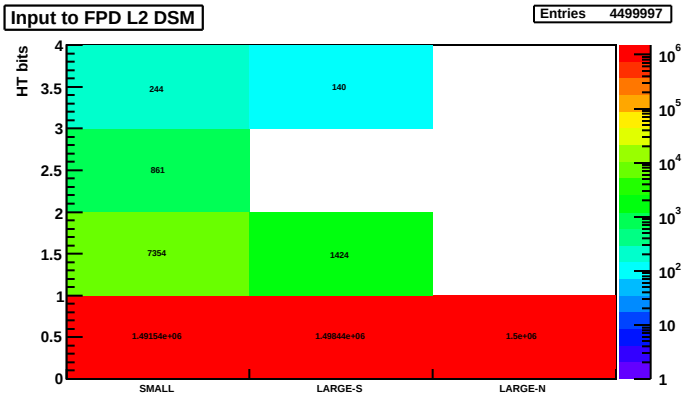

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

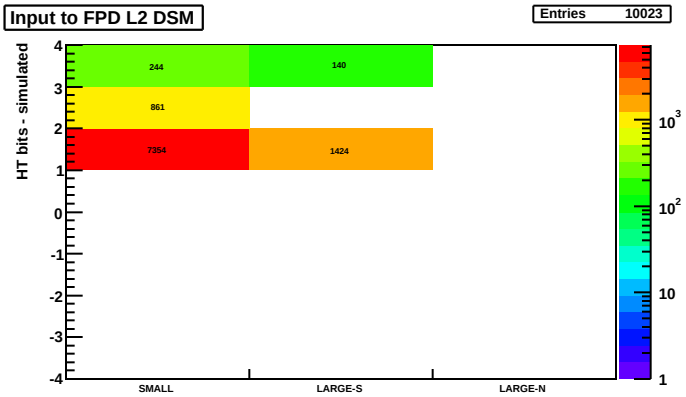

Input to FPD L2 DSM
Entries 1.799999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

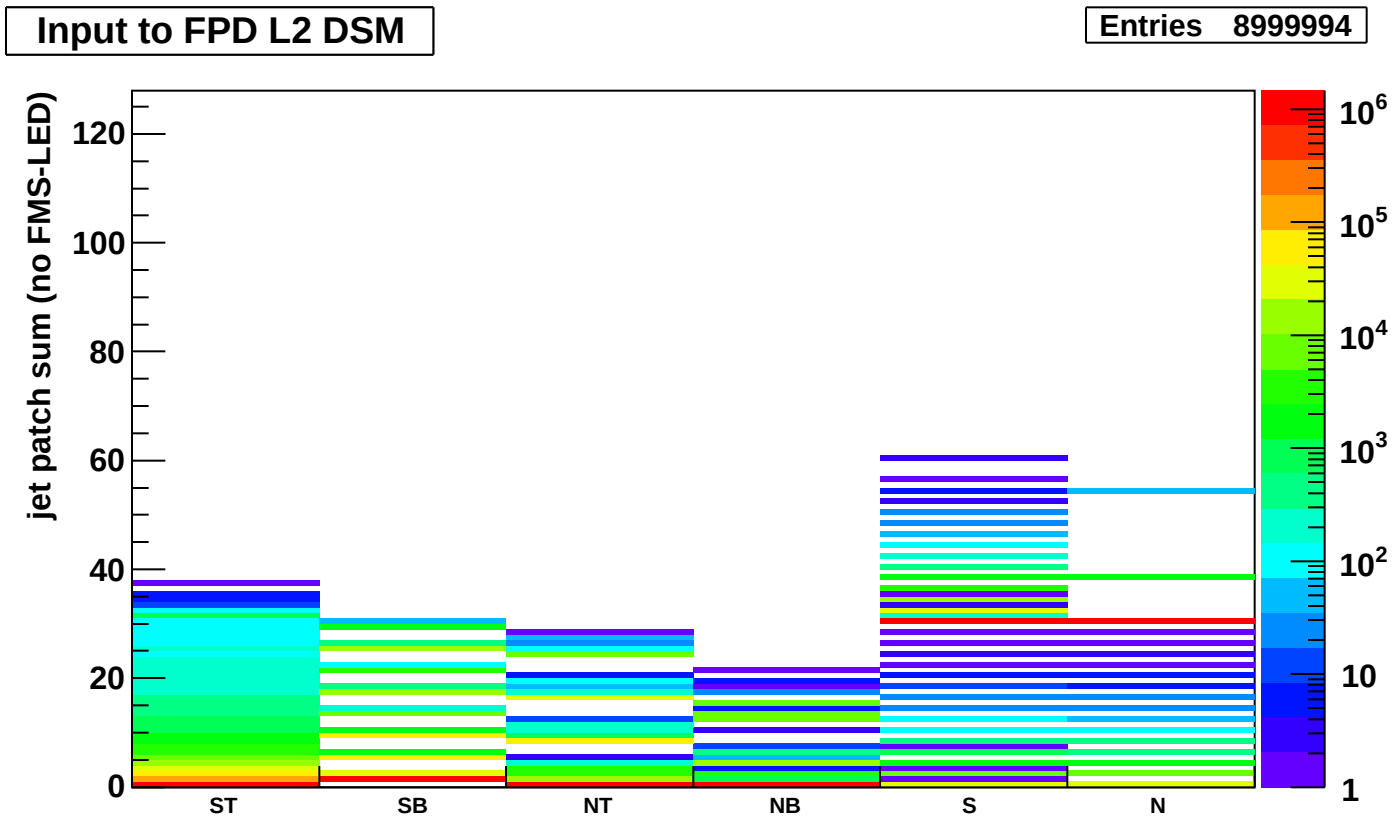

Input to FPD L2 DSM
Entries 3682402
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

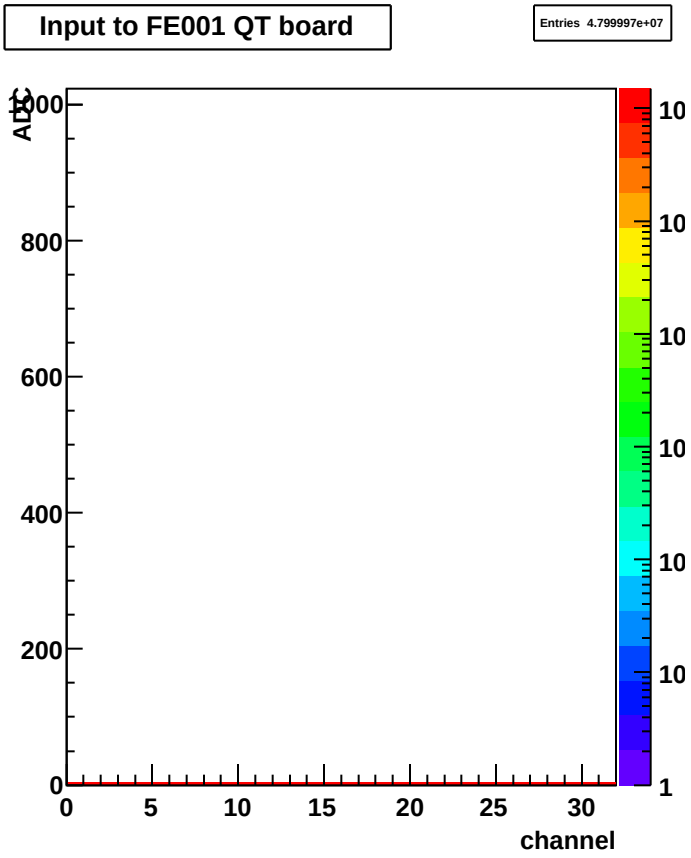

Input to FPD L2 DSM
Entries 4499997
CL bits
1.5e+06
6
7
5
33
111
618
7122
1.4921e+06
SMALL
LARGE-S
LARGE-N

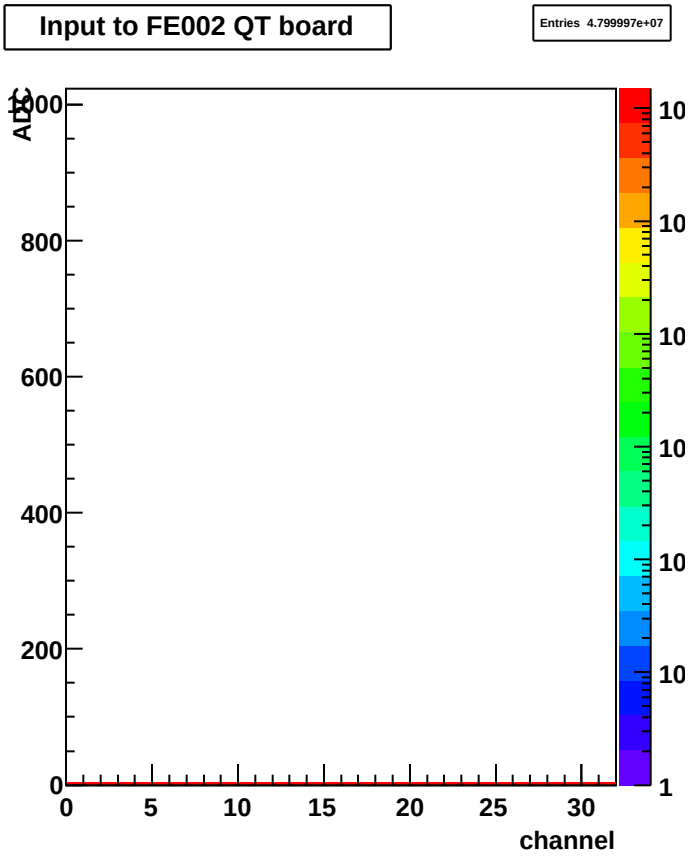

Input to FPD L2 DSM
Entries 1507901
CL bits - simulated
1.5e+06
6
7
5
33
111
618
7122
SMALL
LARGE-S
LARGE-N

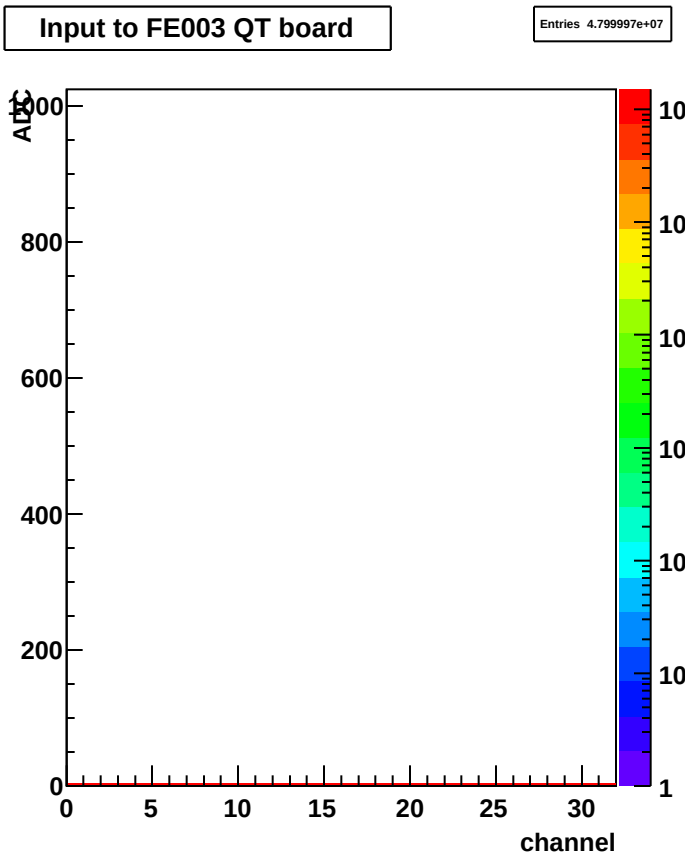

Input to FPD L2 DSM
Entries 4499997
HT bits
244
140
861
7354
1424
1.49154e+06
1.49844e+06
1.5e+06
SMALL
LARGE-S
LARGE-N

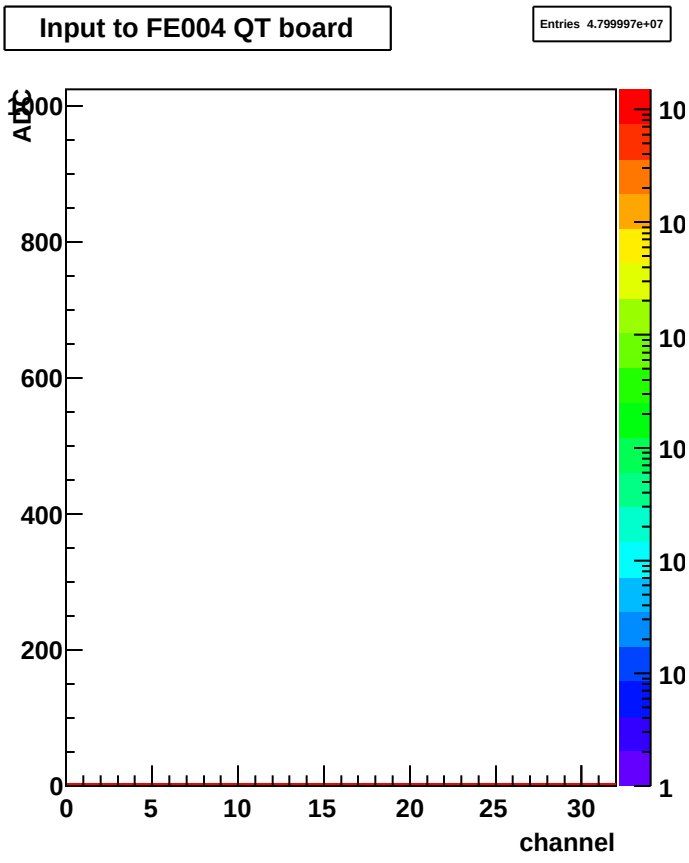

Input to FPD L2 DSM
Entries 10023
HT bits - simulated
244
140
861
7354
1424
SMALL
LARGE-S
LARGE-N

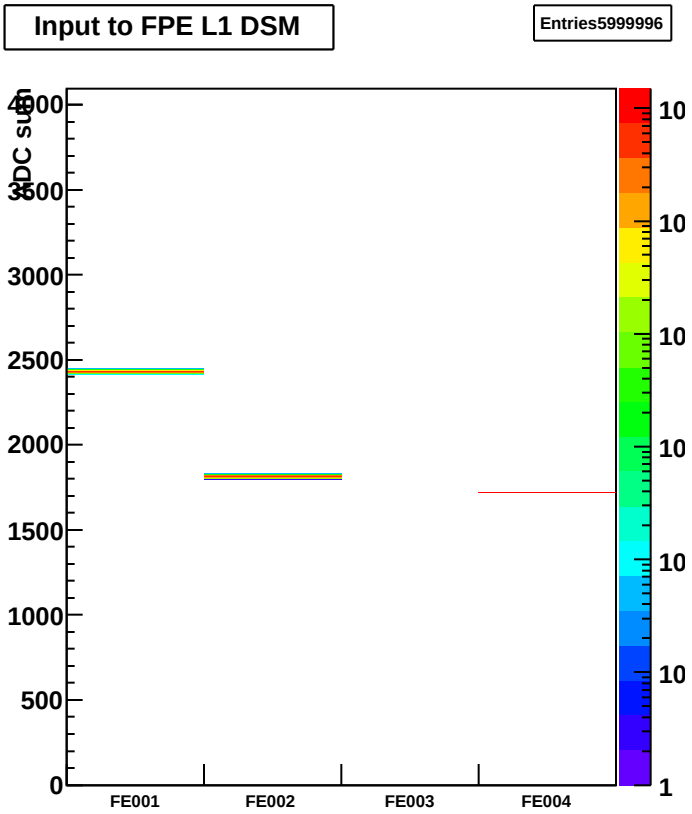

Input to FPD L2 DSM
Entries 8999994
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

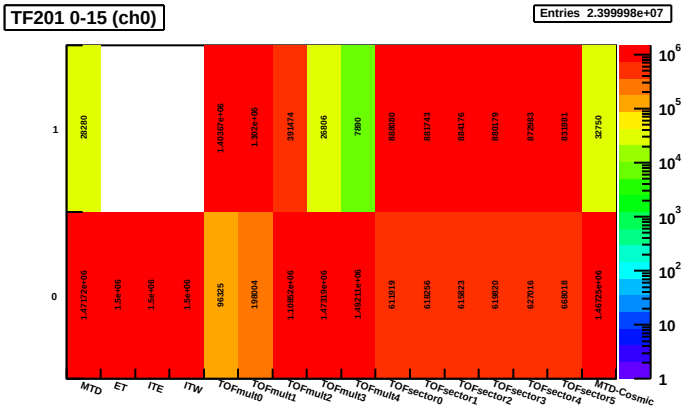

Input to FE001 QT board
Entries 4.799997e+07
ADC
channel

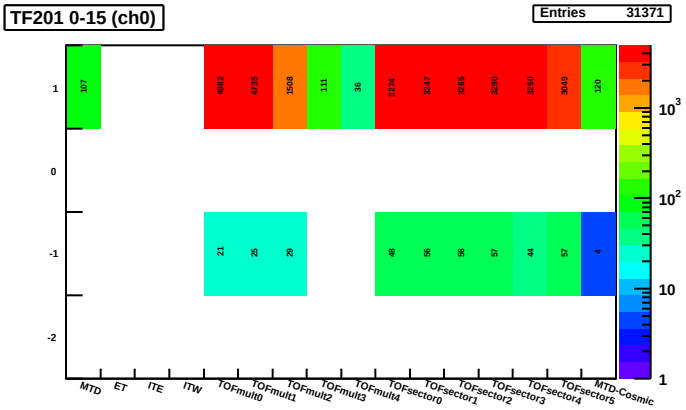

Input to FE002 QT board
Entries 4.799997e+07
ADC
channel


Input to FE003 QT board
Entries 4.799997e+07
ADC
channel


Input to FE004 QT board
Entries 4.799997e+07
ADC
channel


Input to FPE L1 DSM
Entries5999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.399998e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 31371
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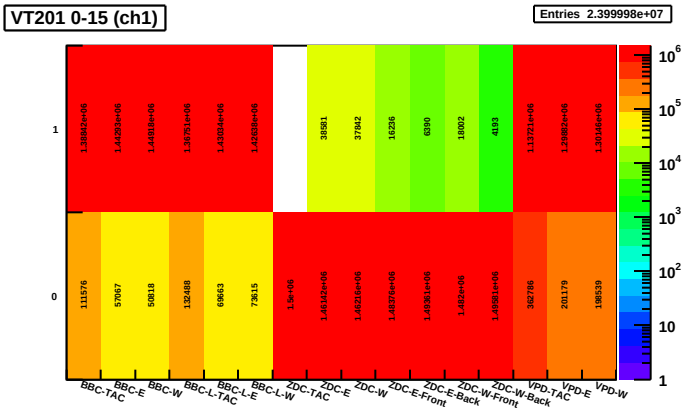
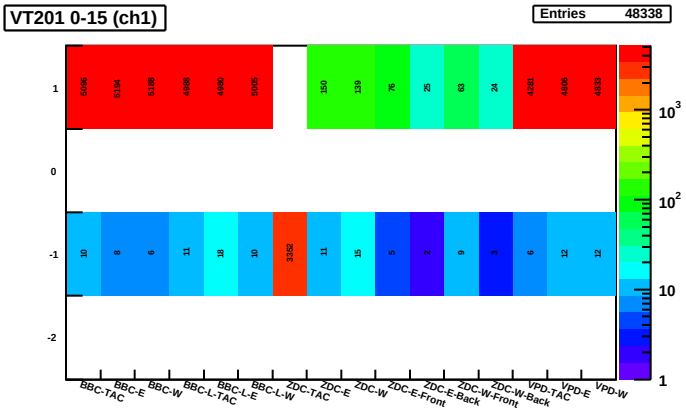

VT201 0-15 (ch1)
Entries 2.399998e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 48338
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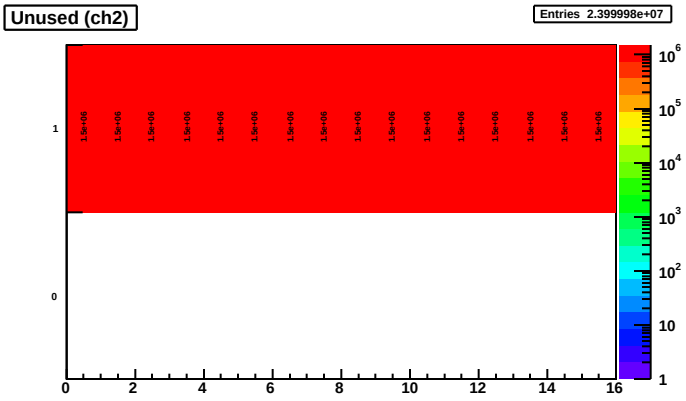
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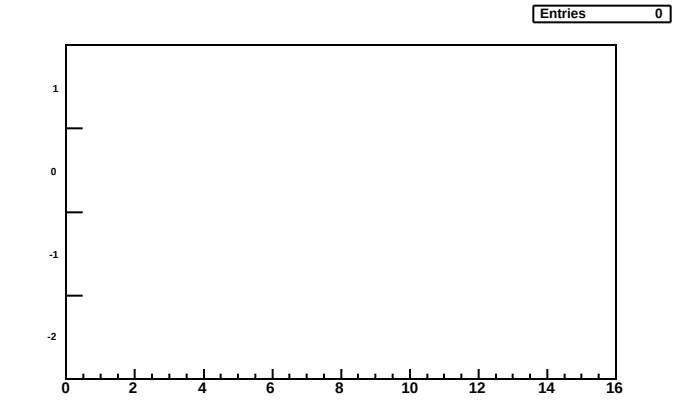
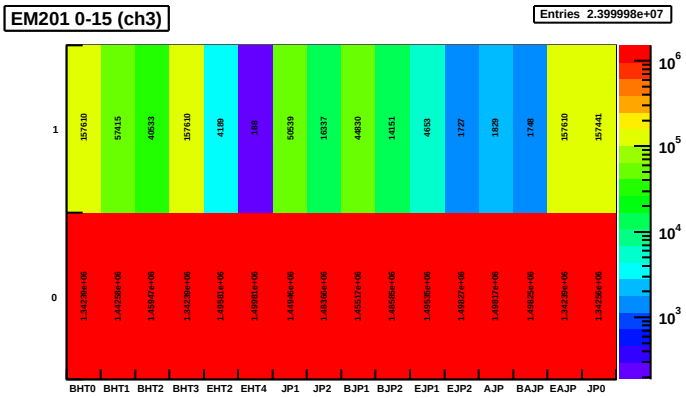

Entries 0


EM201 0-15 (ch3)
Entries 2.399998e+07
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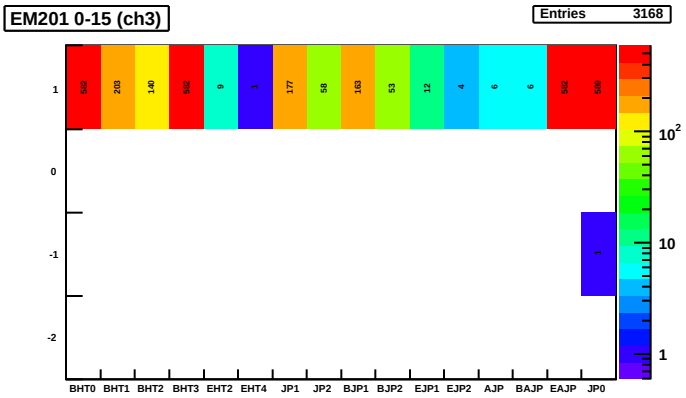
EM201 0-15 (ch3)
Entries 3168
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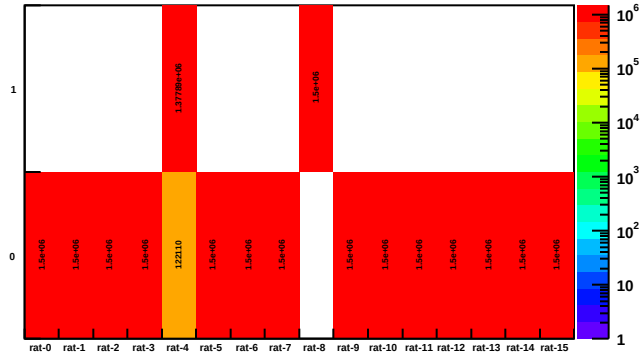
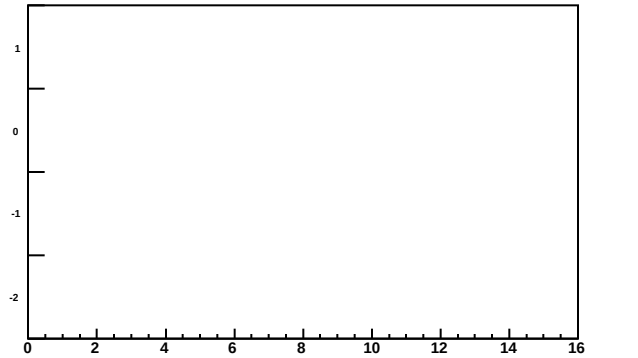
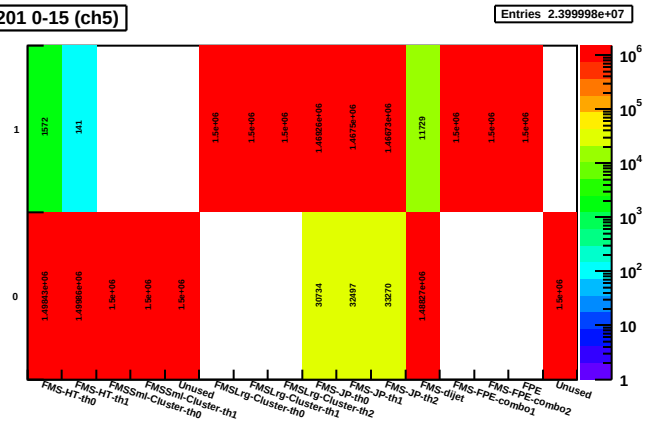
RAT board (ch4)





FP201 0-15 (ch5)



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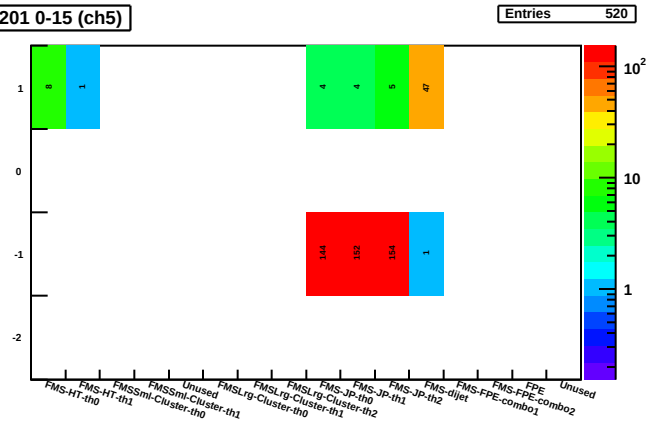
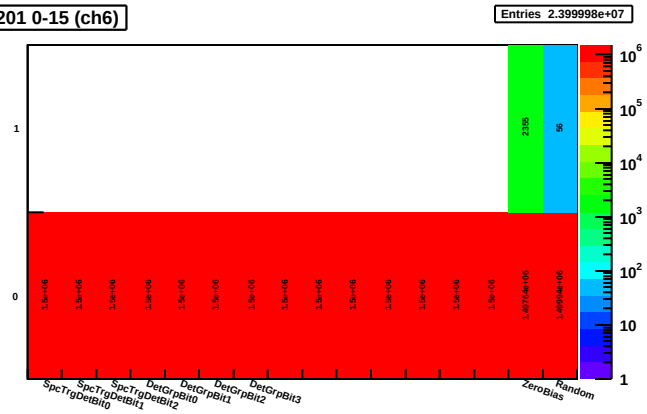
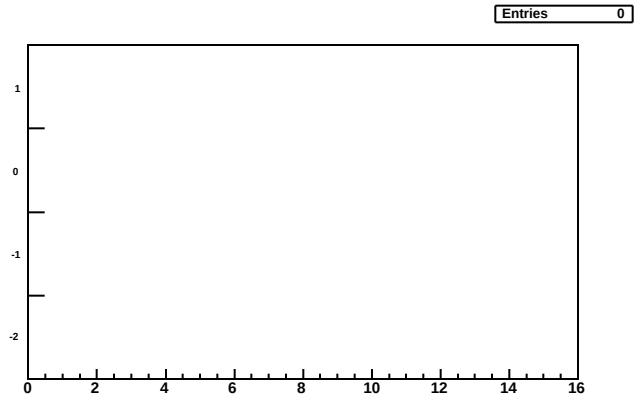
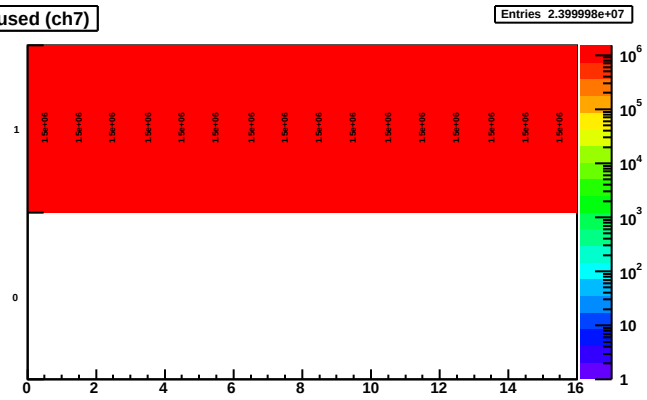
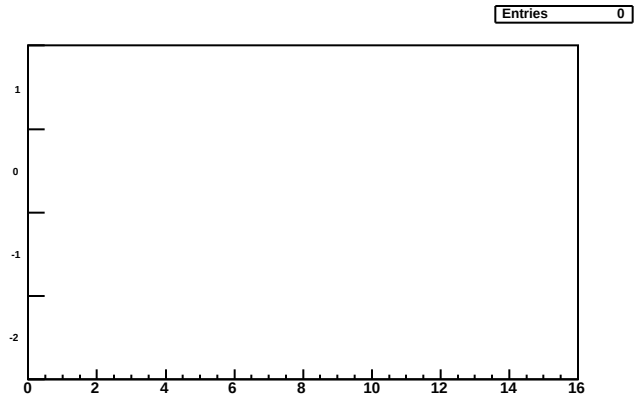


ST201 0-15 (ch6)





Unused (ch7)