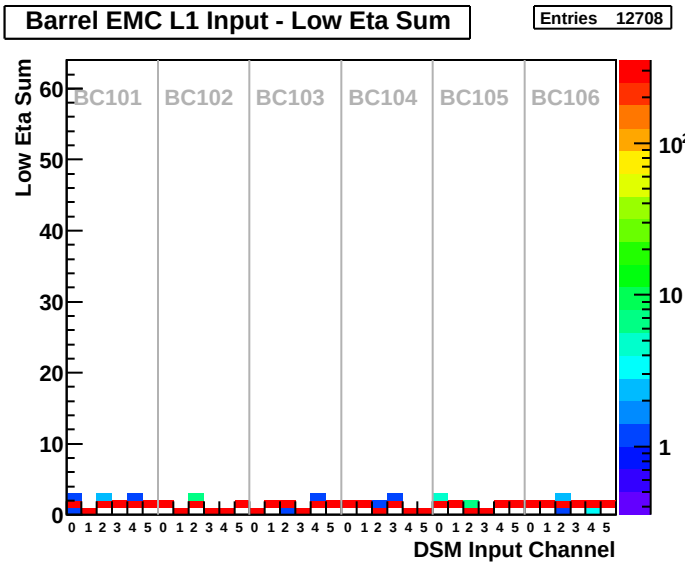
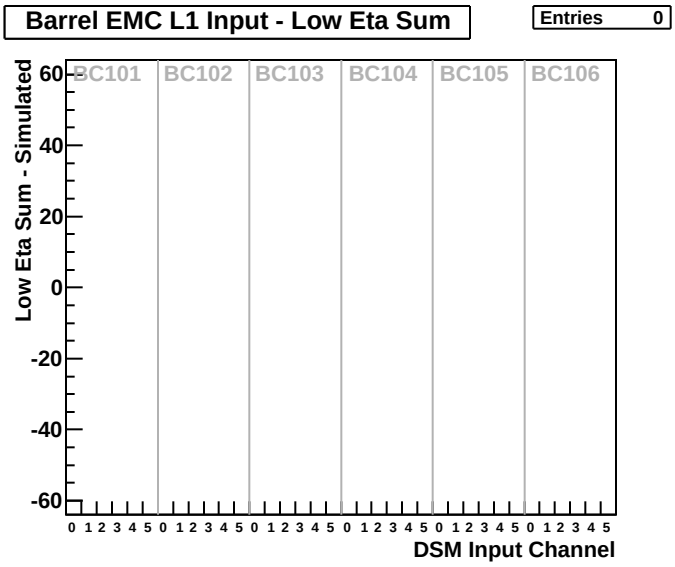
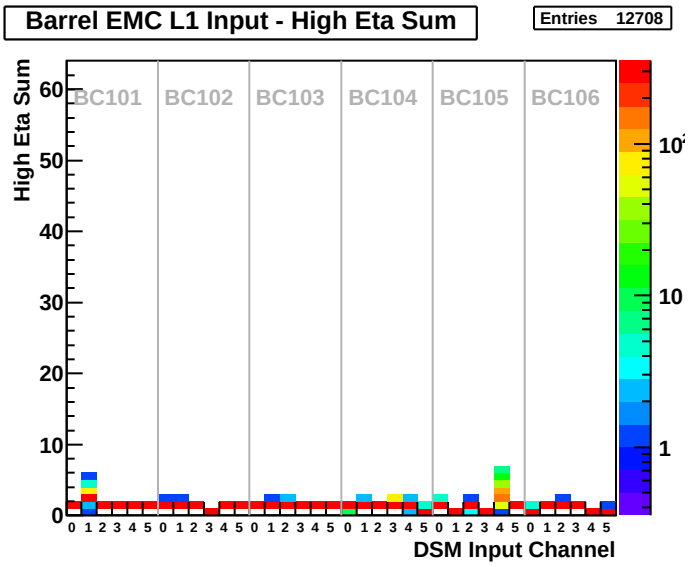
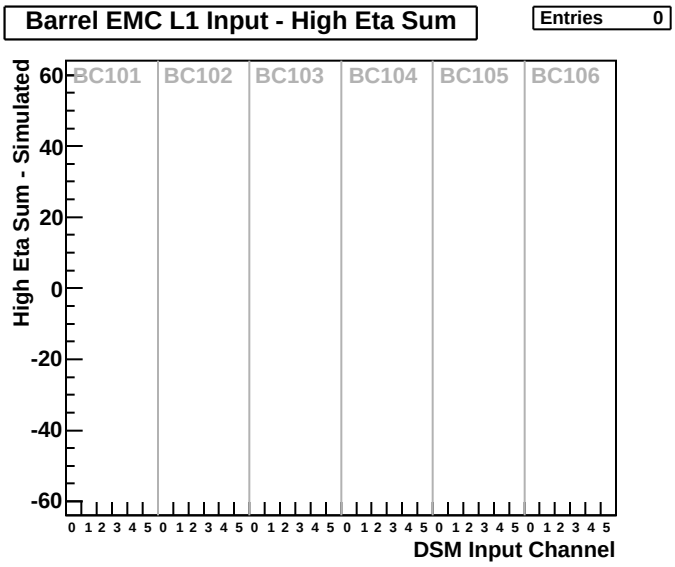
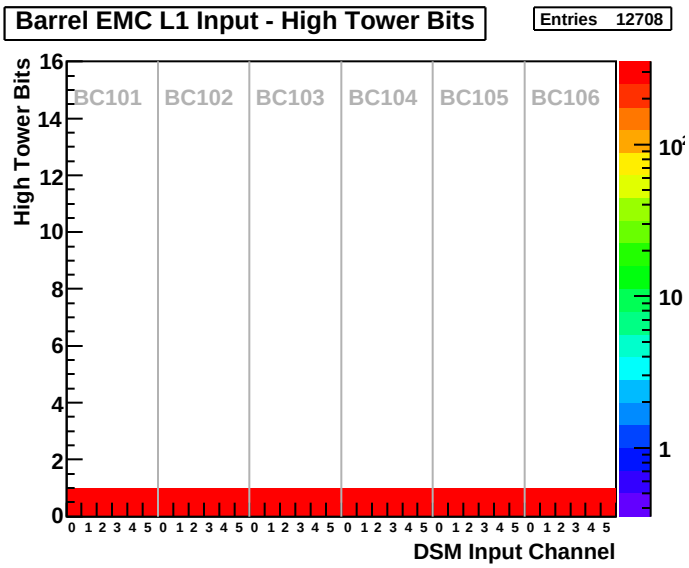
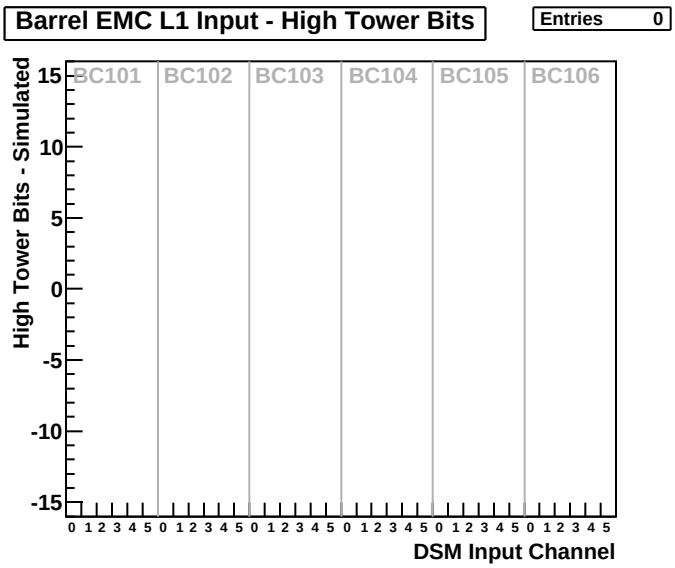

Barrel EMC L1 Input - Low Eta Sum
Entries 12708
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

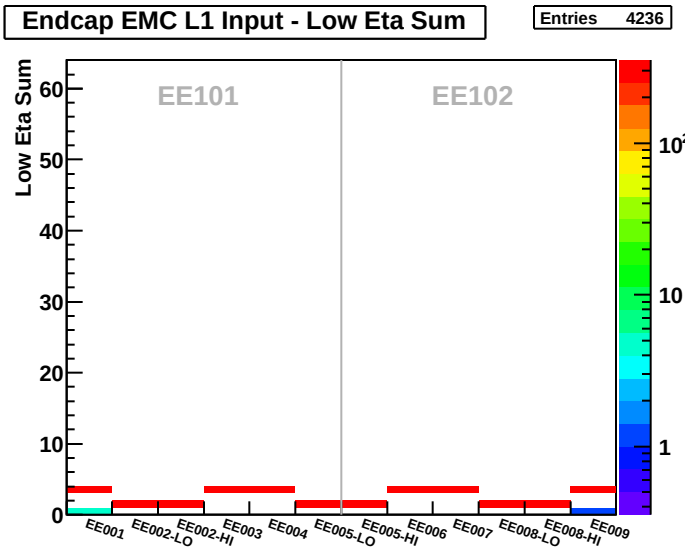

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

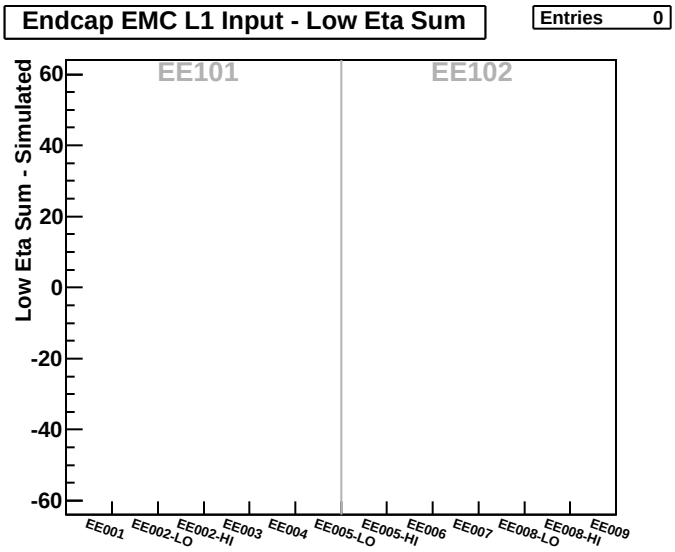

Barrel EMC L1 Input - High Eta Sum
Entries 12708
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

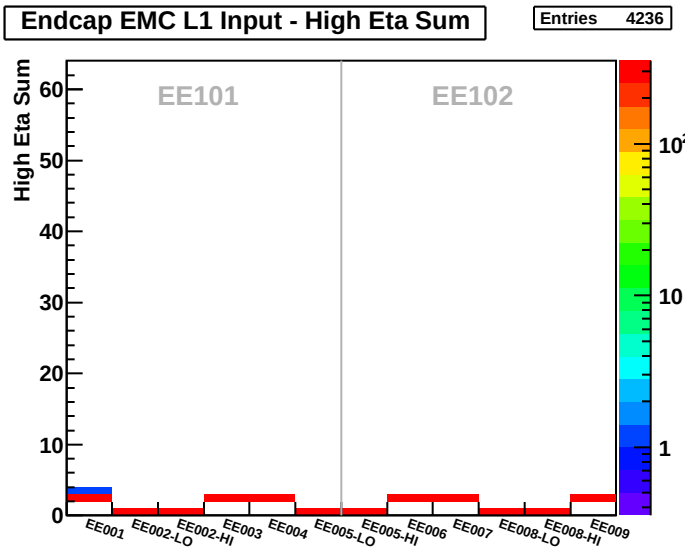

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

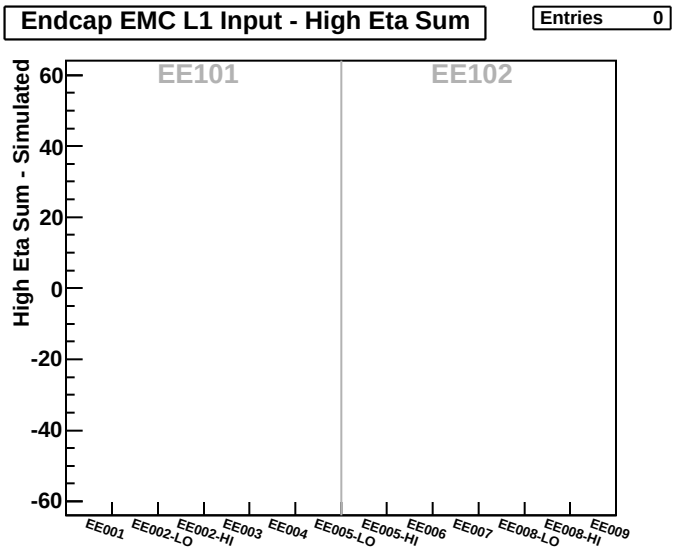

Barrel EMC L1 Input - High Tower Bits
Entries 12708
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

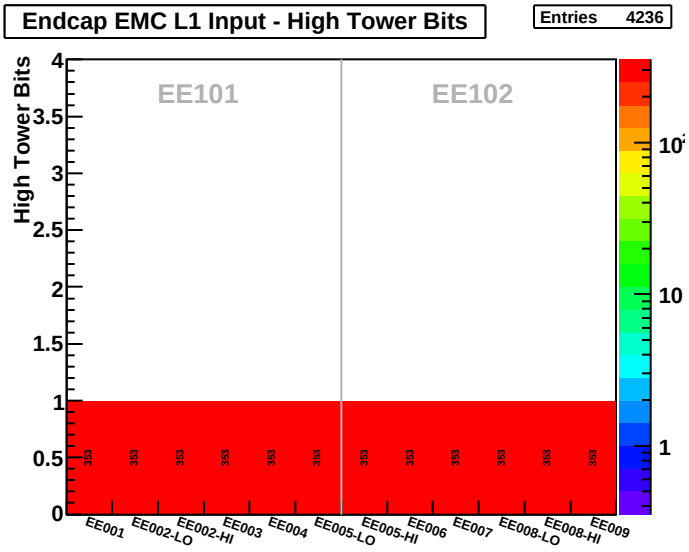

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

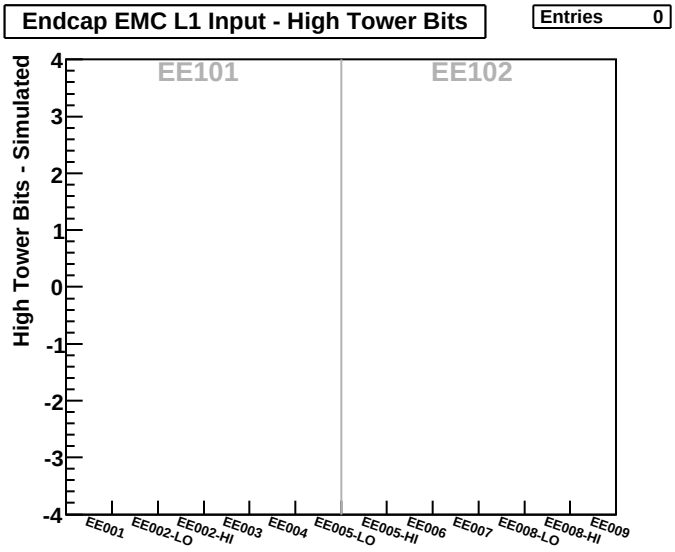

Endcap EMC L1 Input - Low Eta Sum
Entries 4236
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

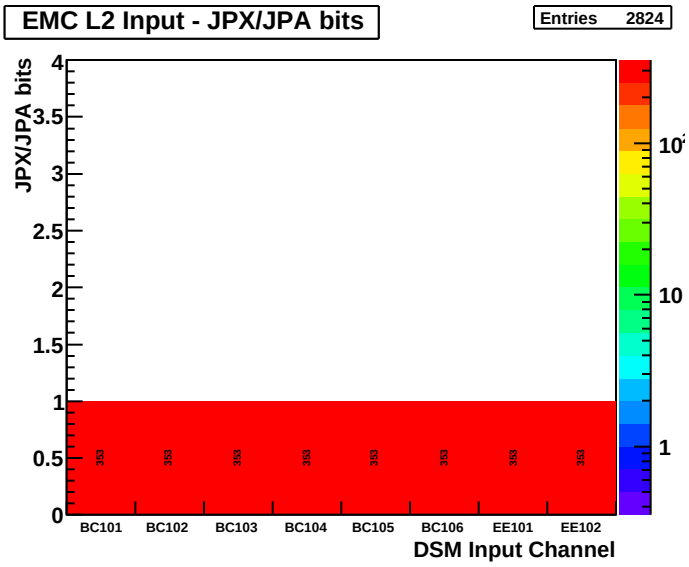

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

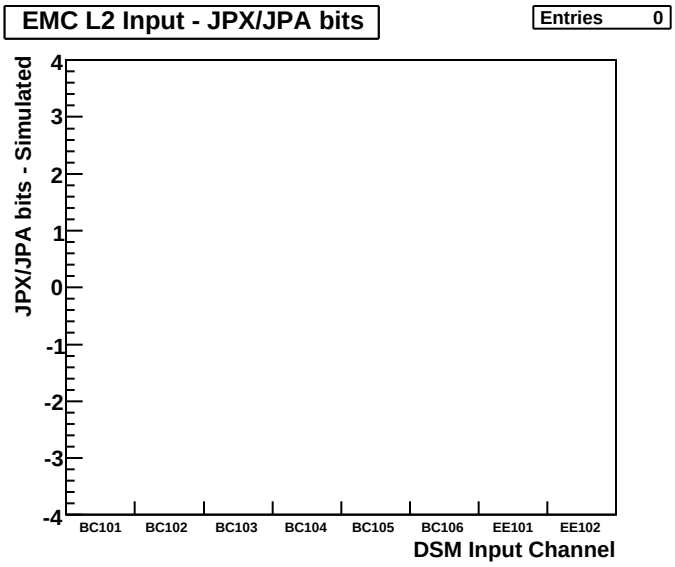

Endcap EMC L1 Input - High Eta Sum
Entries 4236
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

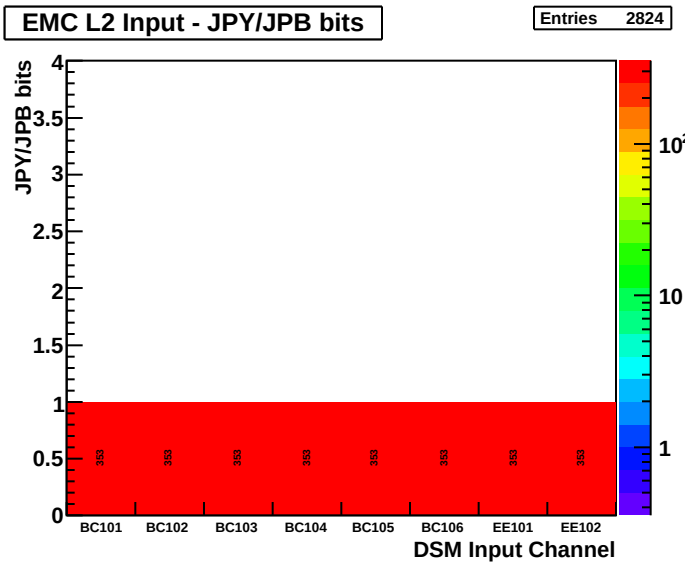

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

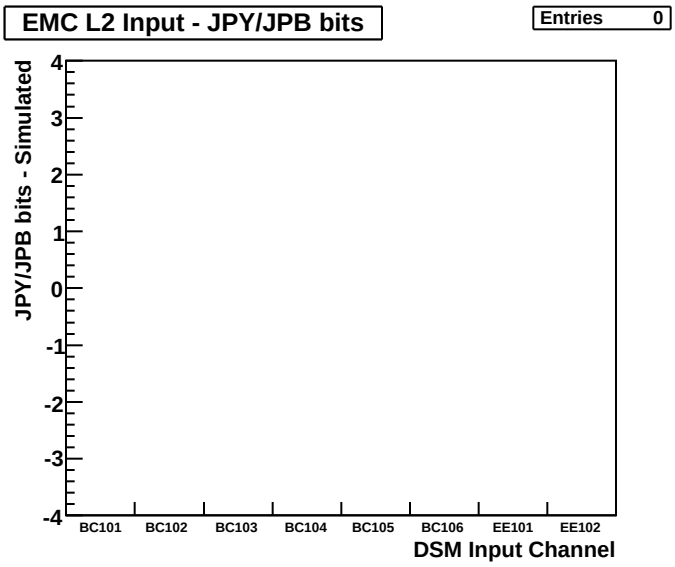

Endcap EMC L1 Input - High Tower Bits
Entries 4236
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

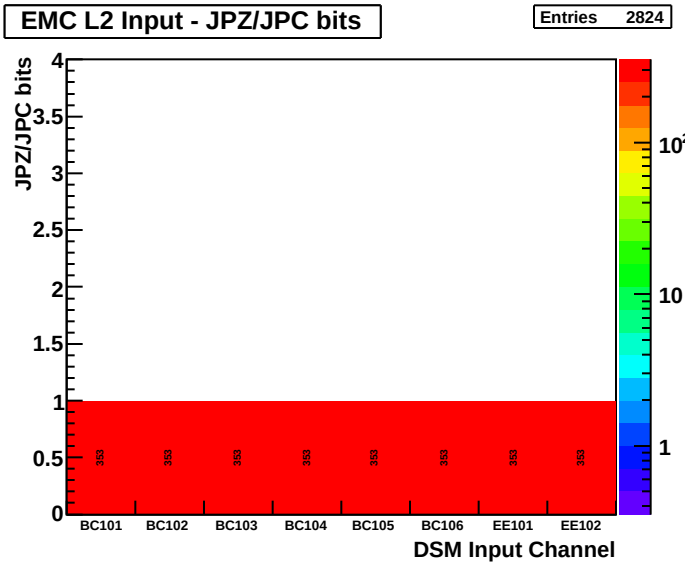

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

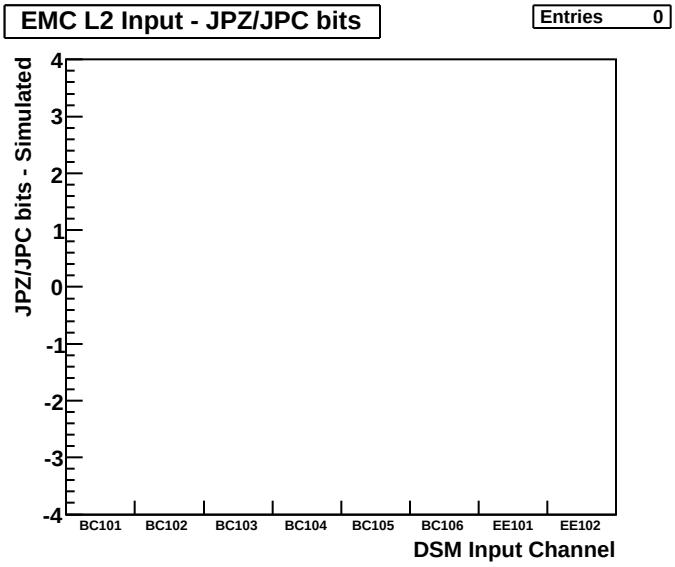

EMC L2 Input - JPX/JPA bits
Entries 2824
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

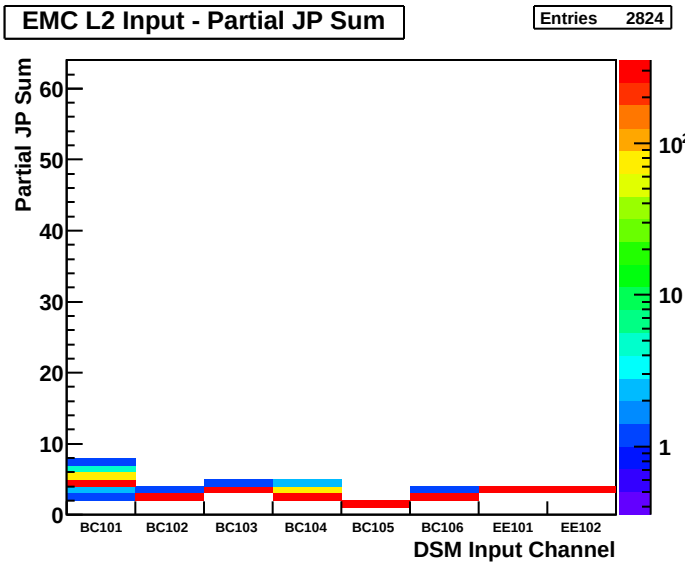

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

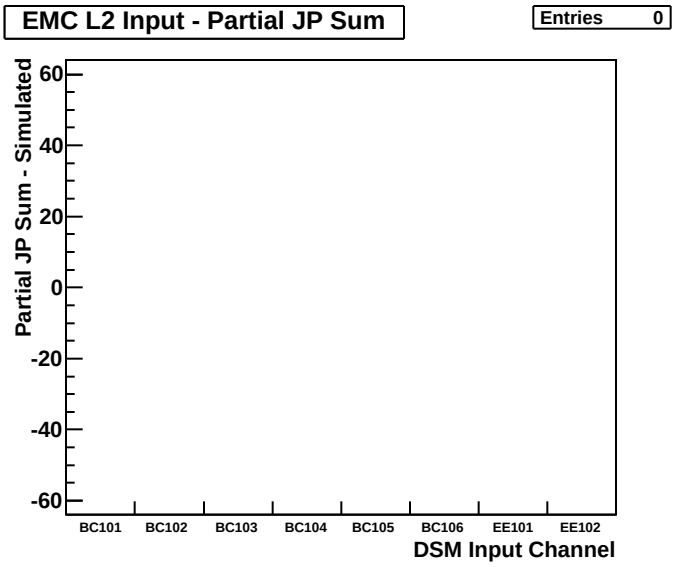

EMC L2 Input - JPY/JPB bits
Entries 2824
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

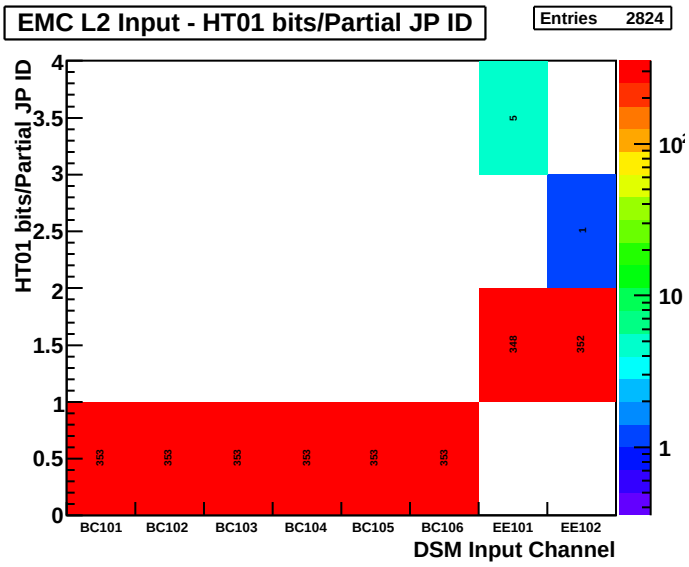

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

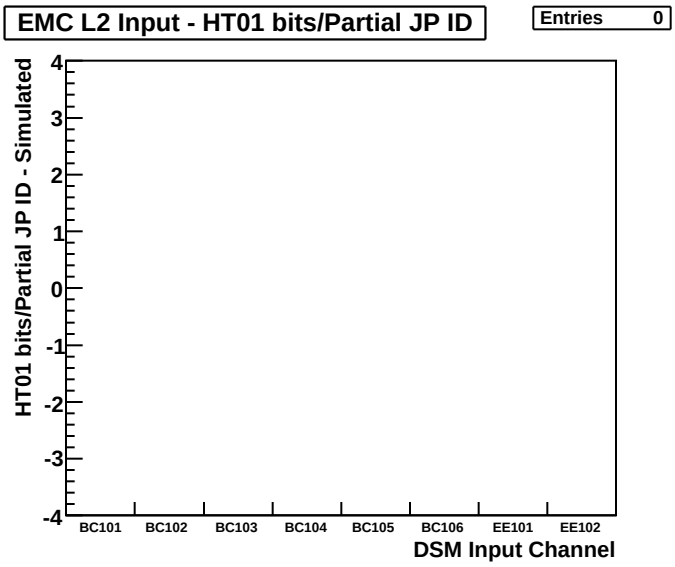

EMC L2 Input - JPZ/JPC bits
Entries 2824
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

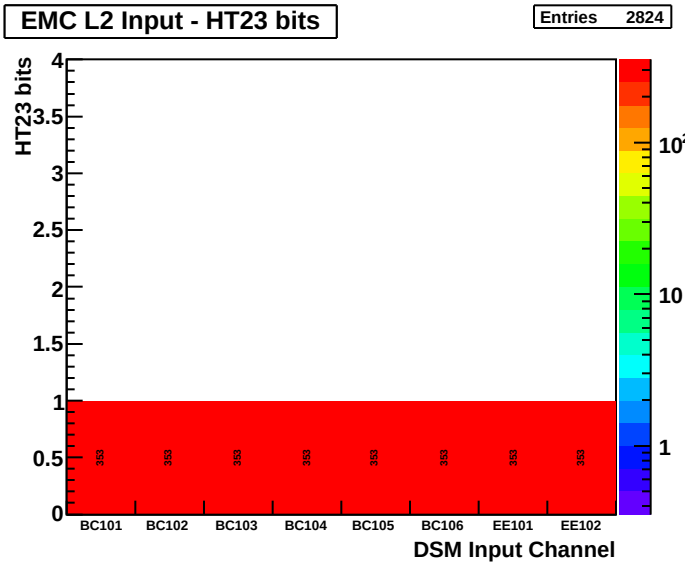

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

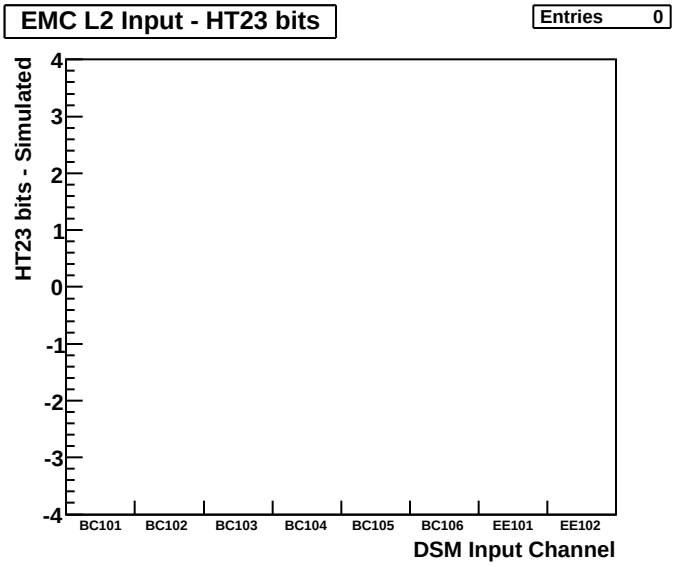

EMC L2 Input - Partial JP Sum
Entries 2824
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

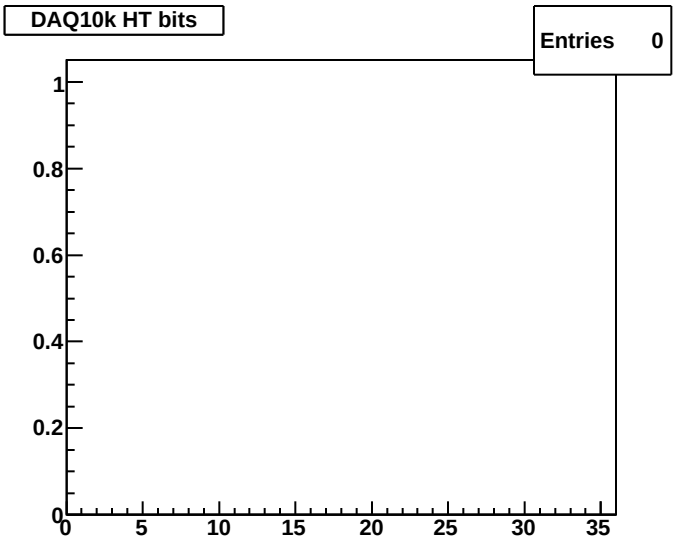

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

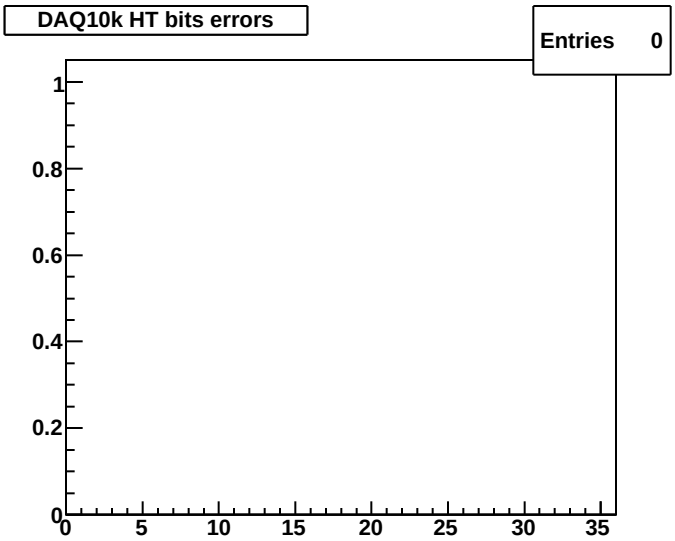

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2824
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

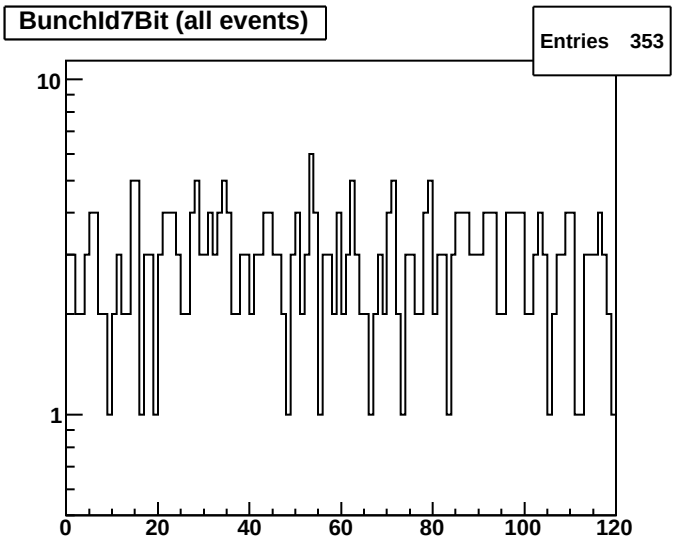

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

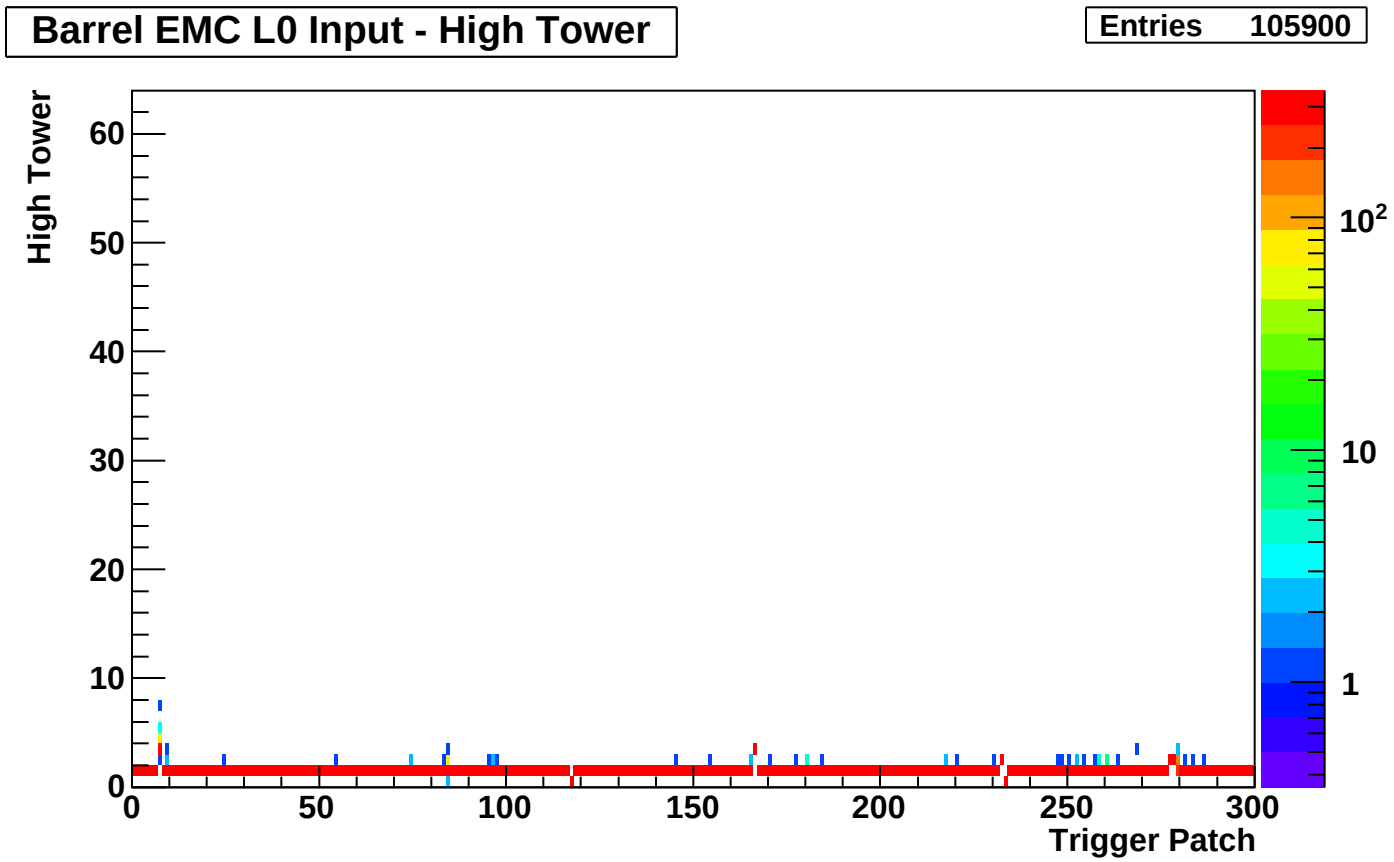

EMC L2 Input - HT23 bits
Entries 2824
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

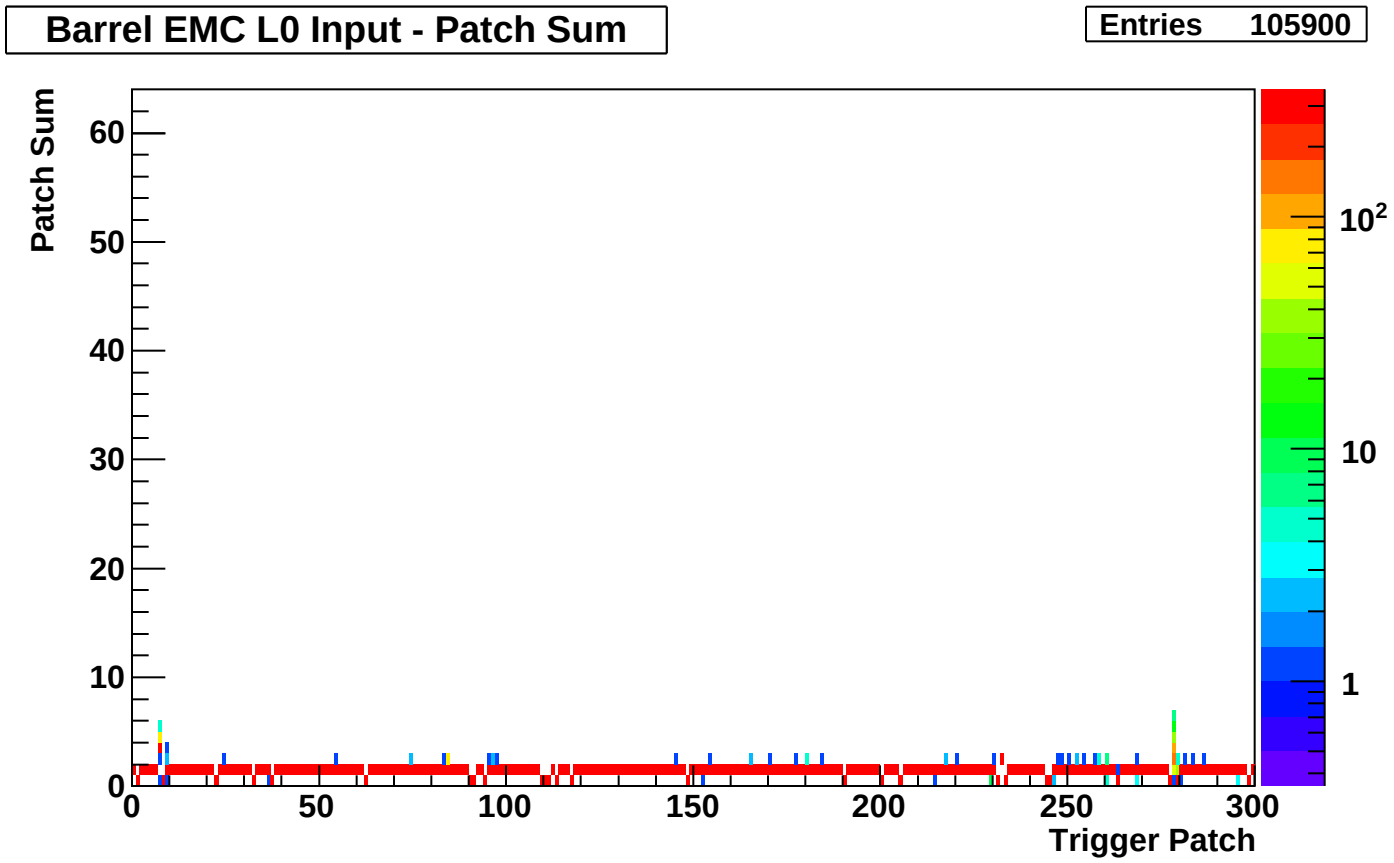

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

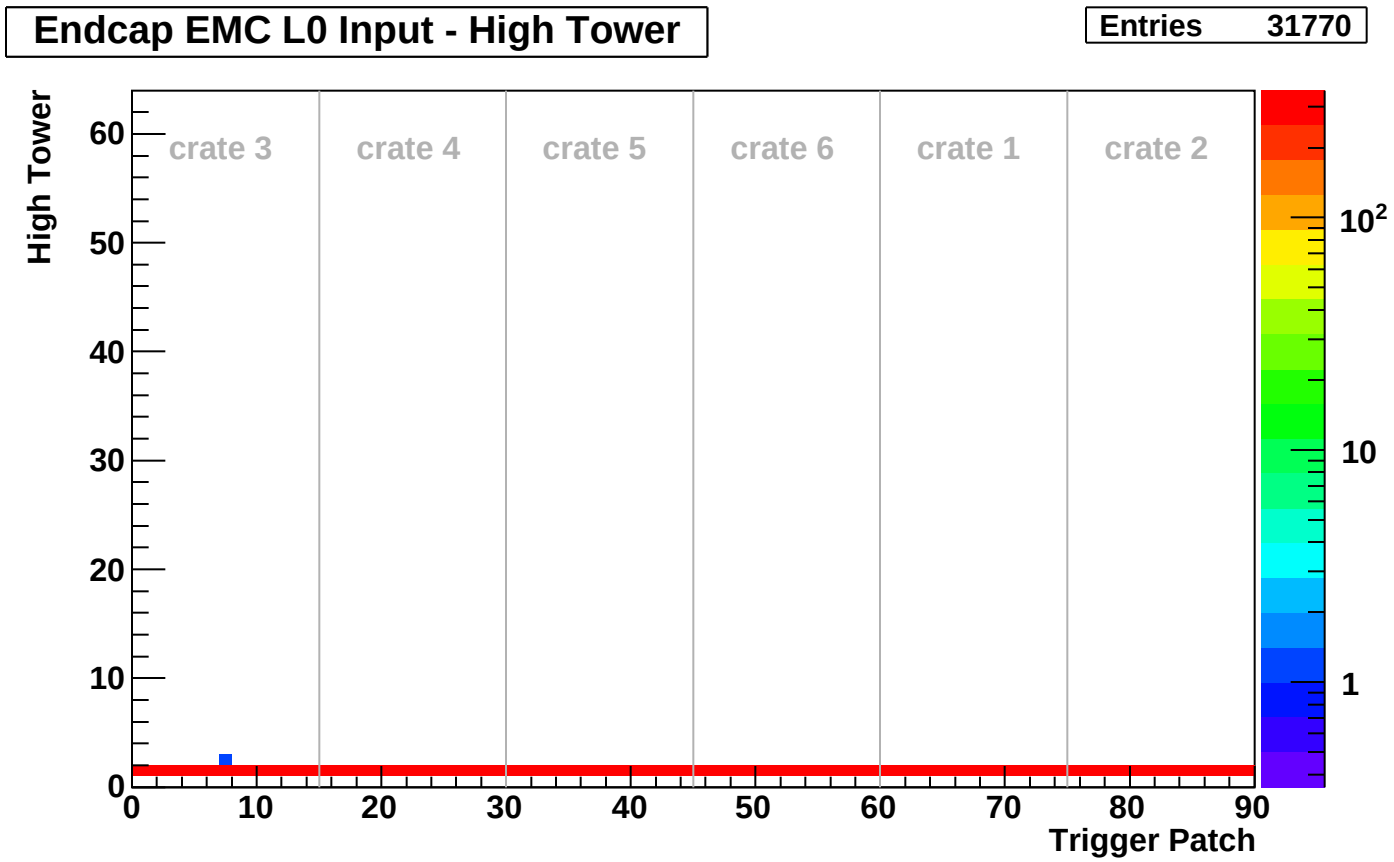

DAQ10k HT bits
Entries 0
1
0.8
0.6
0.4
0.2
0
0
5
10
15
20
25
30
35

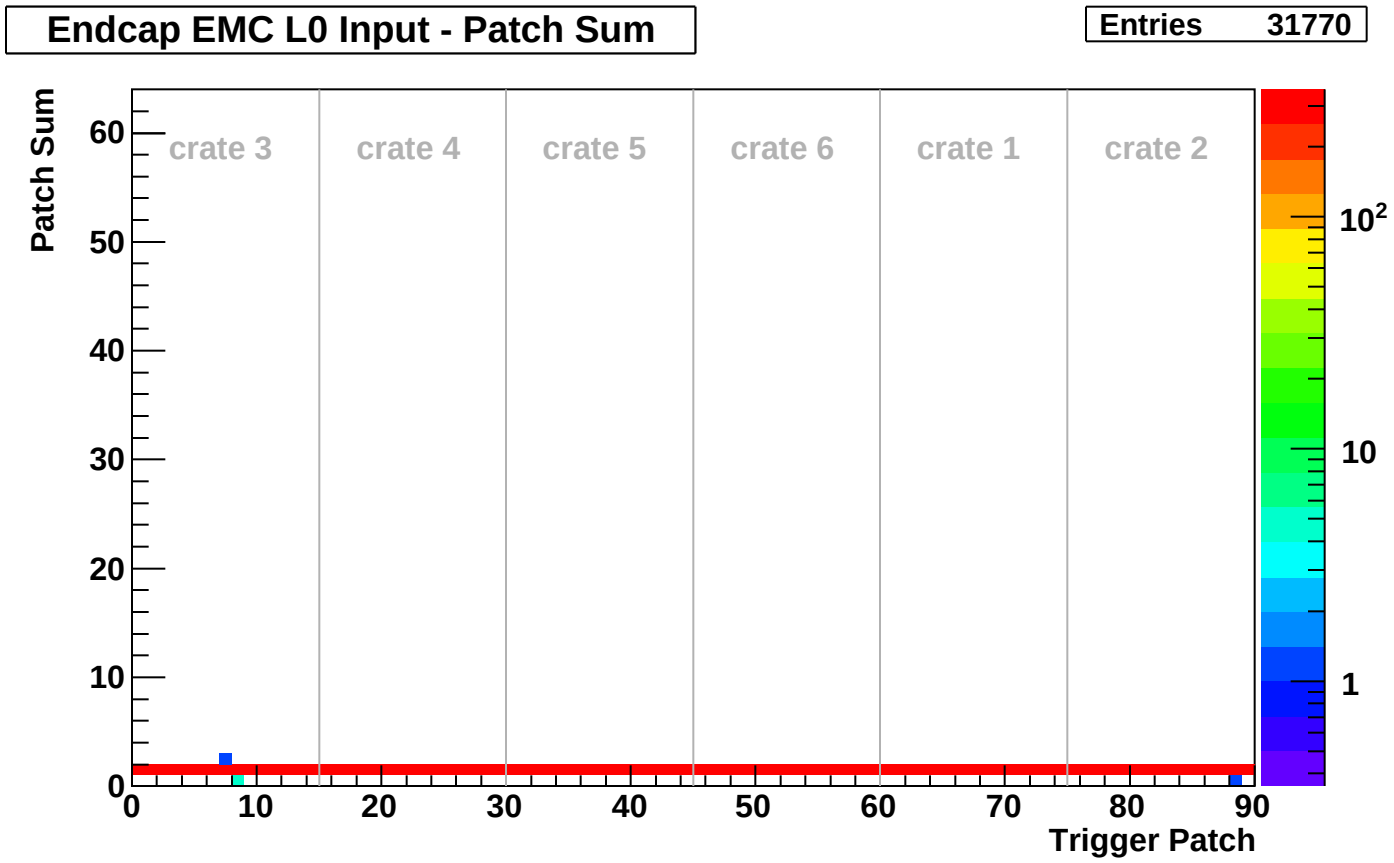

DAQ10k HT bits errors
Entries 0
1
0.8
0.6
0.4
0.2
0
0
5
10
15
20
25
30
35

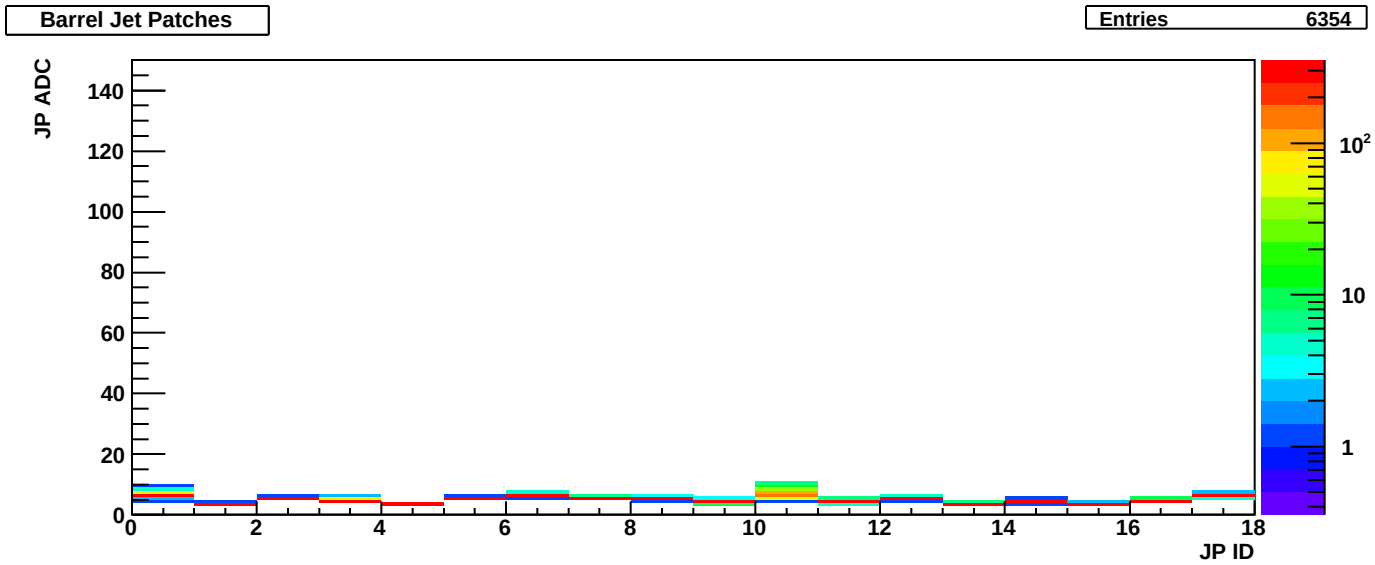

BunchId7Bit (all events)
Entries 353
10
1
0
20
40
60
80
100
120

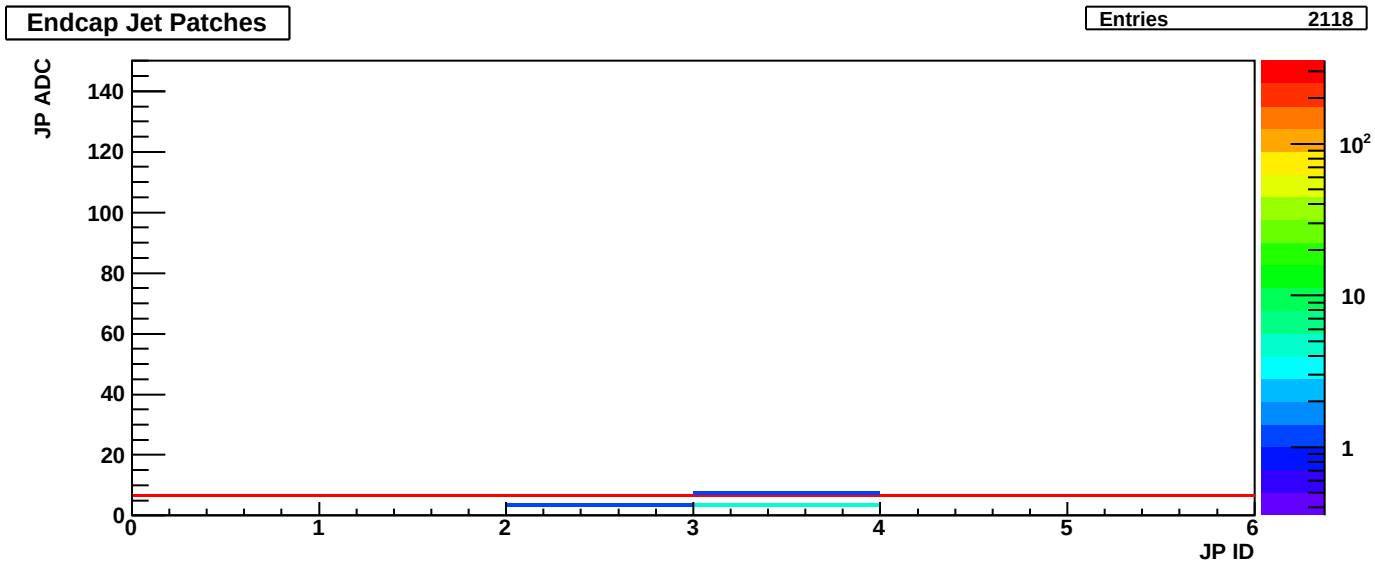

Barrel EMC L0 Input - High Tower
Entries 105900
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
10^2
10
1

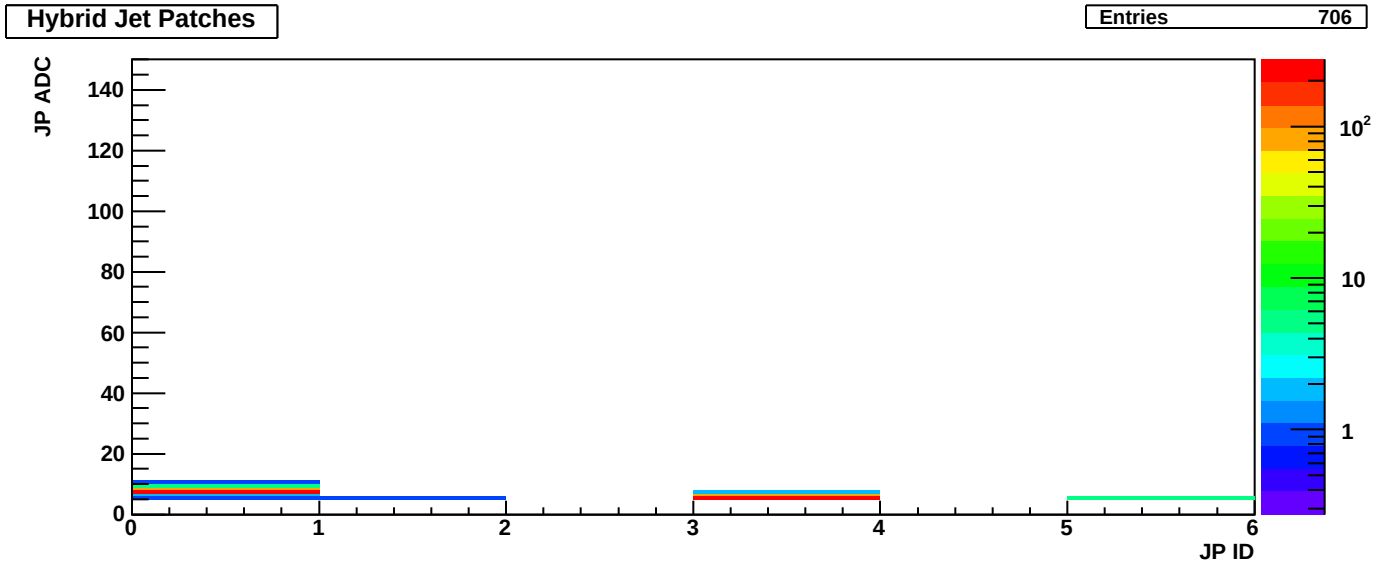

Barrel EMC L0 Input - Patch Sum
Entries 105900
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
10^2
10
1

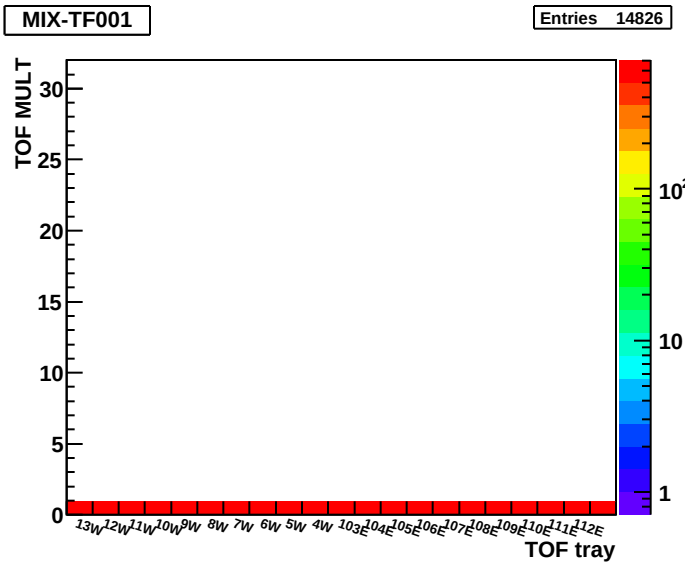

Endcap EMC L0 Input - High Tower
Entries 31770
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
10^2
10
1

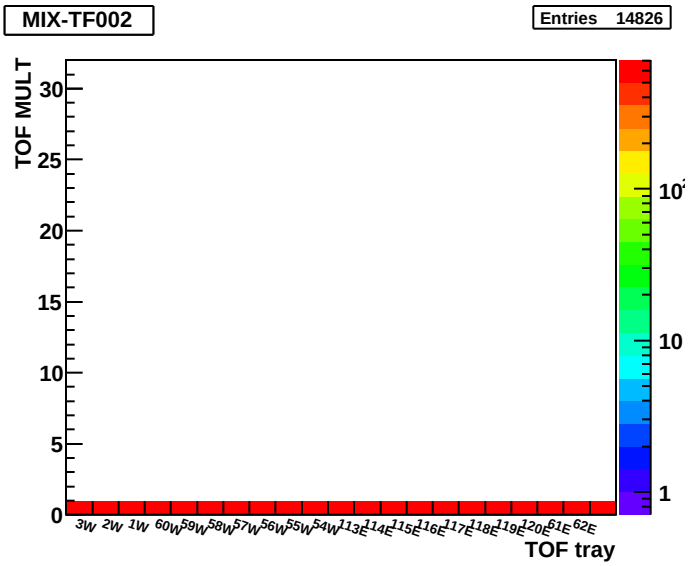

Endcap EMC L0 Input - Patch Sum
Entries 31770
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
10^2
10
1

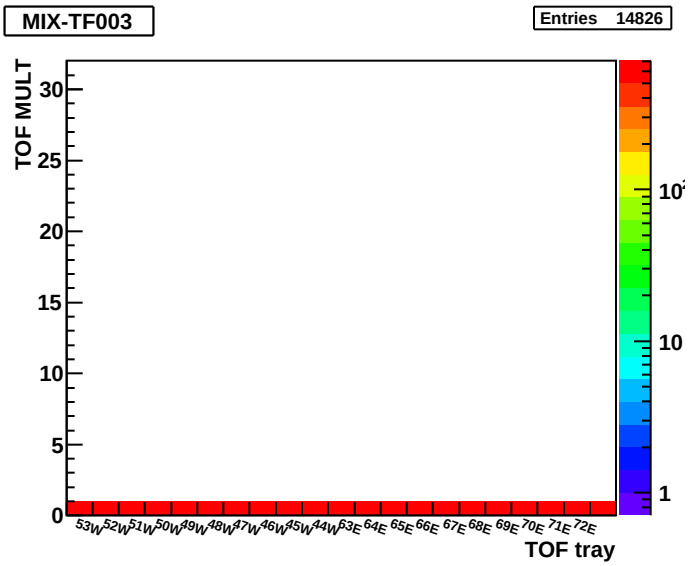

Barrel Jet Patches
Entries 6354
JP ADC
JP ID

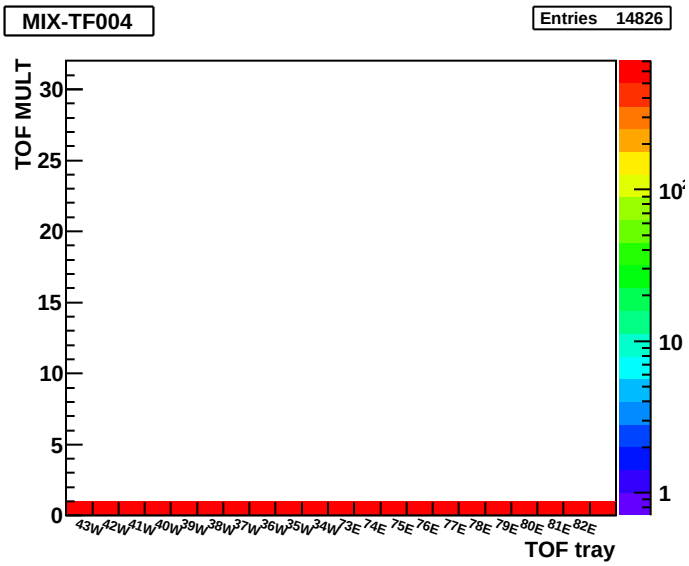

Endcap Jet Patches
Entries 2118
JP ADC
JP ID

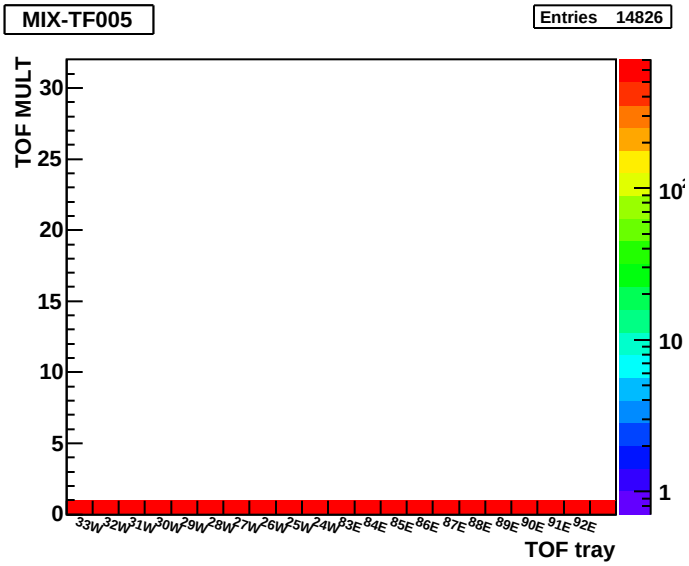

Hybrid Jet Patches
Entries 706
JP ADC
JP ID

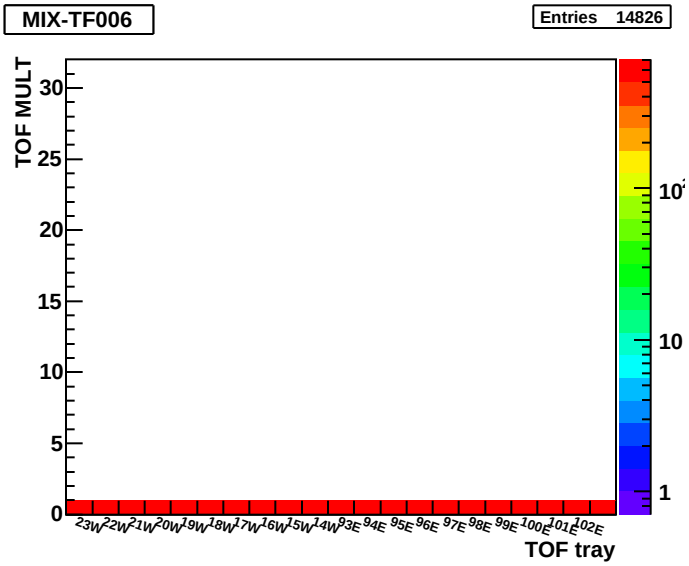

MIX-TF001
Entries 14826
TOF MULT
TOF tray

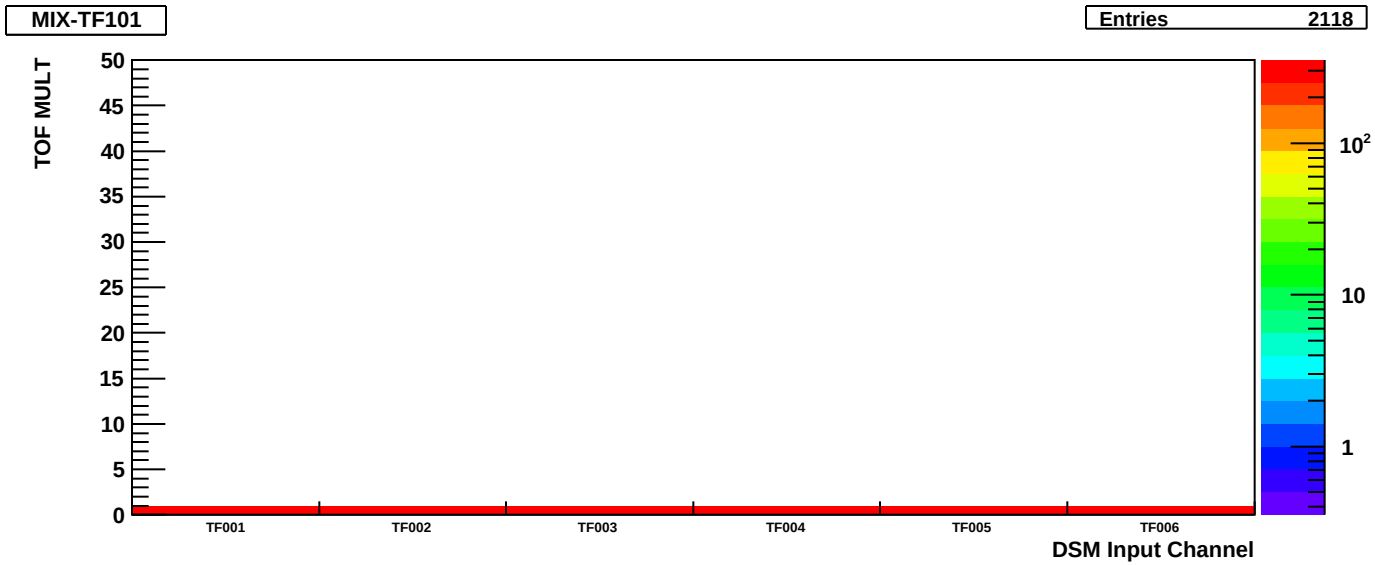

MIX-TF002
Entries 14826
TOF MULT
TOF tray

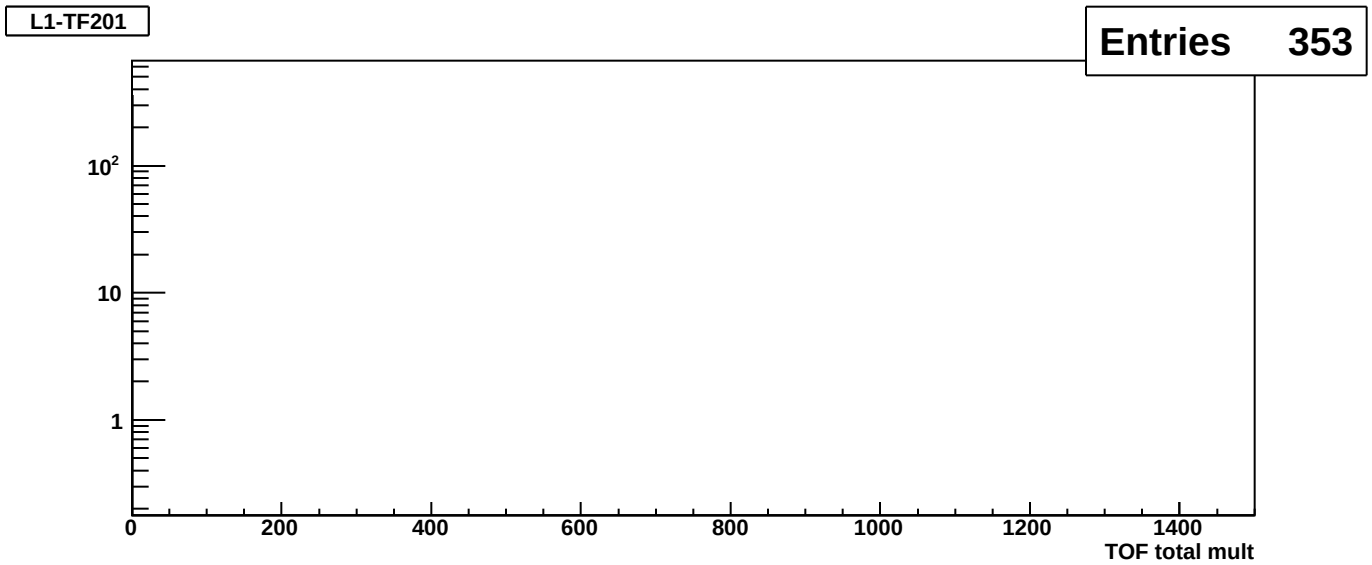

MIX-TF003
Entries 14826
TOF MULT
TOF tray

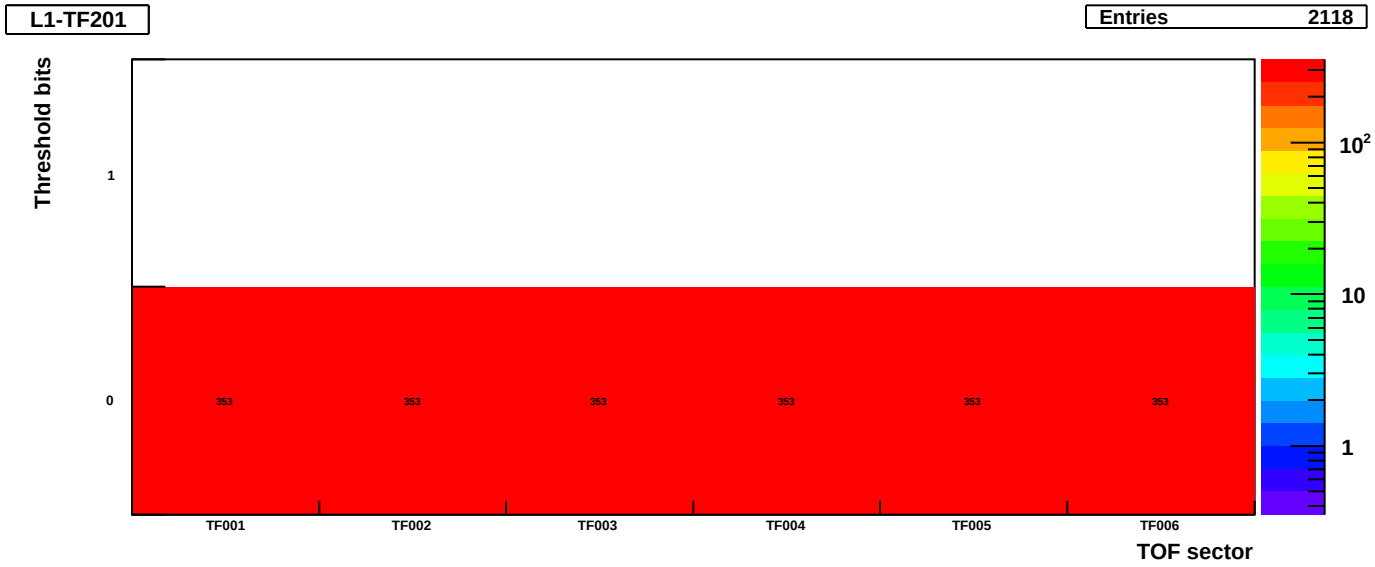

MIX-TF004
Entries 14826
TOF MULT
TOF tray

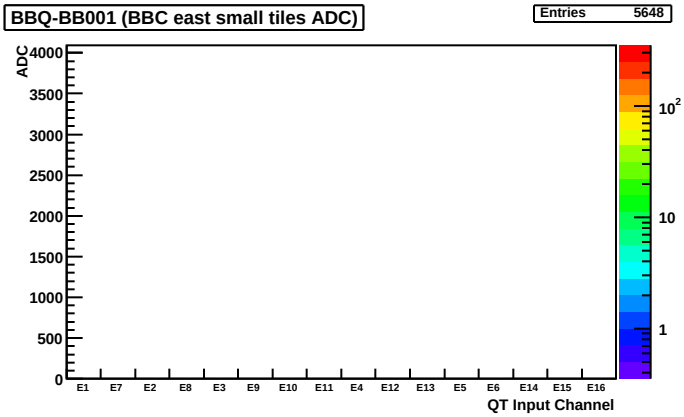

MIX-TF005
Entries 14826
TOF MULT
TOF tray

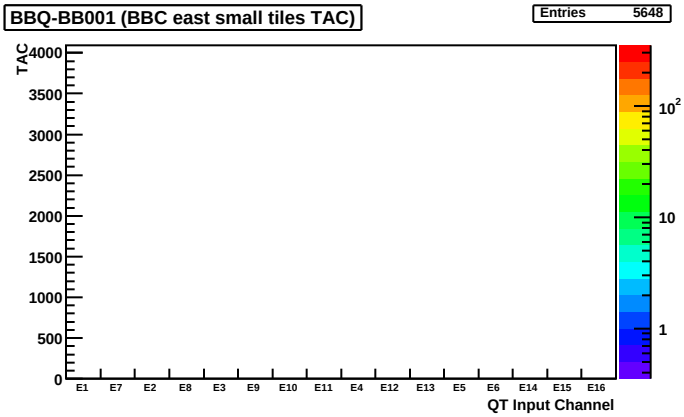

MIX-TF006
Entries 14826
TOF MULT
TOF tray

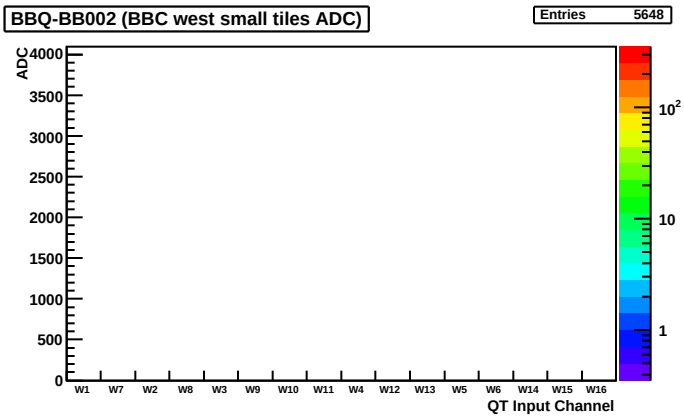

MIX-TF101
Entries 2118
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

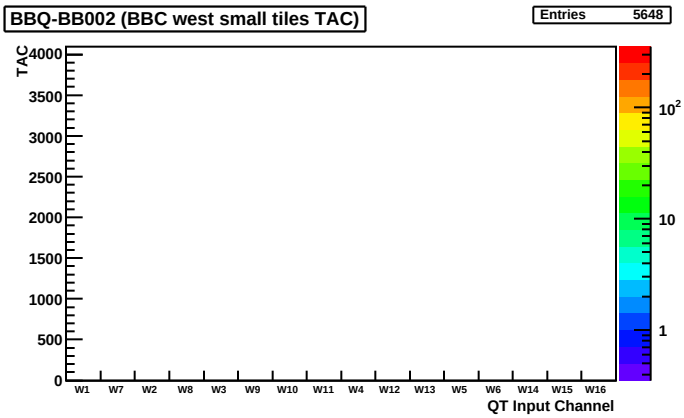

L1-TF201
Entries 353
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

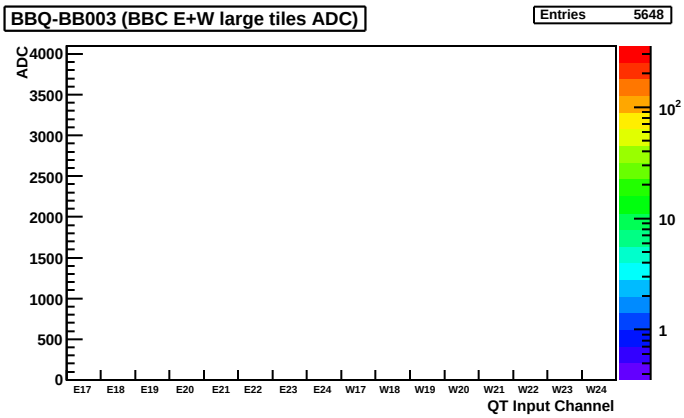

L1-TF201
Entries 2118
Threshold bits
1
0
353
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^2
10
1

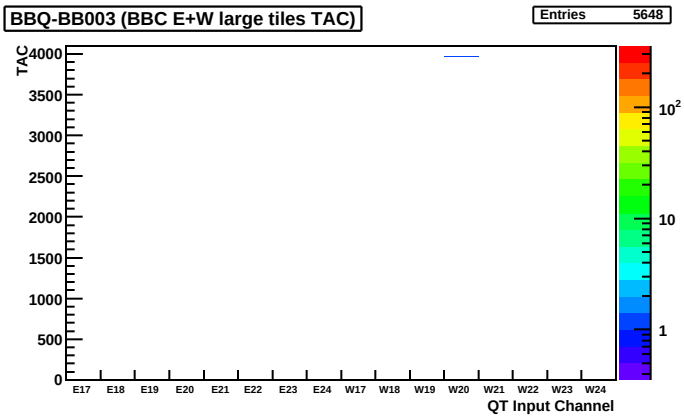

BBQ-BB001 (BBC east small tiles ADC)
Entries 5648
ADC
QT Input Channel

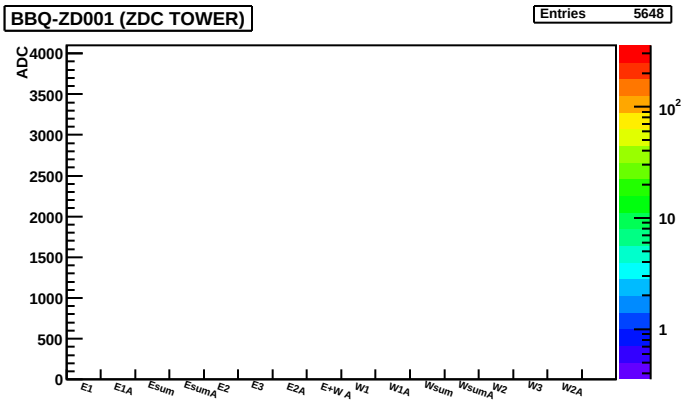

BBQ-BB001 (BBC east small tiles TAC)
Entries 5648
TAC
QT Input Channel

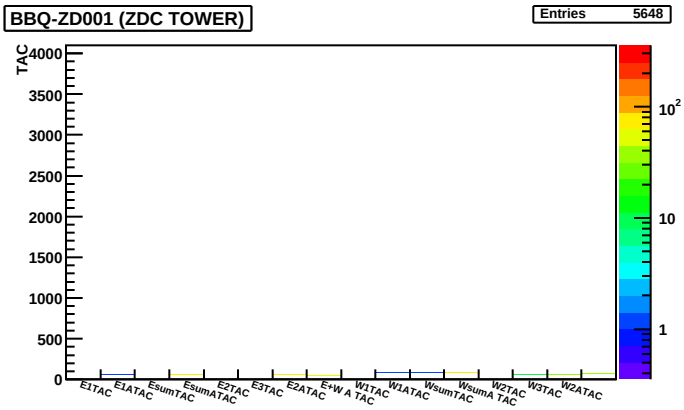

BBQ-BB002 (BBC west small tiles ADC)
Entries 5648
ADC
QT Input Channel

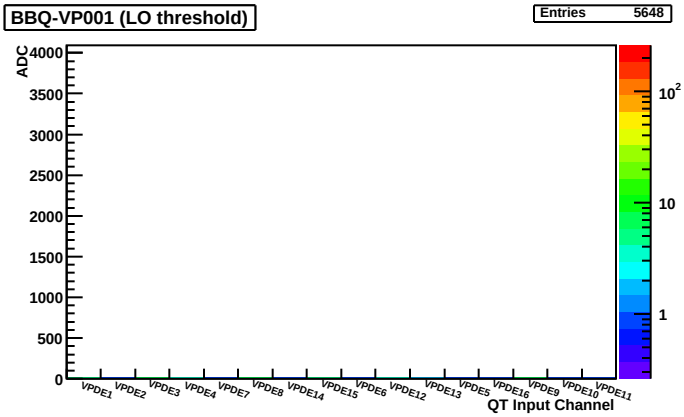

BBQ-BB002 (BBC west small tiles TAC)
Entries 5648
TAC
QT Input Channel

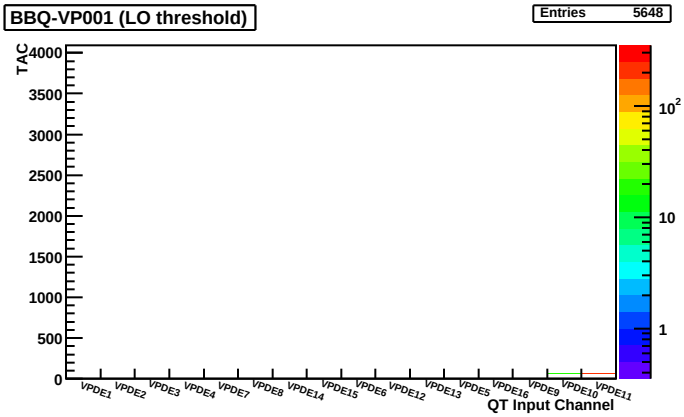

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5648
ADC
QT Input Channel

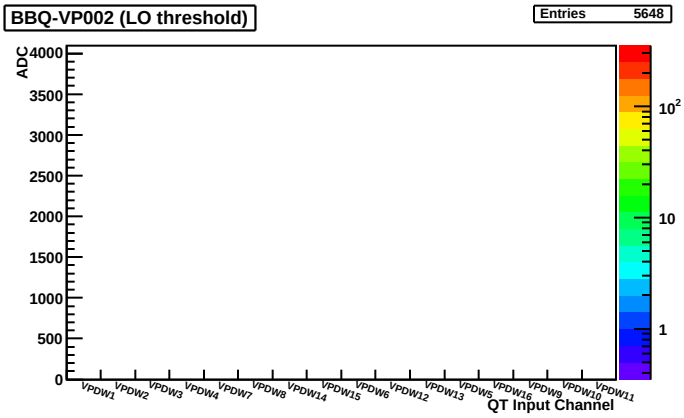

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5648
TAC
QT Input Channel

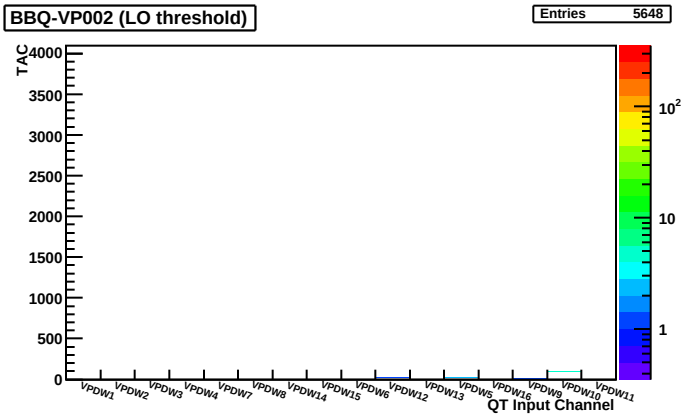

BBQ-ZD001 (ZDC TOWER)
Entries 5648
ADC

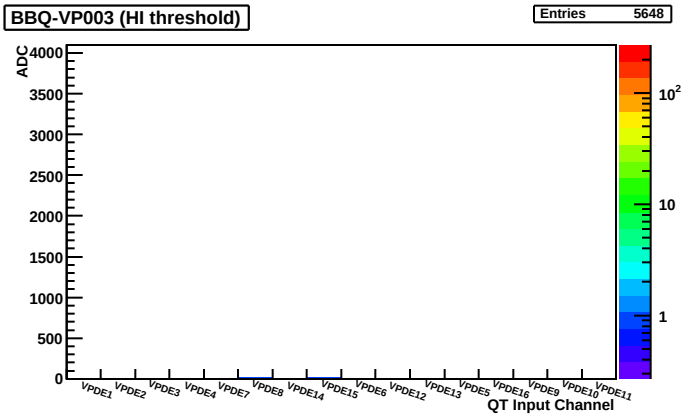

BBQ-ZD001 (ZDC TOWER)
Entries 5648
TAC

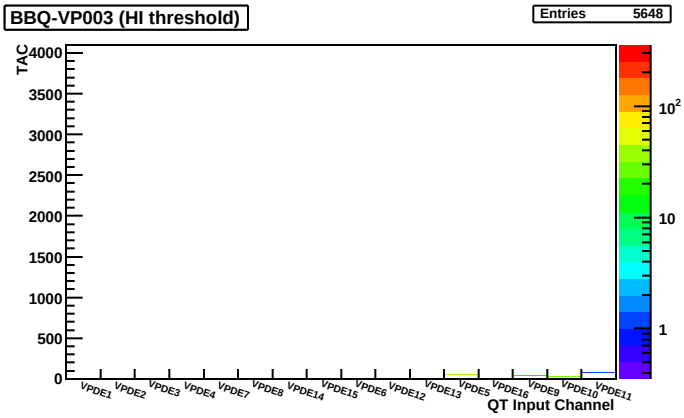

BBQ-VP001 (LO threshold)
Entries 5648
ADC
QT Input Channel

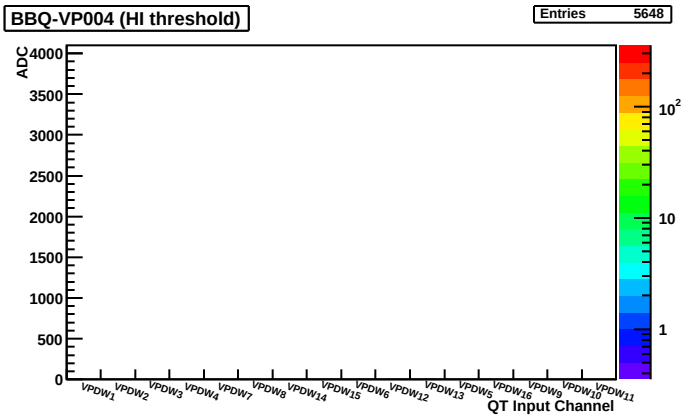

BBQ-VP001 (LO threshold)
Entries 5648
TAC
QT Input Channel

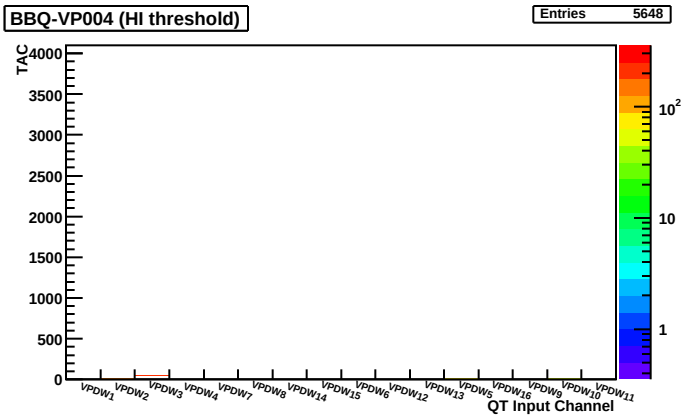

BBQ-VP002 (LO threshold)
Entries 5648
ADC
QT Input Channel

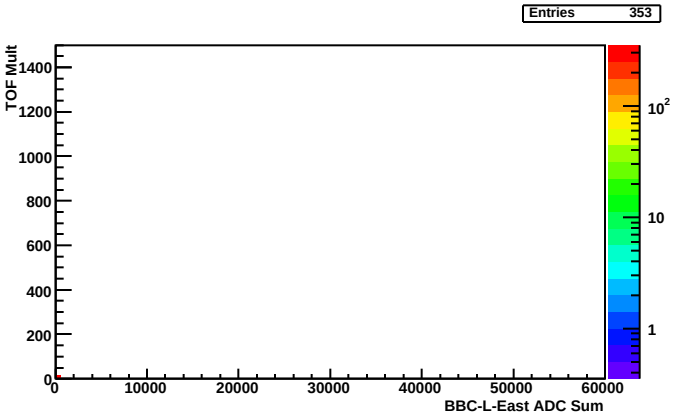

BBQ-VP002 (LO threshold)
Entries 5648
TAC
QT Input Channel

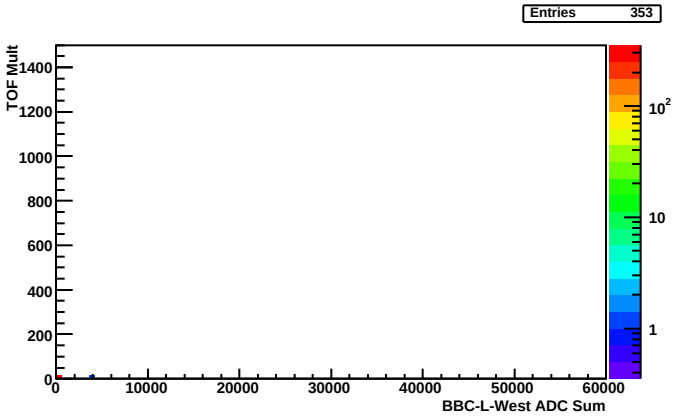

BBQ-VP003 (HI threshold)
Entries 5648
ADC
QT Input Channel

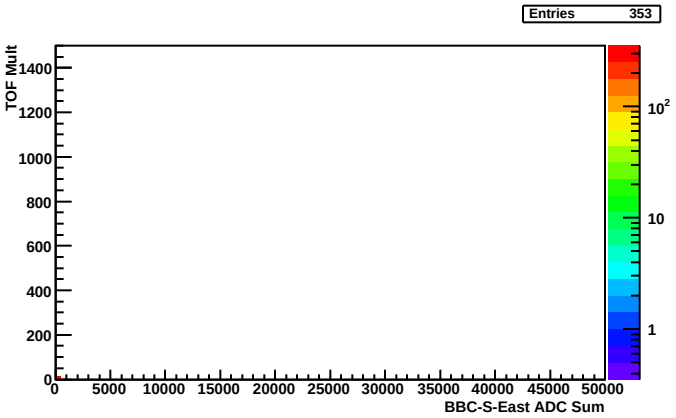

BBQ-VP003 (HI threshold)
Entries 5648
TAC
QT Input Channel

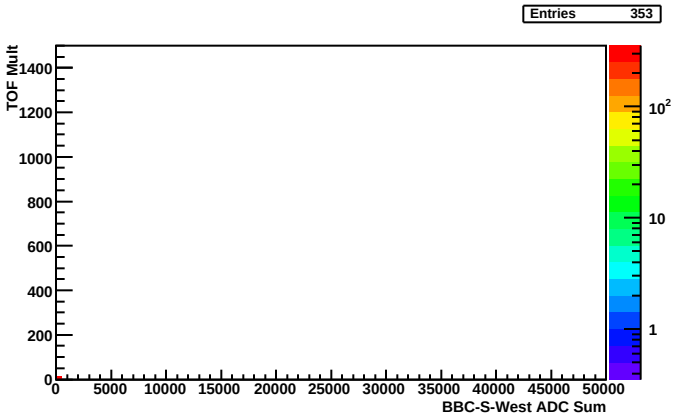

BBQ-VP004 (HI threshold)
Entries 5648
ADC
QT Input Channel

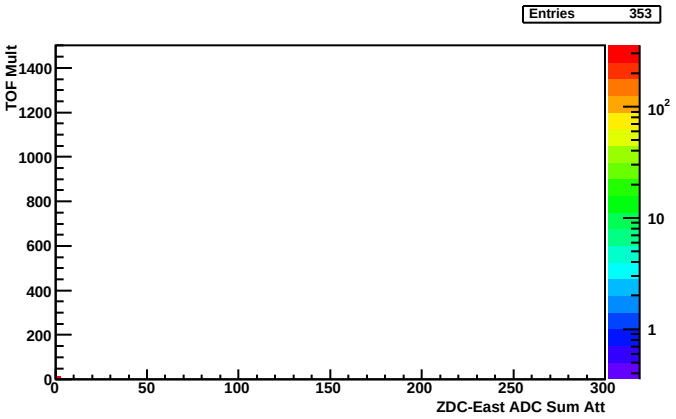

BBQ-VP004 (HI threshold)
Entries 5648
TAC
QT Input Channel

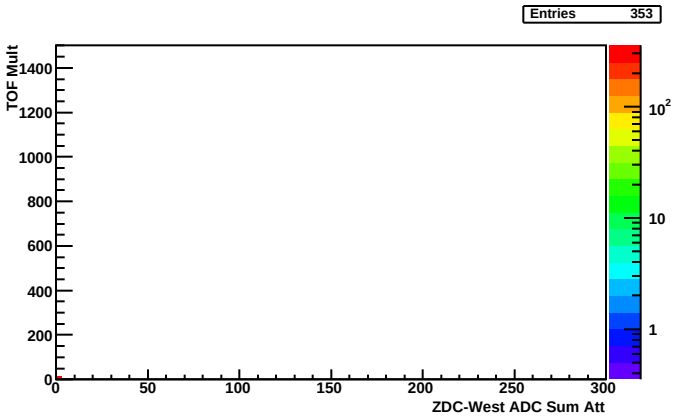

Entries 353
TOF Mult
BBC-L-East ADC Sum

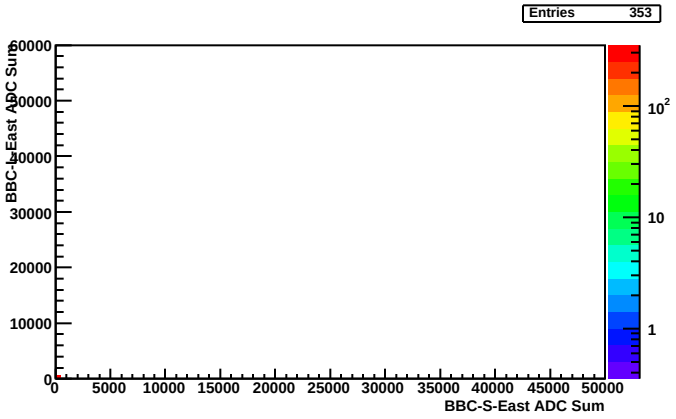

Entries 353
TOF Mult
BBC-L-West ADC Sum

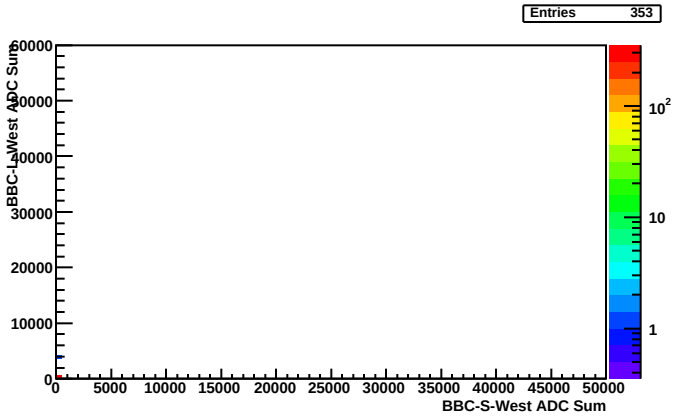

Entries 353
TOF Mult
BBC-S-East ADC Sum

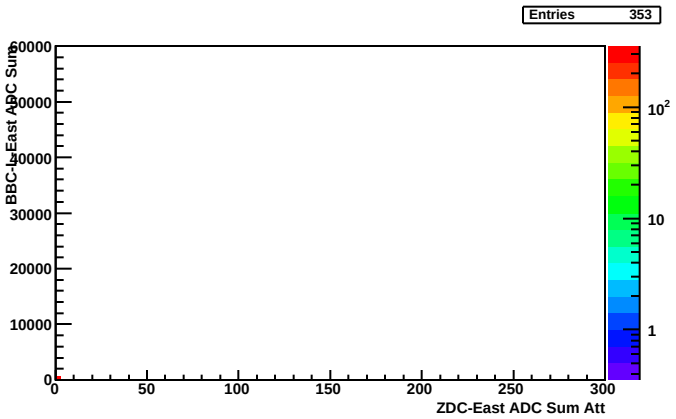

Entries 353
TOF Mult
BBC-S-West ADC Sum

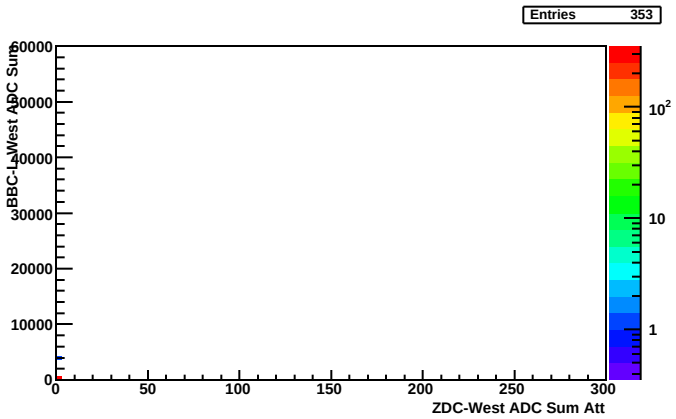

Entries 353
TOF Mult
ZDC-East ADC Sum Att

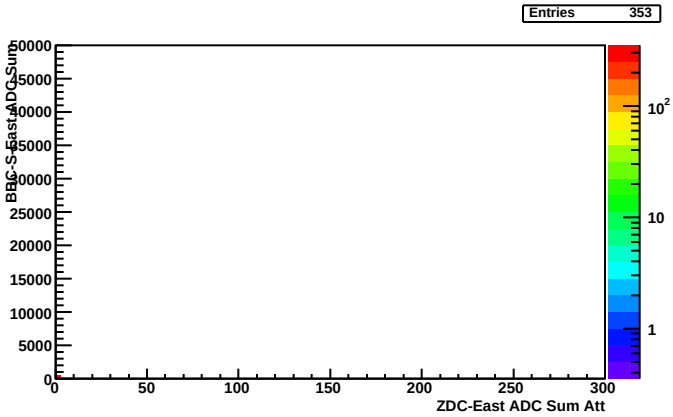

Entries 353
TOF Mult
ZDC-West ADC Sum Att

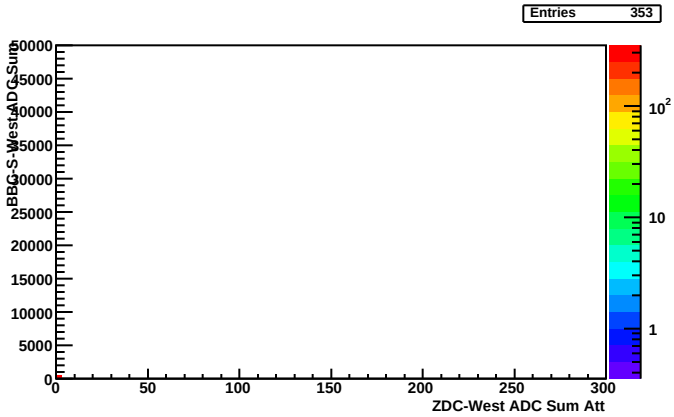

Entries 353
BBC-L-East ADC Sum
BBC-S-East ADC Sum

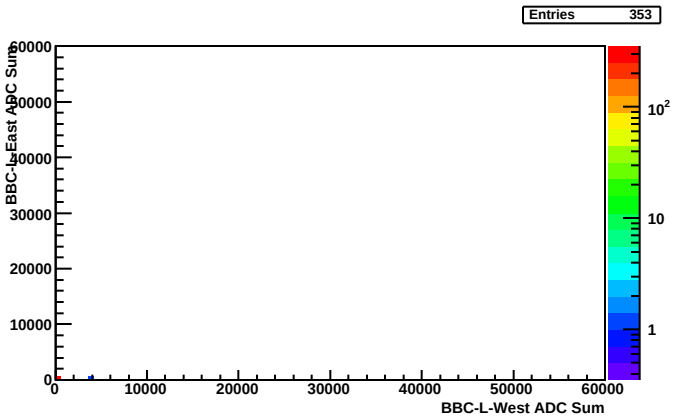

Entries 353
BBC-L-West ADC Sum
BBC-S-West ADC Sum

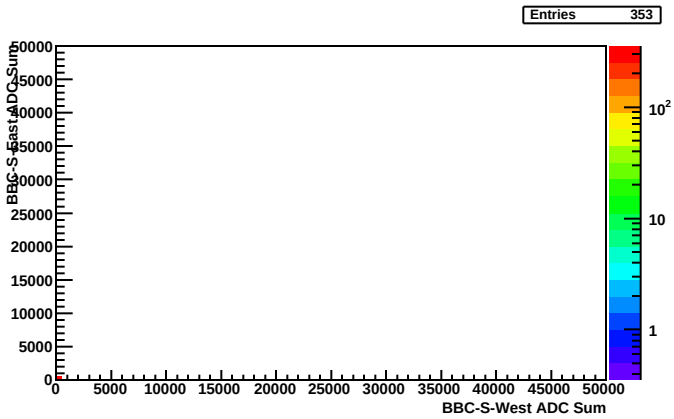

Entries 353
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

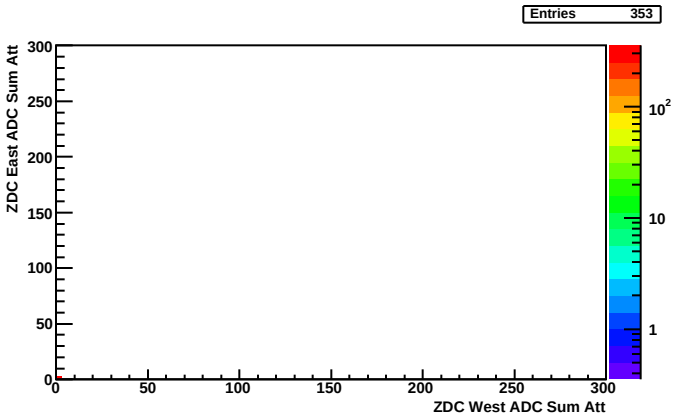

Entries 353
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

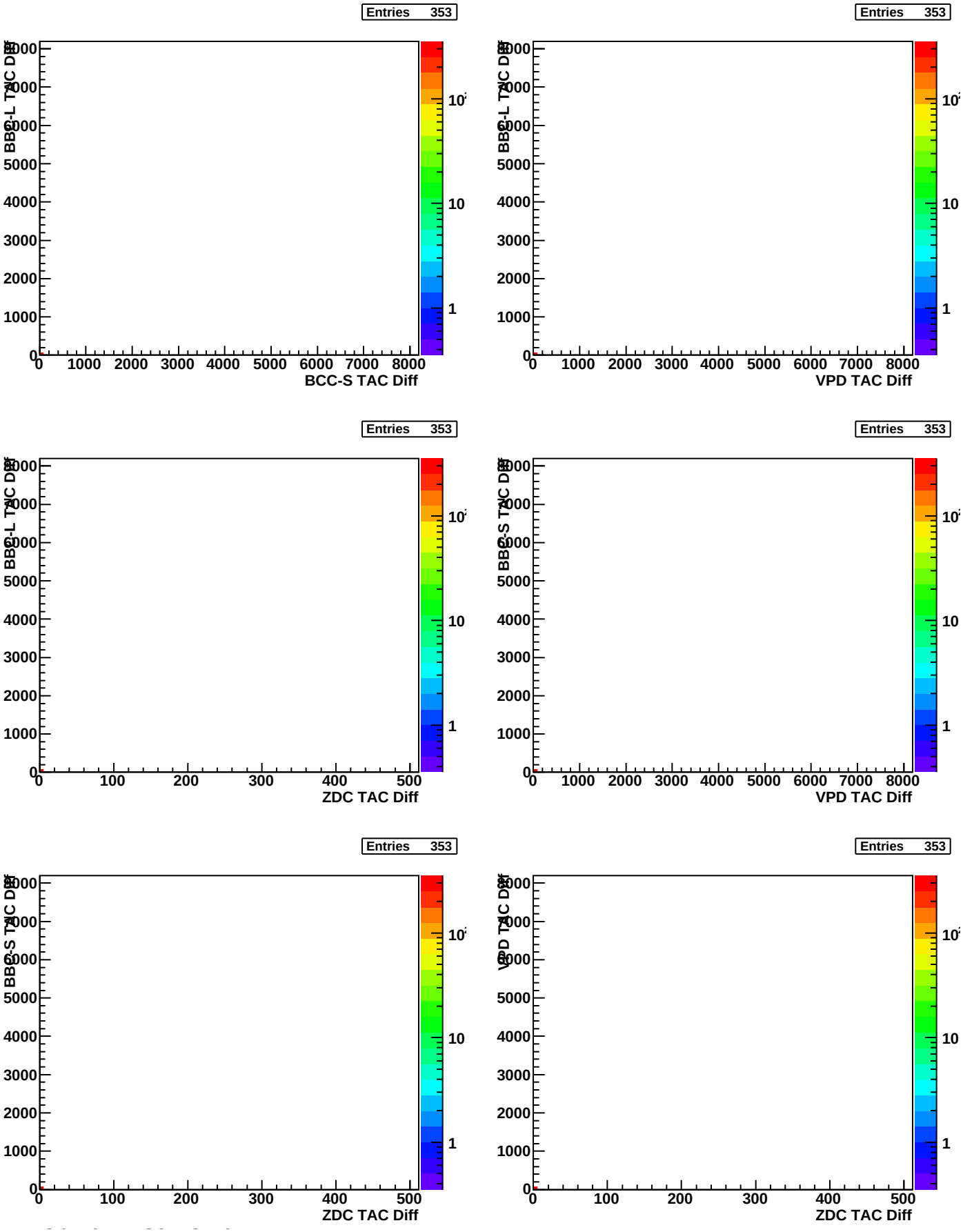

Entries 353
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

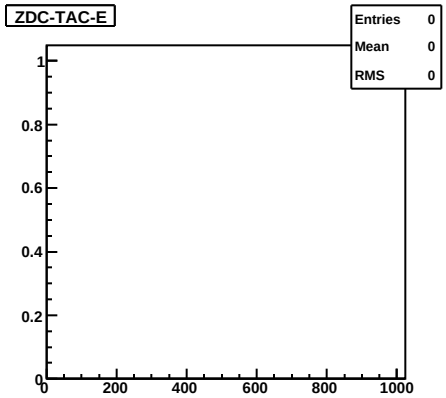

Entries 353
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

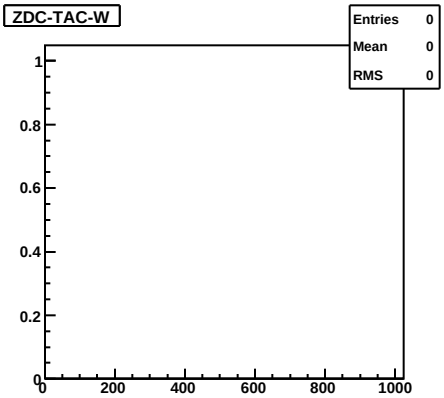

Entries 353
BBC-L-East ADC Sum
BBC-L-West ADC Sum

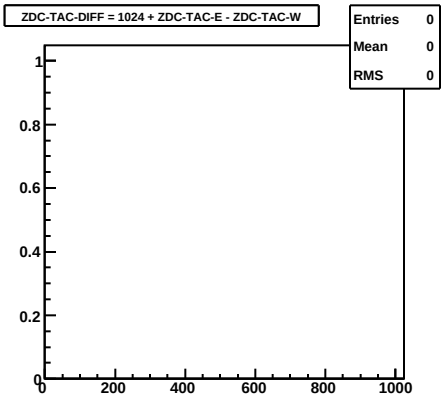

Entries 353
BBC-S-East ADC Sum
BBC-S-West ADC Sum

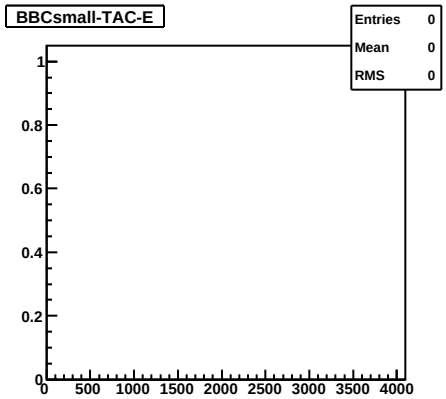

Entries 353
ZDC East ADC Sum Att
ZDC West ADC Sum Att

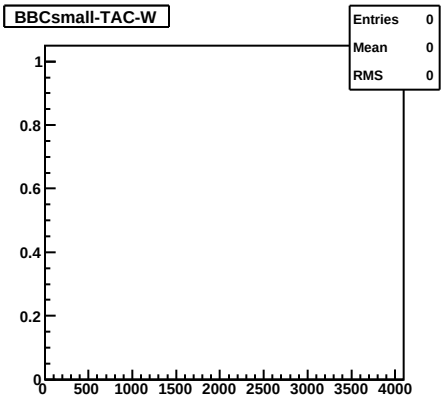

Entries 353
BBC-L TAC Diff
BCC-S TAC Diff
Entries 353
BBC-L TAC Diff
VPD TAC Diff
Entries 353
BBC-L TAC Diff
ZDC TAC Diff
Entries 353
BBC-S TAC Diff
VPD TAC Diff
Entries 353
BBC-S TAC Diff
ZDC TAC Diff
Entries 353
VPD TAC Diff
ZDC TAC Diff

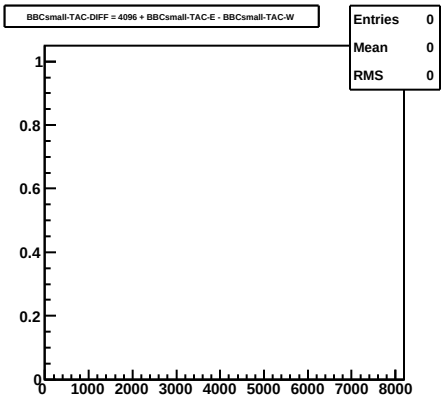

ZDC-TAC-E
Entries 0
Mean 0
RMS 0

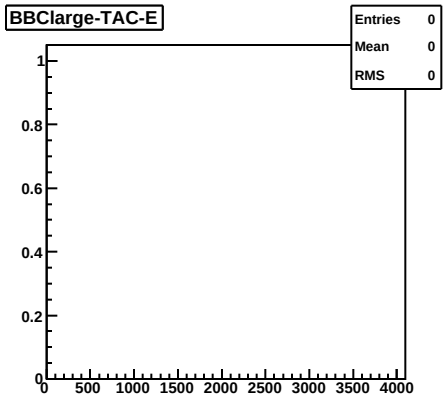

ZDC-TAC-W
Entries 0
Mean 0
RMS 0

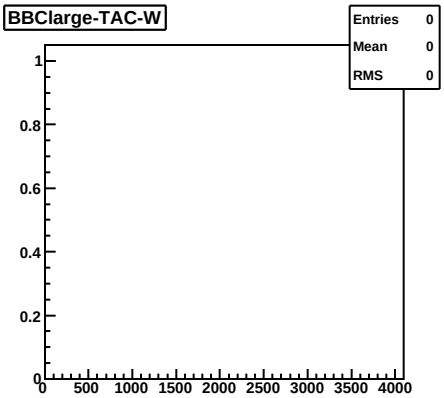

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

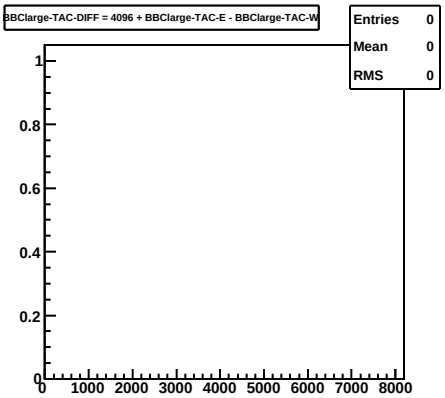

BBCsmall-TAC-E
Entries 0
Mean 0
RMS 0

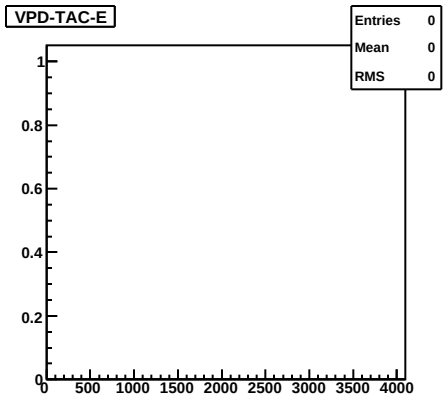

BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

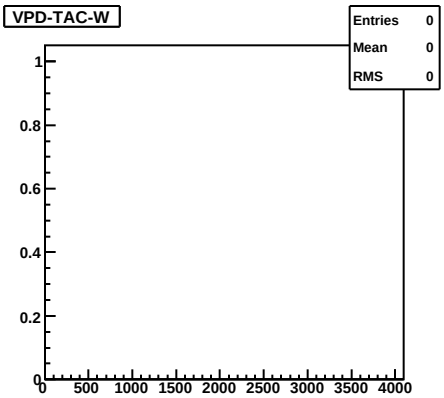

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

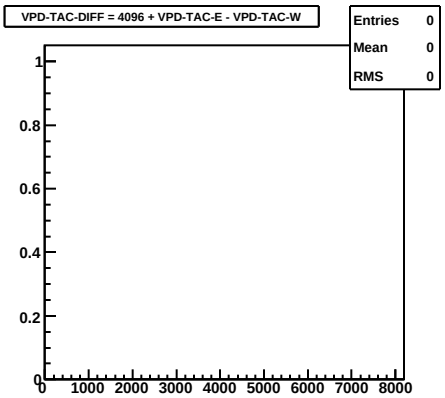

BBClarge-TAC-E
Entries 0
Mean 0
RMS 0

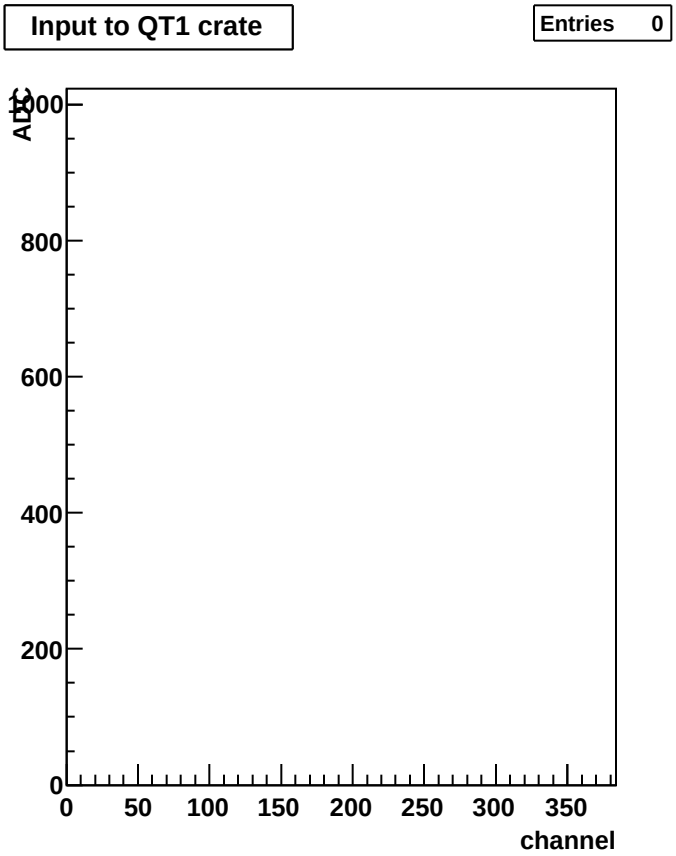

BBClarge-TAC-W
Entries 0
Mean 0
RMS 0

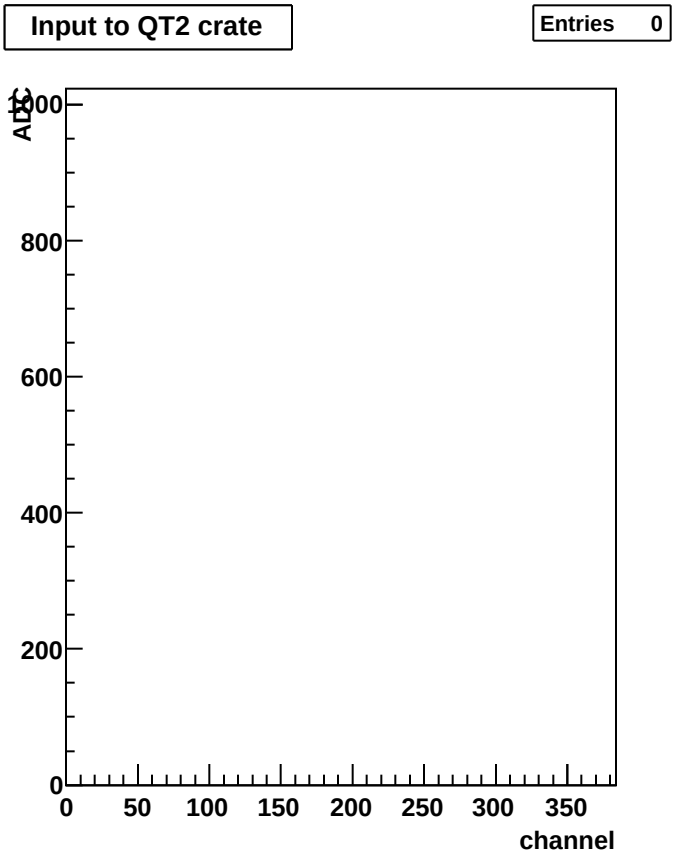

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 0
Mean 0
RMS 0

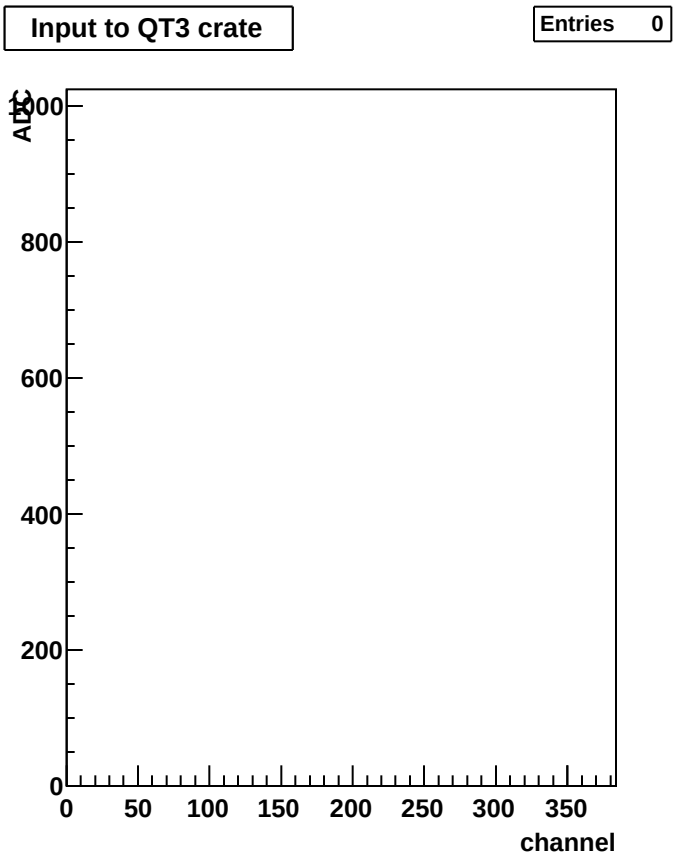

VPD-TAC-E
Entries 0
Mean 0
RMS 0

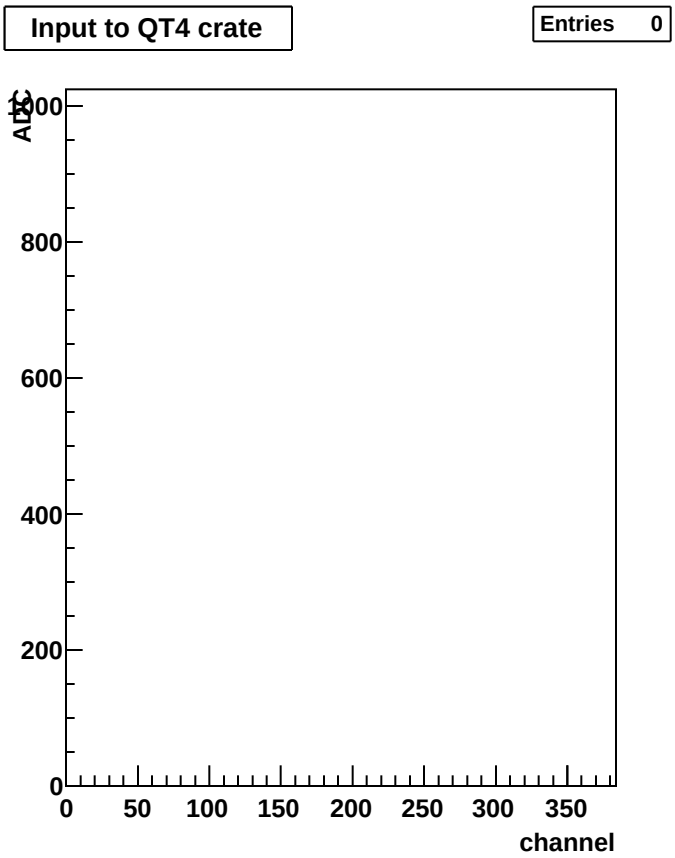

VPD-TAC-W
Entries 0
Mean 0
RMS 0

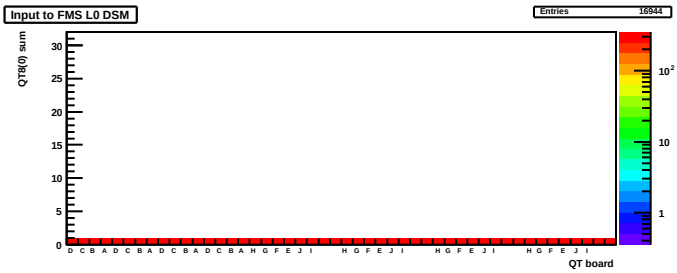

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

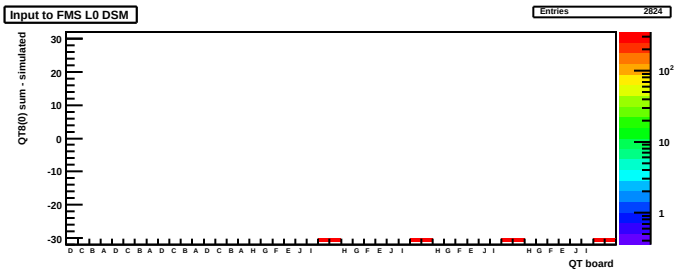

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

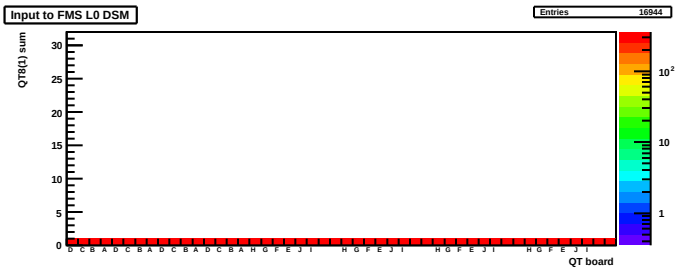

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

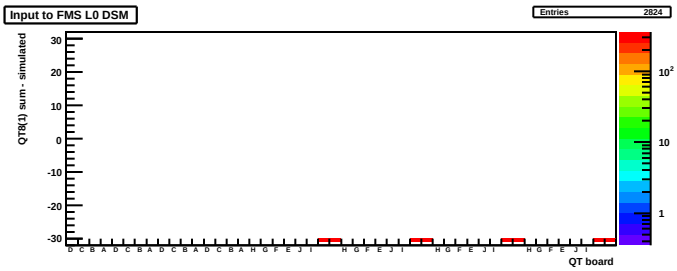

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

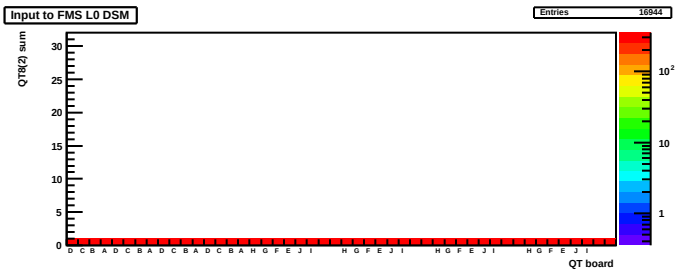

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

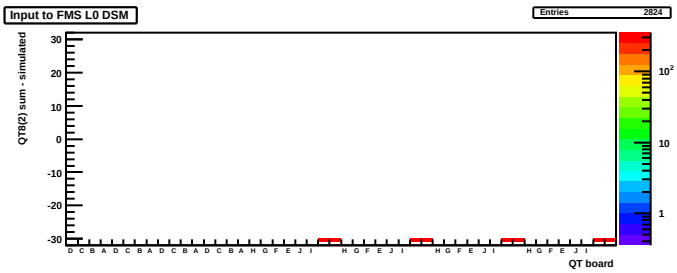

Input to FMS L0 DSM
Entries 16944
QT8(0) sum
QT board

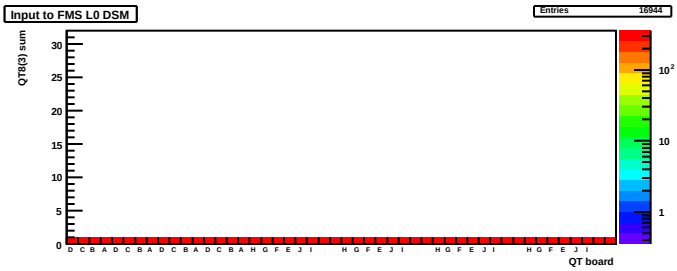

Input to FMS L0 DSM
Entries 2824
QT8(0) sum - simulated
QT board

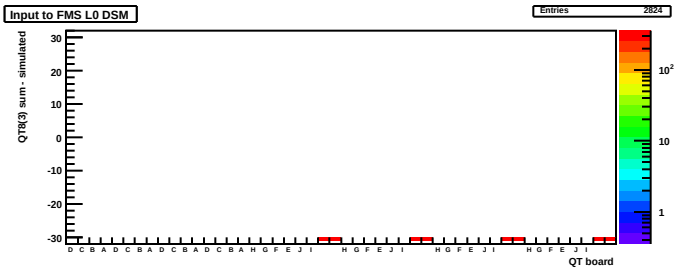

Input to FMS L0 DSM
Entries 16944
QT8(1) sum
QT board

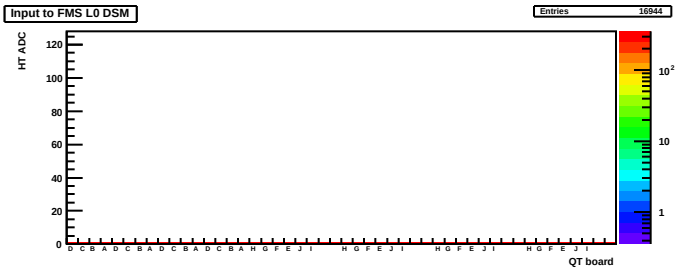

Input to FMS L0 DSM
Entries 2824
QT8(1) sum - simulated
QT board

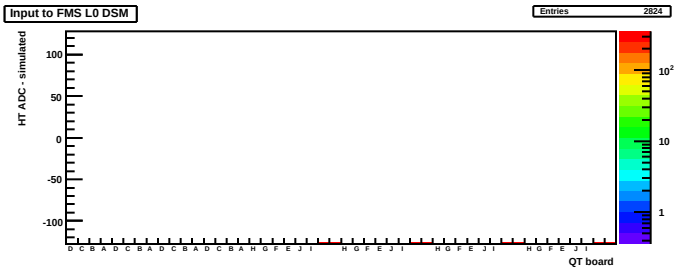

Input to FMS L0 DSM
Entries 16944
QT8(2) sum
QT board

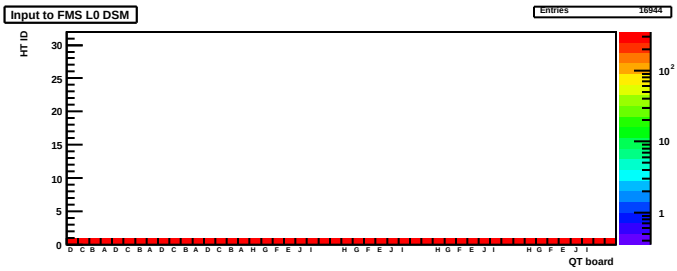

Input to FMS L0 DSM
Entries 2824
QT8(2) sum - simulated
QT board

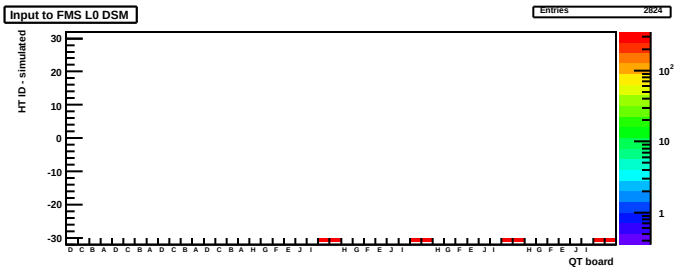

Input to FMS L0 DSM
Entries 16944
QT8(3) sum
QT board

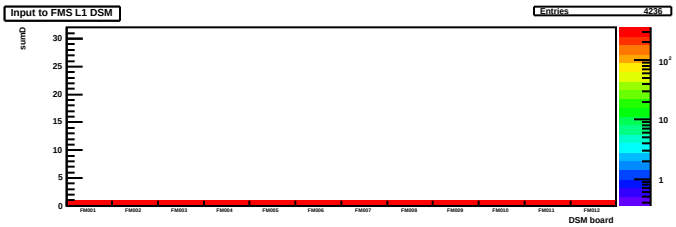

Input to FMS L0 DSM
Entries 2824
QT8(3) sum - simulated
QT board

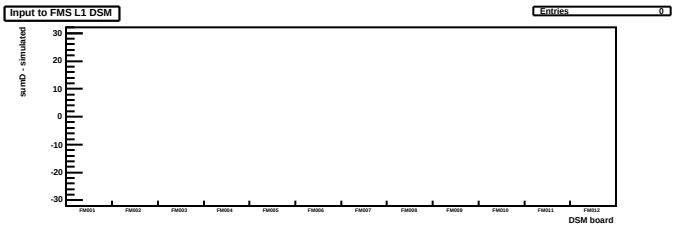

Input to FMS L0 DSM
Entries 16944
HT ADC
QT board

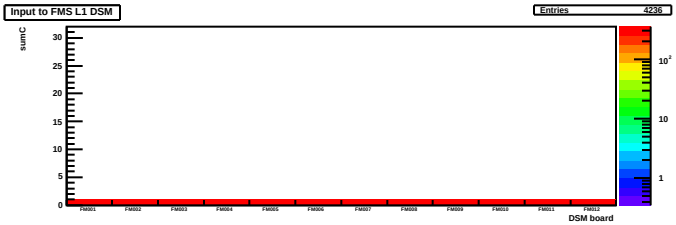

Input to FMS L0 DSM
Entries 2824
HT ADC - simulated
QT board

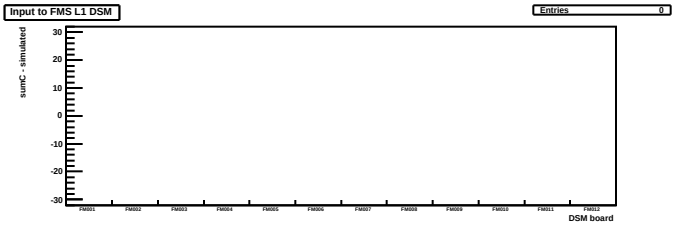

Input to FMS L0 DSM
Entries 16944
HT ID
QT board

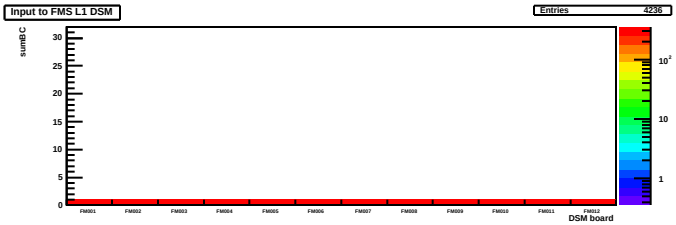

Input to FMS L0 DSM
Entries 2824
HT ID - simulated
QT board

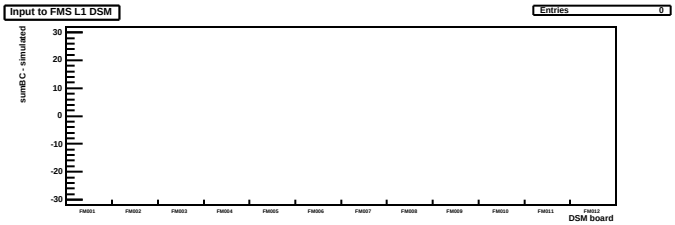

Input to FMS L1 DSM
Entries 4236
sumD
DSM board

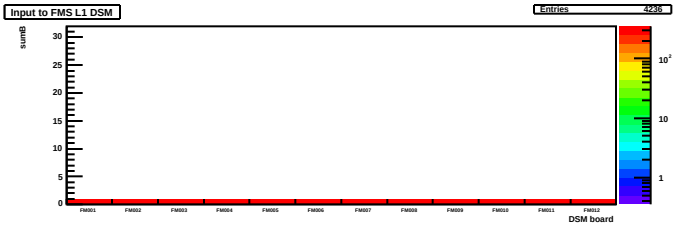

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

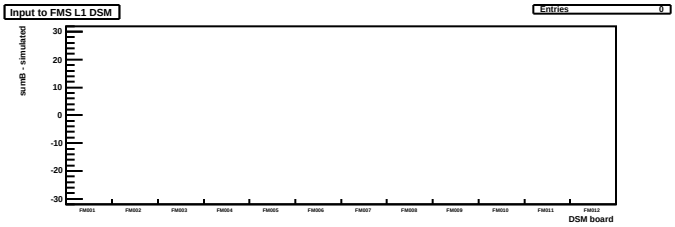

Input to FMS L1 DSM
Entries 4236
sumC
DSM board

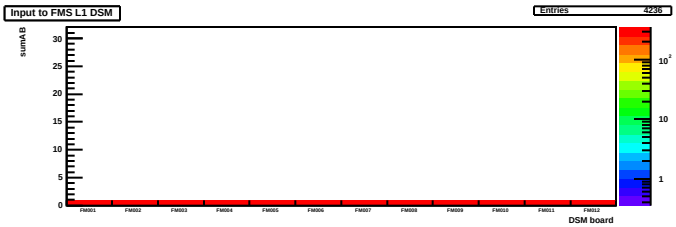

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

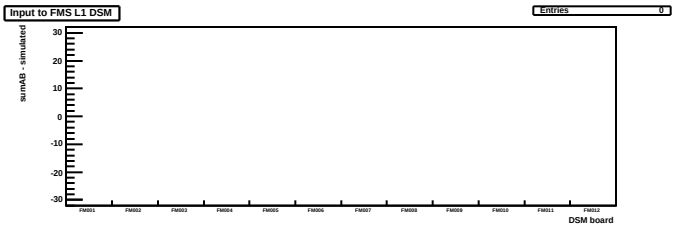

Input to FMS L1 DSM
Entries 4236
sumBC
DSM board

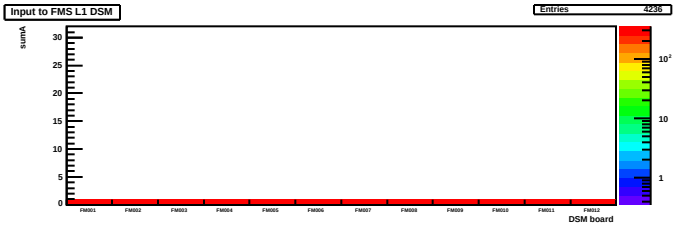

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

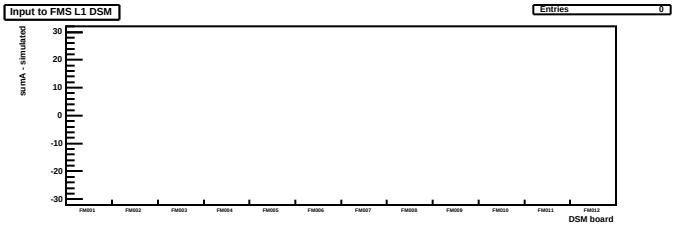

Input to FMS L1 DSM
Entries 4236
sumB
DSM board

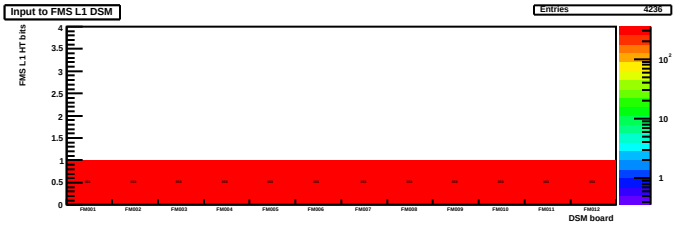

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

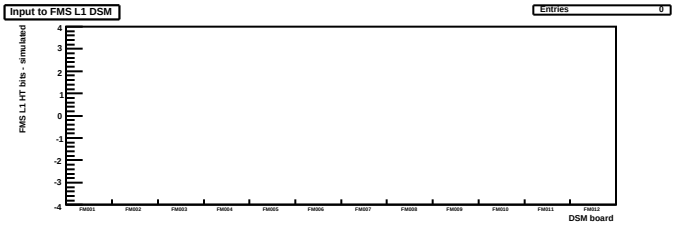

Input to FMS L1 DSM
Entries 4236
sumAB
DSM board

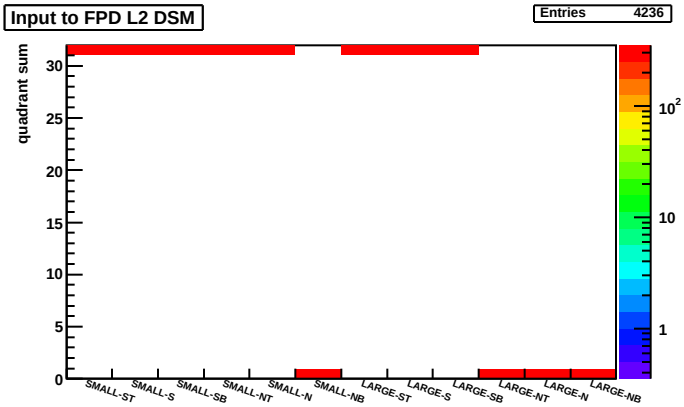

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

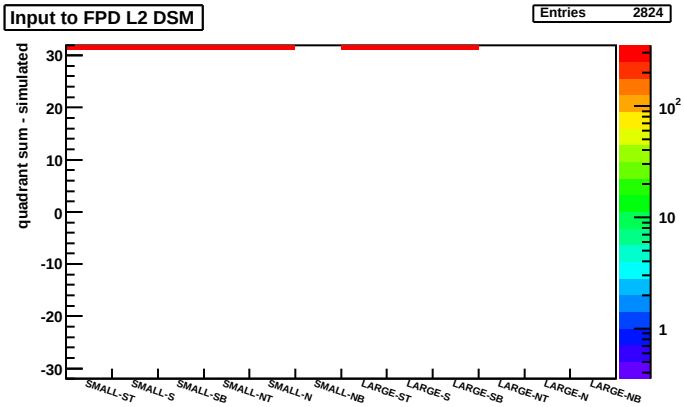

Input to FMS L1 DSM
Entries 4236
sumA
DSM board

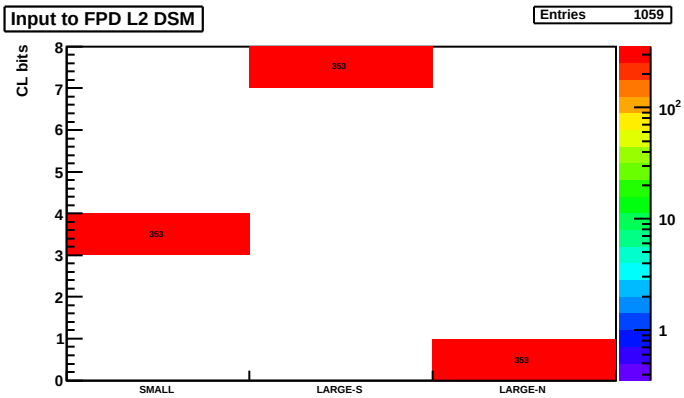

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

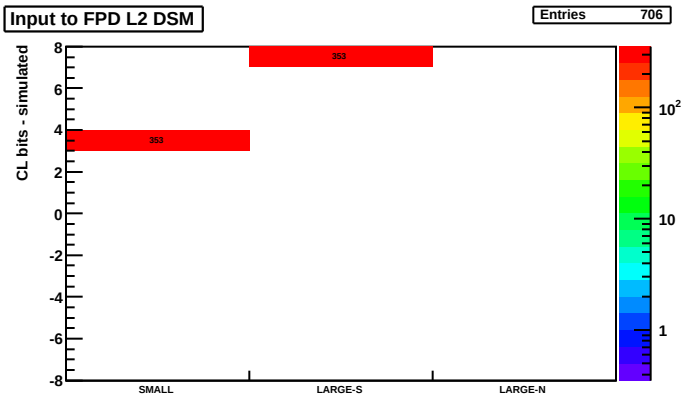

Input to FMS L1 DSM
Entries 4236
FMS L1 HT bits
DSM board

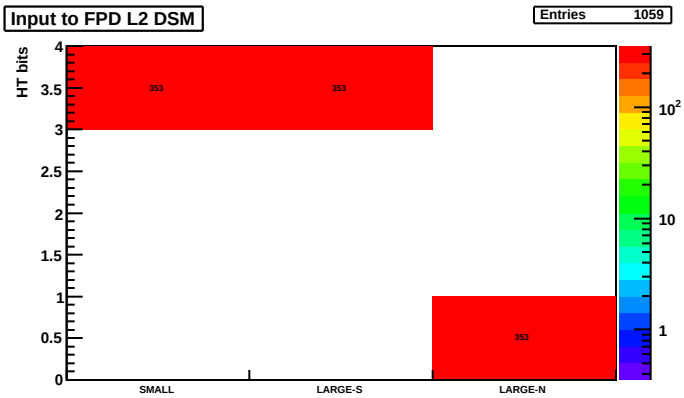

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

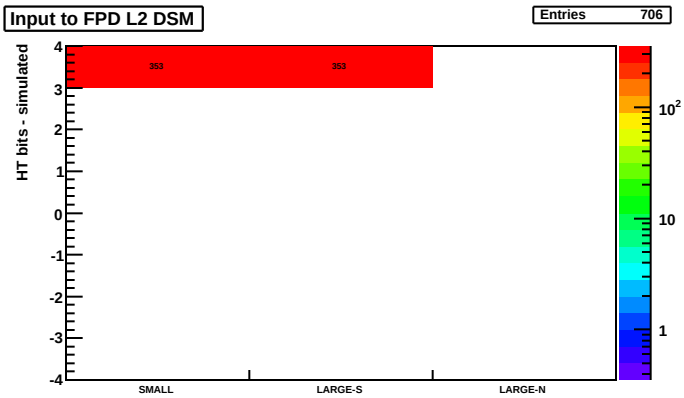

Input to FPD L2 DSM
Entries 4236
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

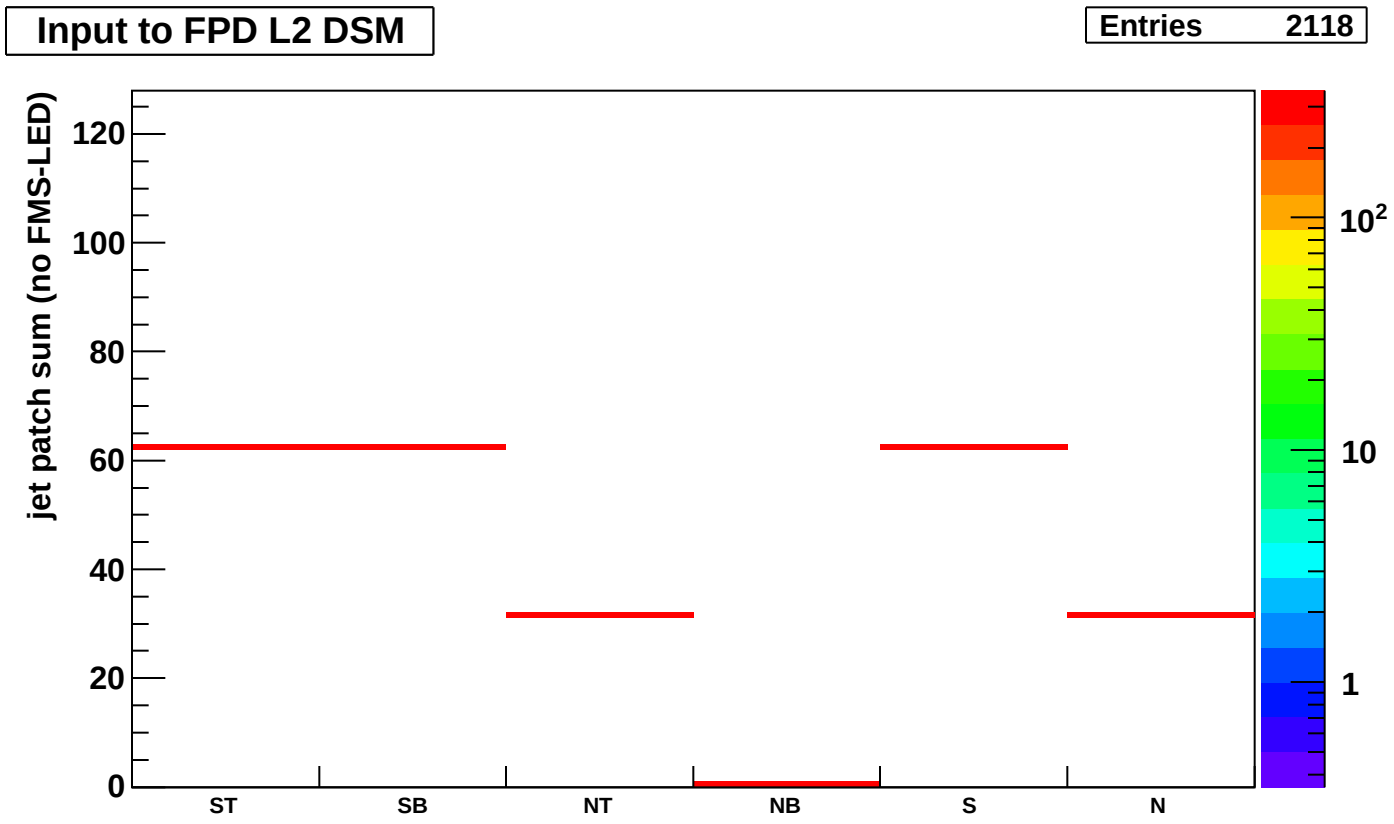

Input to FPD L2 DSM
Entries 2824
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

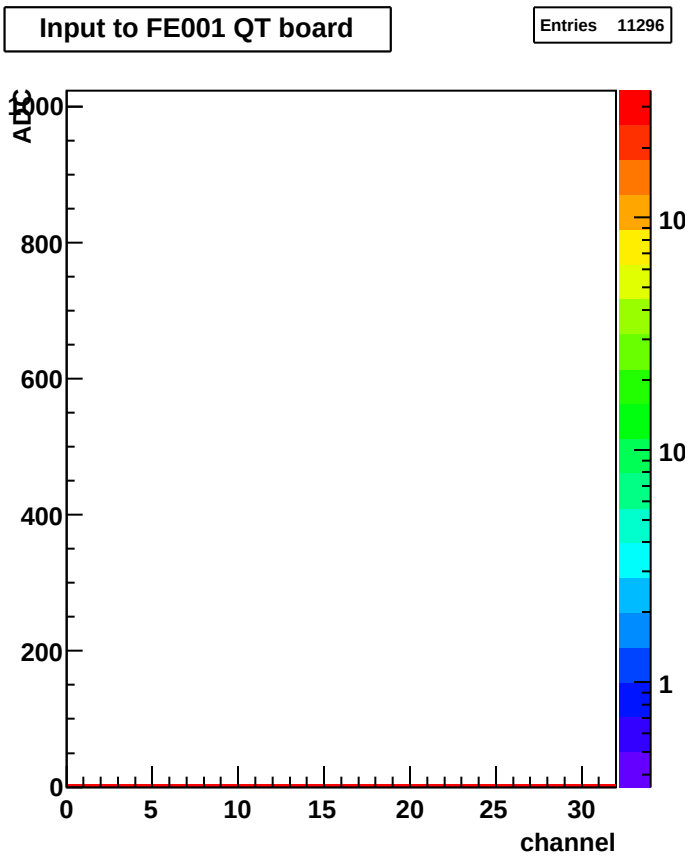

Input to FPD L2 DSM
Entries 1059
CL bits
353
353
353
SMALL
LARGE-S
LARGE-N

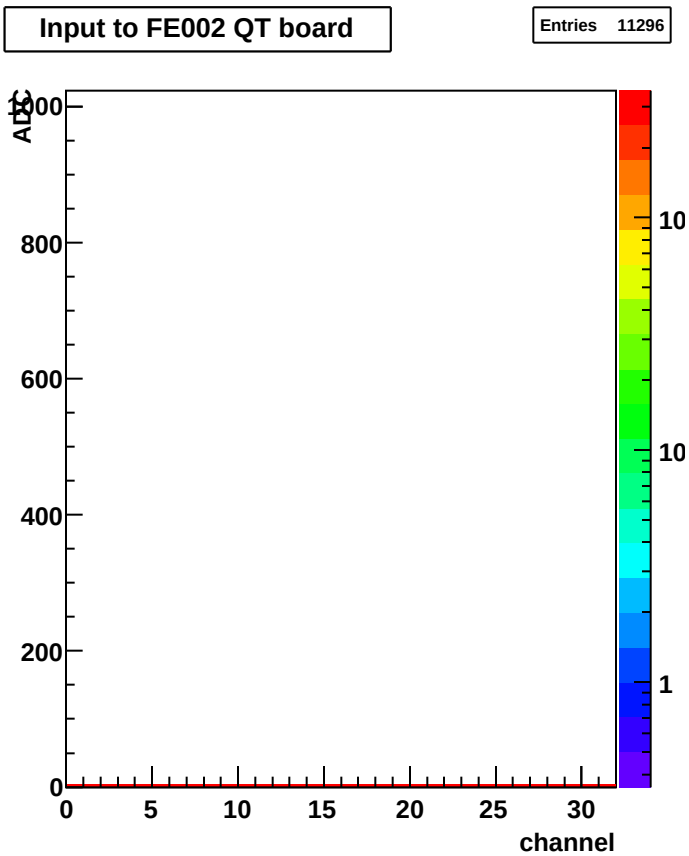

Input to FPD L2 DSM
Entries 706
CL bits - simulated
353
353
SMALL
LARGE-S
LARGE-N

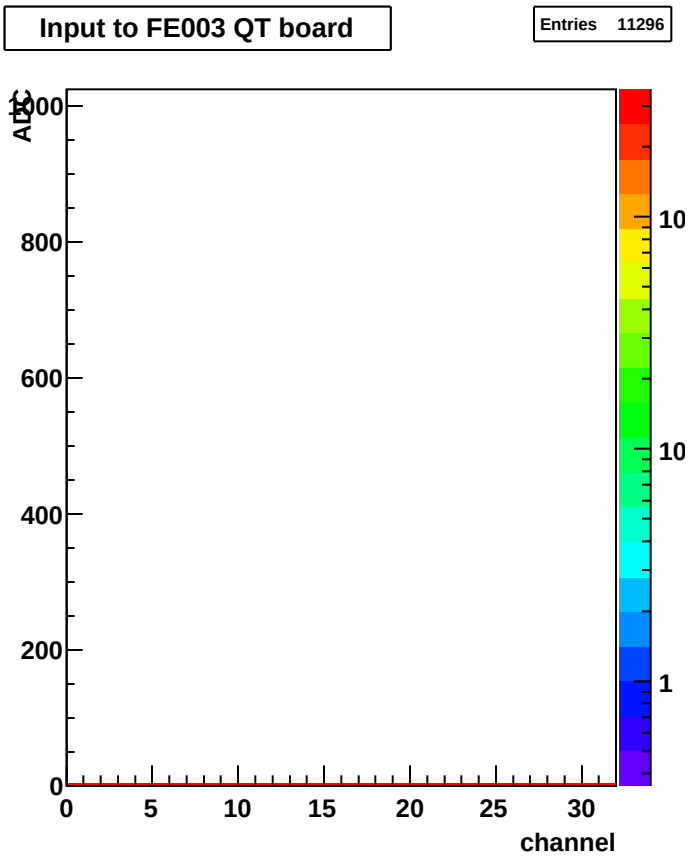

Input to FPD L2 DSM
Entries 1059
HT bits
353
353
353
SMALL
LARGE-S
LARGE-N

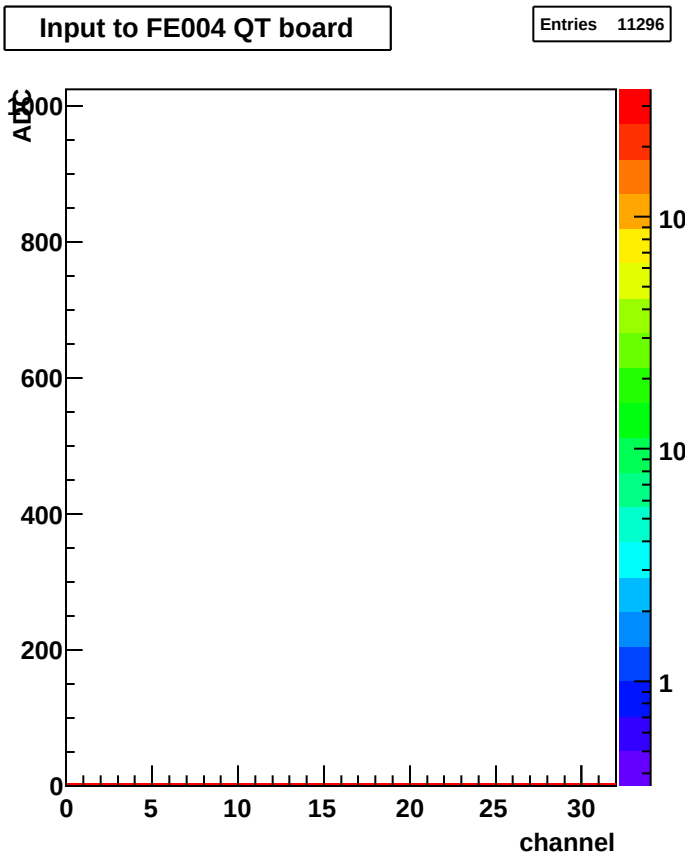

Input to FPD L2 DSM
Entries 706
HT bits - simulated
353
353
SMALL
LARGE-S
LARGE-N

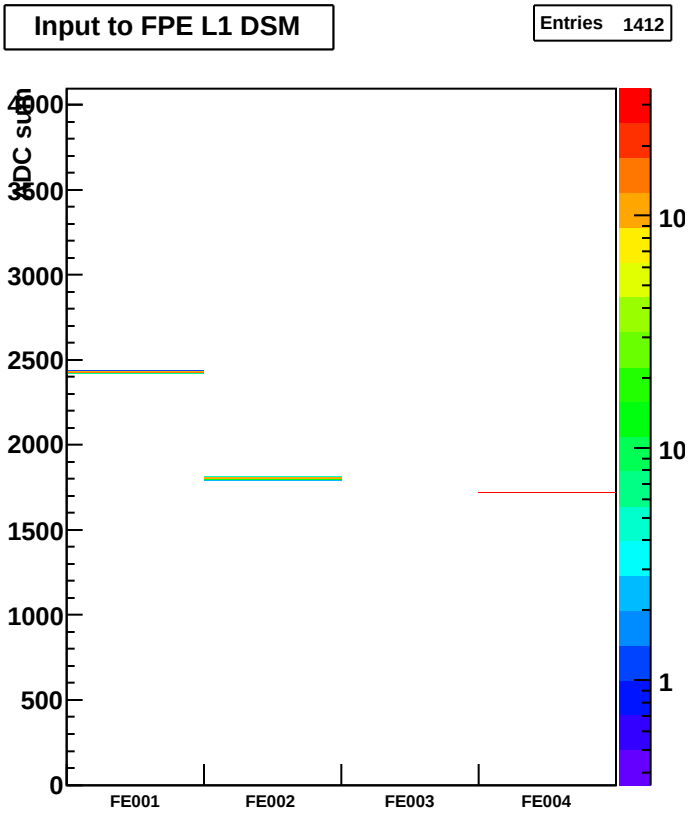

Input to FPD L2 DSM
Entries 2118
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
S
N
10^2
10
1


Input to FE001 QT board
Entries 11296
ADC
channel

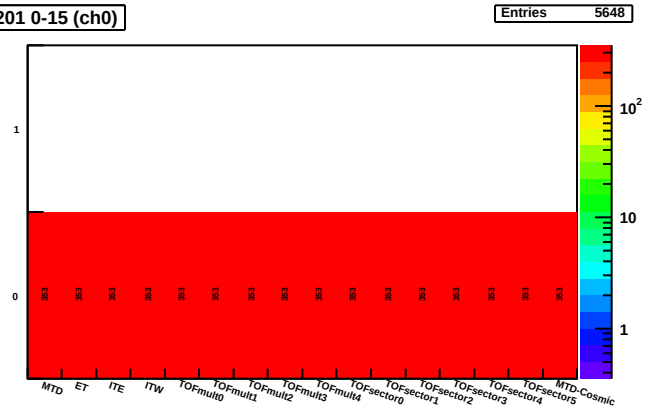

Input to FE002 QT board
Entries 11296
ADC
channel


Input to FE003 QT board
Entries 11296
ADC
channel

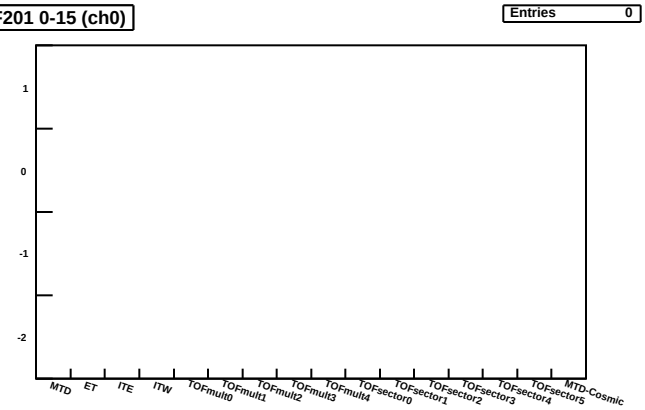

Input to FE004 QT board
Entries 11296
ADC
channel


Input to FPE L1 DSM
Entries 1412
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
1

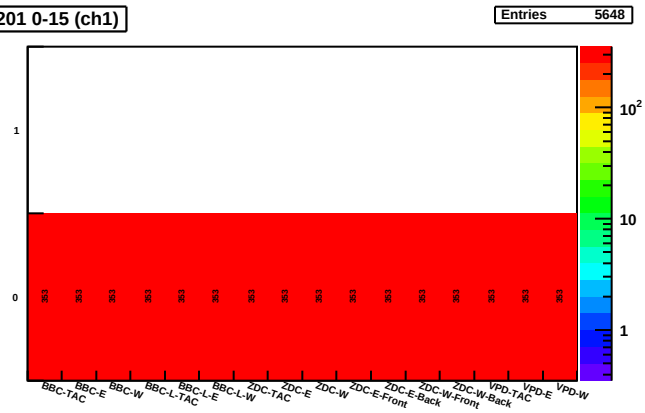
TF201 0-15 (ch0)



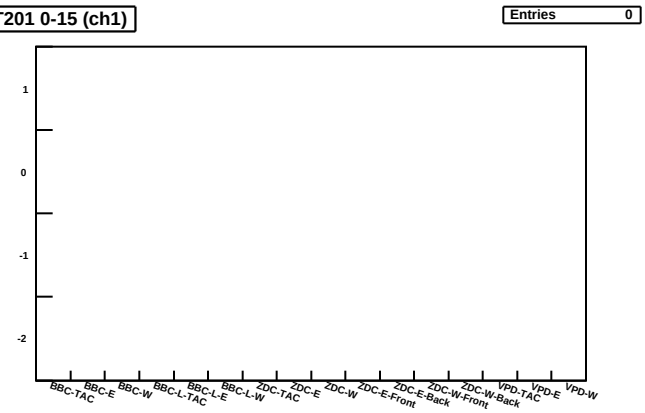
TF201 0-15 (ch0)



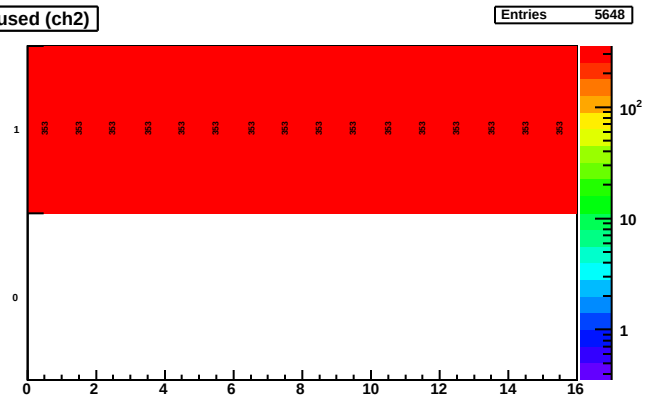
VT201 0-15 (ch1)

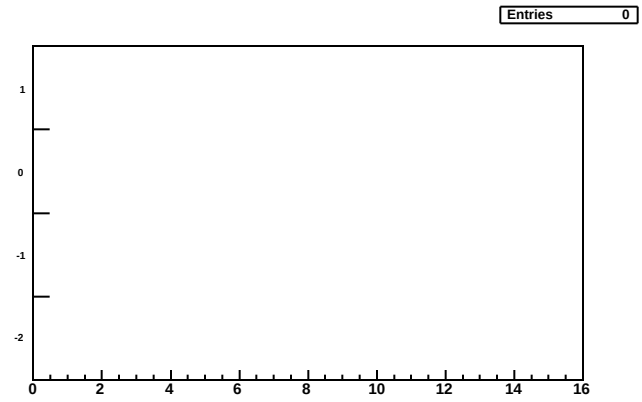


VT201 0-15 (ch1)

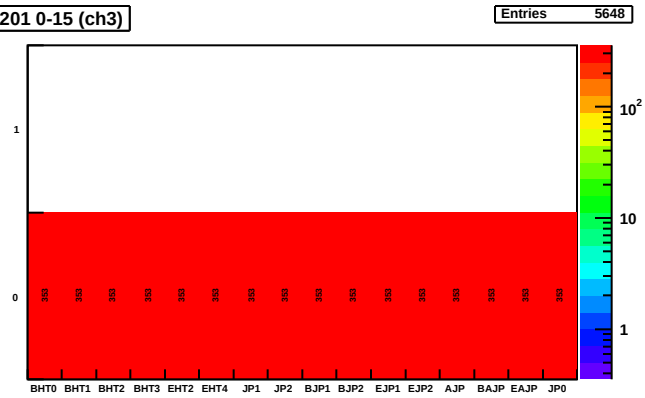


Unused (ch2)

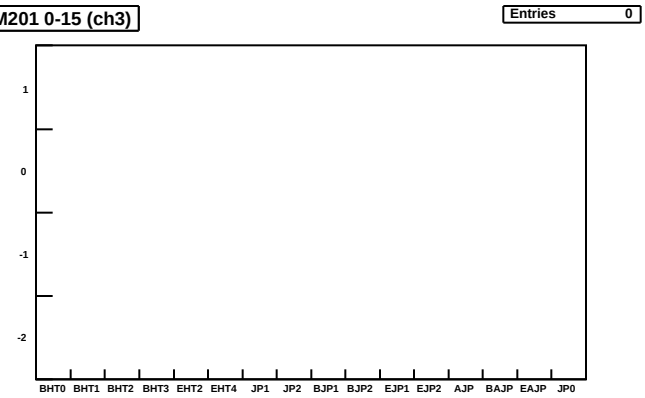




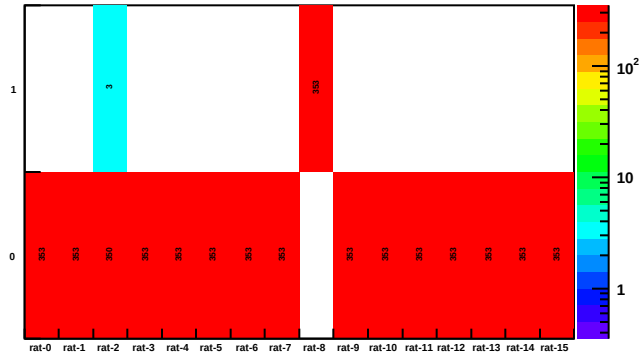
EM201 0-15 (ch3)

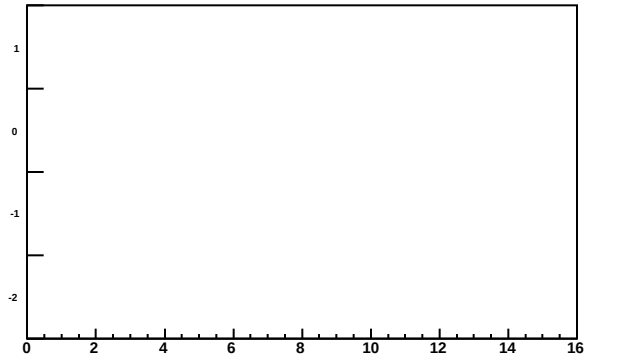


EM201 0-15 (ch3)

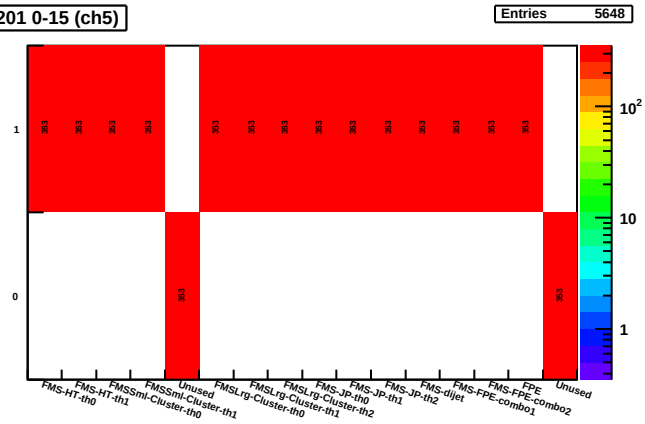


RAT board (ch4)

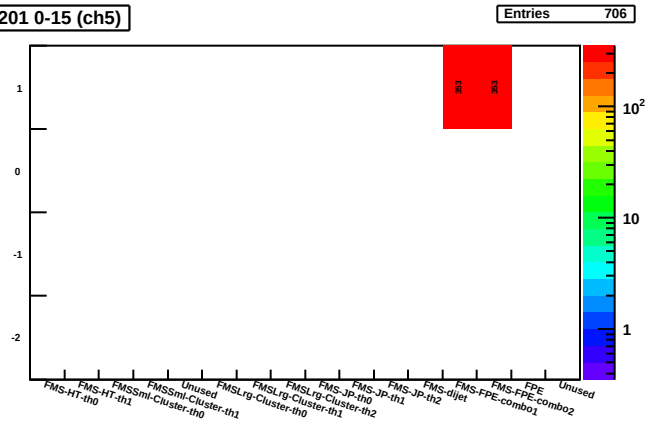




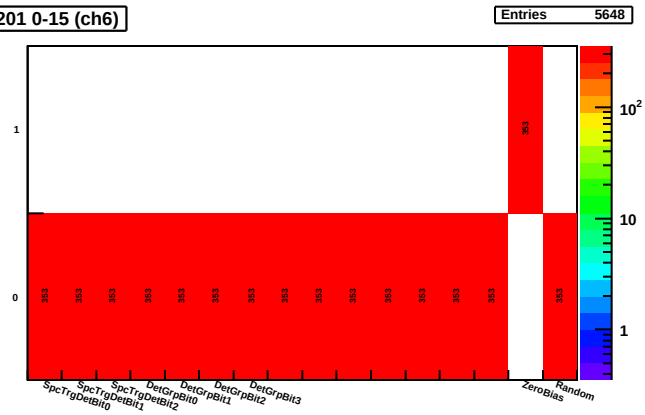
FP201 0-15 (ch5)

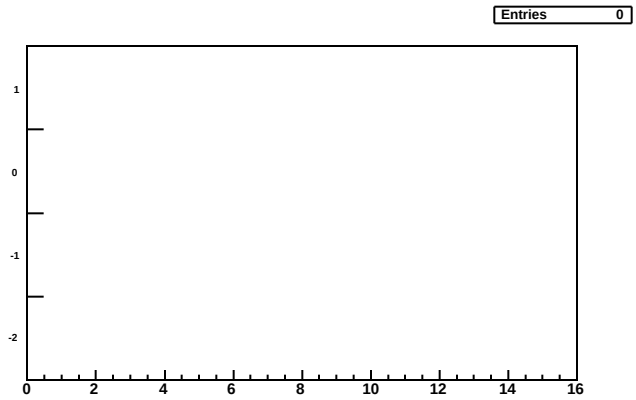
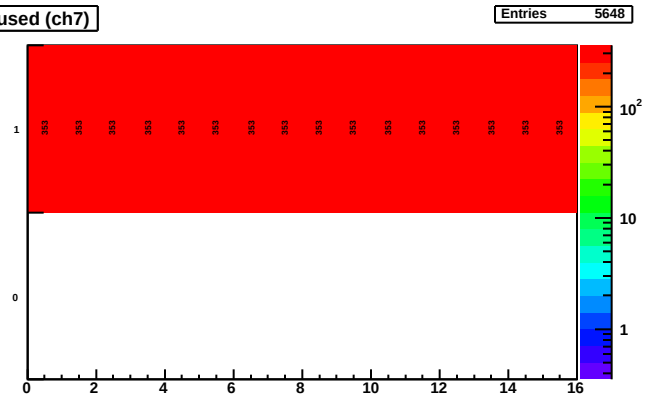
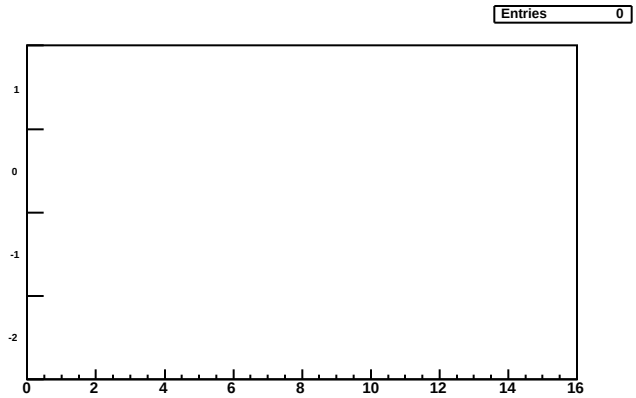


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)