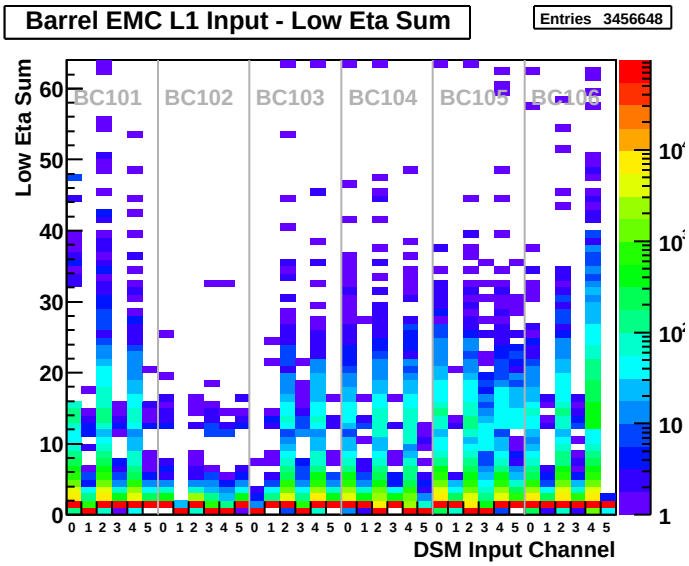
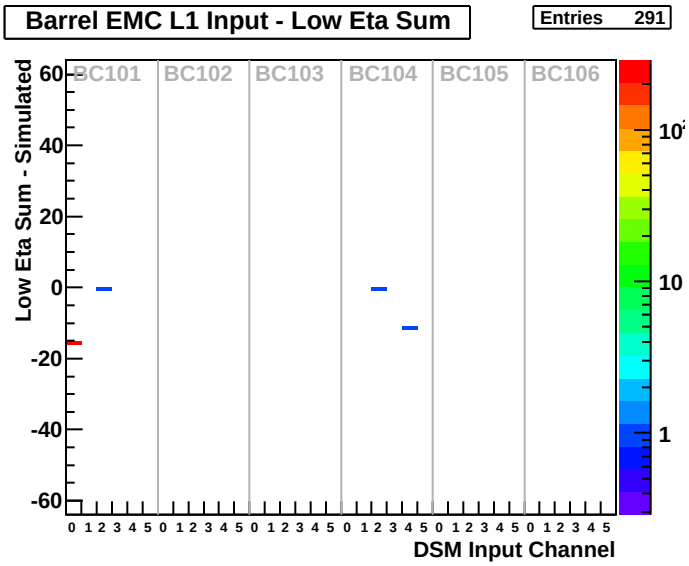
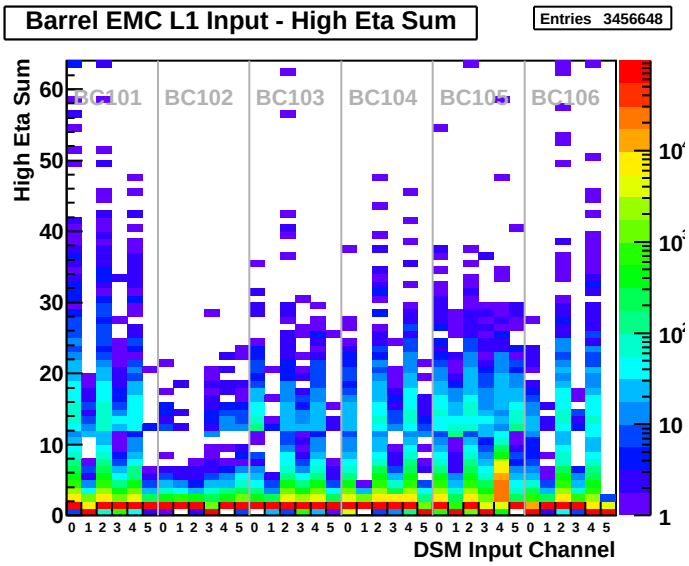
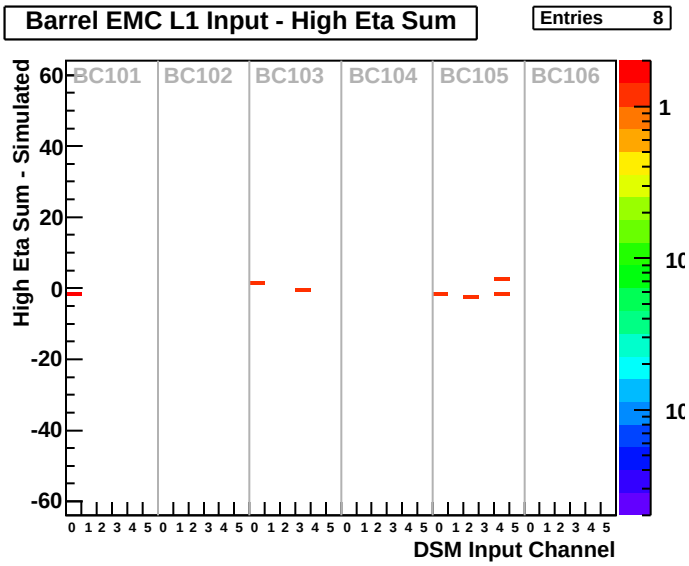
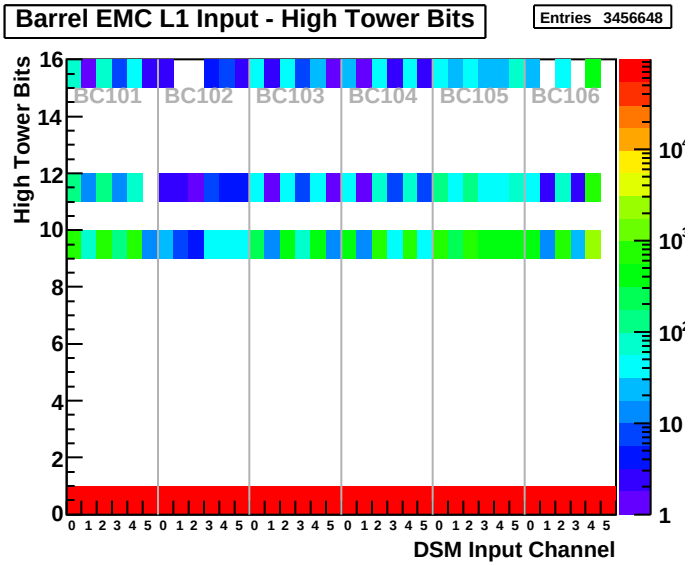
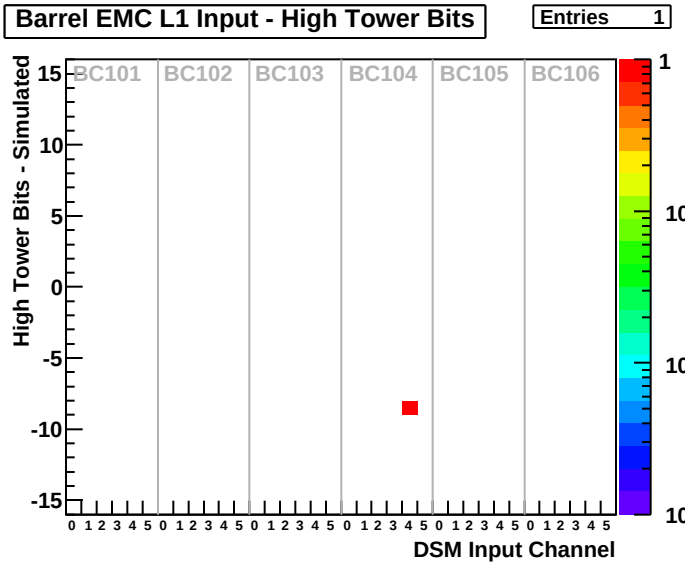

Barrel EMC L1 Input - Low Eta Sum
Entries 3456648
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

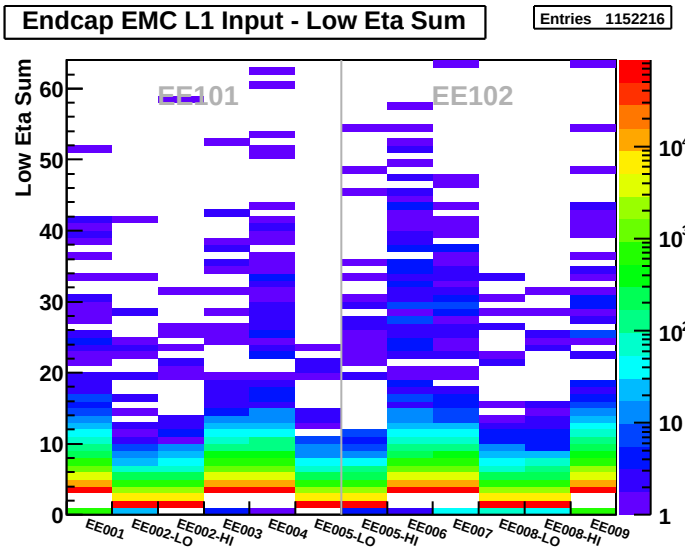

Barrel EMC L1 Input - Low Eta Sum
Entries 291
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

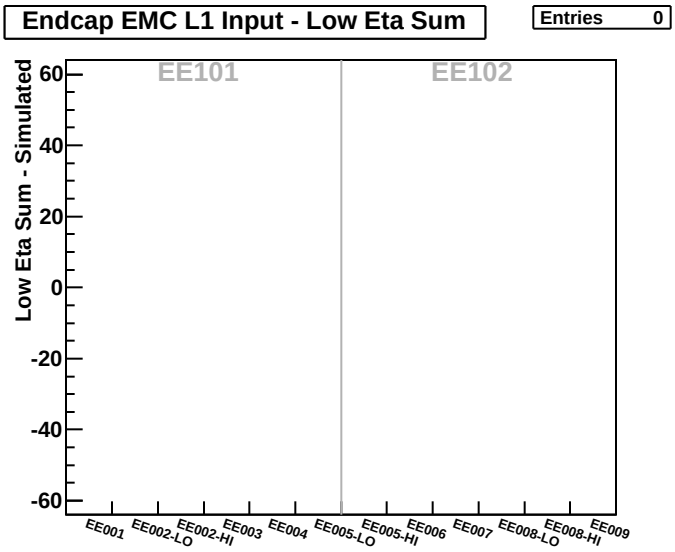

Barrel EMC L1 Input - High Eta Sum
Entries 3456648
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

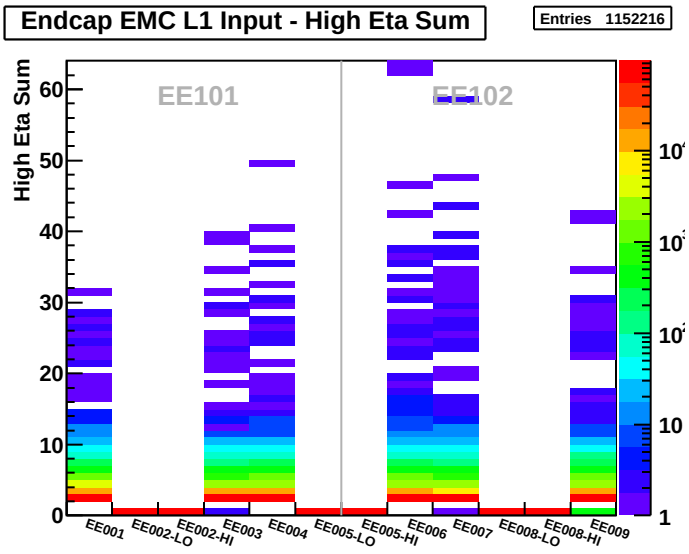

Barrel EMC L1 Input - High Eta Sum
Entries 8
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

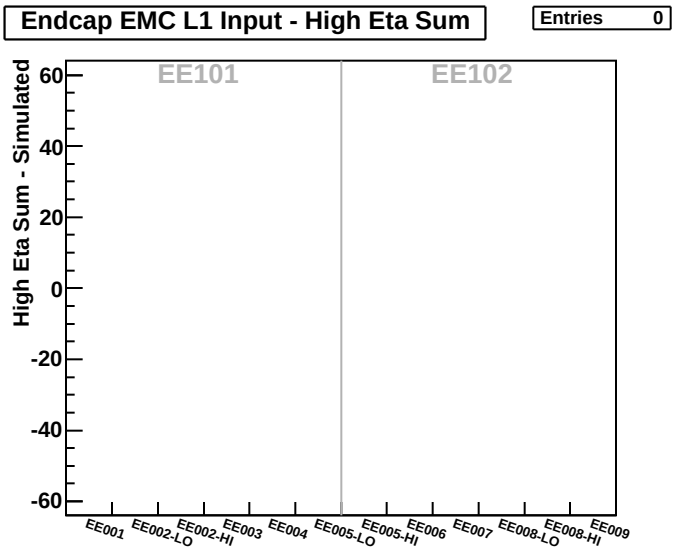

Barrel EMC L1 Input - High Tower Bits
Entries 3456648
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

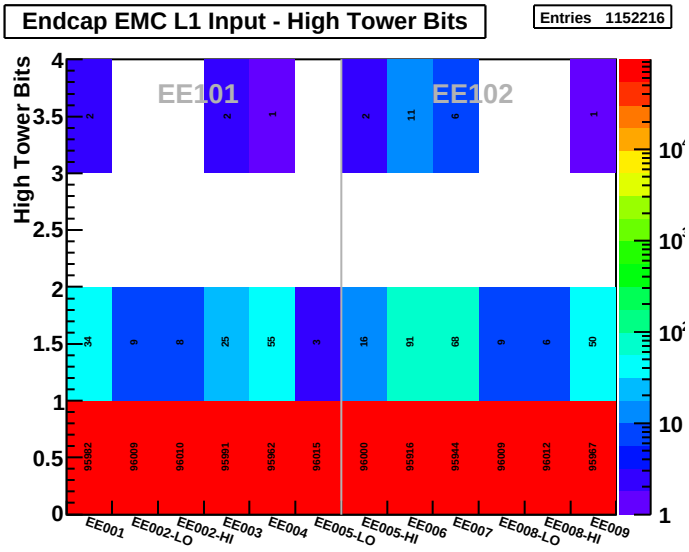

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

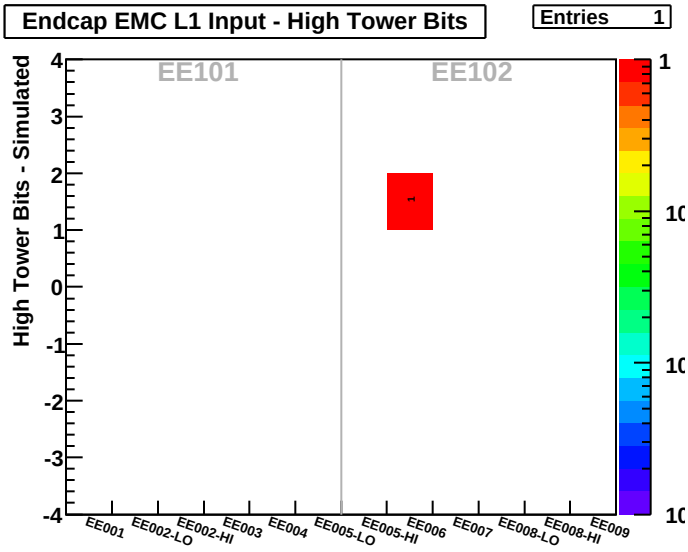

Endcap EMC L1 Input - Low Eta Sum
Entries 1152216
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

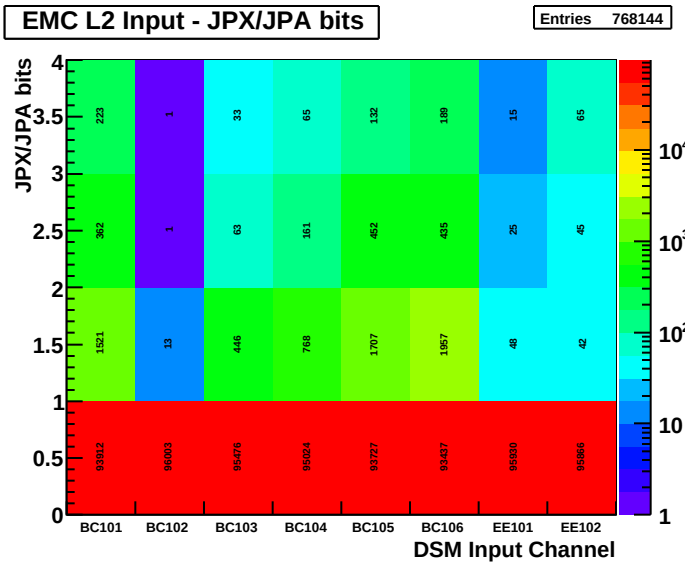

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

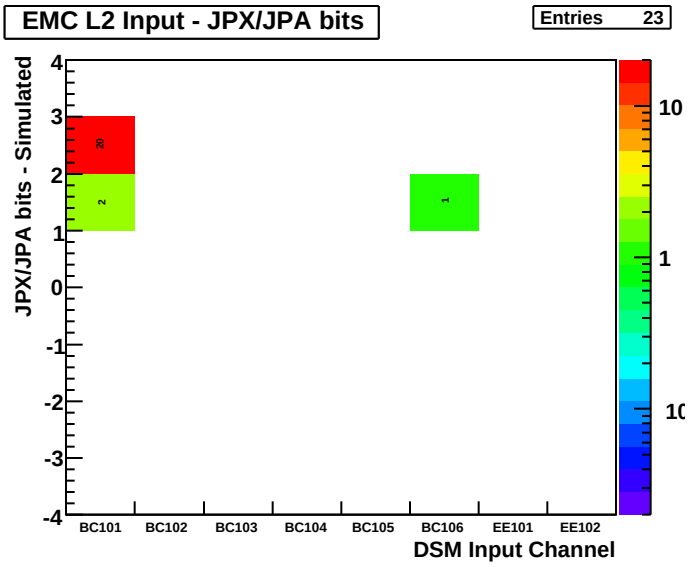

Endcap EMC L1 Input - High Eta Sum
Entries 1152216
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

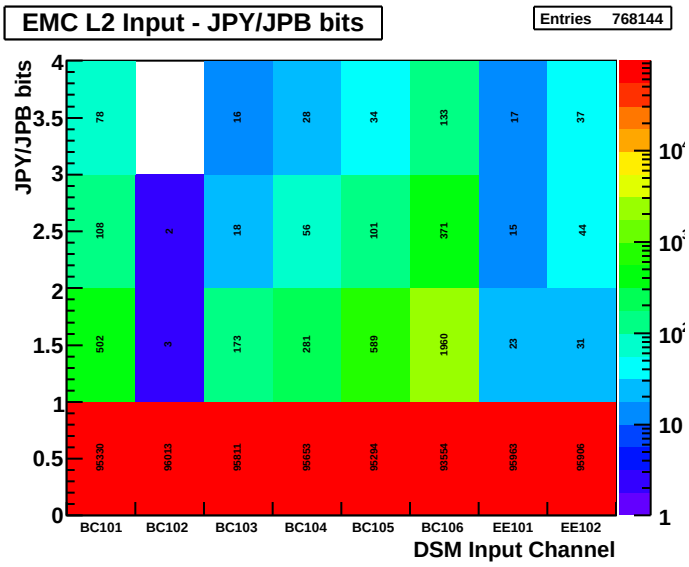

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

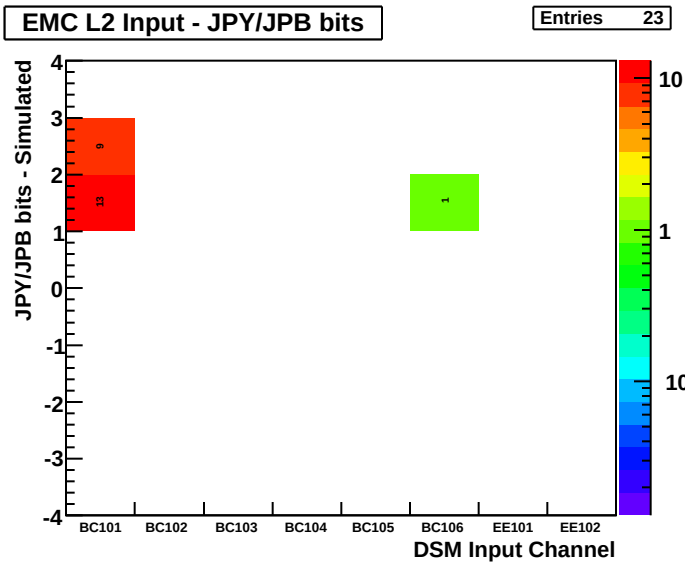

Endcap EMC L1 Input - High Tower Bits
Entries 1152216
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

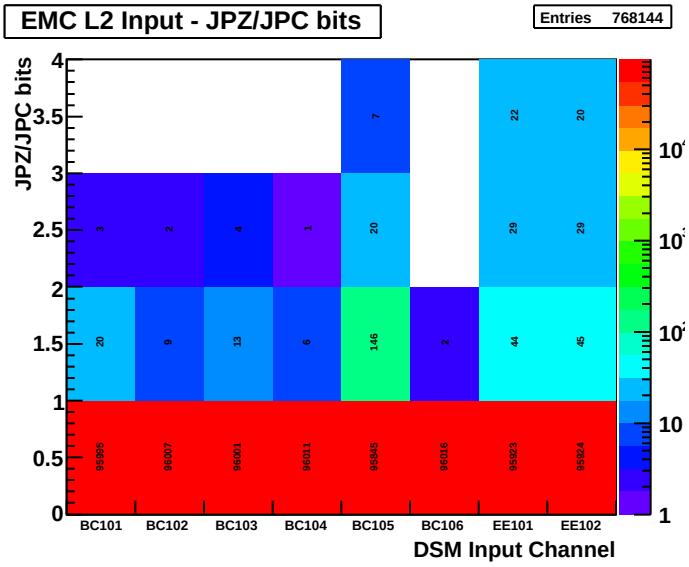

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

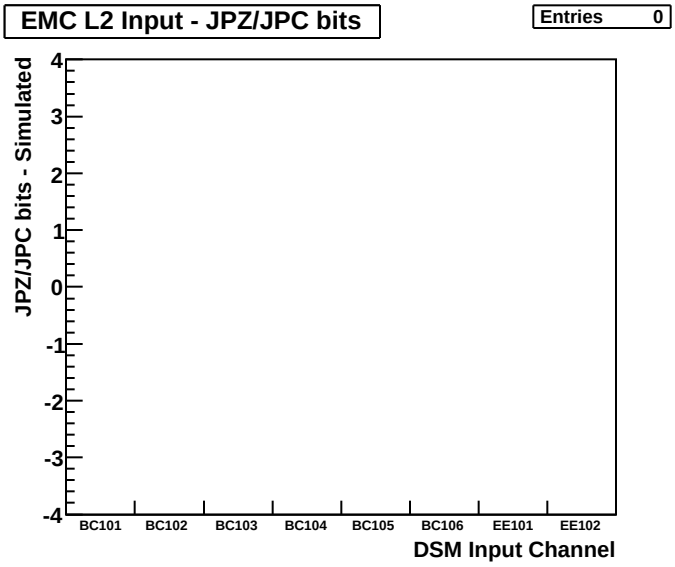

EMC L2 Input - JPX/JPA bits
Entries 768144
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

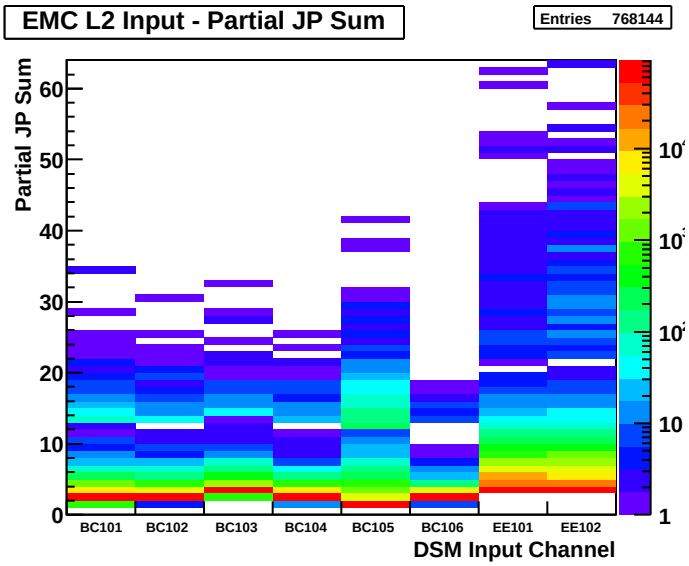

EMC L2 Input - JPX/JPA bits
Entries 23
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

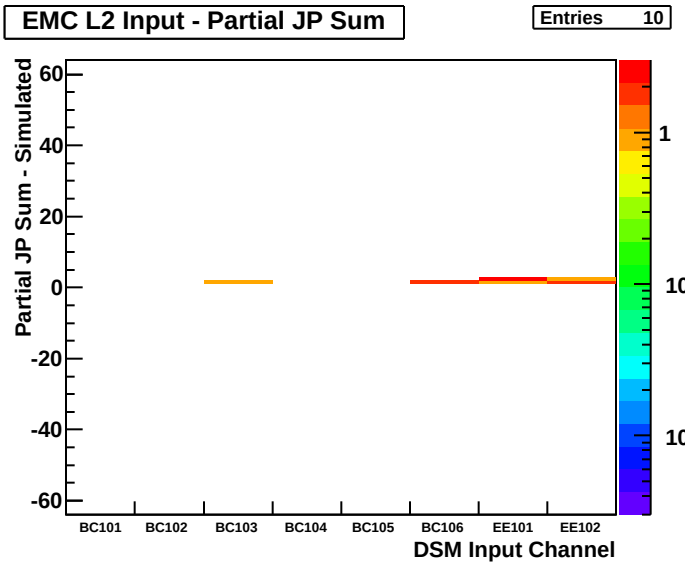

EMC L2 Input - JPY/JPB bits
Entries 768144
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

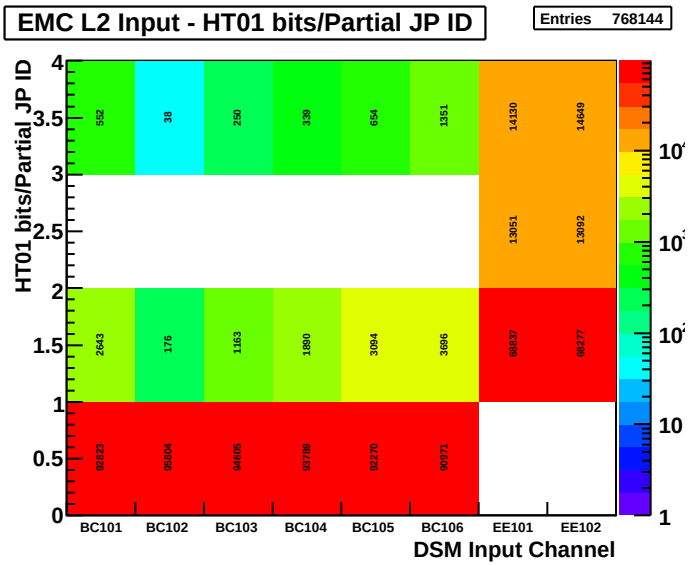

EMC L2 Input - JPY/JPB bits
Entries 23
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

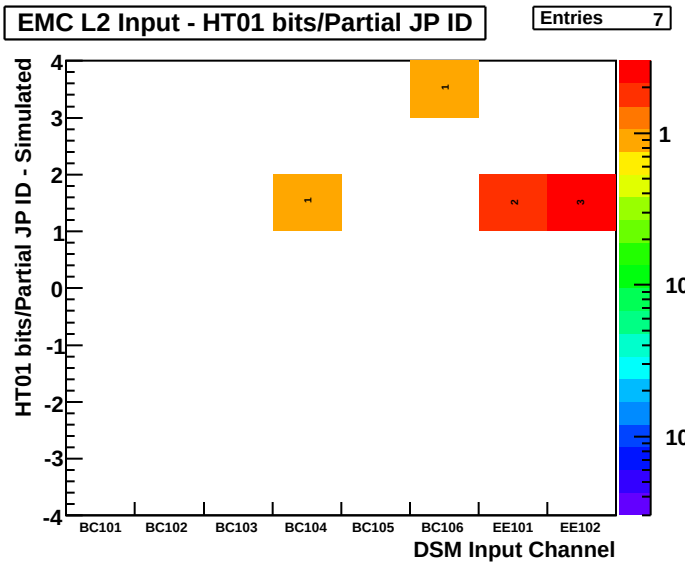

EMC L2 Input - JPZ/JPC bits
Entries 768144
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

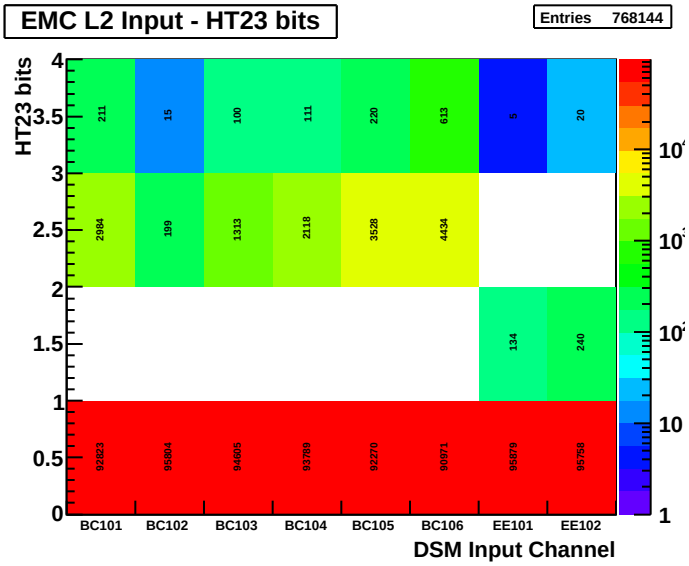

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

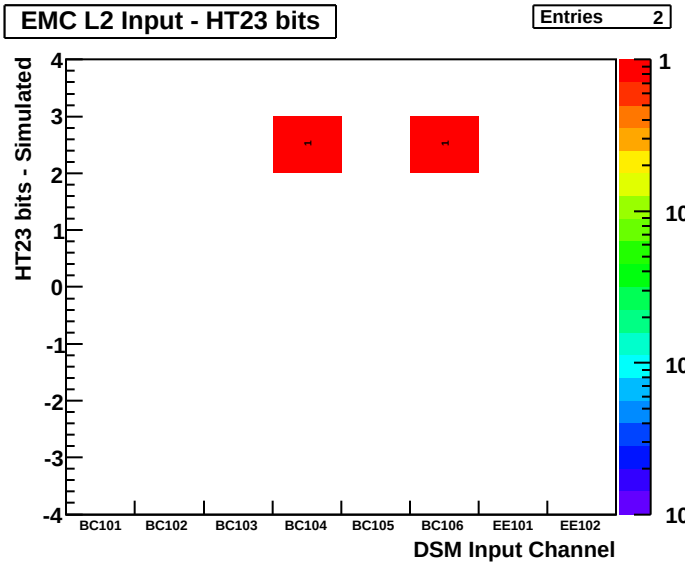

EMC L2 Input - Partial JP Sum
Entries 768144
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

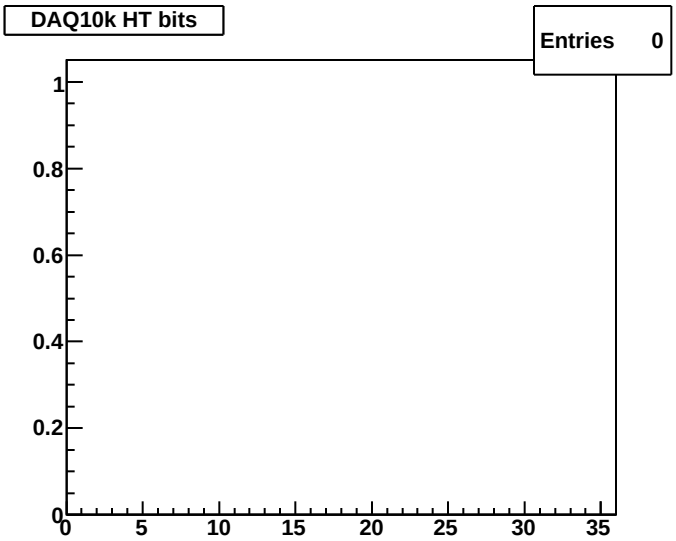

EMC L2 Input - Partial JP Sum
Entries 10
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

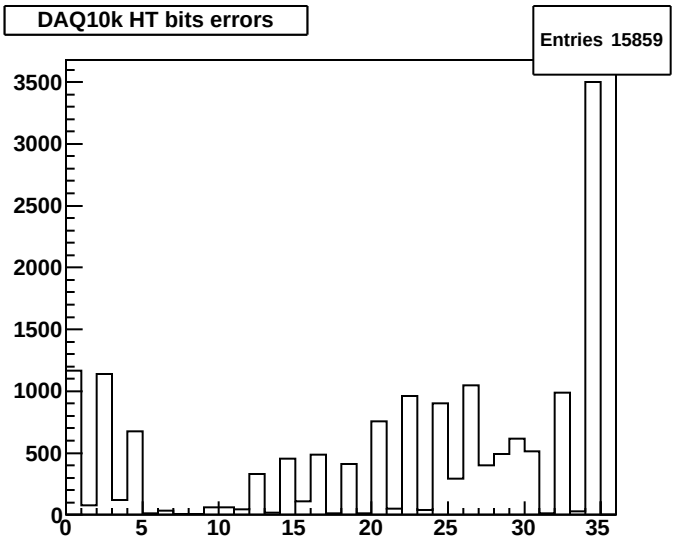

EMC L2 Input - HT01 bits/Partial JP ID
Entries 768144
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

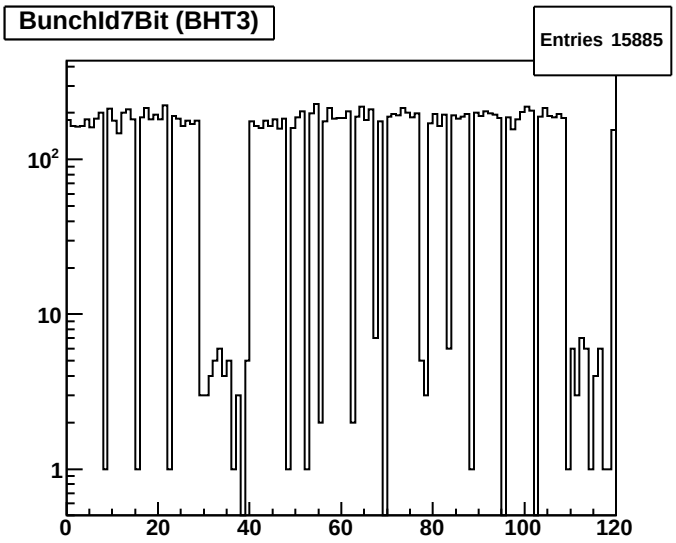

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

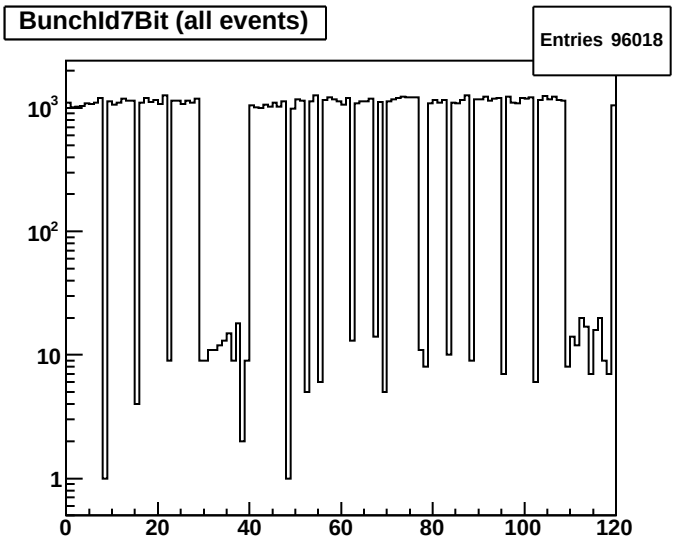

EMC L2 Input - HT23 bits
Entries 768144
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

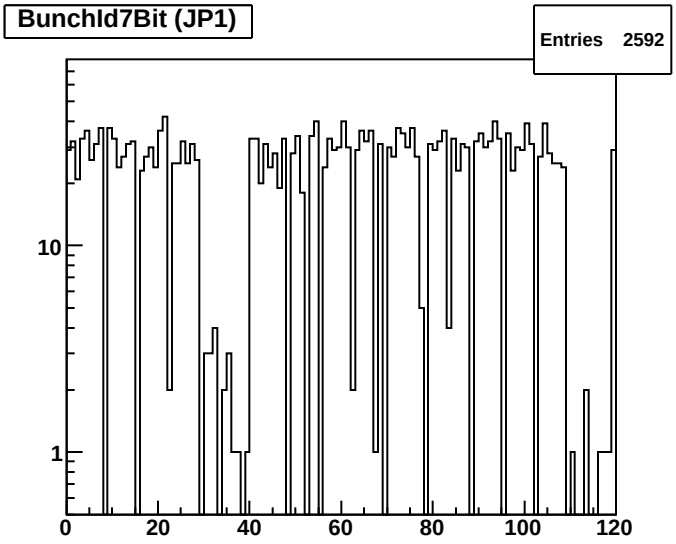

EMC L2 Input - HT23 bits
Entries 2
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

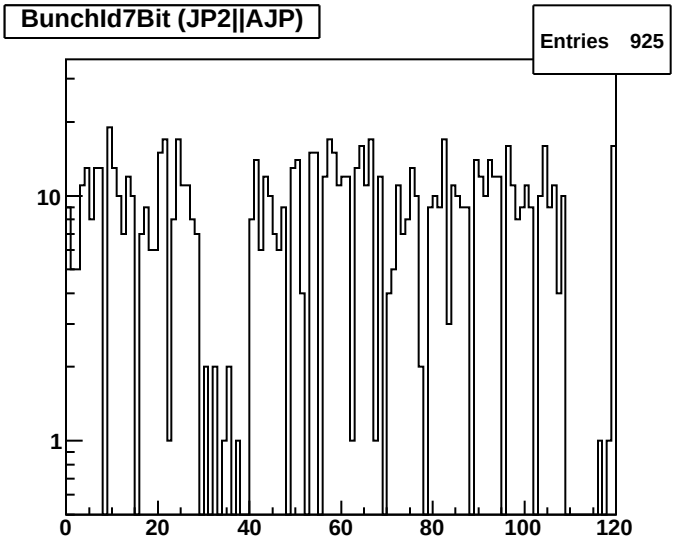

DAQ10k HT bits
Entries 0

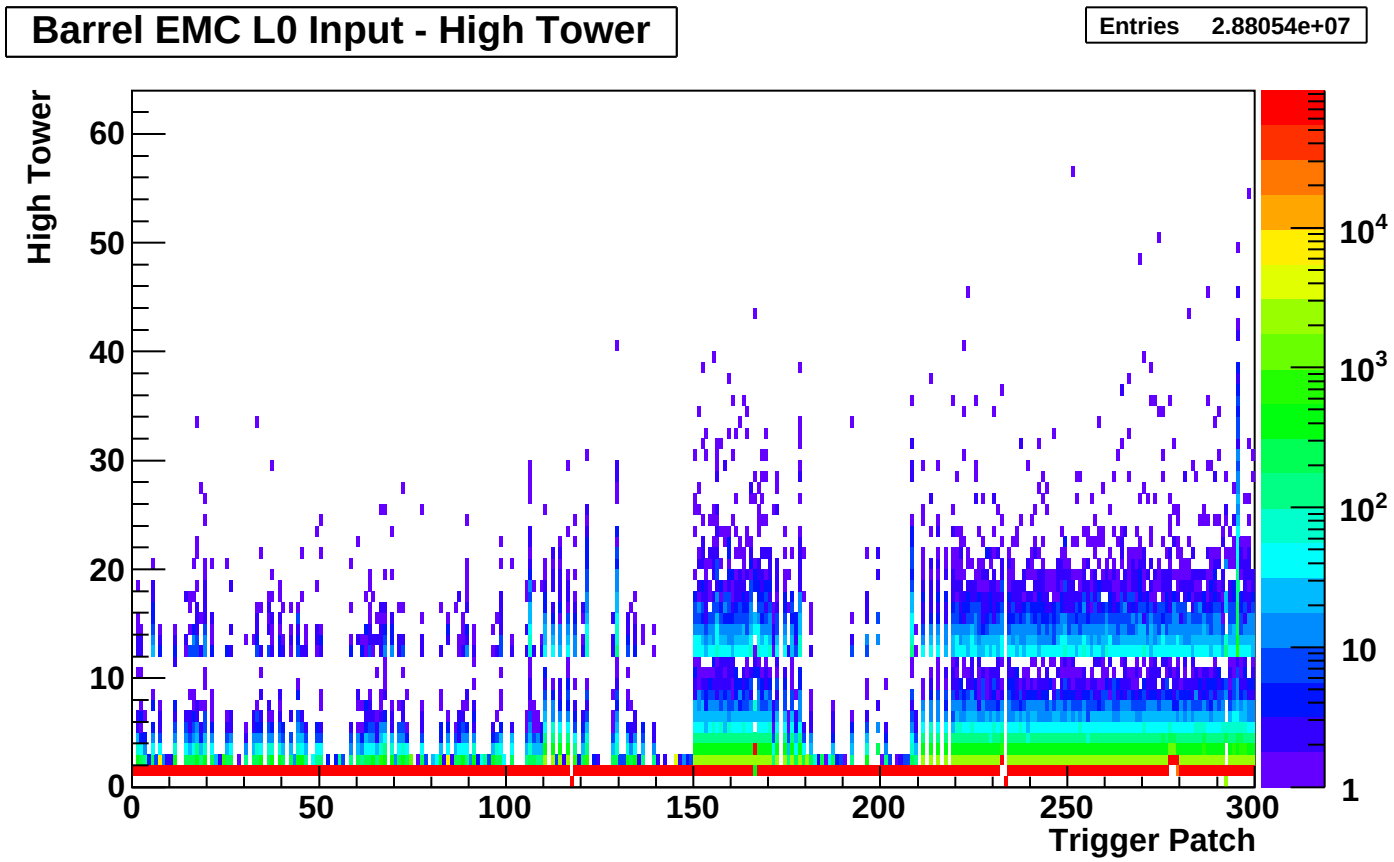

DAQ10k HT bits errors
Entries 15859

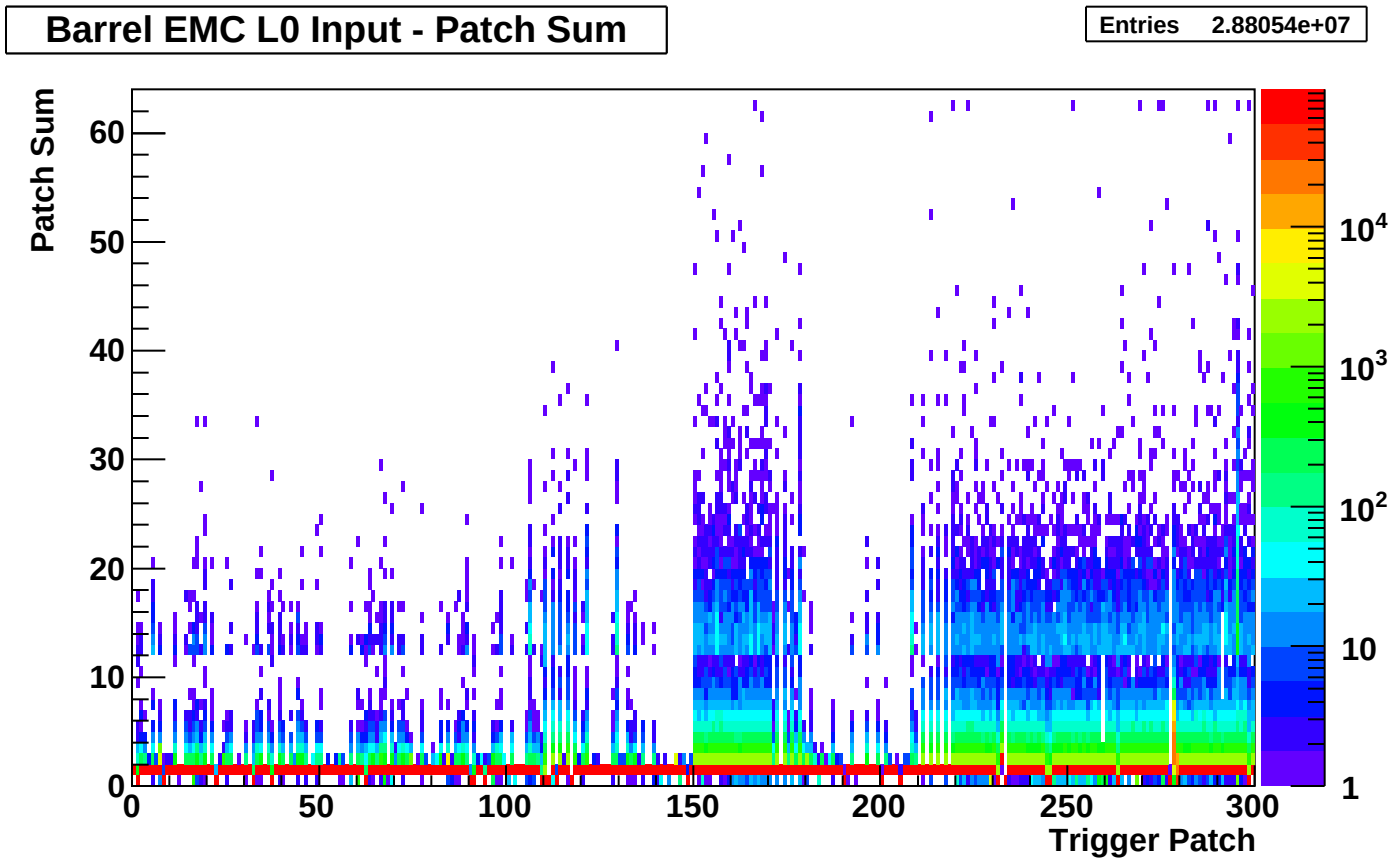

BunchId7Bit (BHT3)
Entries 15885

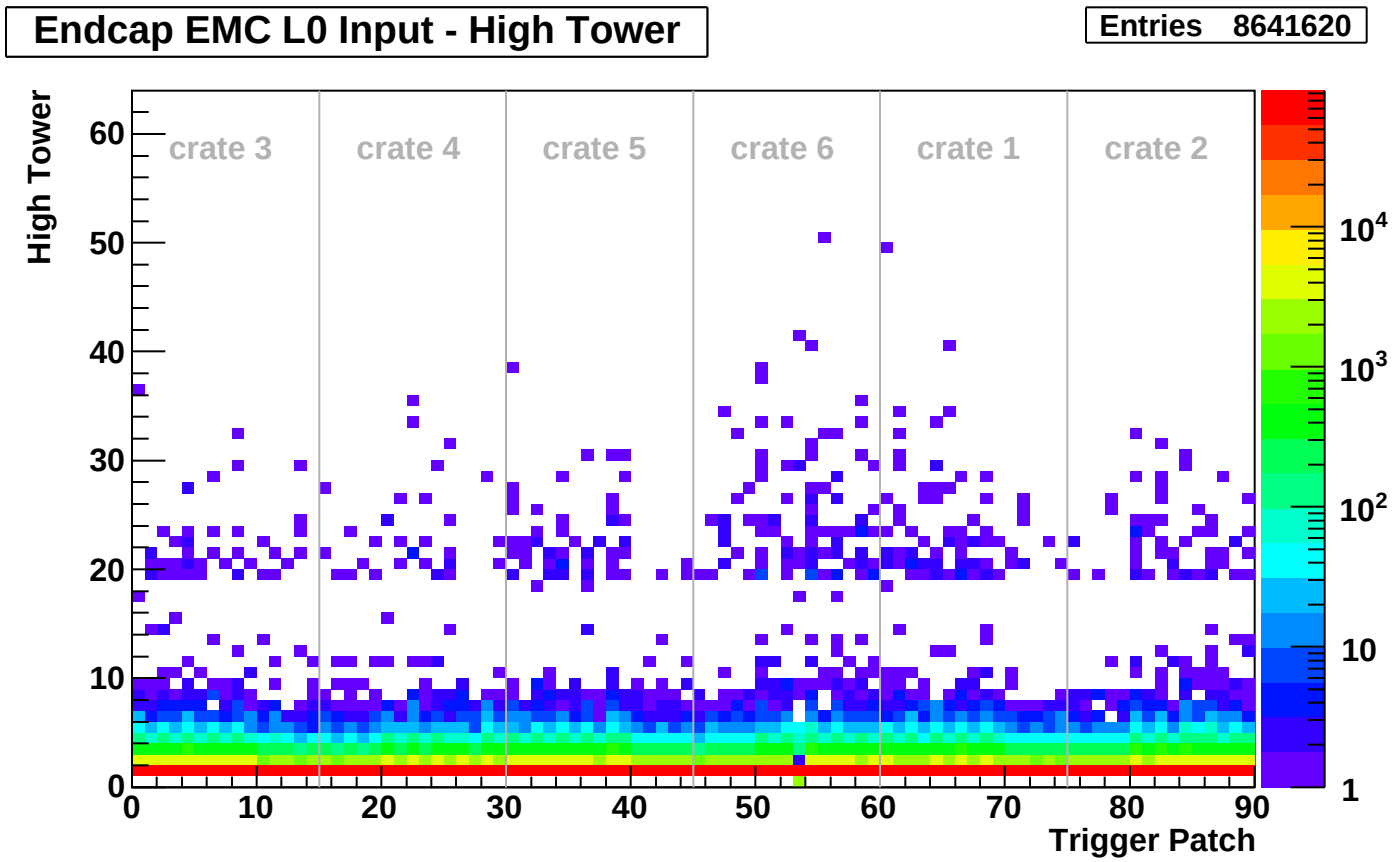

BunchId7Bit (all events)
Entries 96018

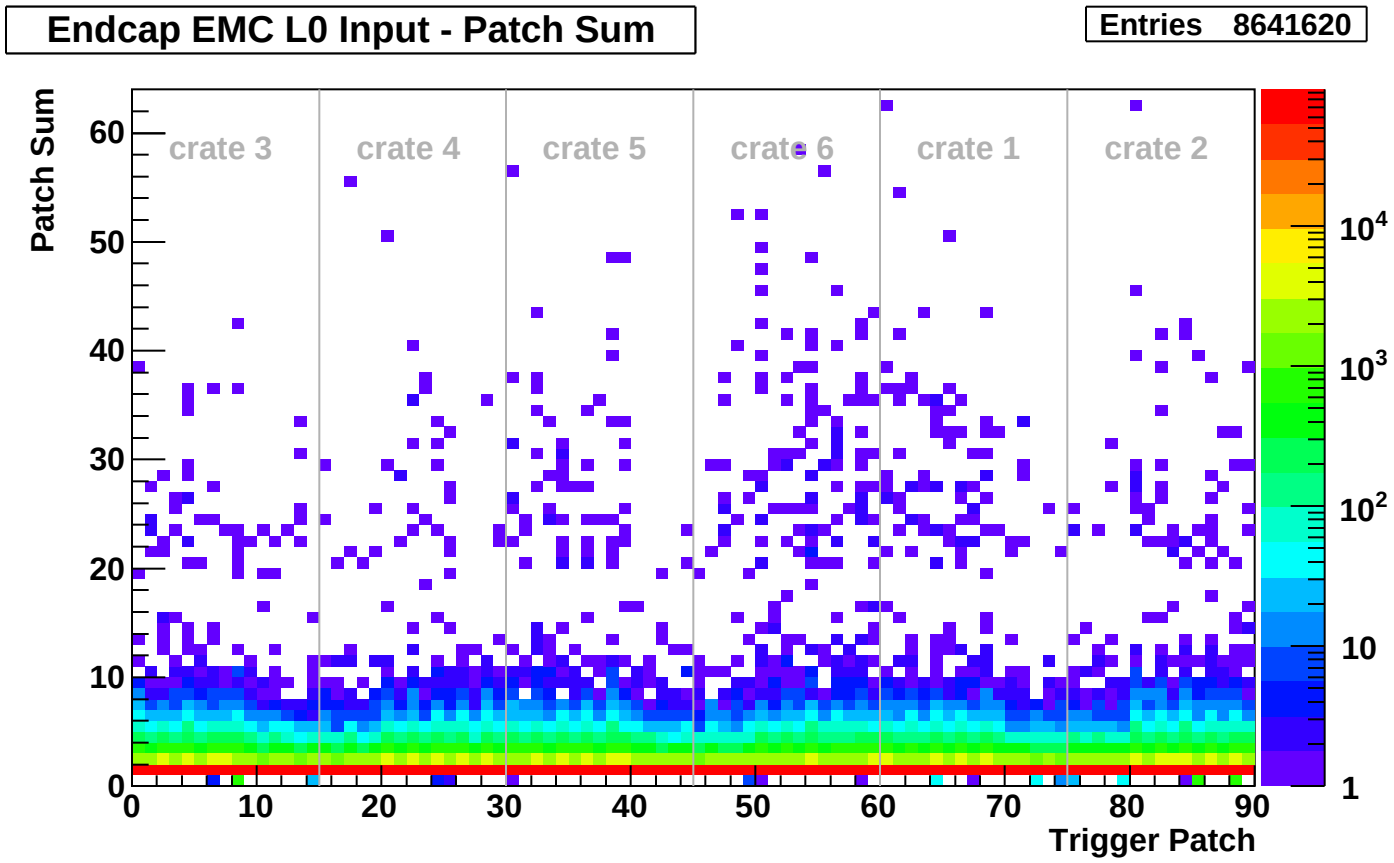

BunchId7Bit (JP1)
Entries 2592

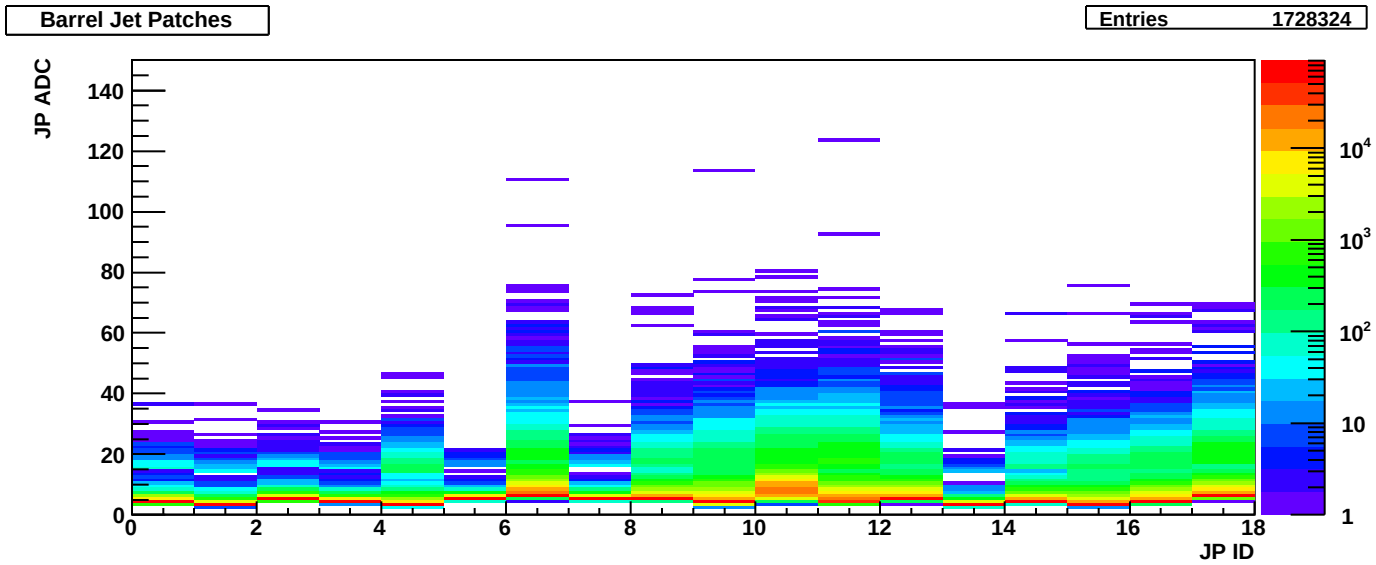

BunchId7Bit (JP2||AJP)
Entries 925

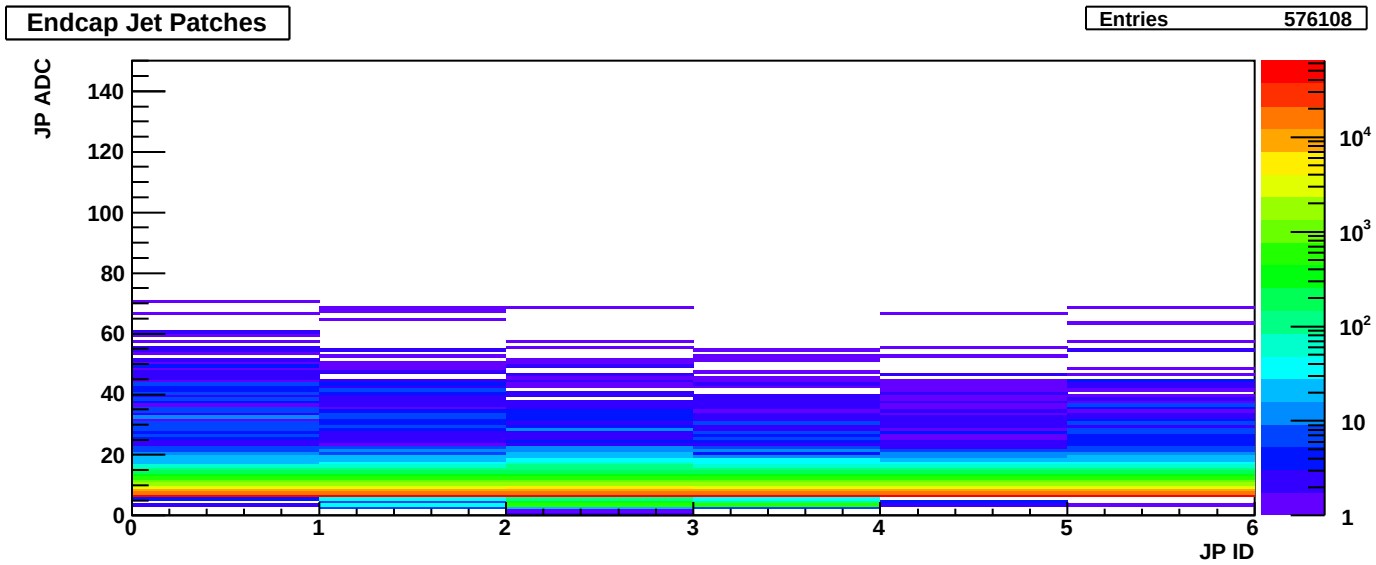

Barrel EMC L0 Input - High Tower
Entries 2.88054e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

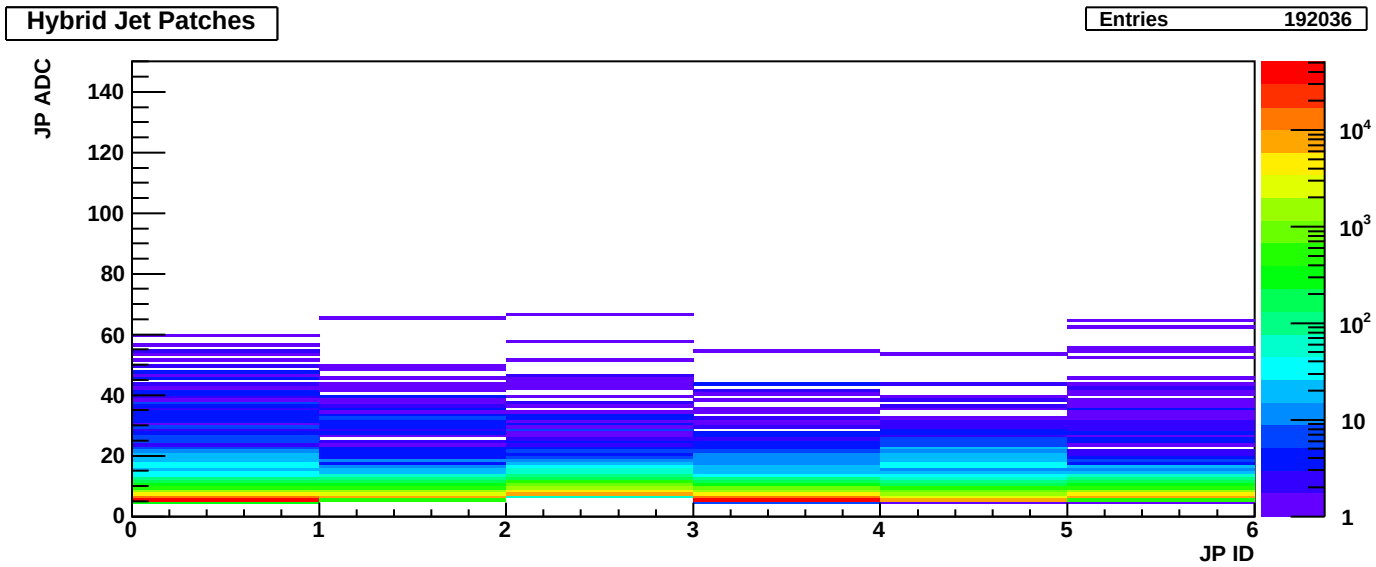

Barrel EMC L0 Input - Patch Sum
Entries 2.88054e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

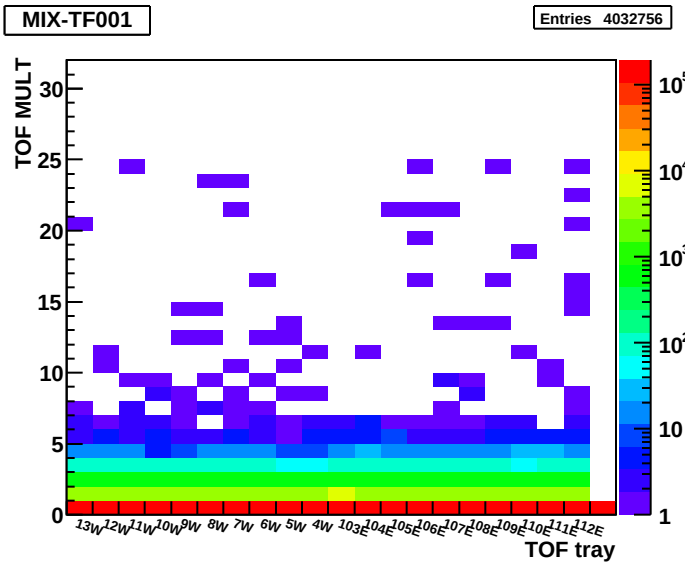

Endcap EMC L0 Input - High Tower
Entries 8641620
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

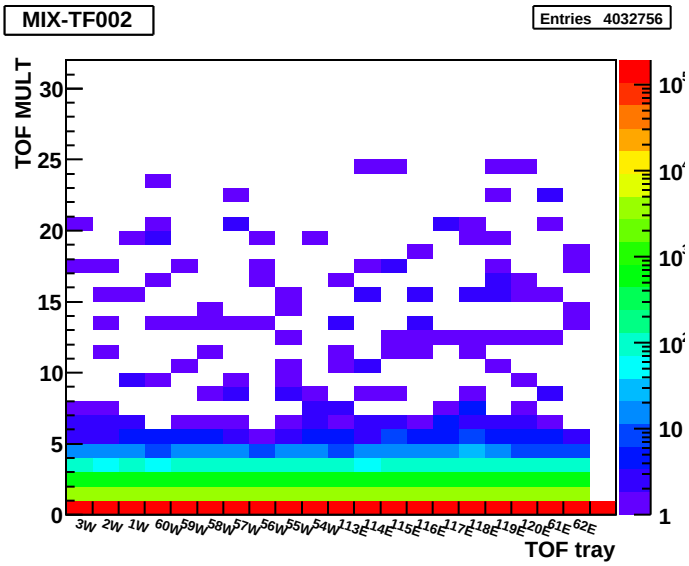

Endcap EMC L0 Input - Patch Sum
Entries 8641620
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

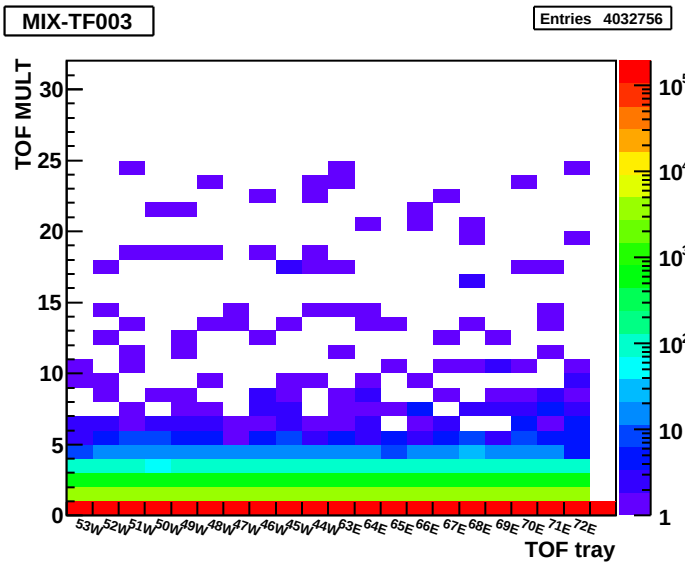

Barrel Jet Patches
Entries 1728324
JP ADC
JP ID

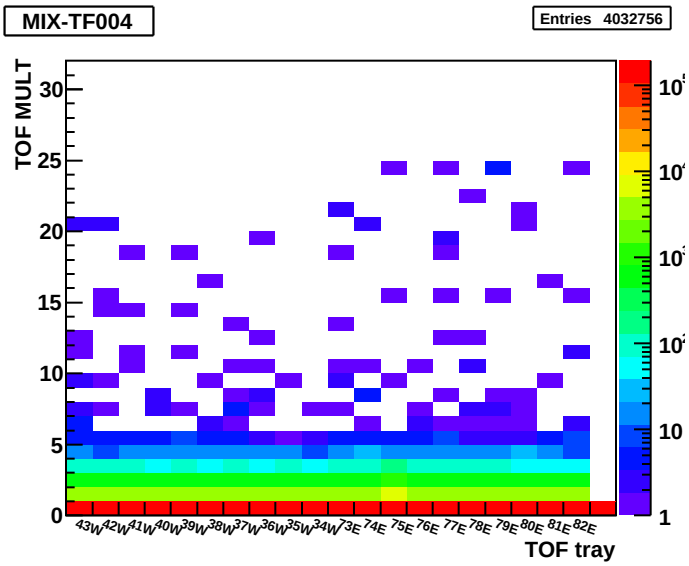

Endcap Jet Patches
Entries 576108
JP ADC
JP ID

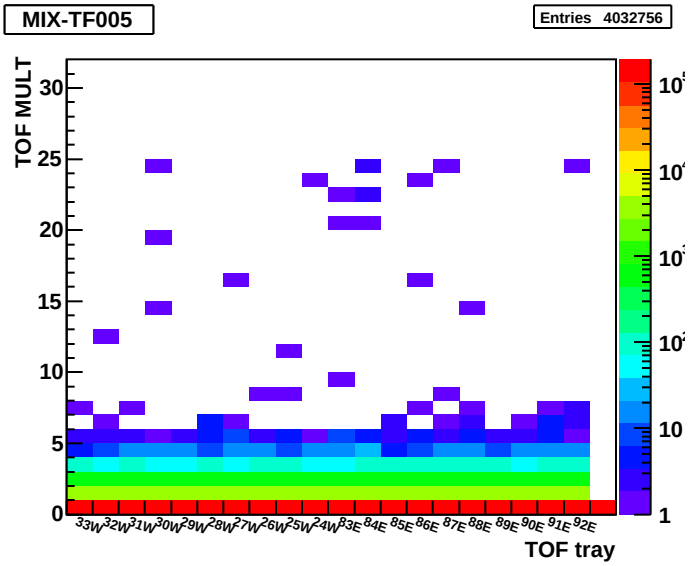

Hybrid Jet Patches
Entries 192036
JP ADC
JP ID

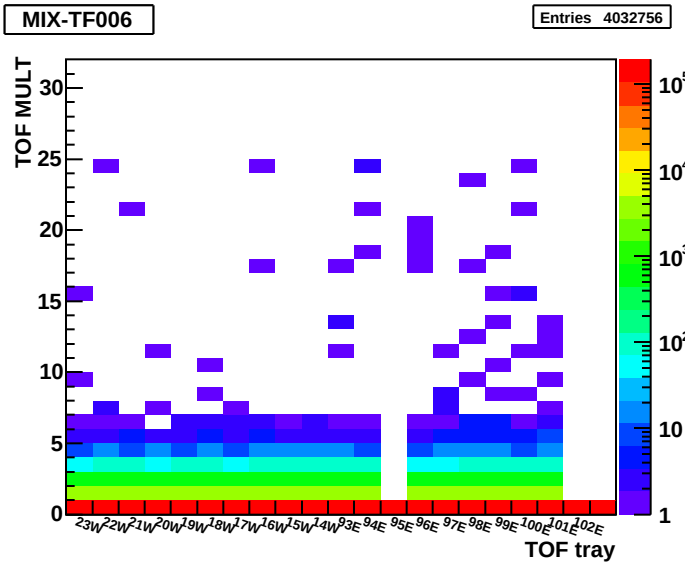

MIX-TF001
Entries 4032756
TOF MULT
TOF tray

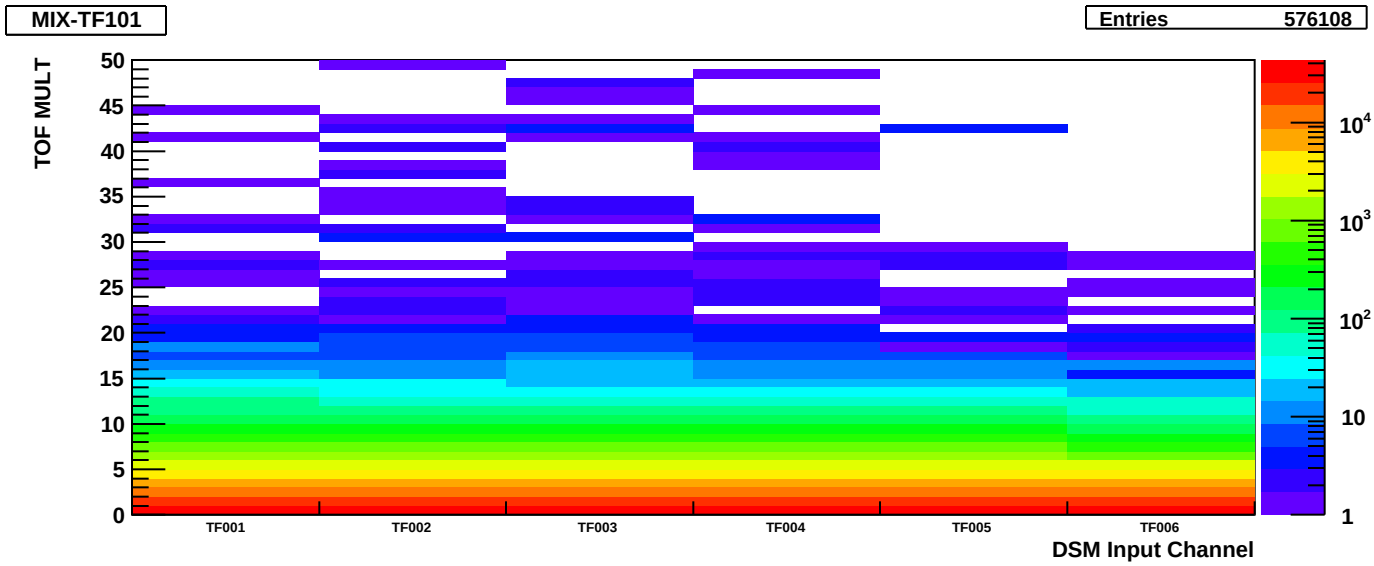

MIX-TF002
Entries 4032756
TOF MULT
TOF tray

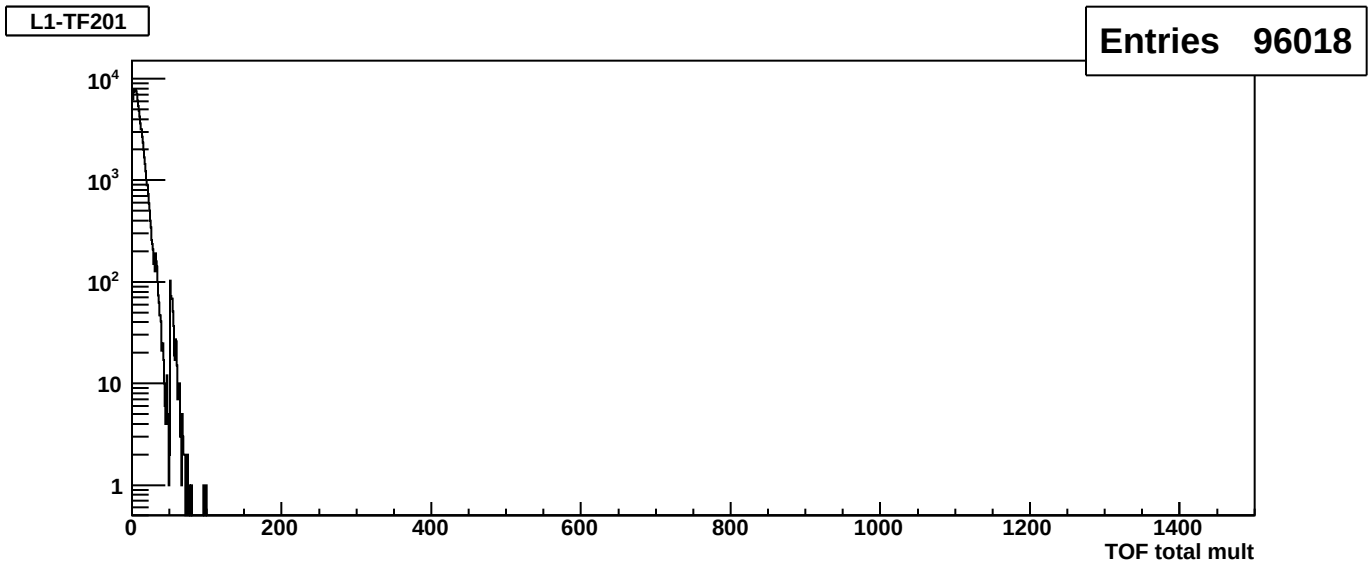

MIX-TF003
Entries 4032756
TOF MULT
TOF tray

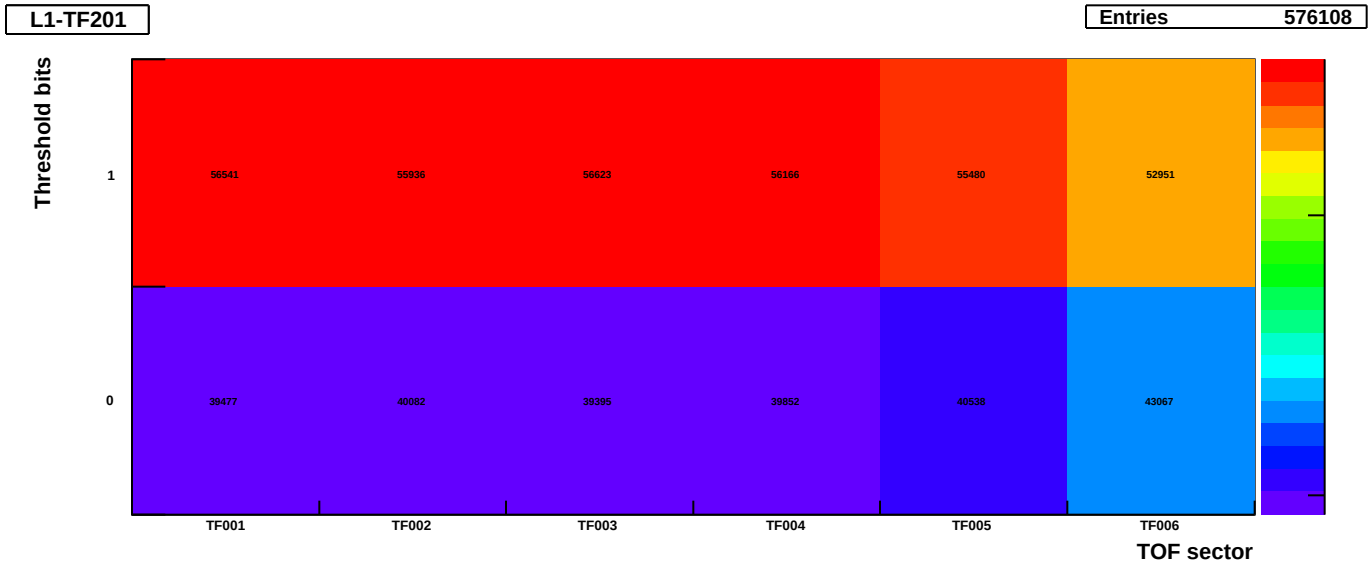

MIX-TF004
Entries 4032756
TOF MULT
TOF tray

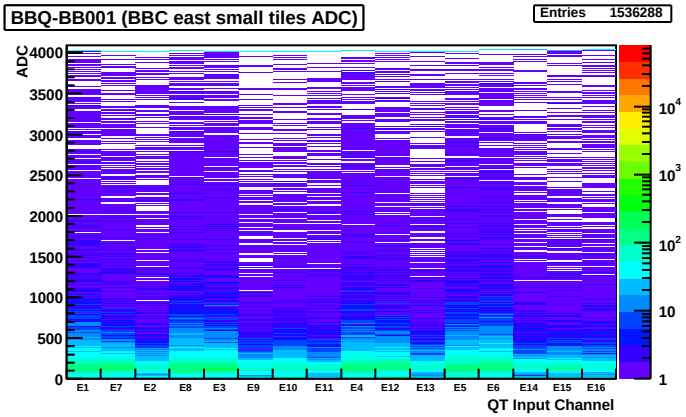

MIX-TF005
Entries 4032756
TOF MULT
TOF tray

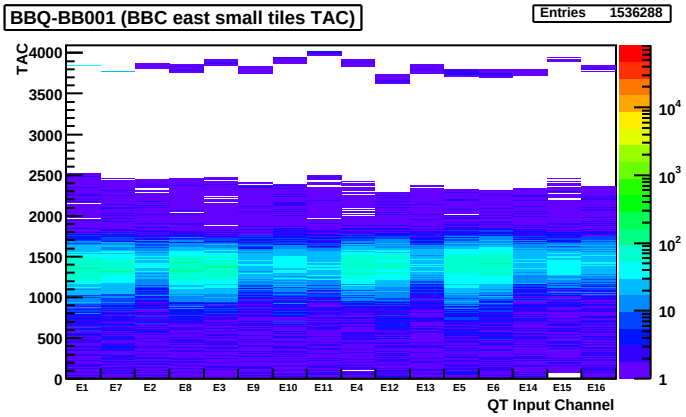

MIX-TF006
Entries 4032756
TOF MULT
TOF tray

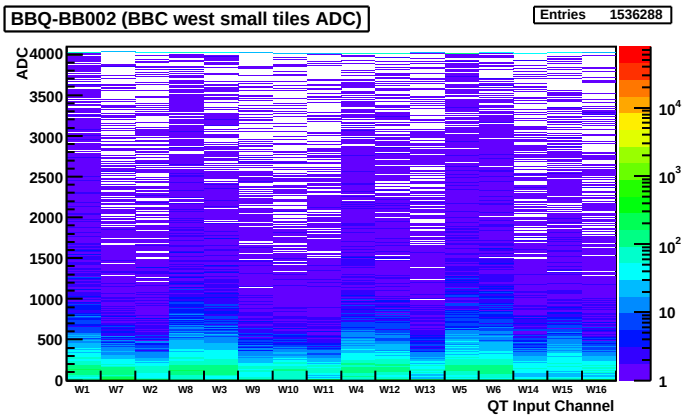

MIX-TF101
Entries 576108
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

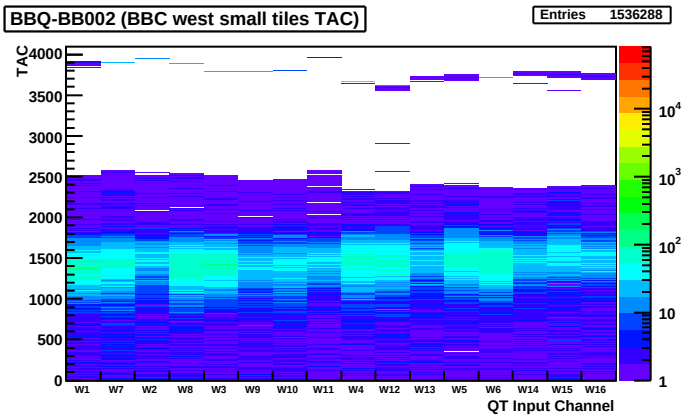

L1-TF201
Entries 96018
TOF total mult

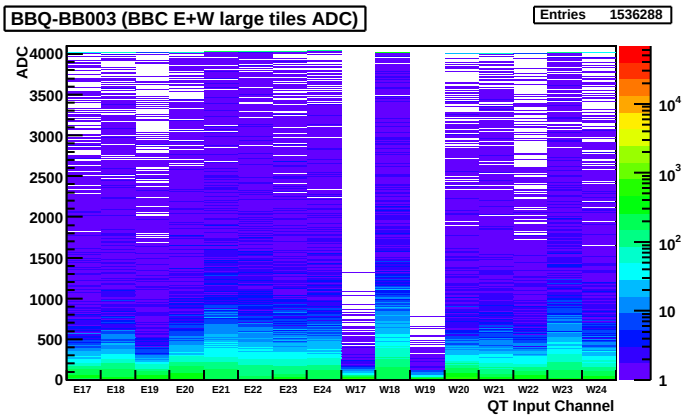

L1-TF201
Entries 576108
Threshold bits
56541
55936
56623
56166
55480
52951
39477
40082
39395
39852
40538
43067
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

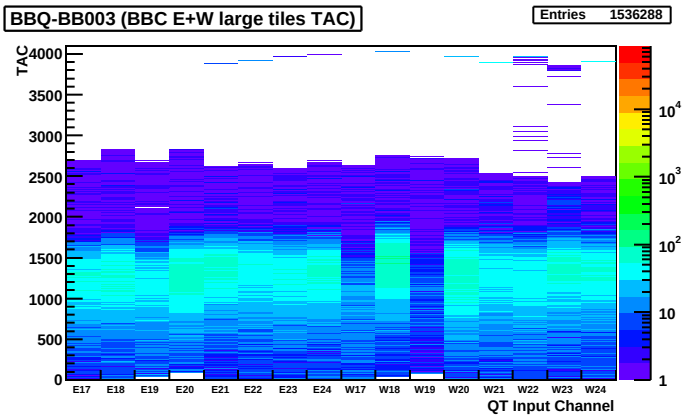

BBQ-BB001 (BBC east small tiles ADC)
Entries 1536288
ADC
QT Input Channel

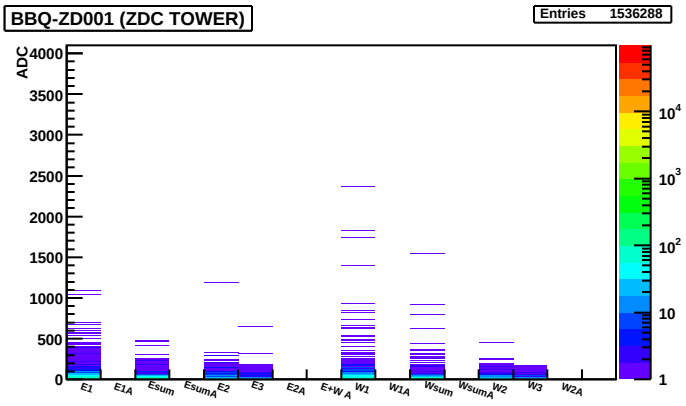

BBQ-BB001 (BBC east small tiles TAC)
Entries 1536288
TAC
QT Input Channel

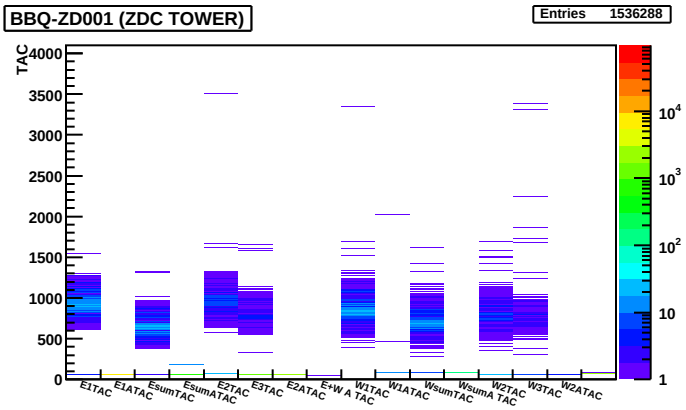

BBQ-BB002 (BBC west small tiles ADC)
Entries 1536288
ADC
QT Input Channel

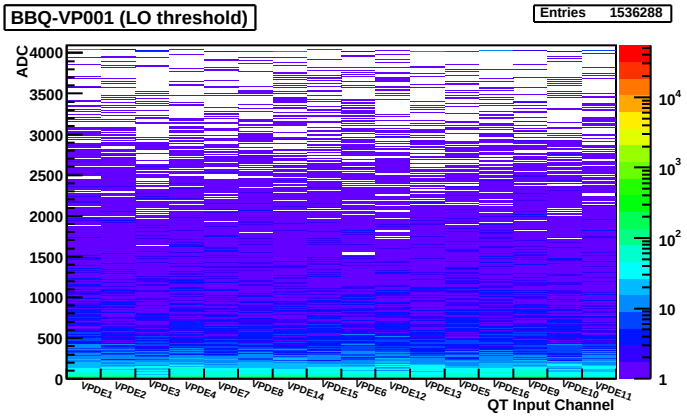

BBQ-BB002 (BBC west small tiles TAC)
Entries 1536288
TAC
QT Input Channel

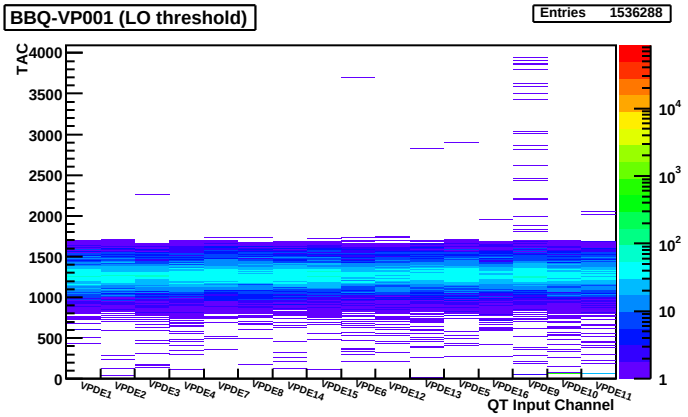

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1536288
ADC
QT Input Channel

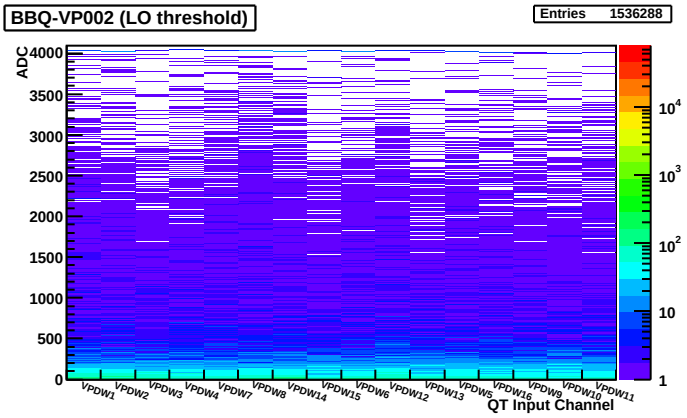

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1536288
TAC
QT Input Channel

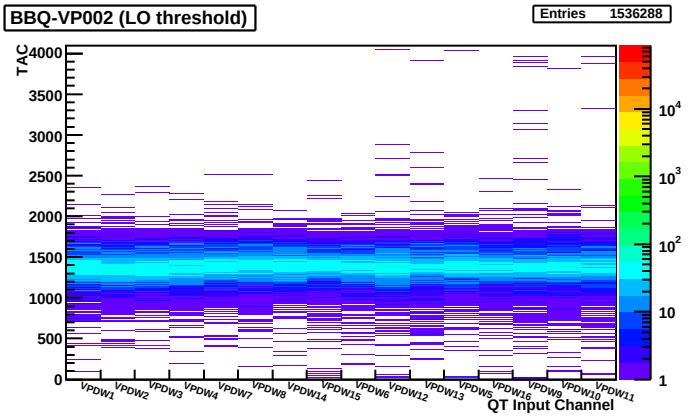

BBQ-ZD001 (ZDC TOWER)
Entries 1536288
ADC

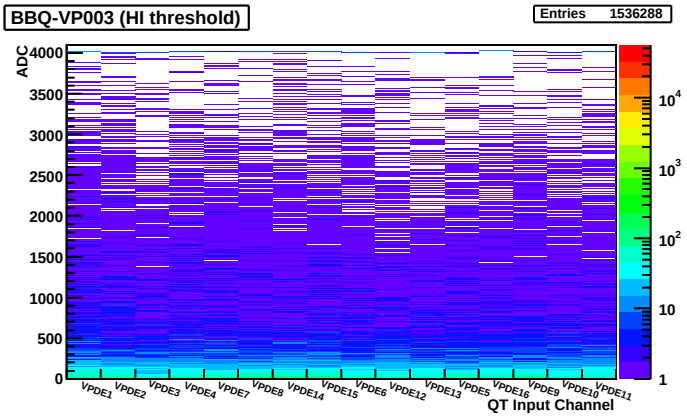

BBQ-ZD001 (ZDC TOWER)
Entries 1536288
TAC

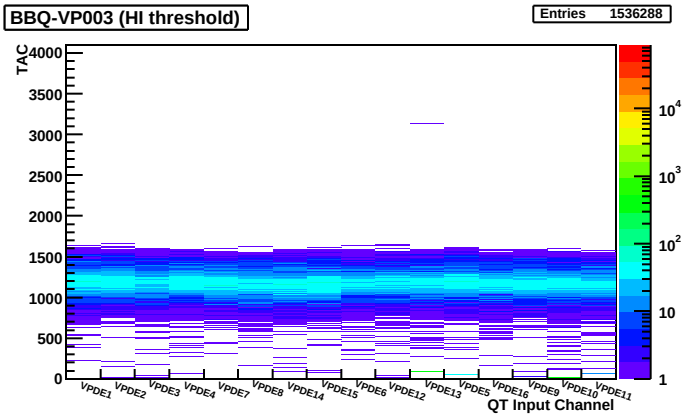

BBQ-VP001 (LO threshold)
Entries 1536288
ADC
QT Input Channel

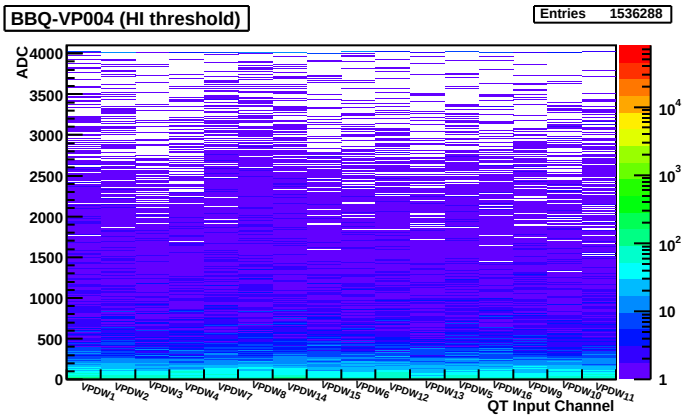

BBQ-VP001 (LO threshold)
Entries 1536288
TAC
QT Input Channel

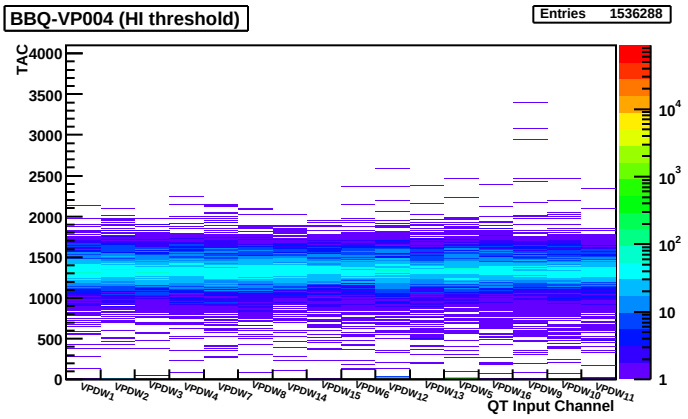

BBQ-VP002 (LO threshold)
Entries 1536288
ADC
QT Input Channel

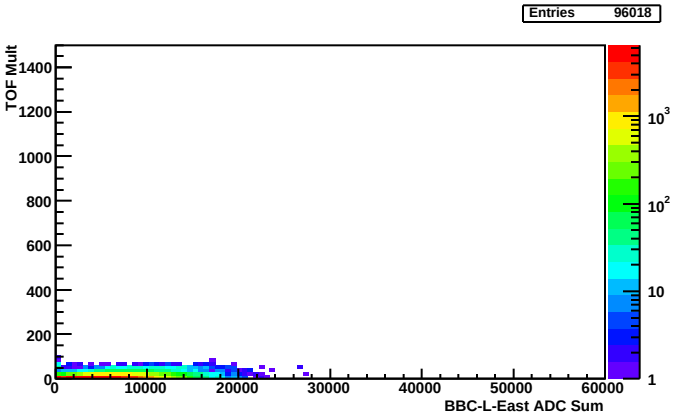

BBQ-VP002 (LO threshold)
Entries 1536288
TAC
QT Input Channel

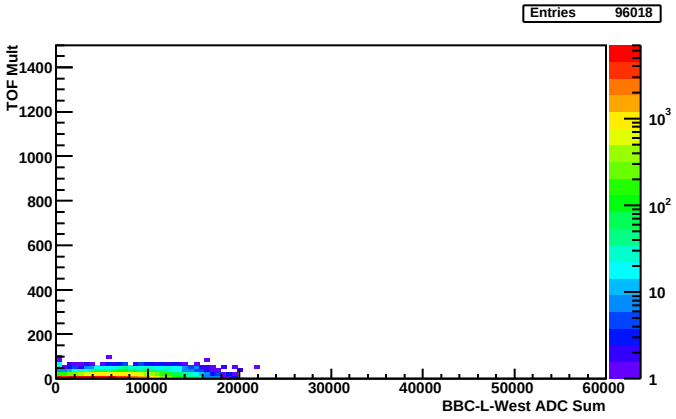

BBQ-VP003 (HI threshold)
Entries 1536288
ADC
QT Input Channel

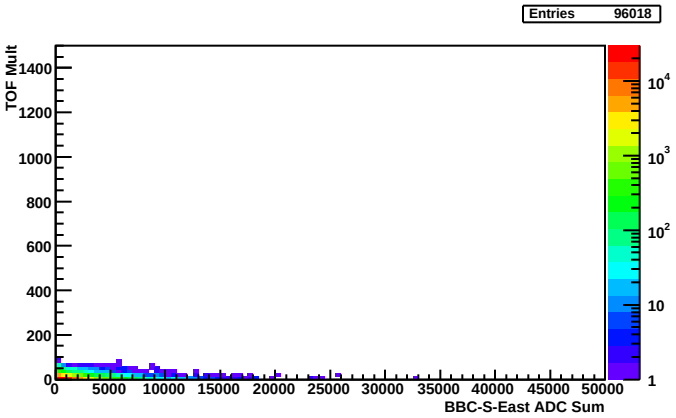

BBQ-VP003 (HI threshold)
Entries 1536288
TAC
QT Input Channel

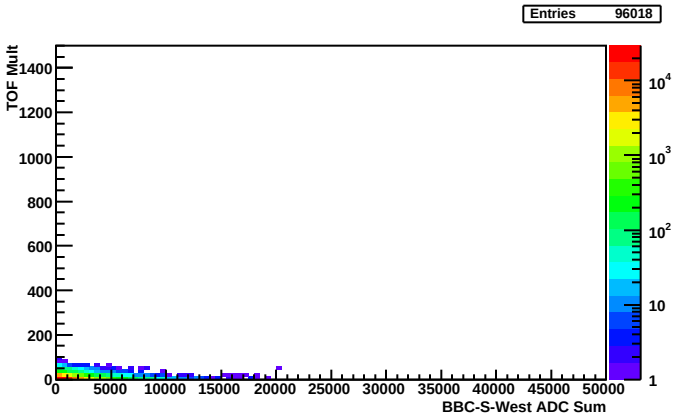

BBQ-VP004 (HI threshold)
Entries 1536288
ADC
QT Input Channel

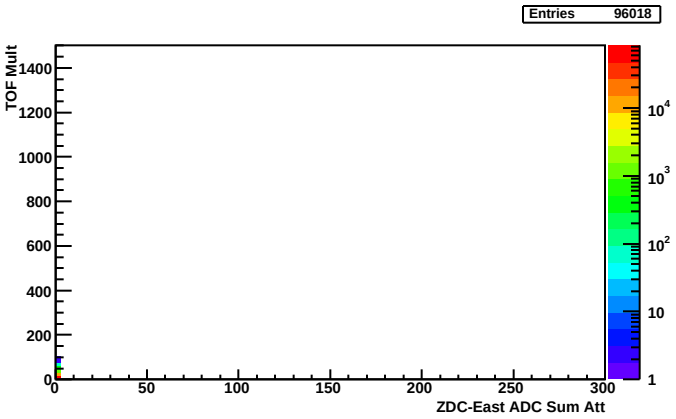

BBQ-VP004 (HI threshold)
Entries 1536288
TAC
QT Input Channel

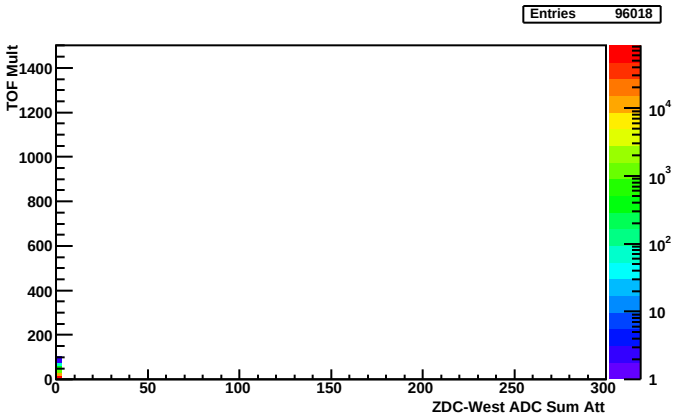

Entries 96018
TOF Mult
BBC-L-East ADC Sum

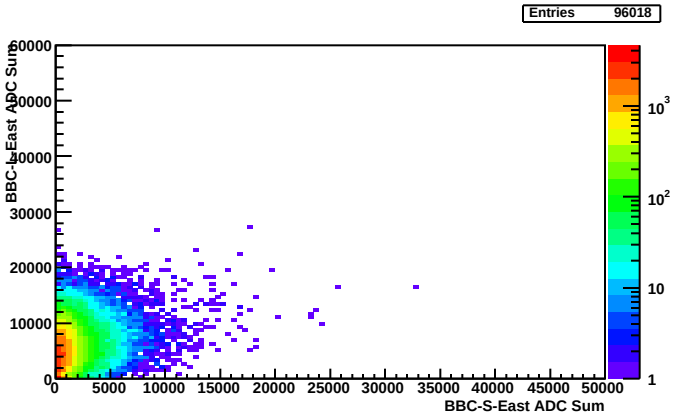

Entries 96018
TOF Mult
BBC-L-West ADC Sum

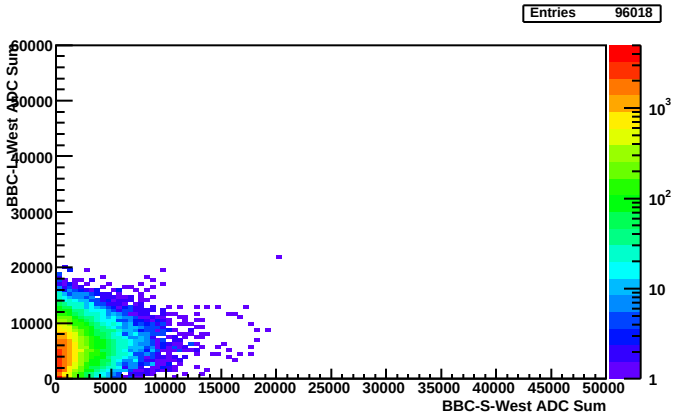

Entries 96018
TOF Mult
BBC-S-East ADC Sum

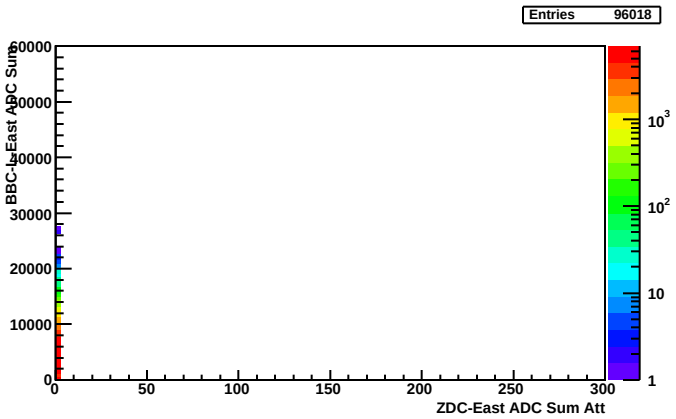

Entries 96018
TOF Mult
BBC-S-West ADC Sum

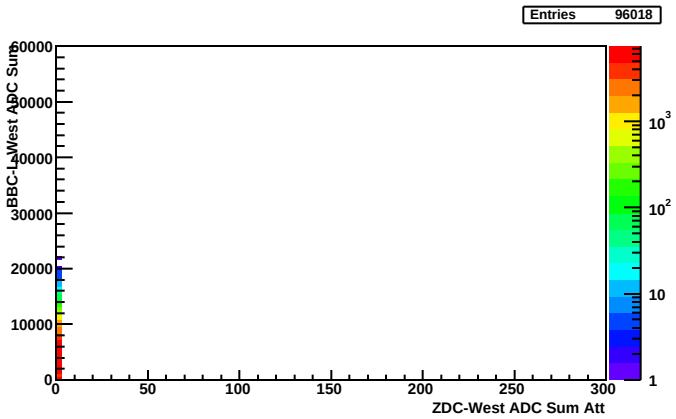

Entries 96018
TOF Mult
ZDC-East ADC Sum Att

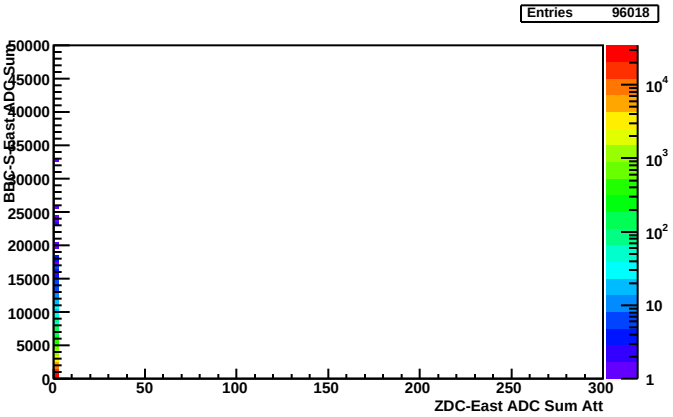

Entries 96018
TOF Mult
ZDC-West ADC Sum Att

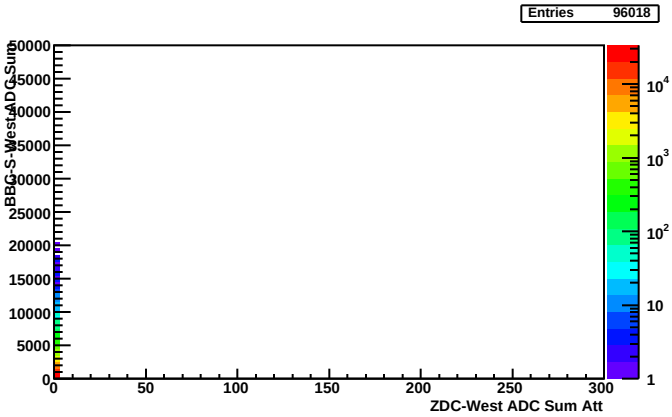

Entries 96018
BBC-L-East ADC Sum
BBC-S-East ADC Sum

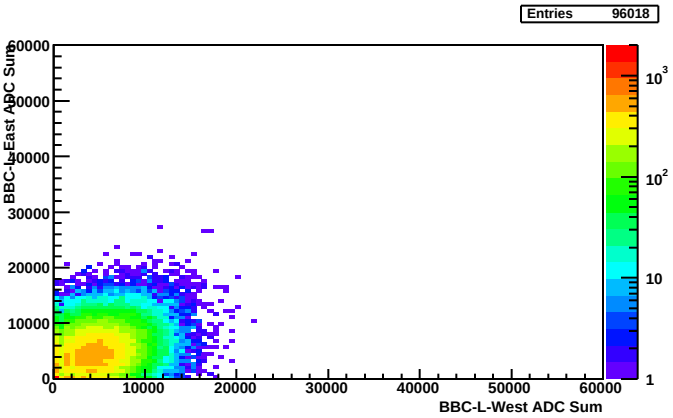

Entries 96018
BBC-L-West ADC Sum
BBC-S-West ADC Sum

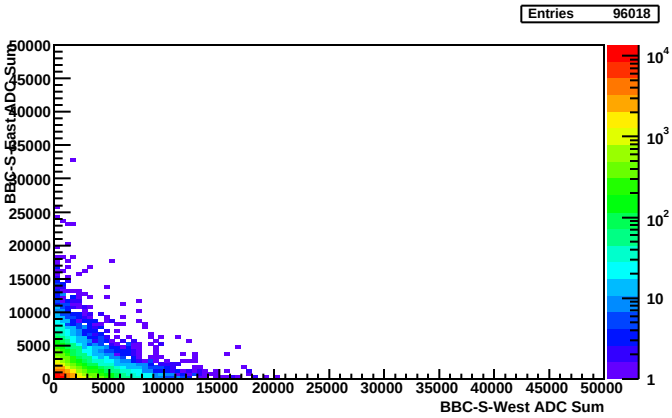

Entries 96018
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

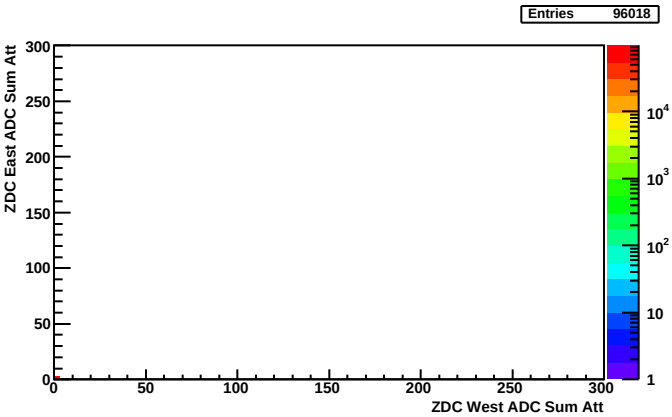

Entries 96018
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

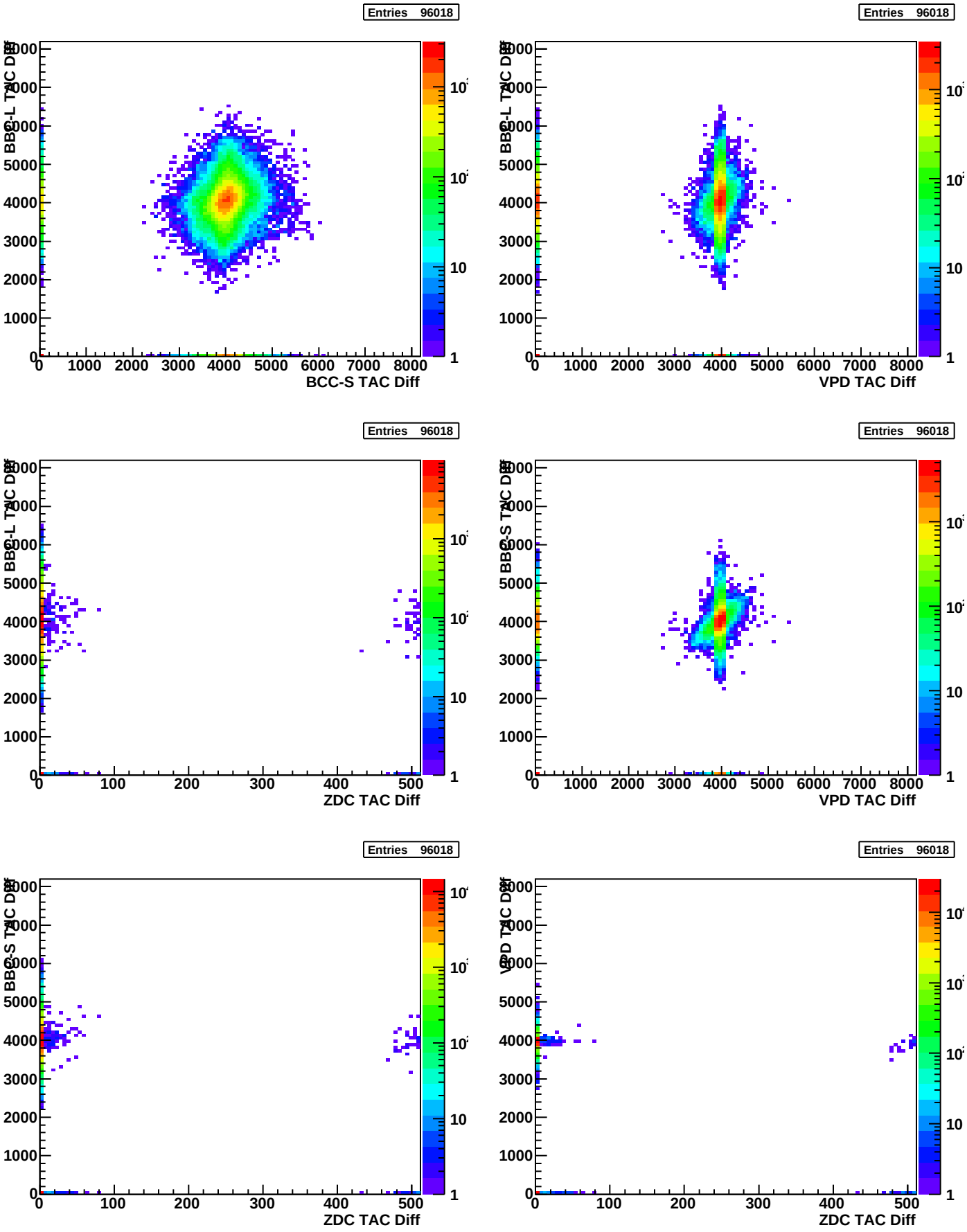

Entries 96018
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

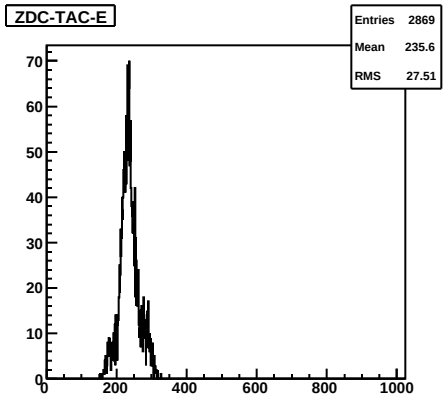

Entries 96018
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

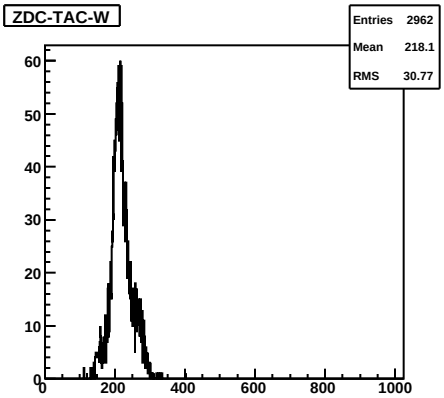

Entries 96018
BBC-L-East ADC Sum
BBC-L-West ADC Sum

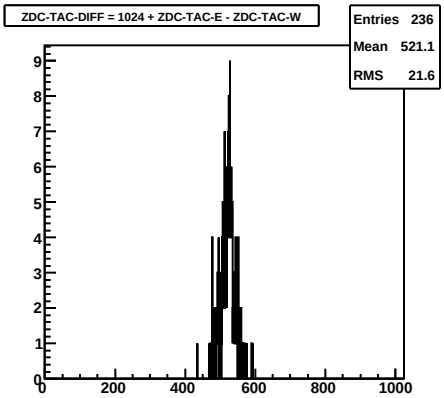

Entries 96018
BBC-S-East ADC Sum
BBC-S-West ADC Sum

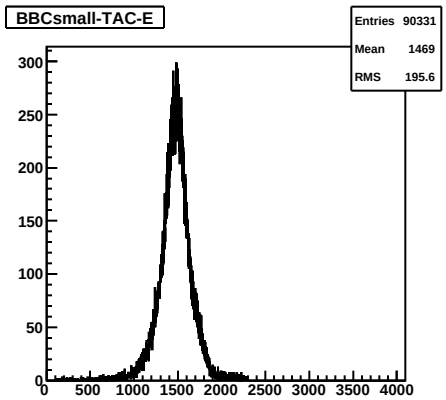

Entries 96018
ZDC East ADC Sum Att
ZDC West ADC Sum Att

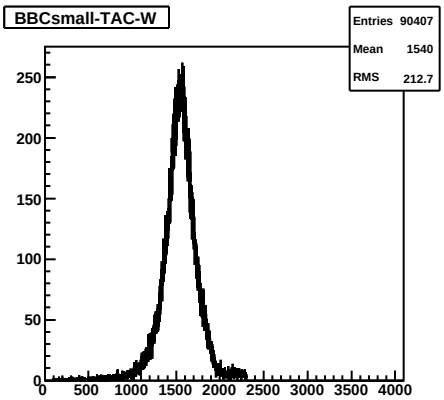

Entries 96018
BBC-L TAC Diff
BCC-S TAC Diff
Entries 96018
BBC-L TAC Diff
VPD TAC Diff
Entries 96018
BBC-L TAC Diff
ZDC TAC Diff
Entries 96018
BBC-S TAC Diff
VPD TAC Diff
Entries 96018
BBC-S TAC Diff
ZDC TAC Diff
Entries 96018
VPD TAC Diff
ZDC TAC Diff

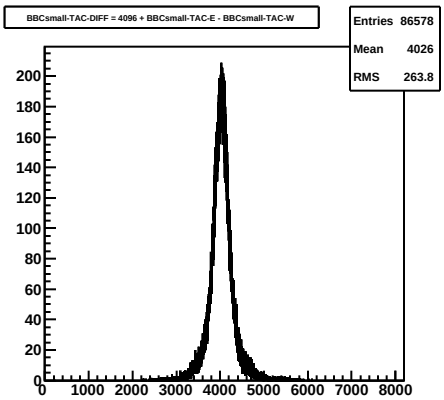

ZDC-TAC-E
Entries 2869
Mean 235.6
RMS 27.51

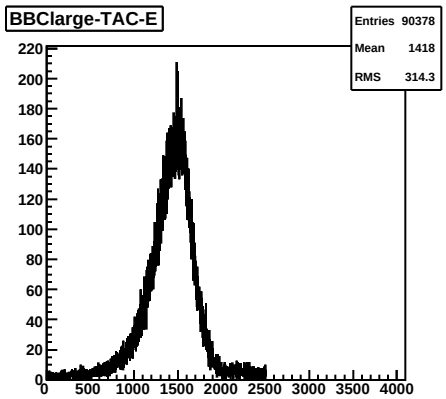

ZDC-TAC-W
Entries 2962
Mean 218.1
RMS 30.77

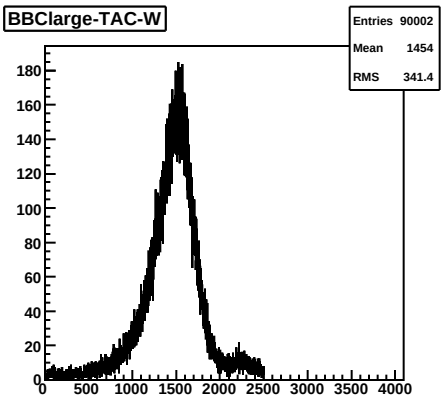

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 236
Mean 521.1
RMS 21.6

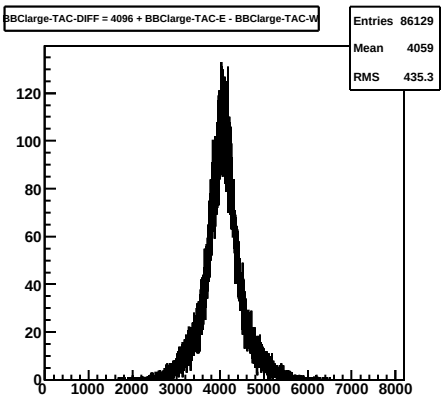

BBCsmall-TAC-E
Entries 90331
Mean 1469
RMS 195.6

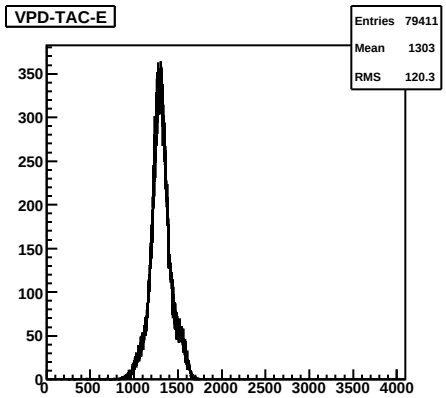

BBCsmall-TAC-W
Entries 90407
Mean 1540
RMS 212.7

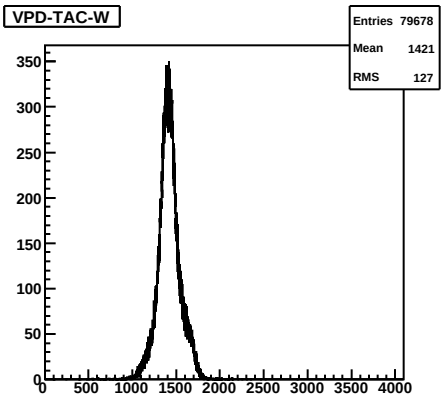

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 86578
Mean 4026
RMS 263.8

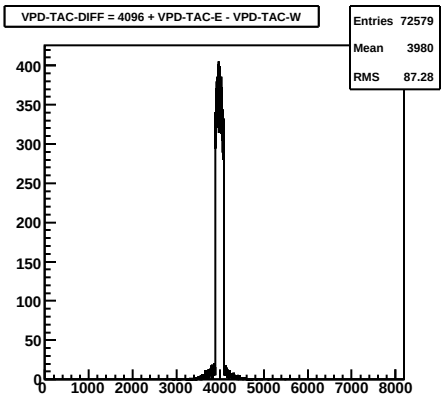

BBClarge-TAC-E
Entries 90378
Mean 1418
RMS 314.3

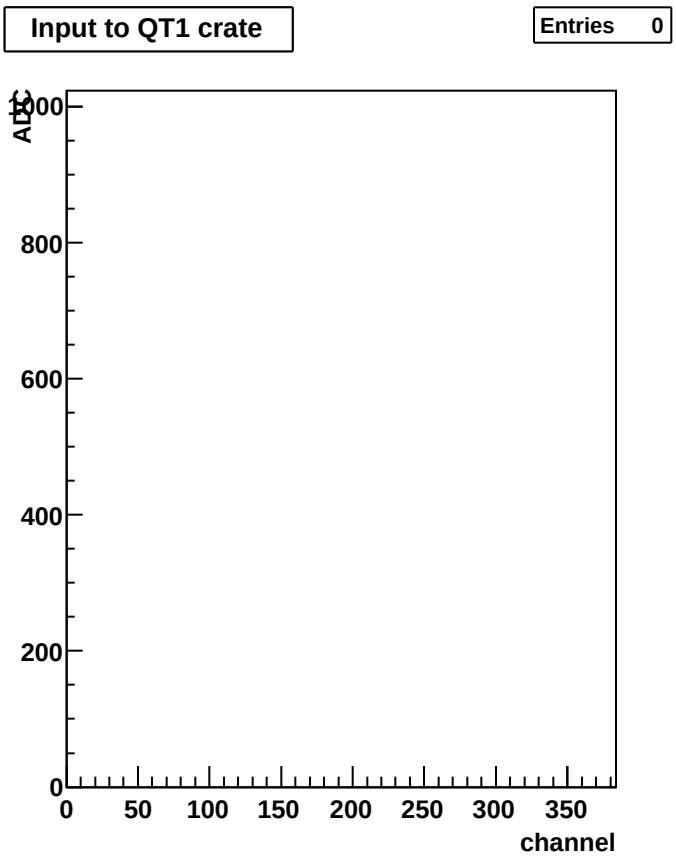

BBClarge-TAC-W
Entries 90002
Mean 1454
RMS 341.4

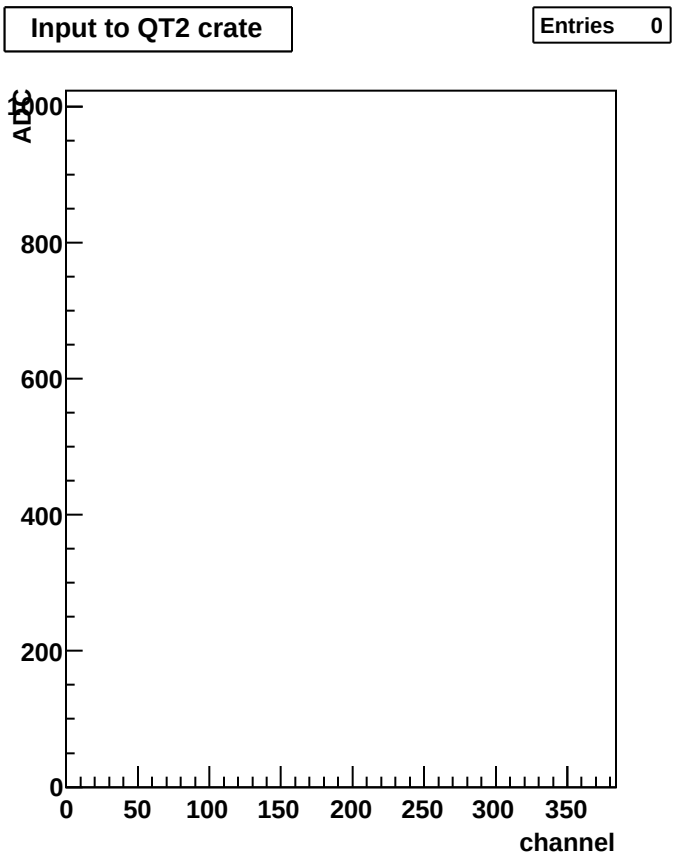

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 86129
Mean 4059
RMS 435.3

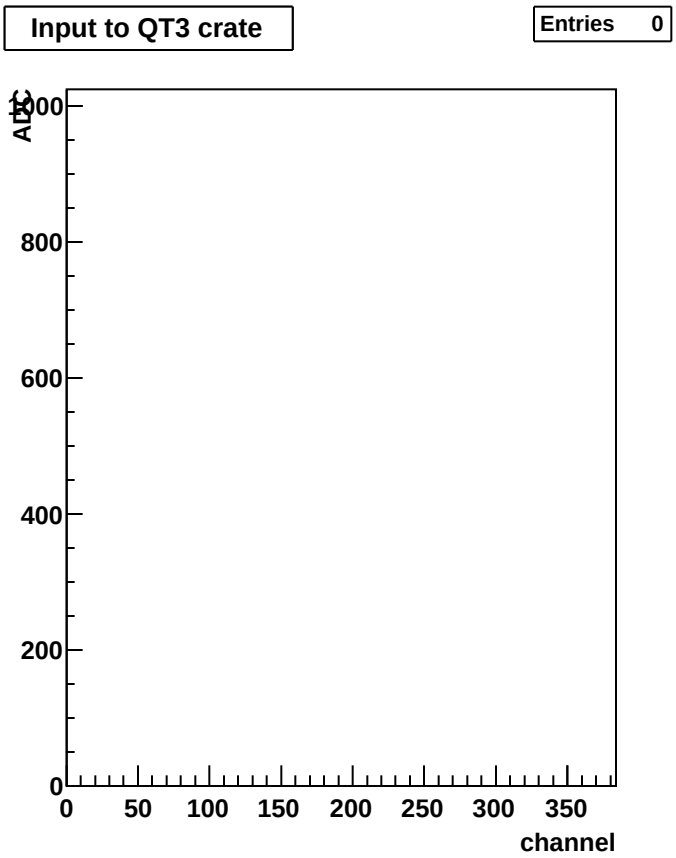

VPD-TAC-E
Entries 79411
Mean 1303
RMS 120.3

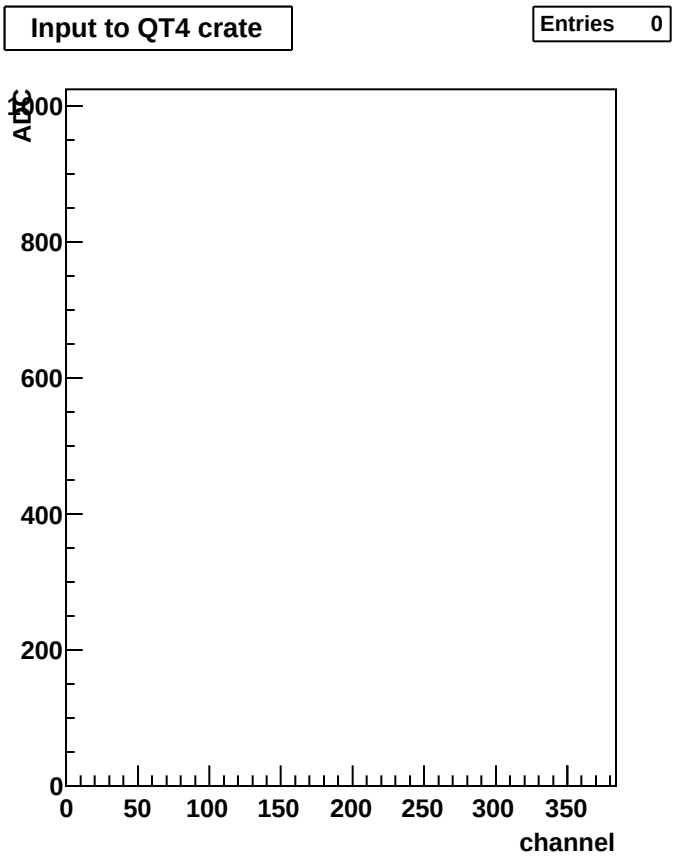

VPD-TAC-W
Entries 79678
Mean 1421
RMS 127

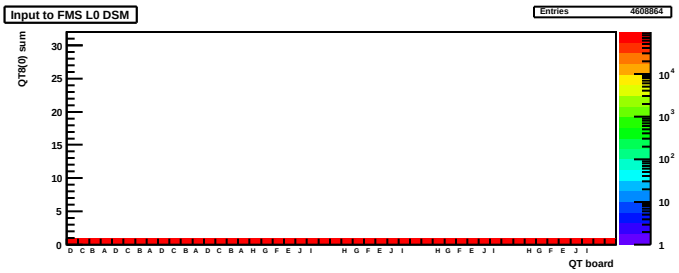

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 72579
Mean 3980
RMS 87.28

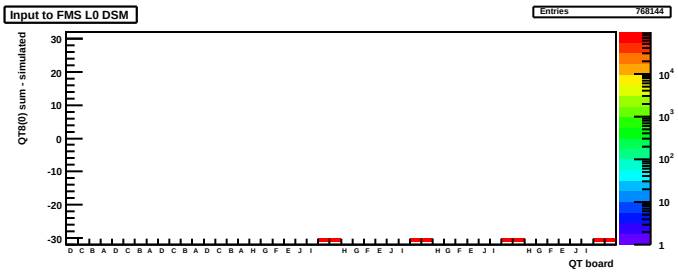

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

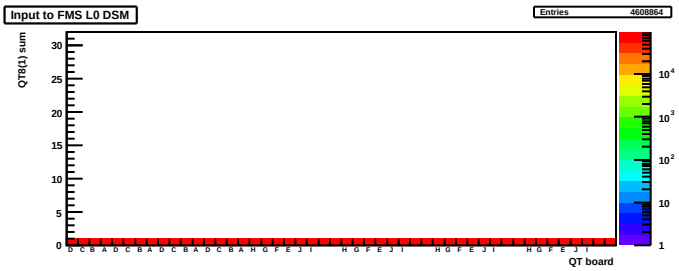

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

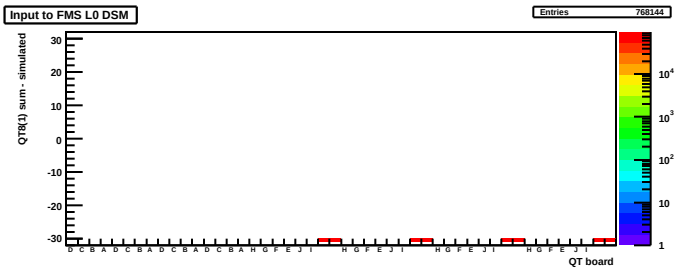

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

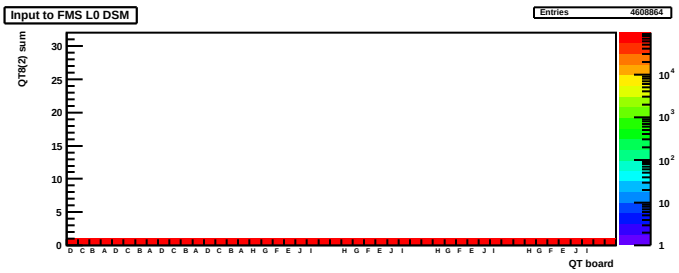

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

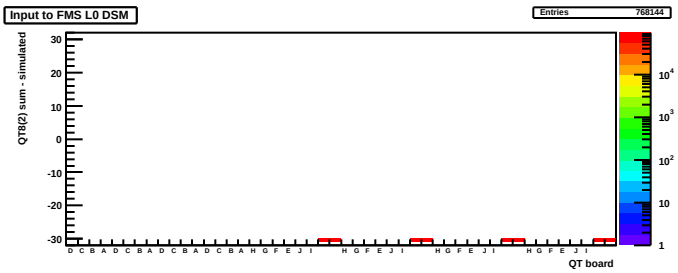

Input to FMS L0 DSM
Entries 4608864
QT8(0) sum
QT board

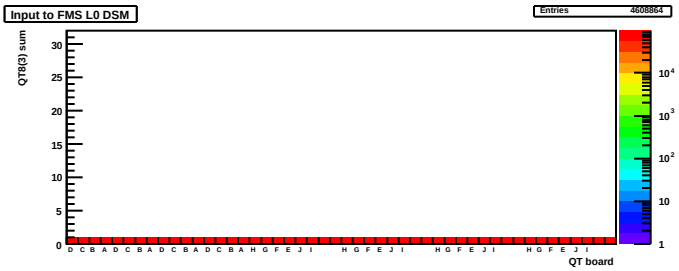

Input to FMS L0 DSM
Entries 768144
QT8(0) sum - simulated
QT board

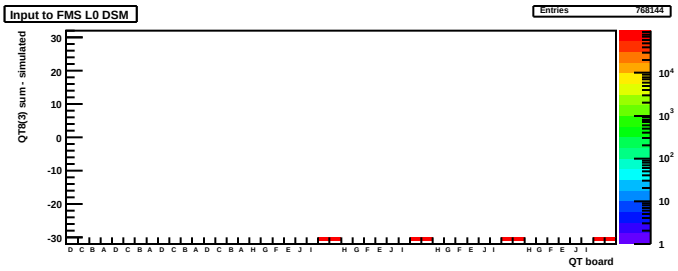

Input to FMS L0 DSM
Entries 4608864
QT8(1) sum
QT board

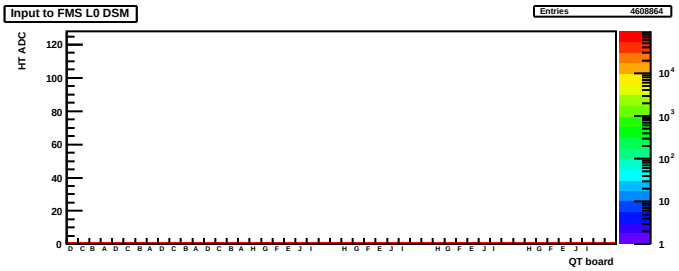

Input to FMS L0 DSM
Entries 768144
QT8(1) sum - simulated
QT board

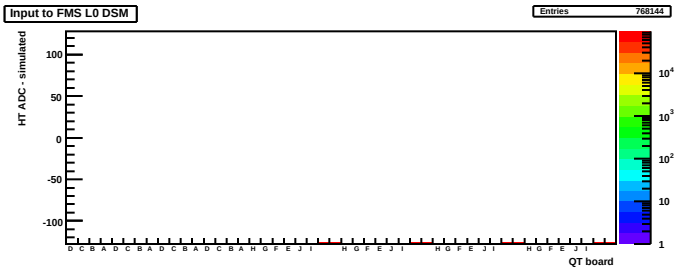

Input to FMS L0 DSM
Entries 4608864
QT8(2) sum
QT board

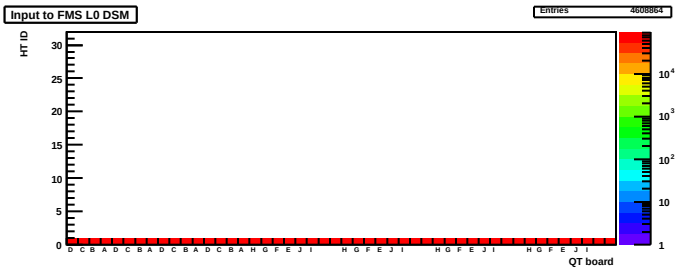

Input to FMS L0 DSM
Entries 768144
QT8(2) sum - simulated
QT board

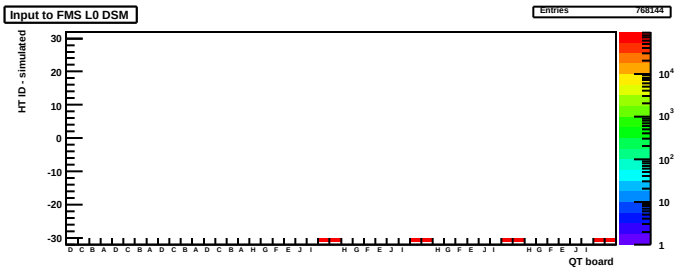

Input to FMS L0 DSM
Entries 4608864
QT8(3) sum
QT board

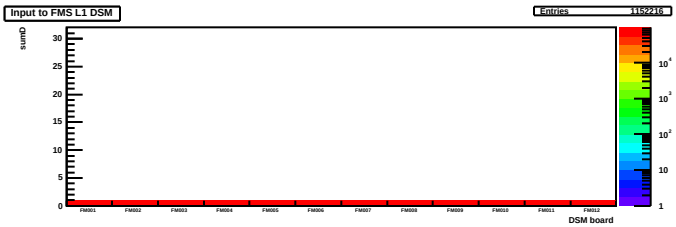

Input to FMS L0 DSM
Entries 768144
QT8(3) sum - simulated
QT board

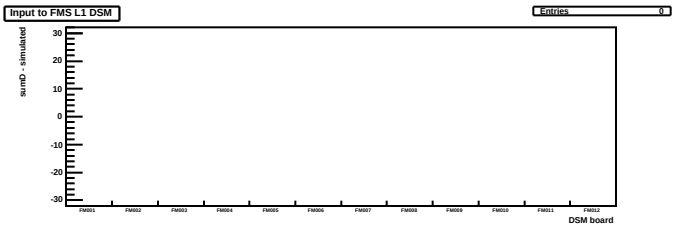

Input to FMS L0 DSM
Entries 4608864
HT ADC
QT board

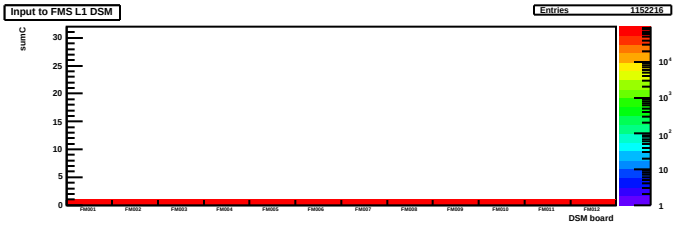

Input to FMS L0 DSM
Entries 768144
HT ADC - simulated
QT board

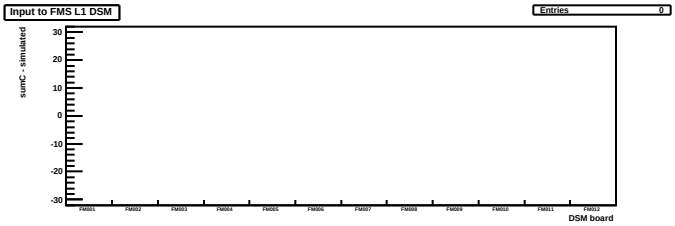

Input to FMS L0 DSM
Entries 4608864
HT ID
QT board

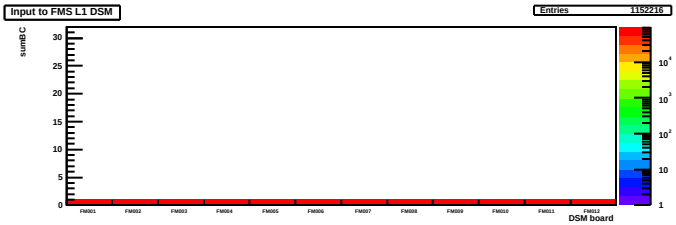

Input to FMS L0 DSM
Entries 768144
HT ID - simulated
QT board

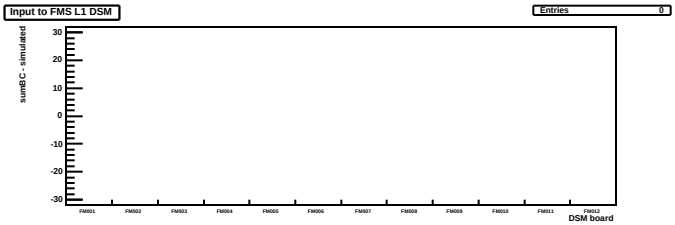

Input to FMS L1 DSM
Entries 1152216
sumD
DSM board

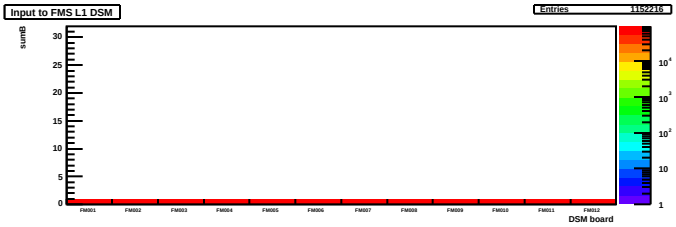

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

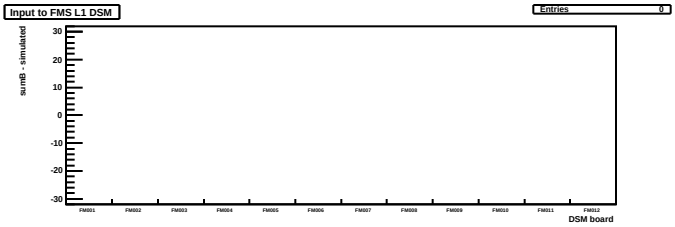

Input to FMS L1 DSM
Entries 1152216
sumC
DSM board

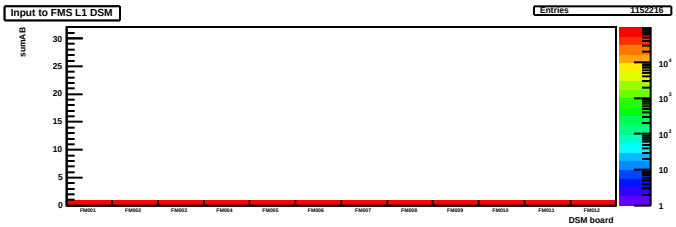

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

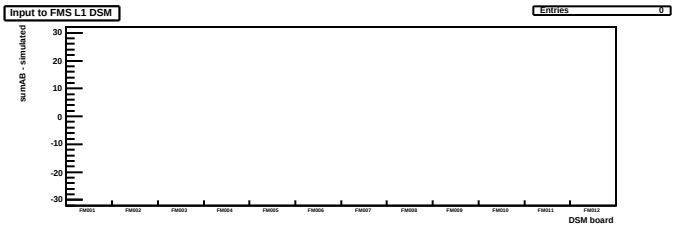

Input to FMS L1 DSM
Entries 1152216
sumBC
DSM board

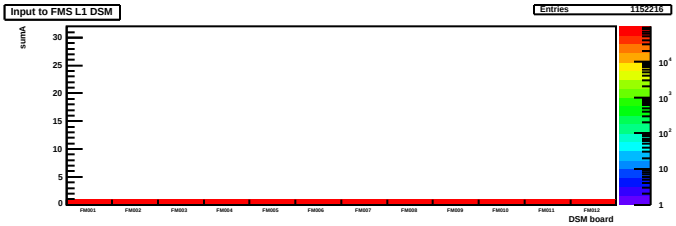

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

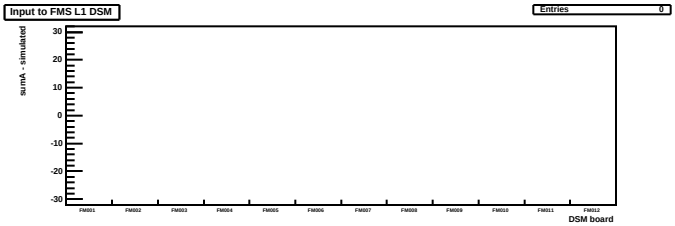

Input to FMS L1 DSM
Entries 1152216
sumB
DSM board

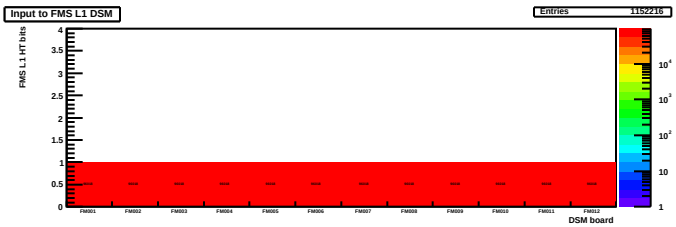

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

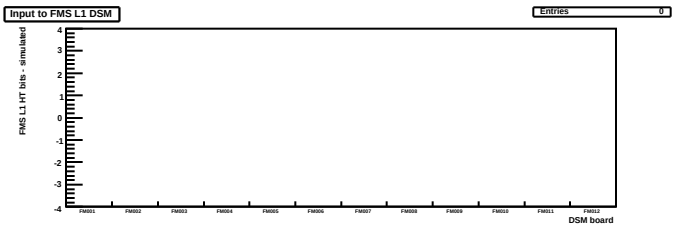

Input to FMS L1 DSM
Entries 1152216
sumAB
DSM board

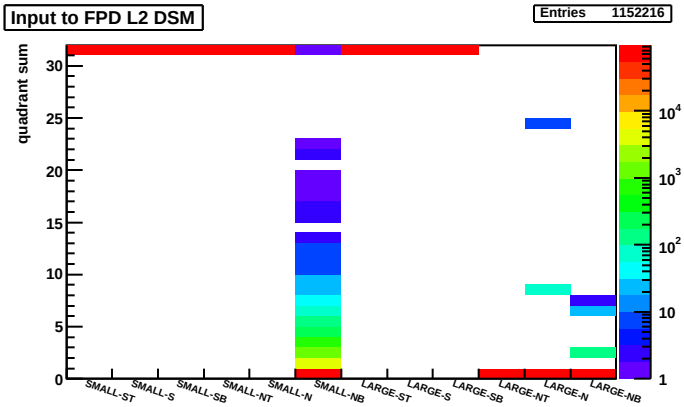

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

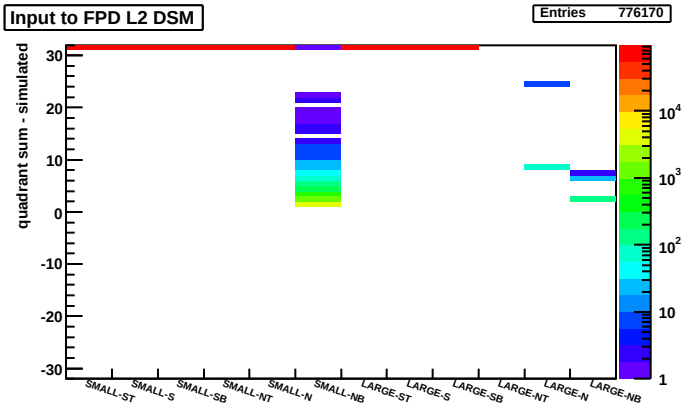

Input to FMS L1 DSM
Entries 1152216
sumA
DSM board

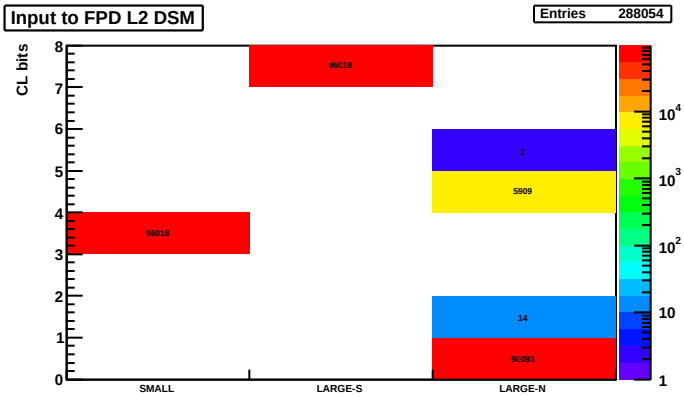

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

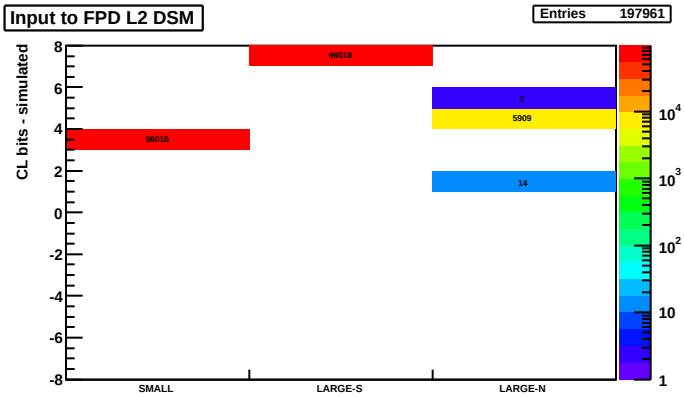

Input to FMS L1 DSM
Entries 1152216
FMS L1 HT bits
DSM board

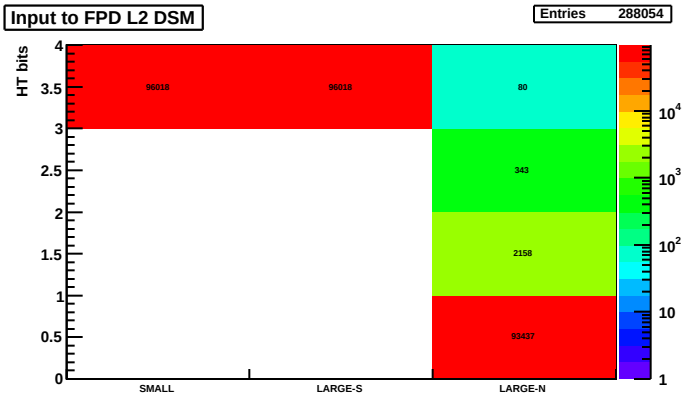

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

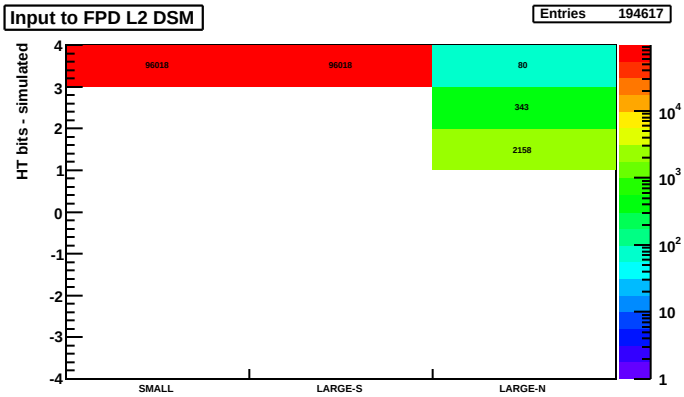

Input to FPD L2 DSM
Entries 1152216
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

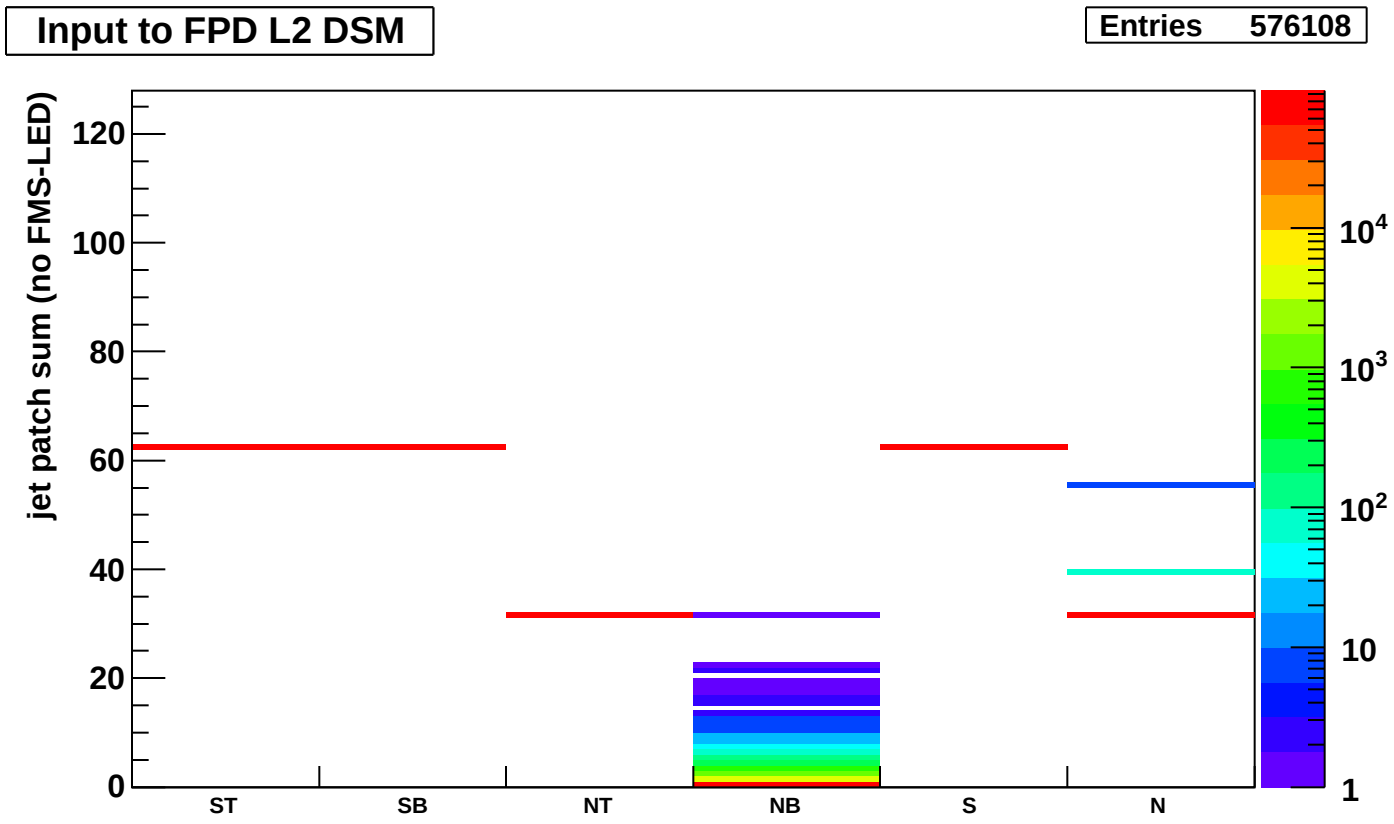

Input to FPD L2 DSM
Entries 776170
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

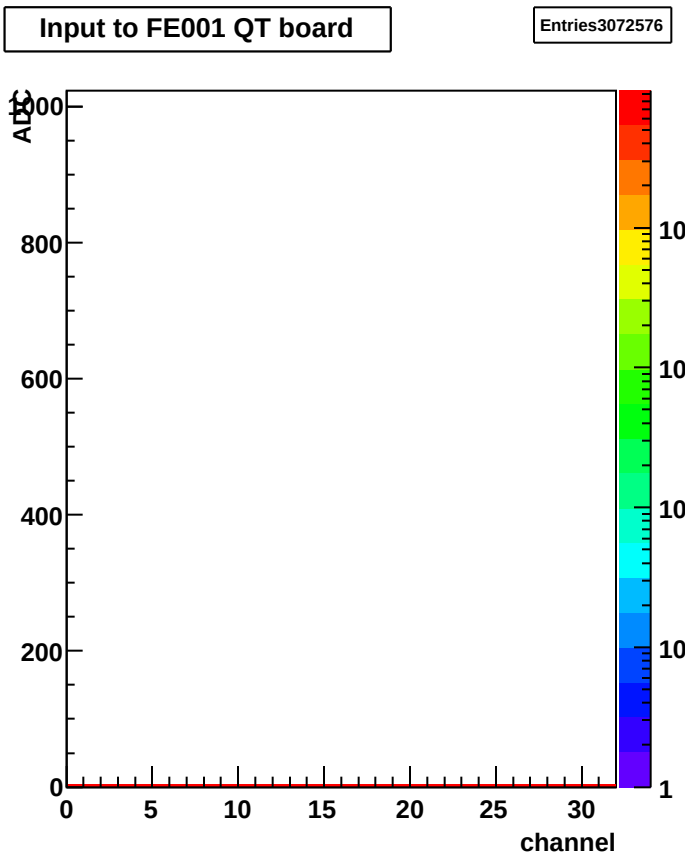

Input to FPD L2 DSM
Entries 288054
CL bits
96018
96018
2
5909
14
90093
SMALL
LARGE-S
LARGE-N

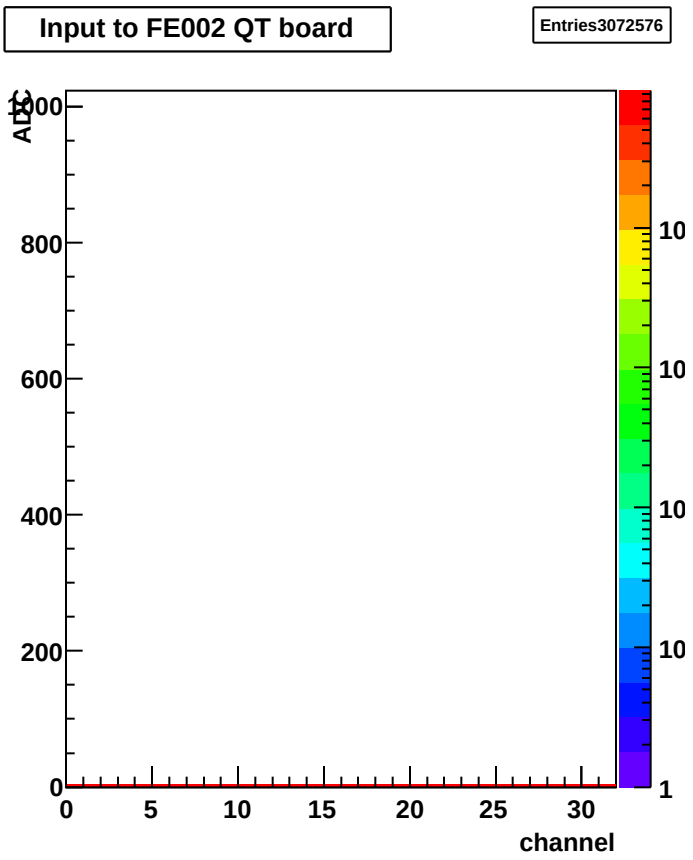

Input to FPD L2 DSM
Entries 197961
CL bits - simulated
96018
96018
2
5909
14
SMALL
LARGE-S
LARGE-N

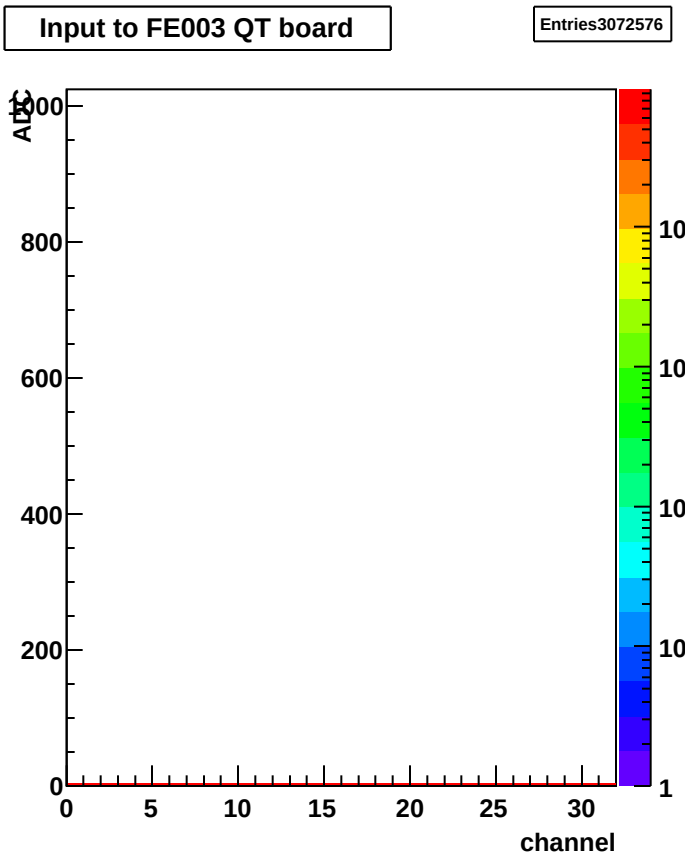

Input to FPD L2 DSM
Entries 288054
HT bits
96018
96018
80
343
2158
93437
SMALL
LARGE-S
LARGE-N

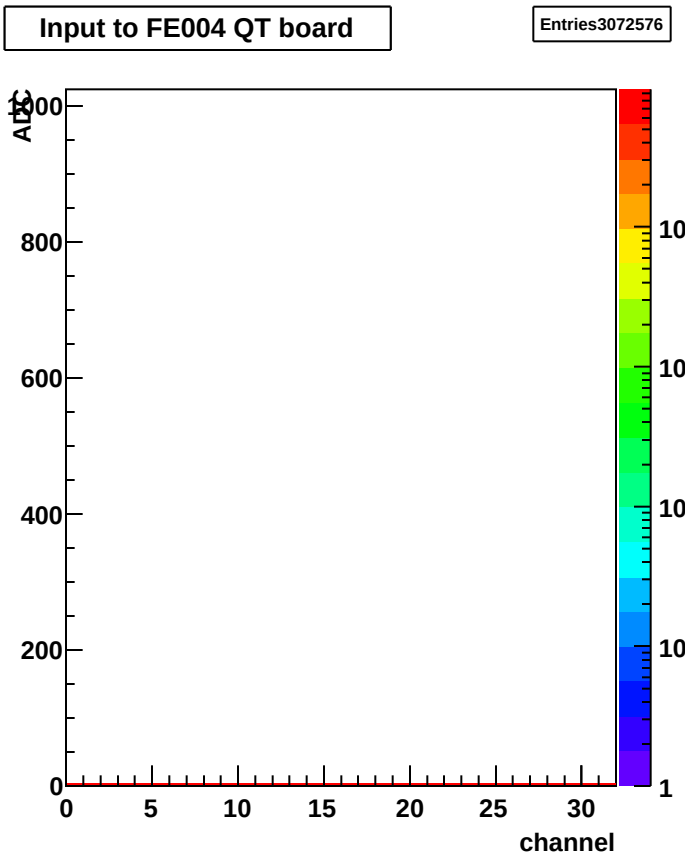

Input to FPD L2 DSM
Entries 194617
HT bits - simulated
96018
96018
80
343
2158
SMALL
LARGE-S
LARGE-N

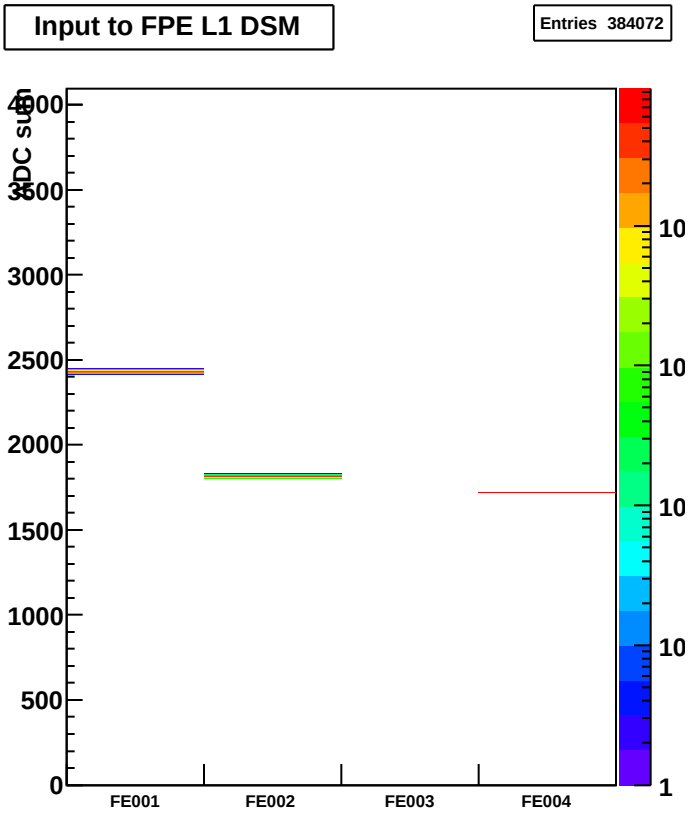

Input to FPD L2 DSM
Entries 576108
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4


Input to FE001 QT board
Entries3072576
ADC
channel

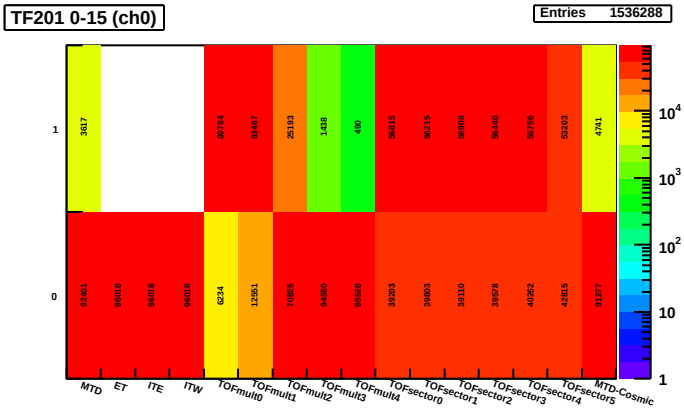

Input to FE002 QT board
Entries3072576
ADC
channel


Input to FE003 QT board
Entries3072576
ADC
channel

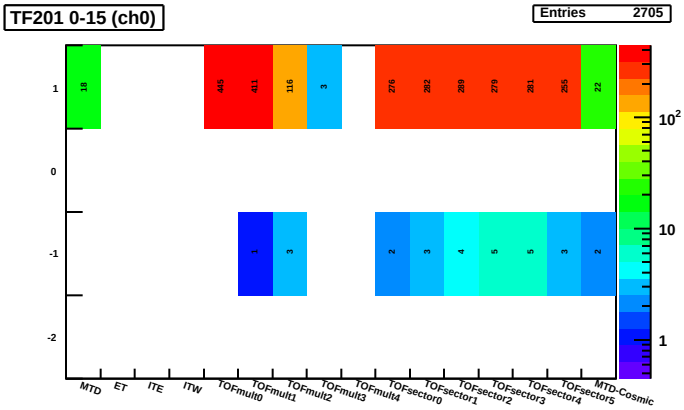

Input to FE004 QT board
Entries3072576
ADC
channel


Input to FPE L1 DSM
Entries 384072
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

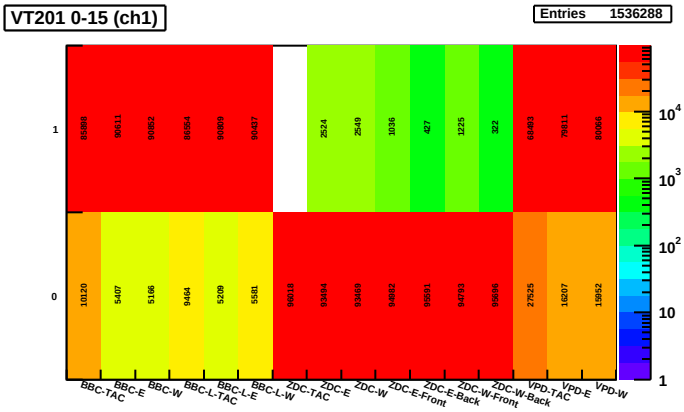
TF201 0-15 (ch0)



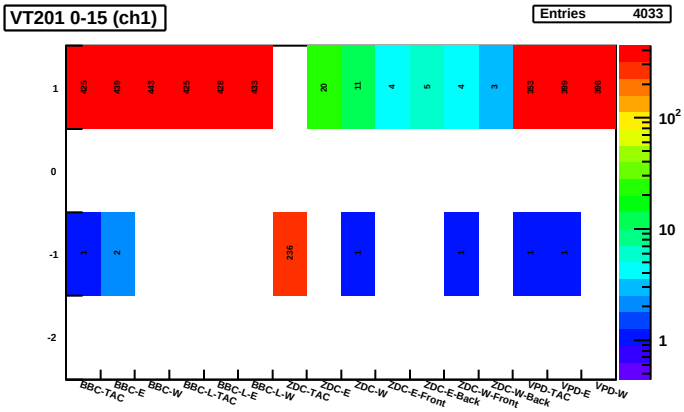
TF201 0-15 (ch0)



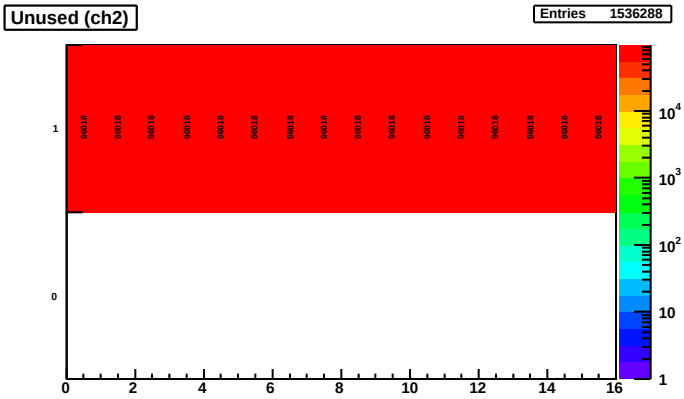
VT201 0-15 (ch1)

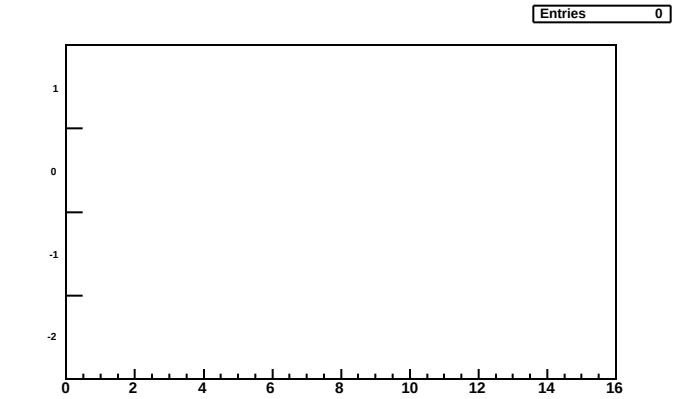


VT201 0-15 (ch1)

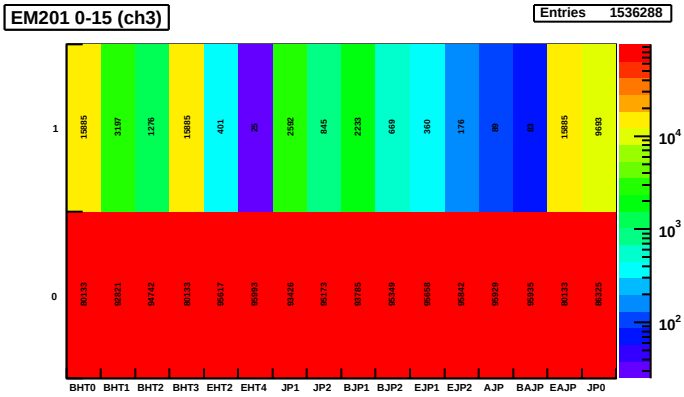


Unused (ch2)

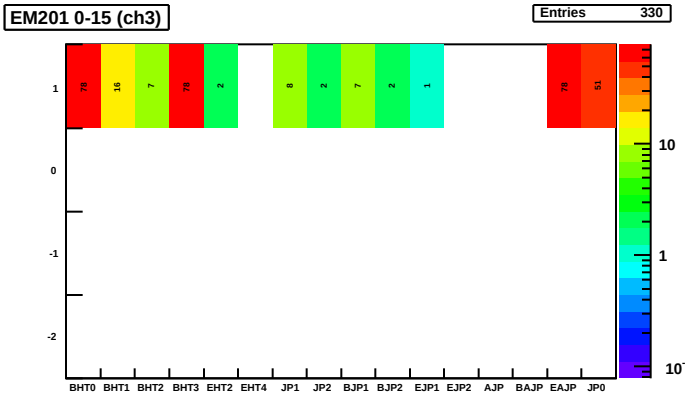




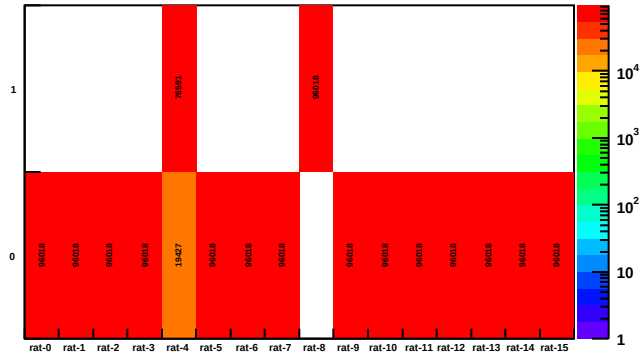
EM201 0-15 (ch3)

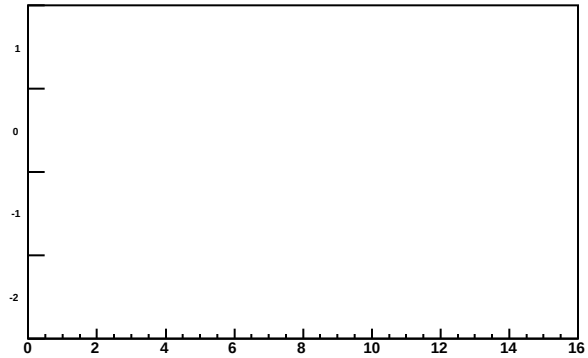


EM201 0-15 (ch3)

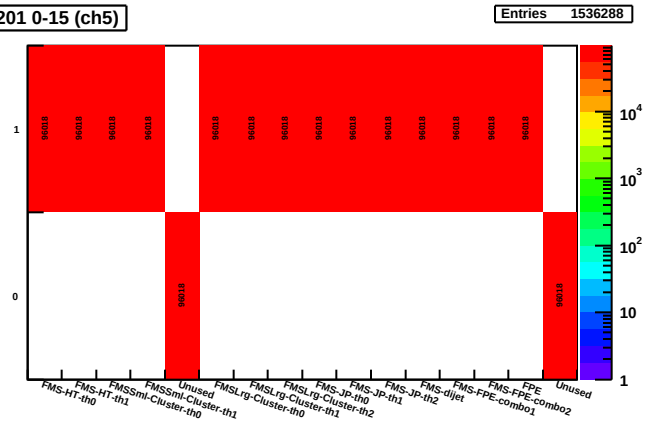


RAT board (ch4)

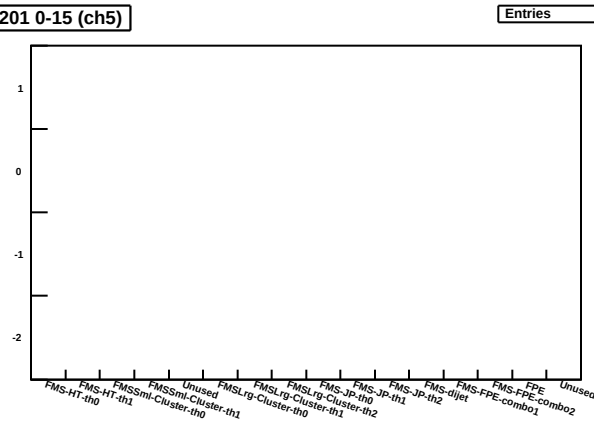




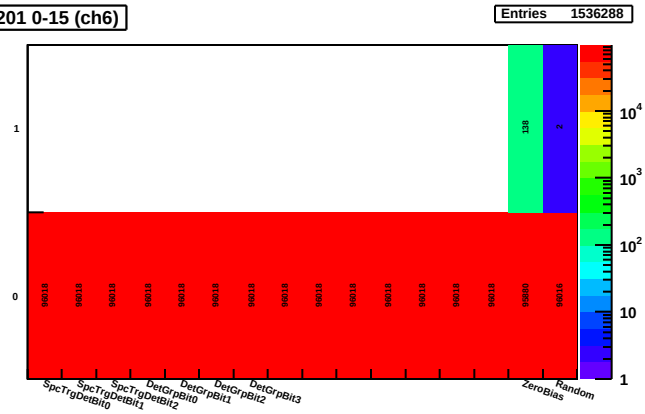
FP201 0-15 (ch5)

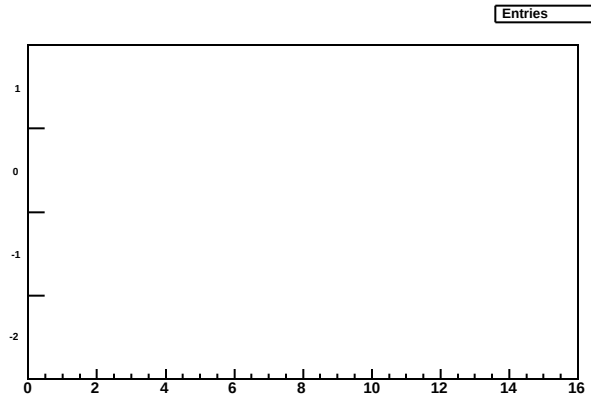
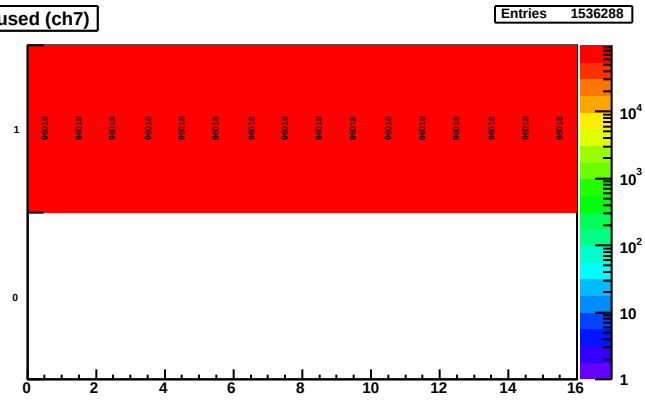
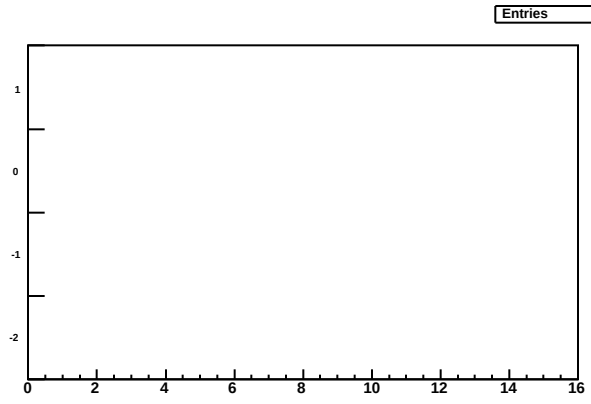


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)