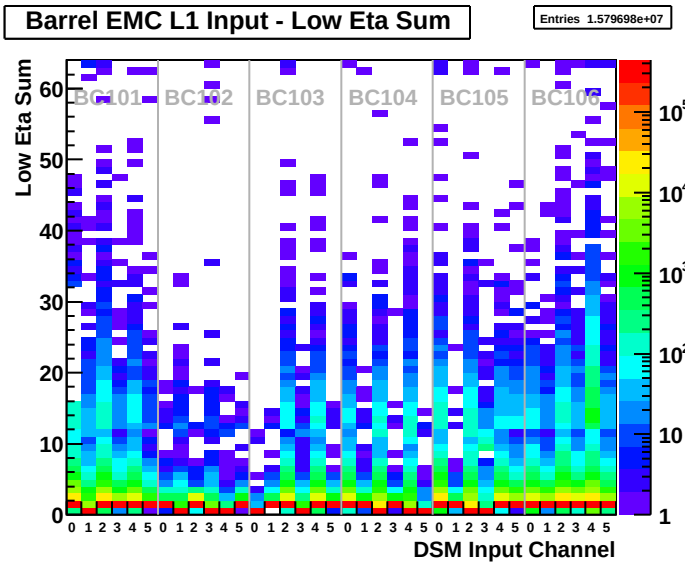
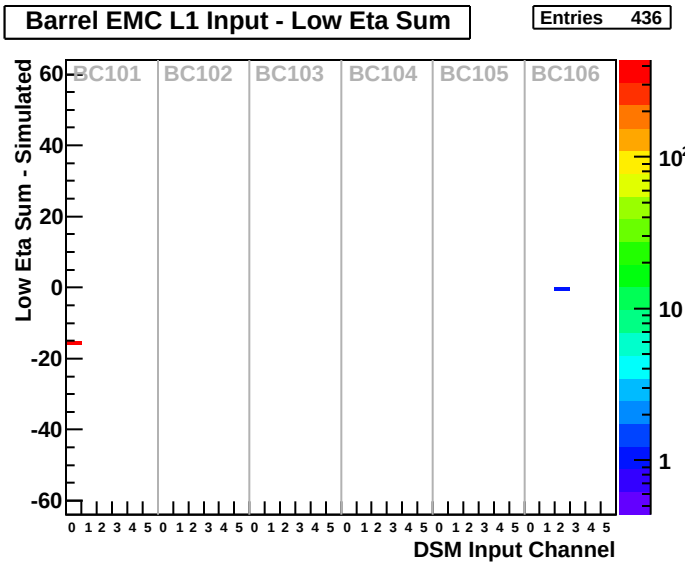
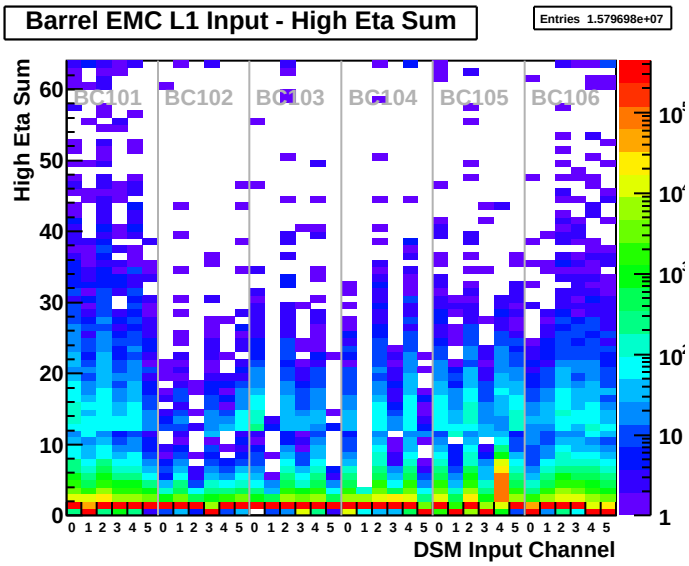
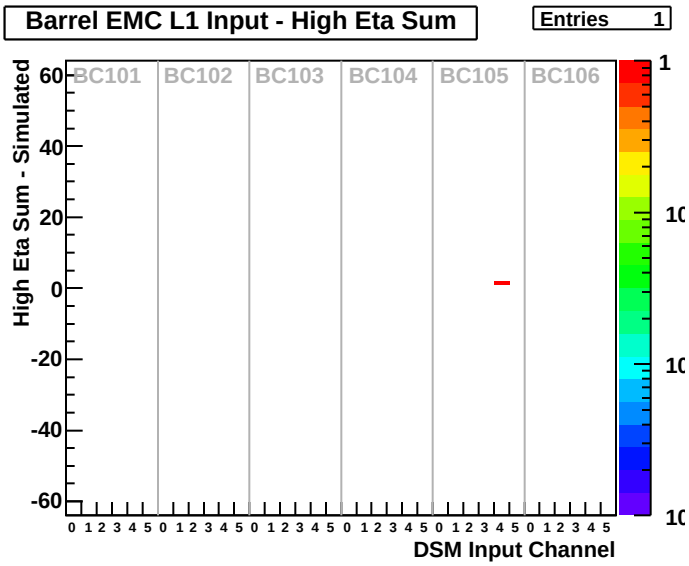
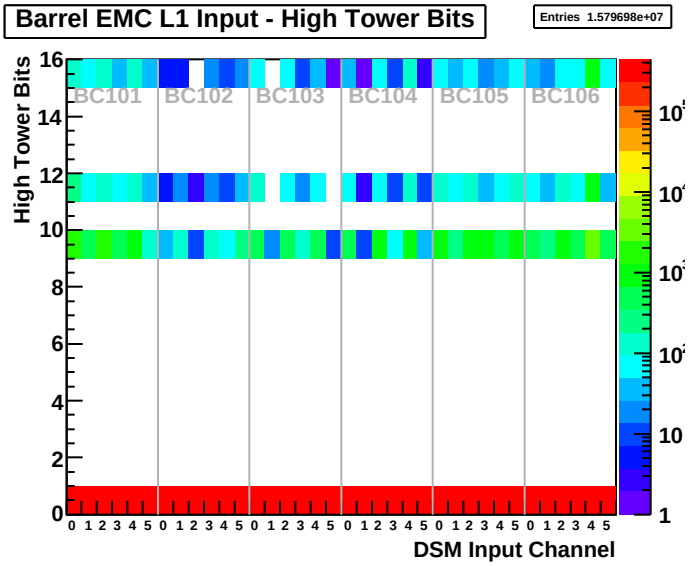
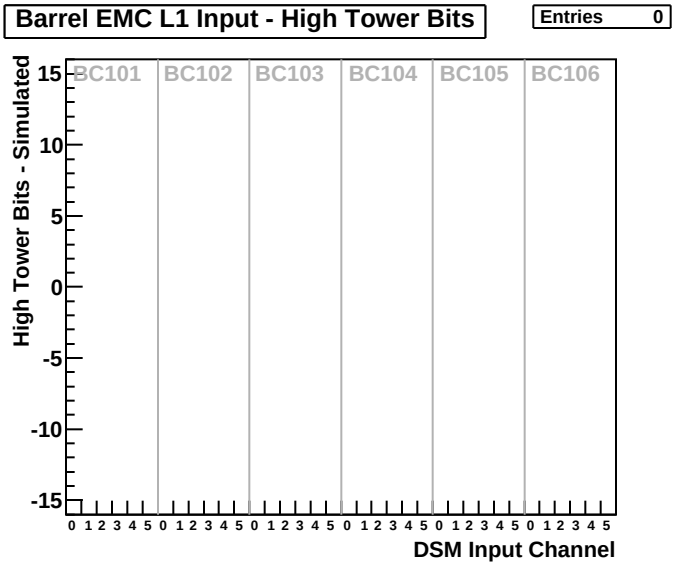

Barrel EMC L1 Input - Low Eta Sum
Entries 1.579698e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

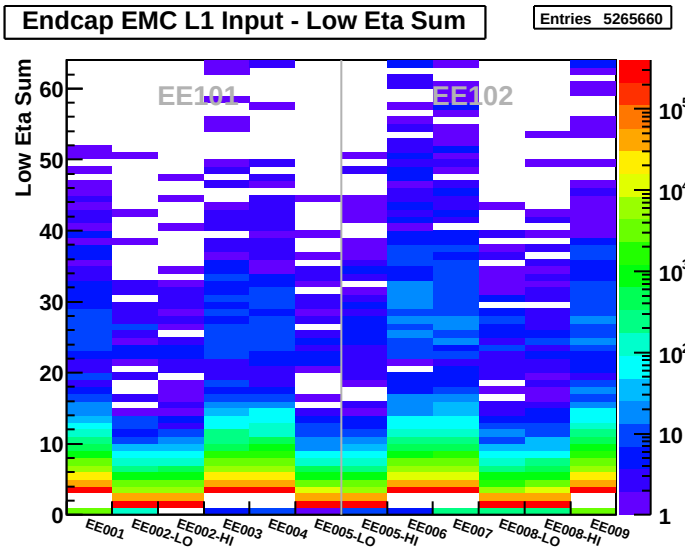

Barrel EMC L1 Input - Low Eta Sum
Entries 436
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

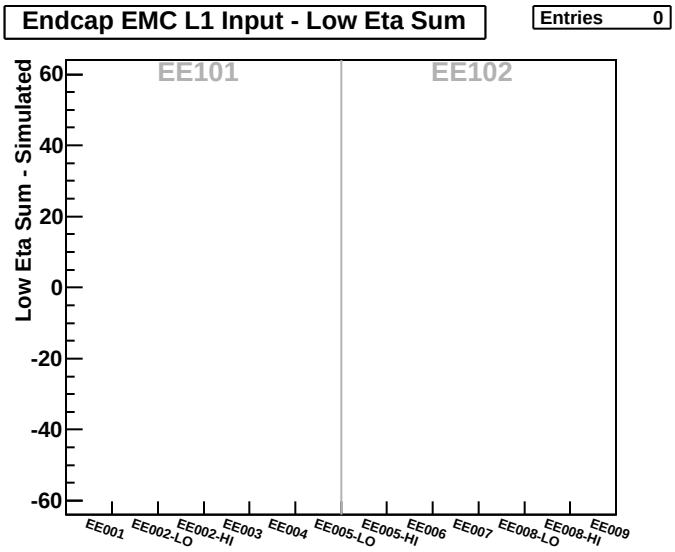

Barrel EMC L1 Input - High Eta Sum
Entries 1.579698e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

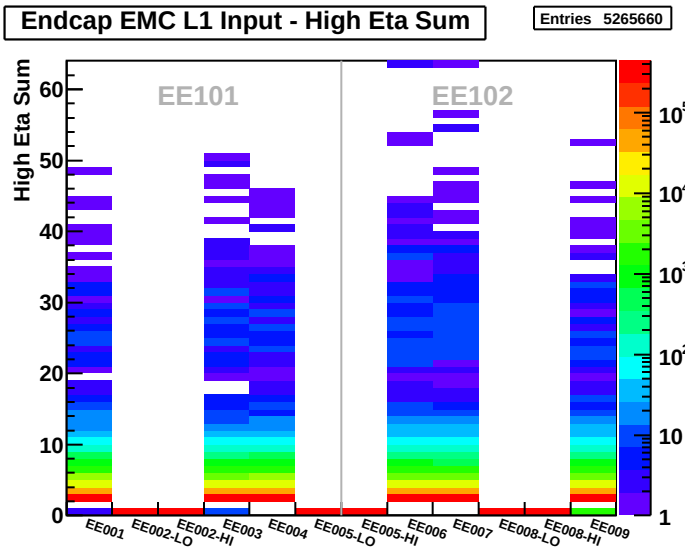

Barrel EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

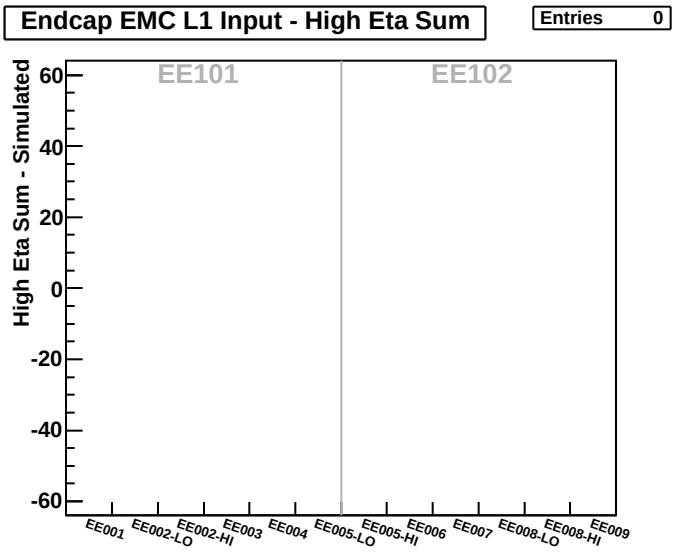

Barrel EMC L1 Input - High Tower Bits
Entries 1.579698e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

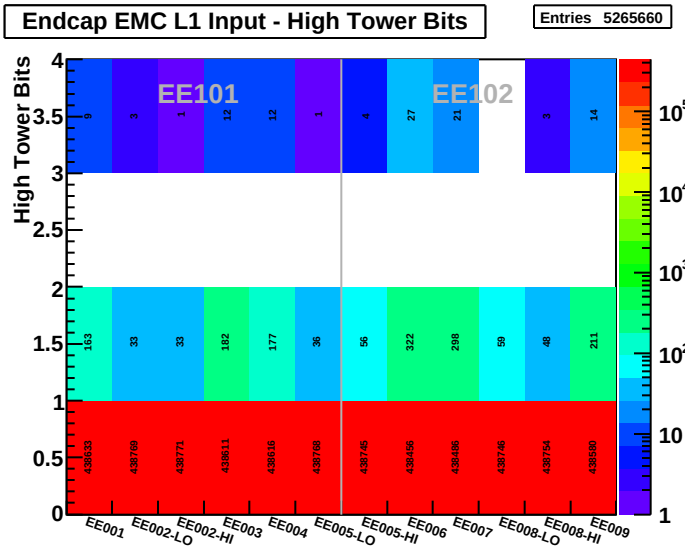

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

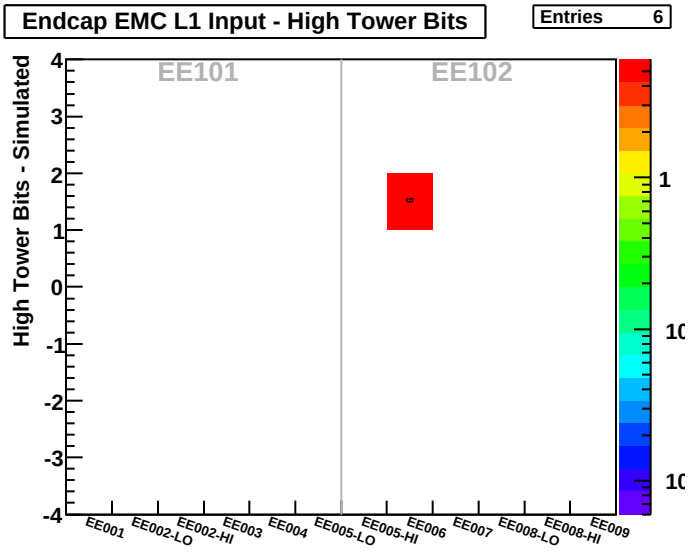

Endcap EMC L1 Input - Low Eta Sum
Entries 5265660
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

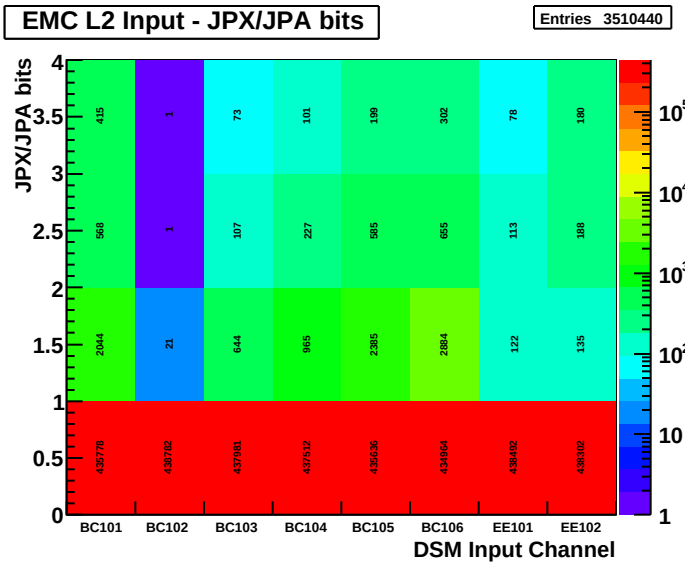

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

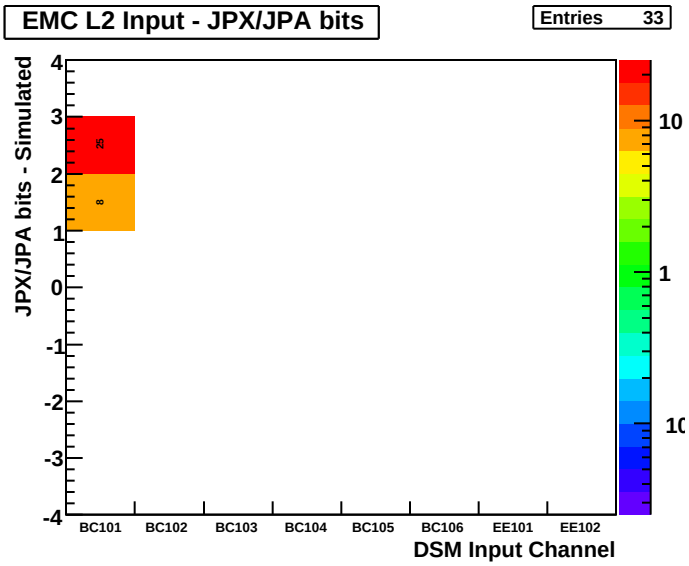

Endcap EMC L1 Input - High Eta Sum
Entries 5265660
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

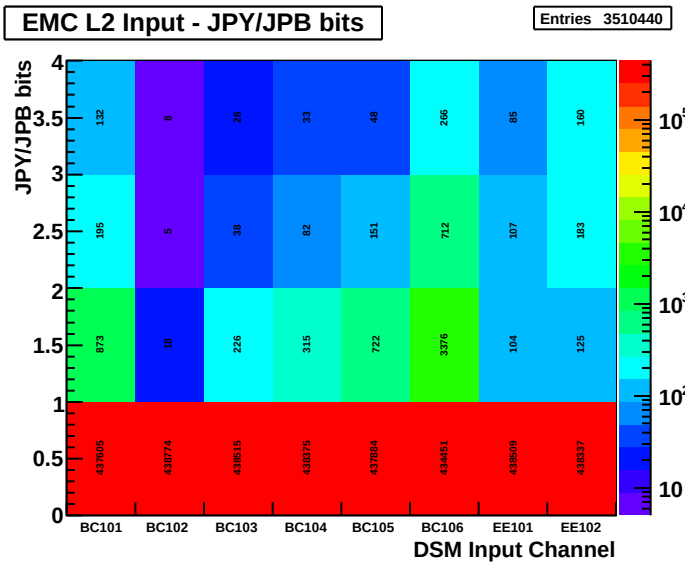

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

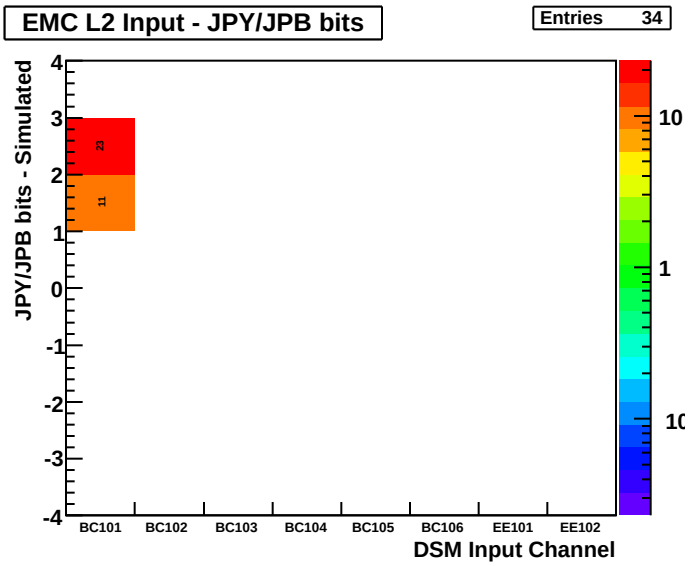

Endcap EMC L1 Input - High Tower Bits
Entries 5265660
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

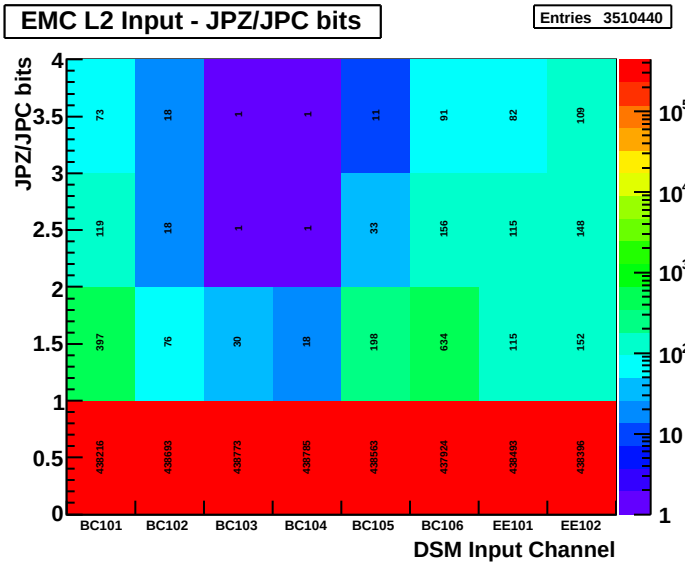

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

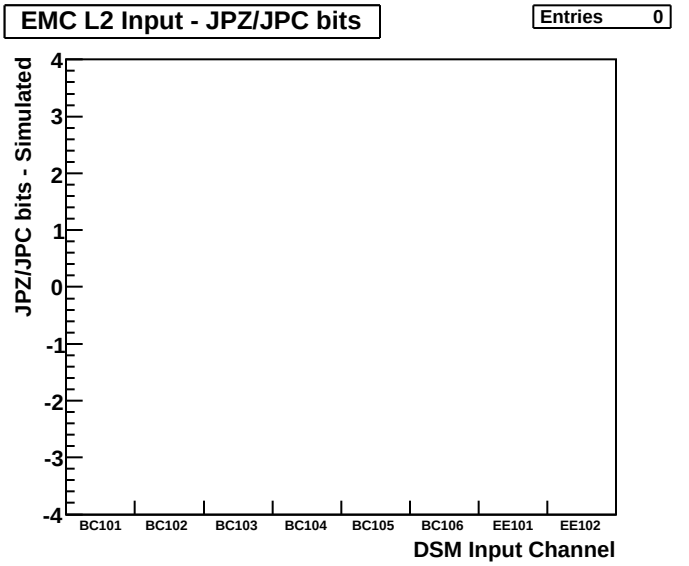

EMC L2 Input - JPX/JPA bits
Entries 3510440
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

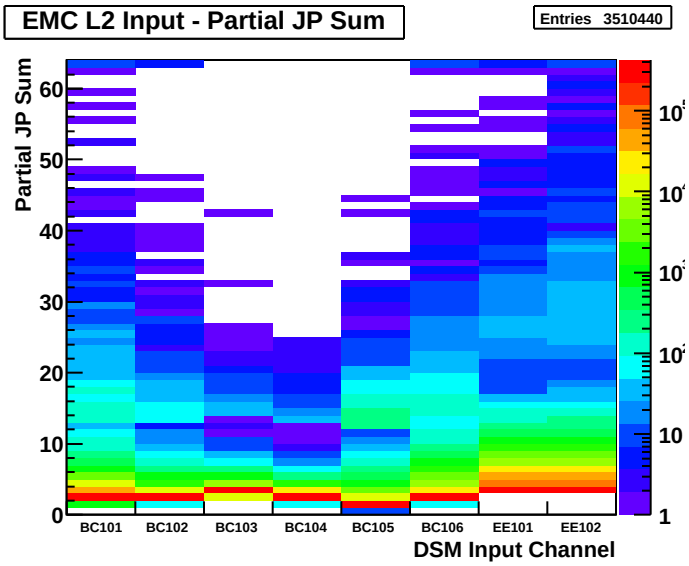

EMC L2 Input - JPX/JPA bits
Entries 33
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

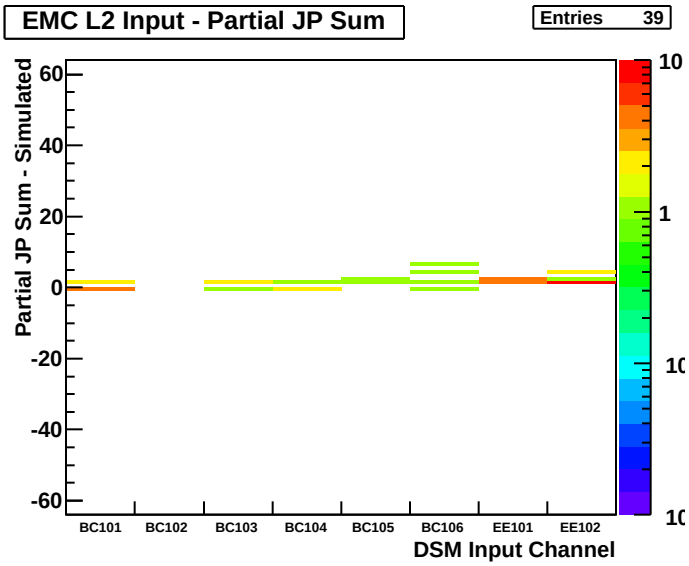

EMC L2 Input - JPY/JPB bits
Entries 3510440
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

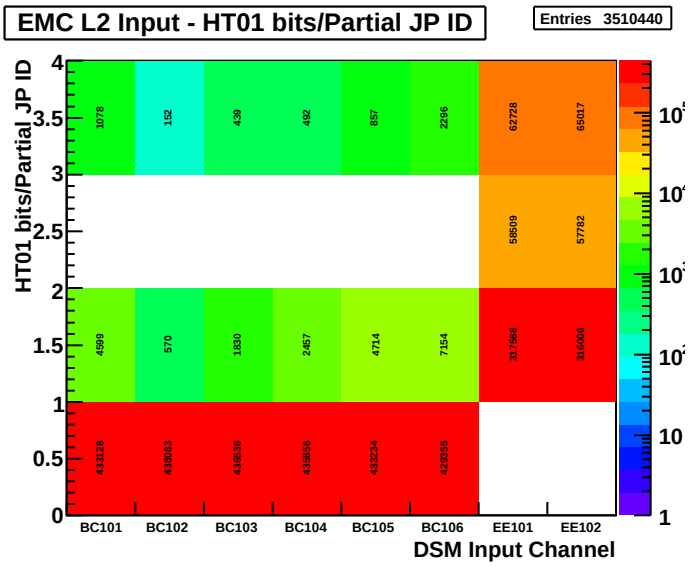

EMC L2 Input - JPY/JPB bits
Entries 34
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

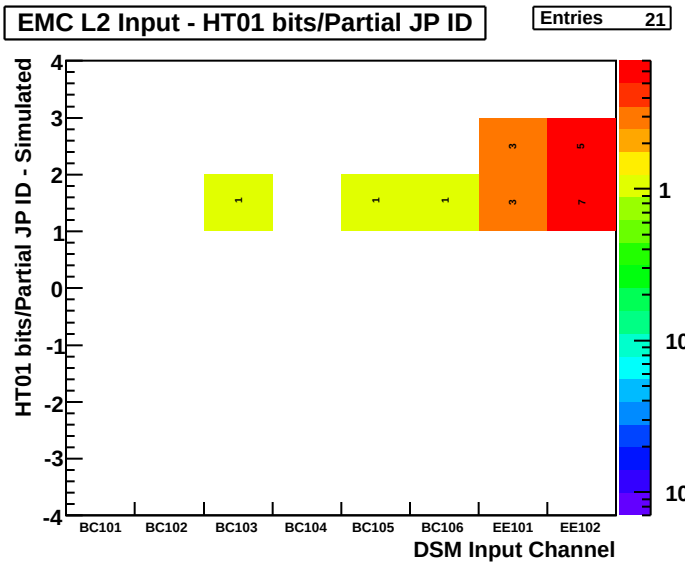

EMC L2 Input - JPZ/JPC bits
Entries 3510440
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

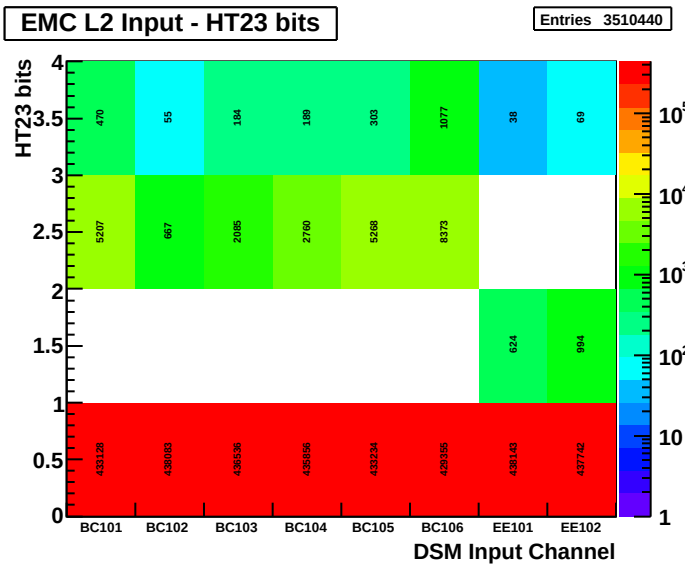

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

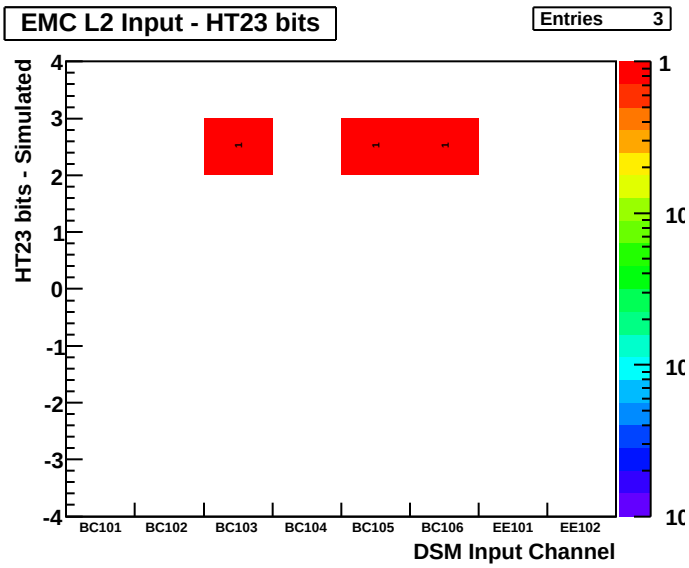

EMC L2 Input - Partial JP Sum
Entries 3510440
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

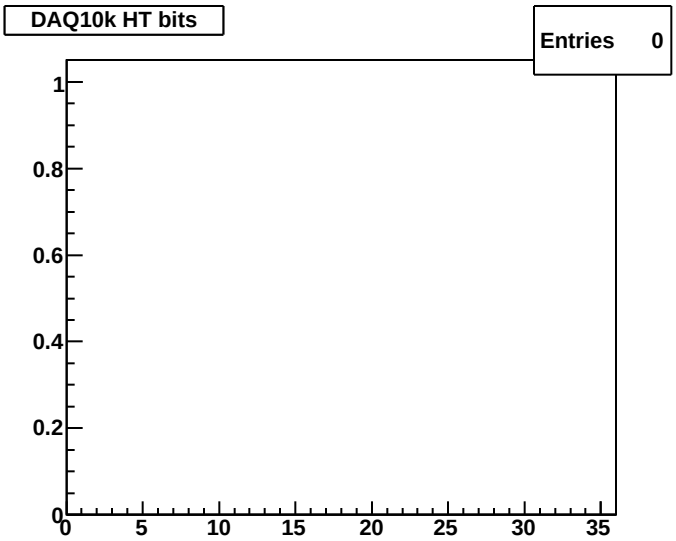

EMC L2 Input - Partial JP Sum
Entries 39
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

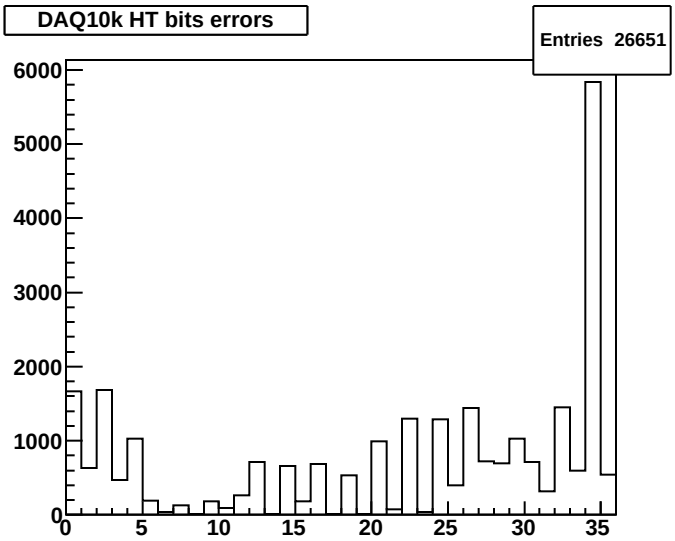

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3510440
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

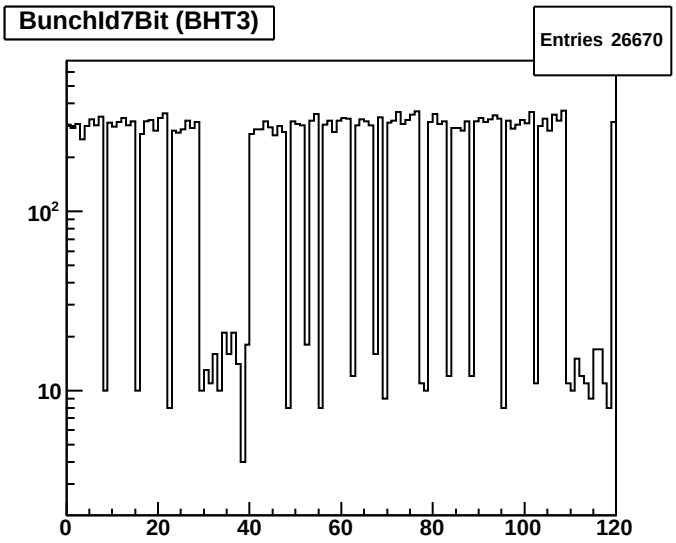

EMC L2 Input - HT01 bits/Partial JP ID
Entries 21
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

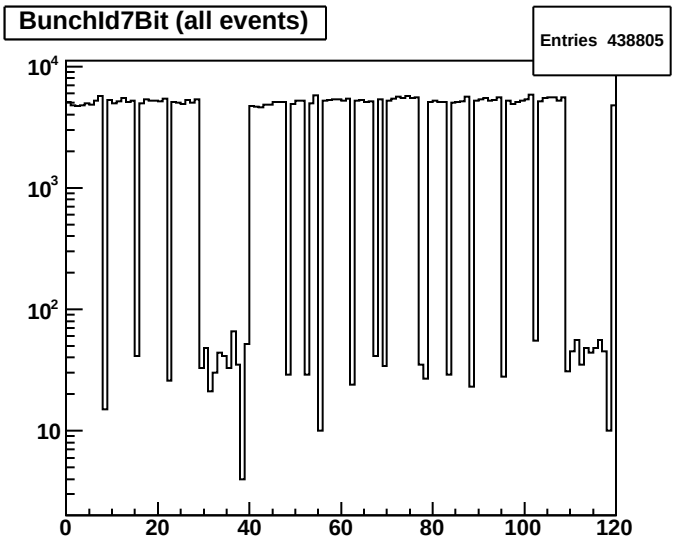

EMC L2 Input - HT23 bits
Entries 3510440
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

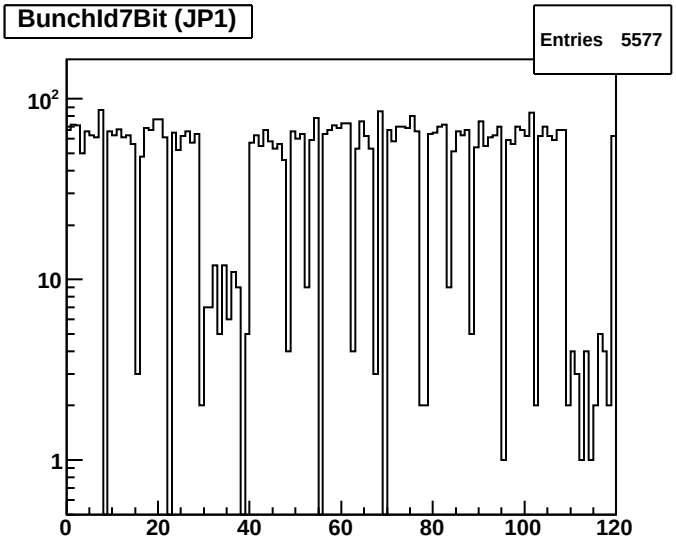

EMC L2 Input - HT23 bits
Entries 3
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

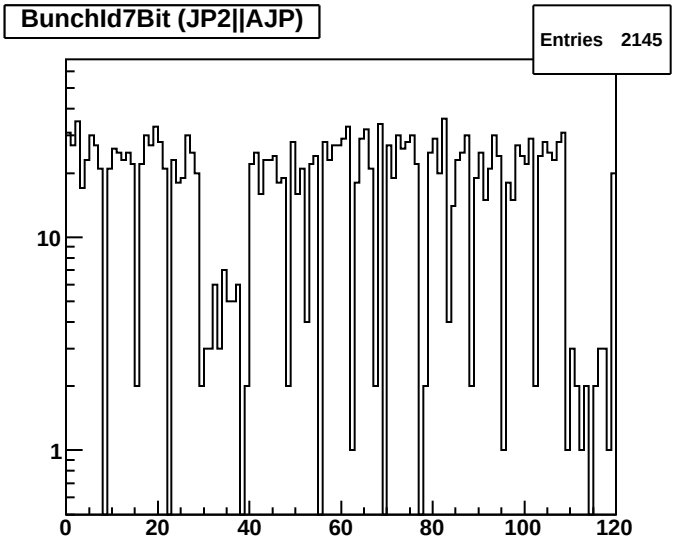

DAQ10k HT bits
Entries 0

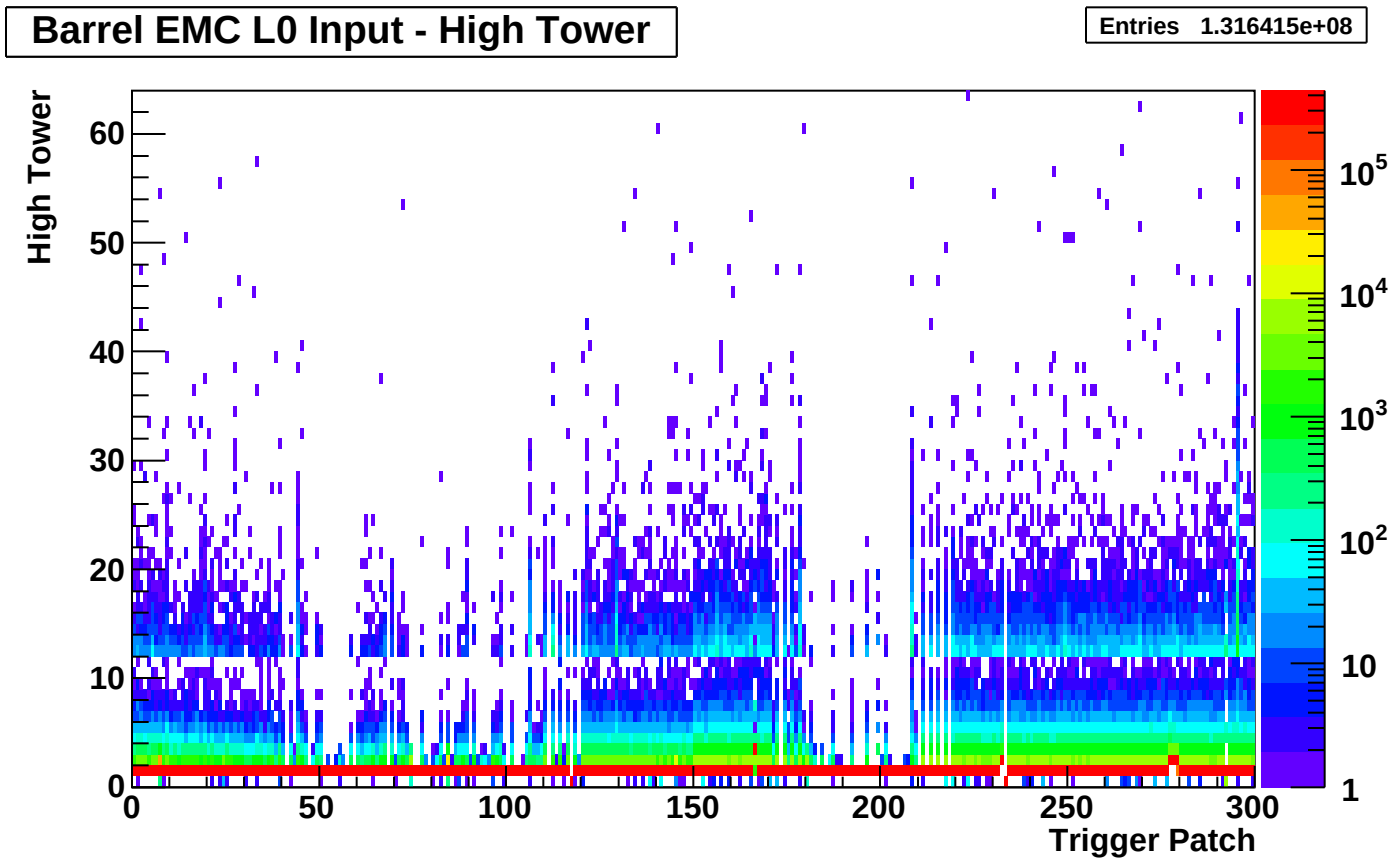

DAQ10k HT bits errors
Entries 26651

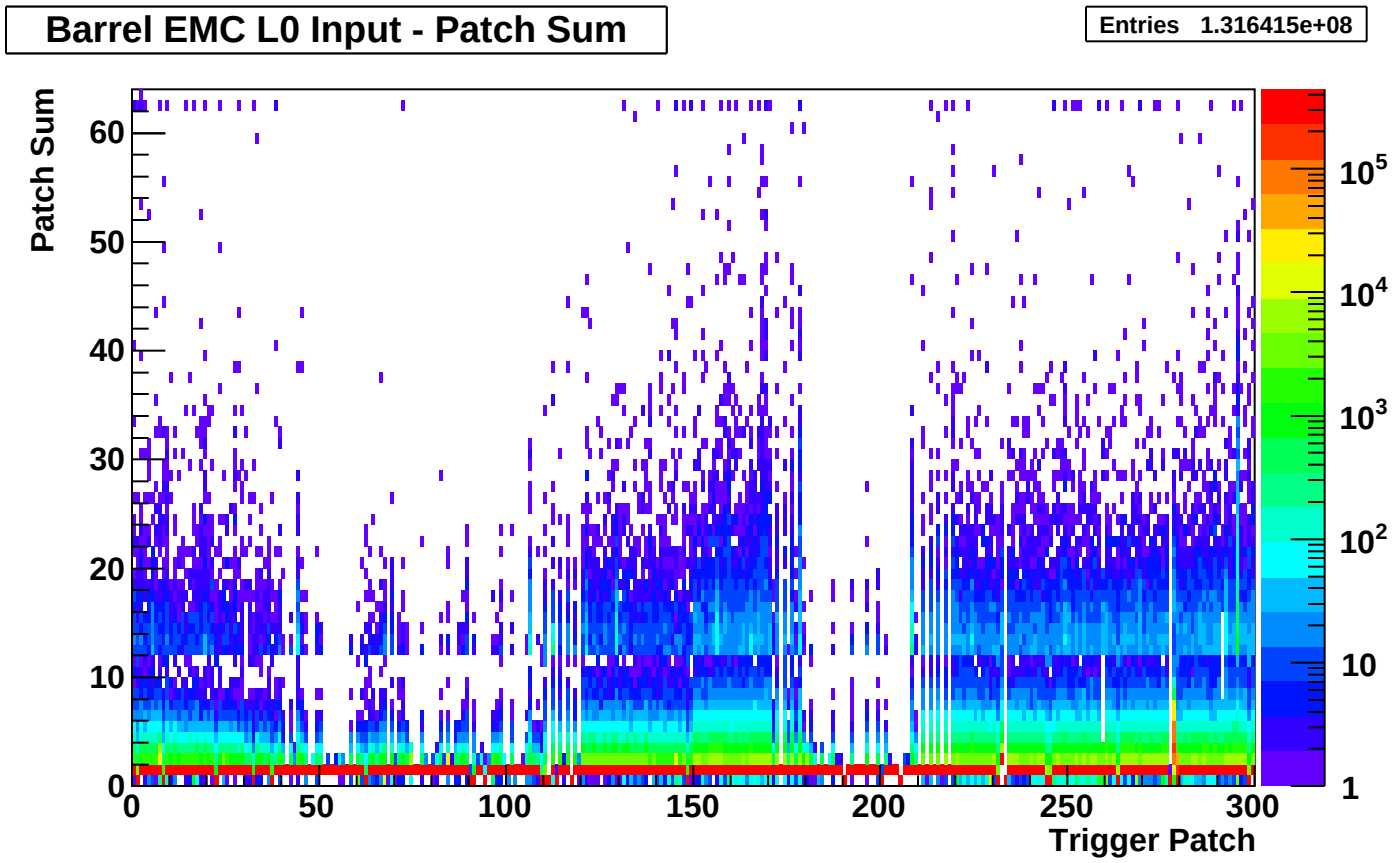

BunchId7Bit (BHT3)
Entries 26670

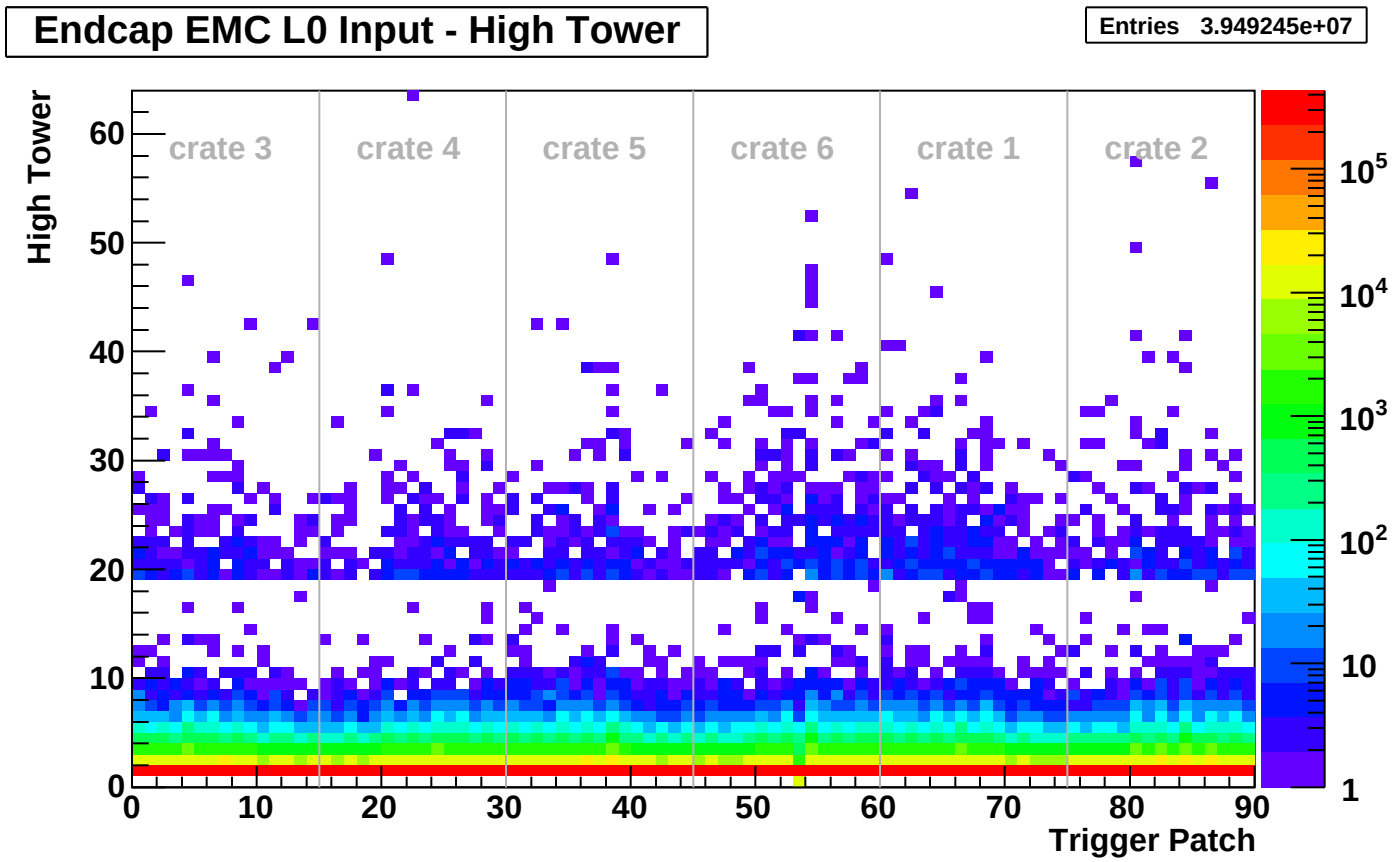

BunchId7Bit (all events)
Entries 438805

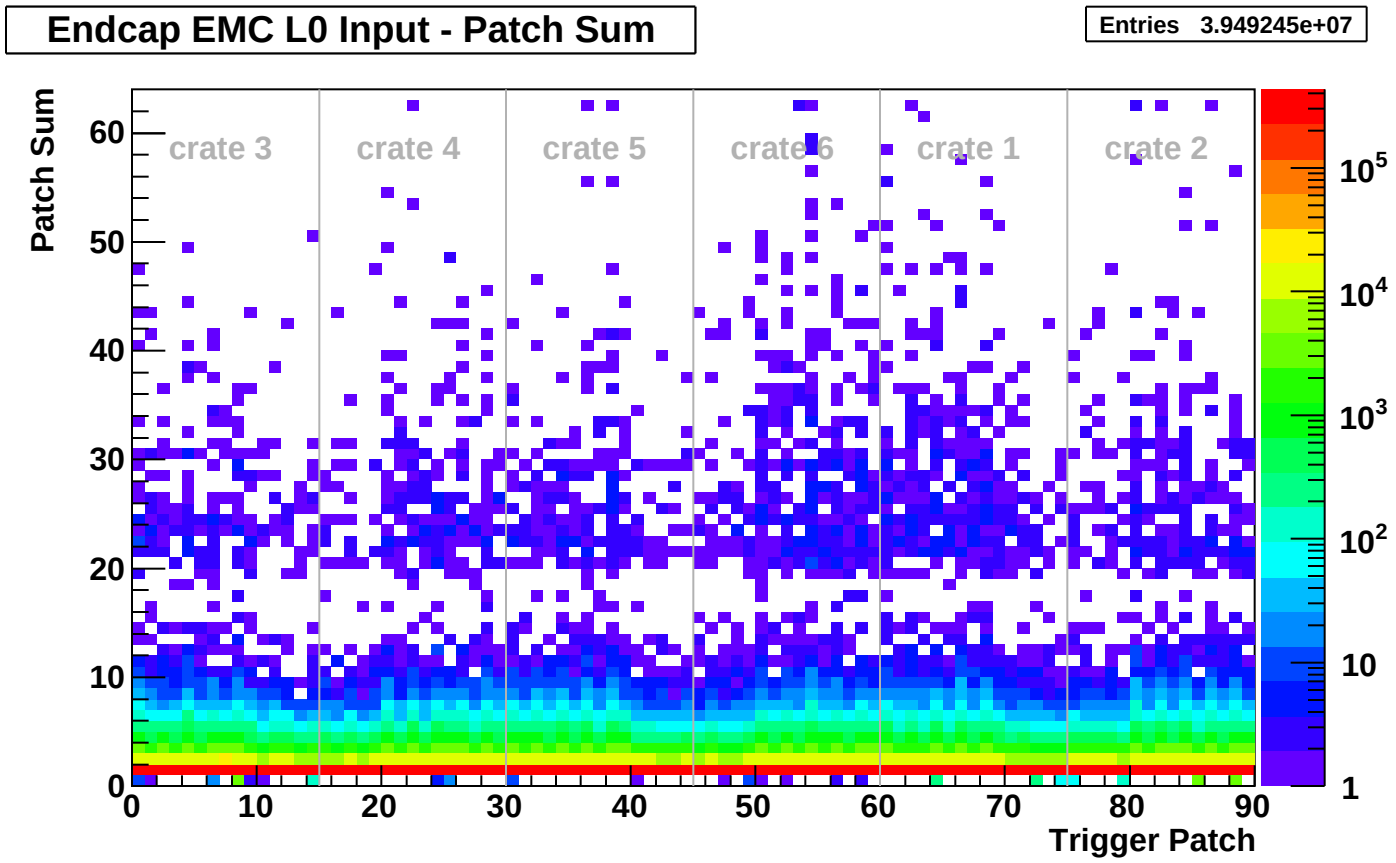

BunchId7Bit (JP1)
Entries 5577

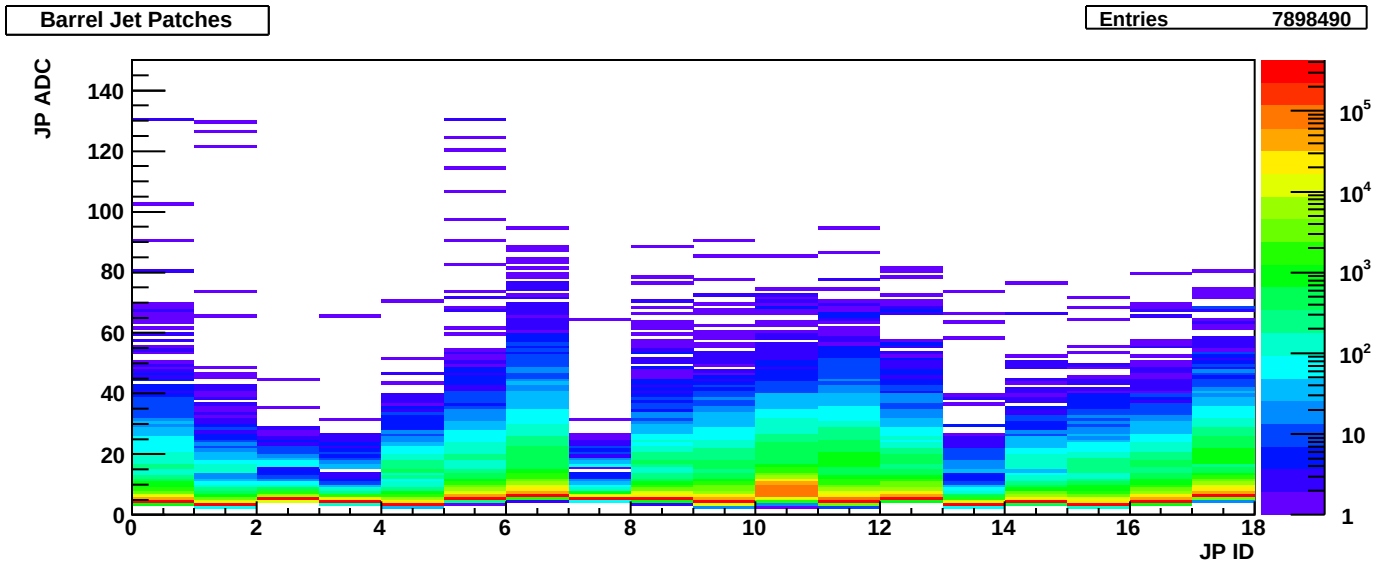

BunchId7Bit (JP2||AJP)
Entries 2145

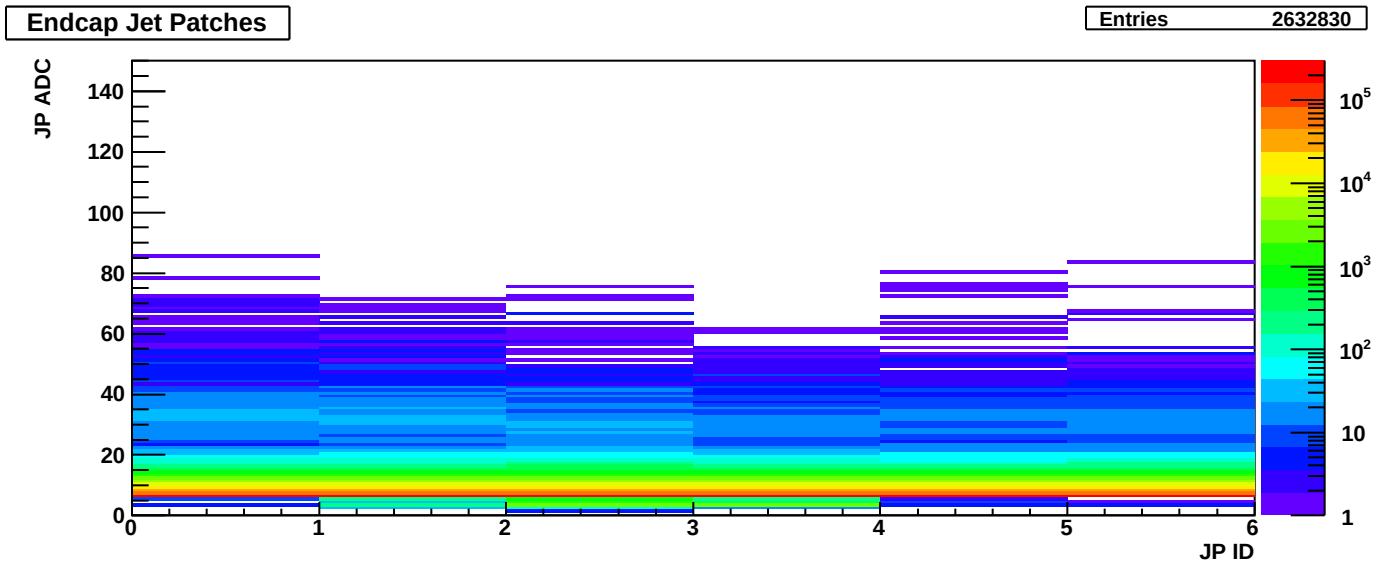

Barrel EMC L0 Input - High Tower
Entries 1.316415e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

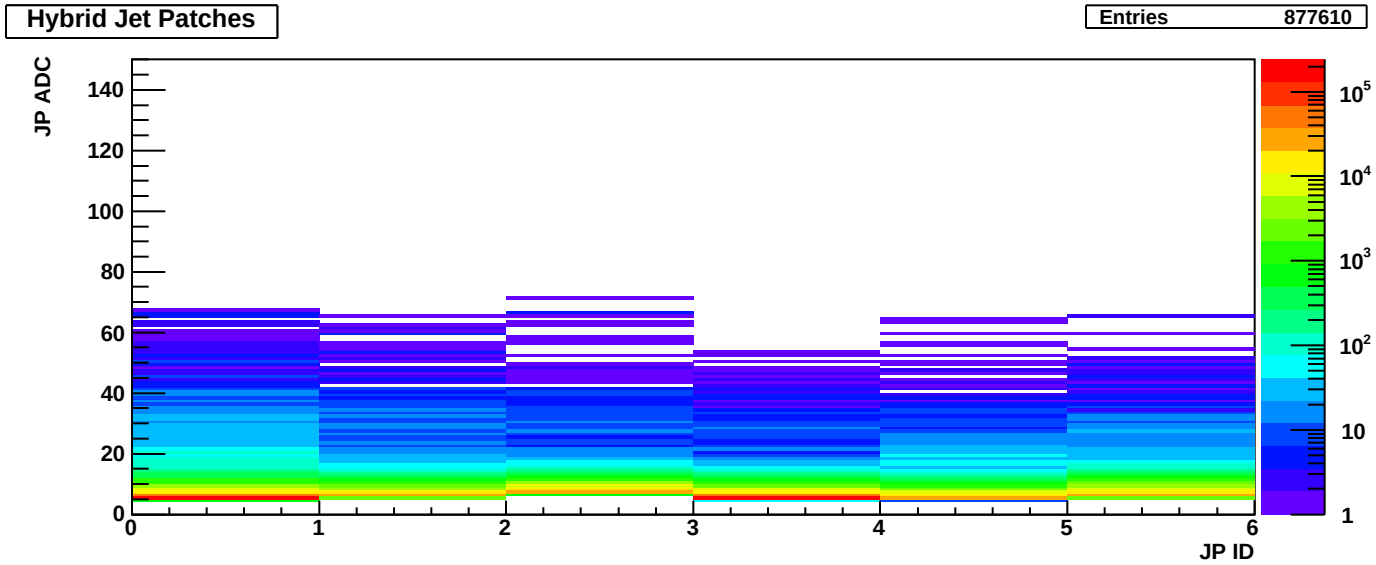

Barrel EMC L0 Input - Patch Sum
Entries 1.316415e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

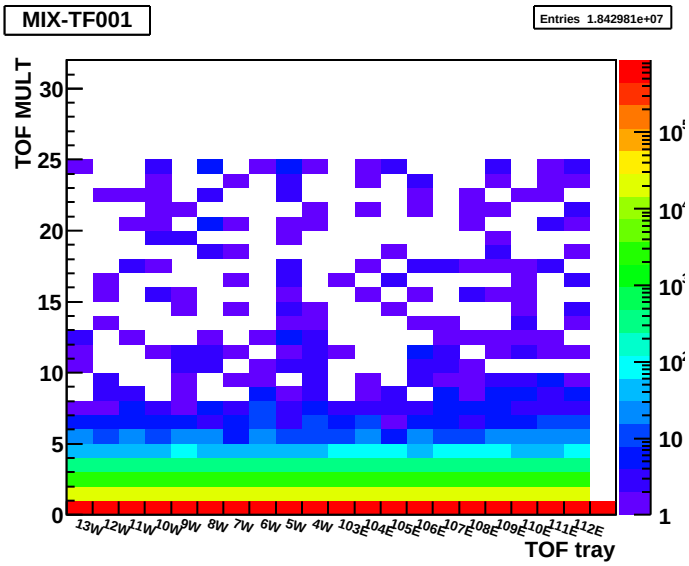

Endcap EMC L0 Input - High Tower
Entries 3.949245e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

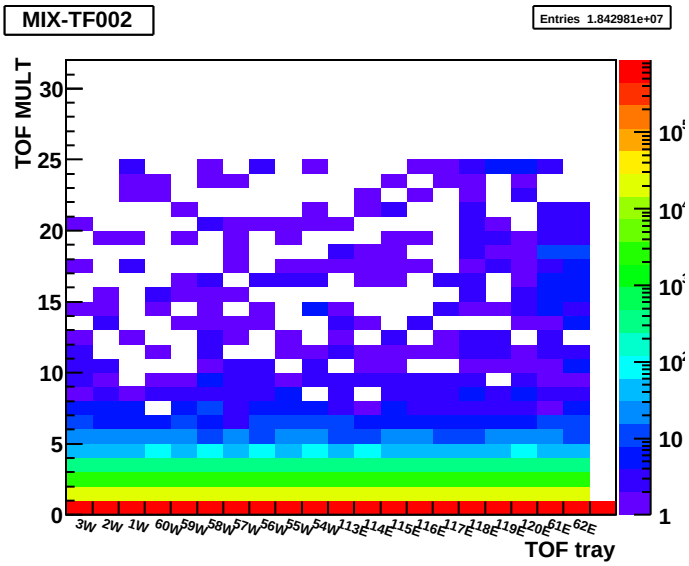

Endcap EMC L0 Input - Patch Sum
Entries 3.949245e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

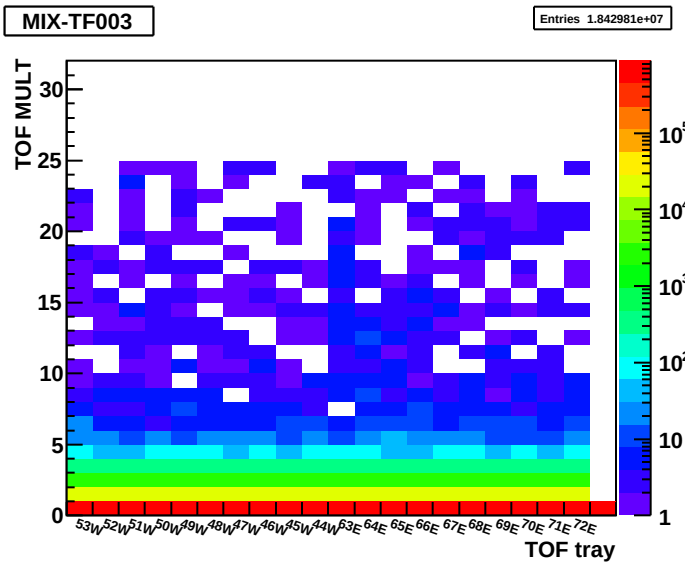

Barrel Jet Patches
Entries 7898490
JP ADC
JP ID

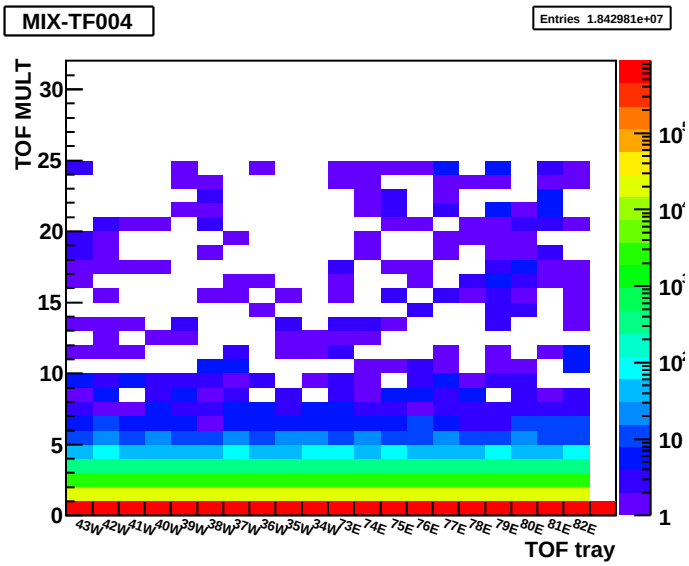

Endcap Jet Patches
Entries 2632830
JP ADC
JP ID

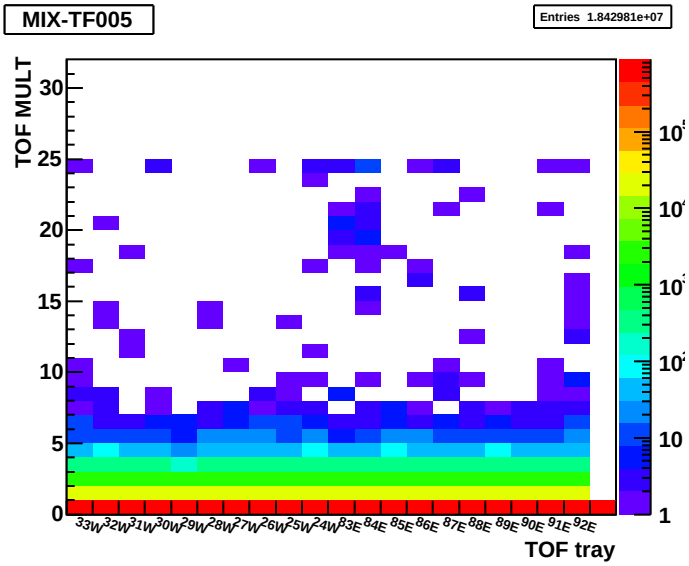

Hybrid Jet Patches
Entries 877610
JP ADC
JP ID

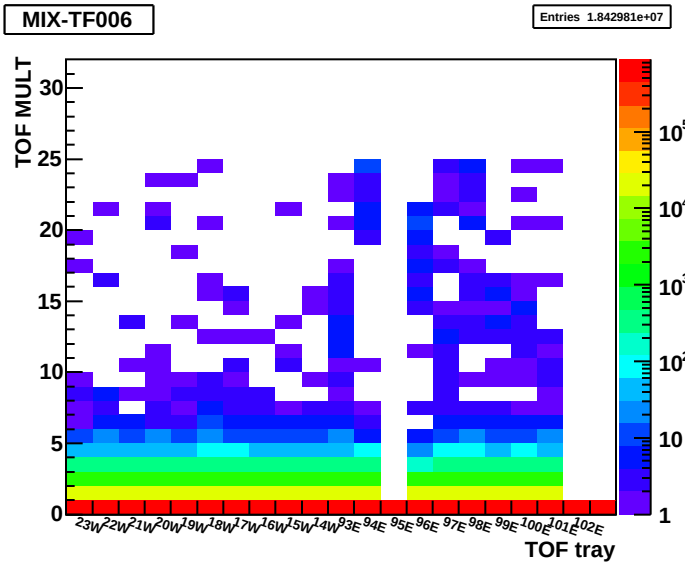

MIX-TF001
Entries 1.842981e+07
TOF MULT
TOF tray

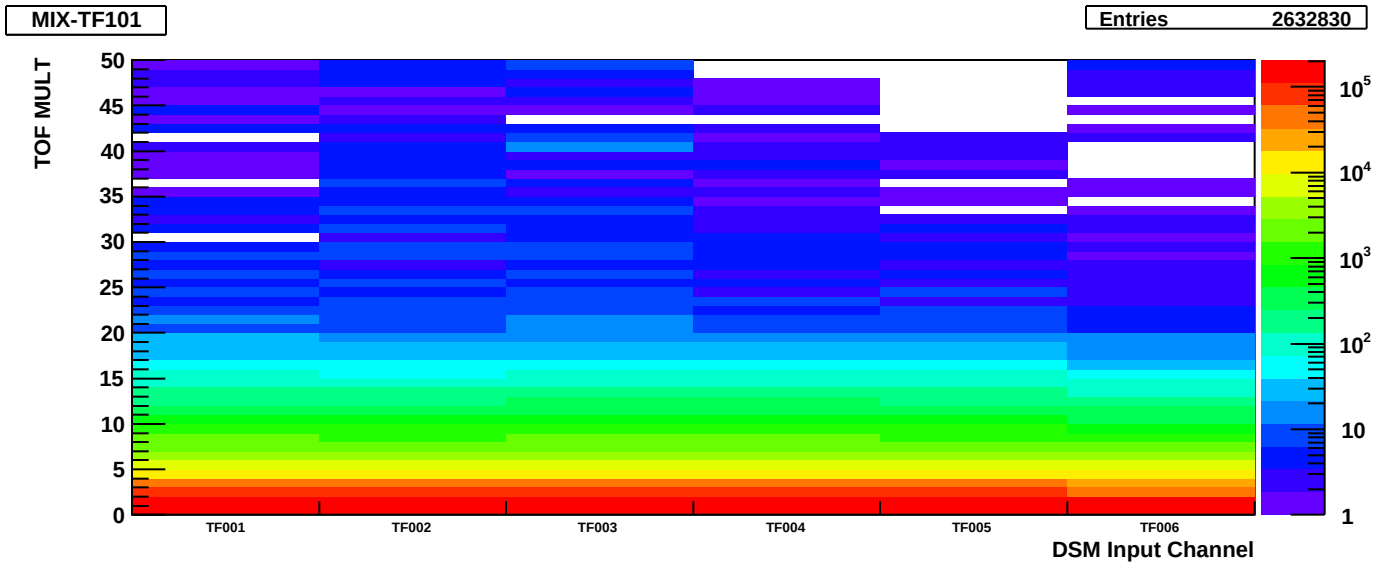

MIX-TF002
Entries 1.842981e+07
TOF MULT
TOF tray

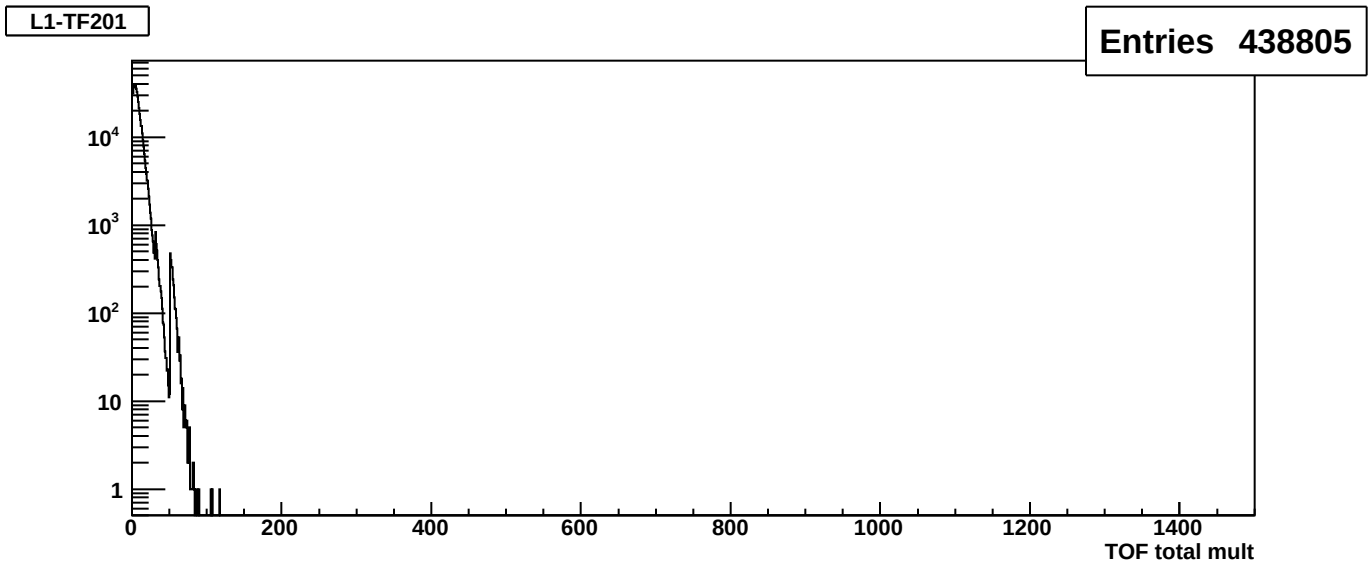

MIX-TF003
Entries 1.842981e+07
TOF MULT
TOF tray

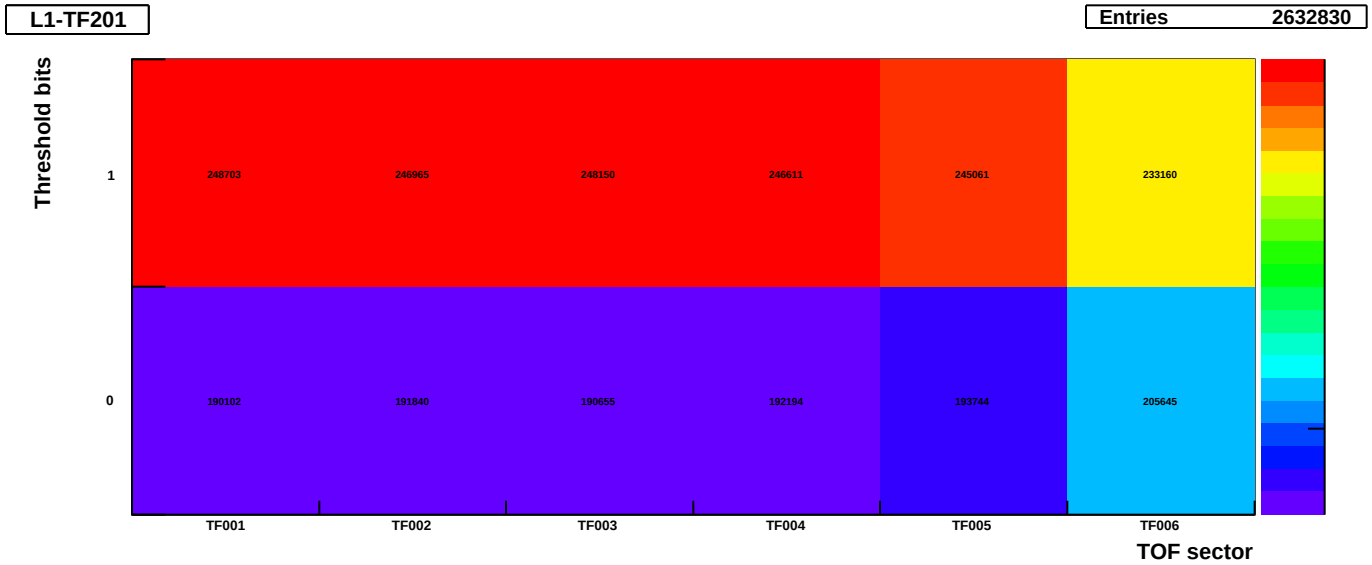

MIX-TF004
Entries 1.842981e+07
TOF MULT
TOF tray

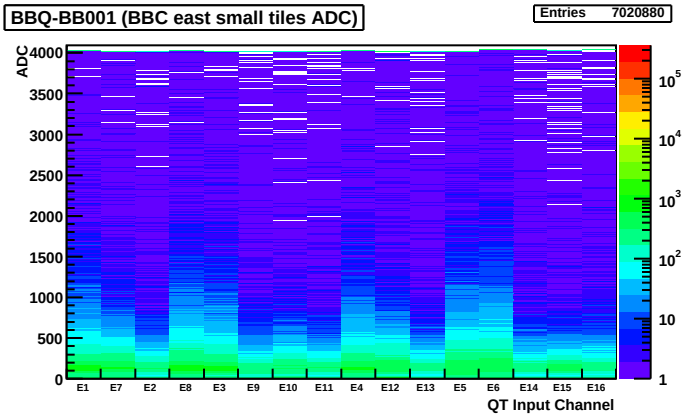

MIX-TF005
Entries 1.842981e+07
TOF MULT
TOF tray

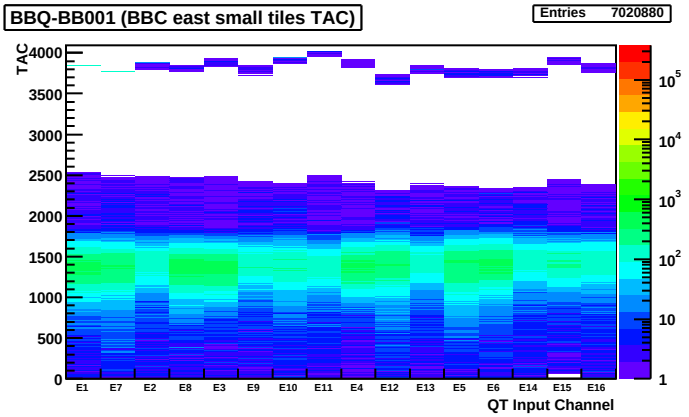

MIX-TF006
Entries 1.842981e+07
TOF MULT
TOF tray

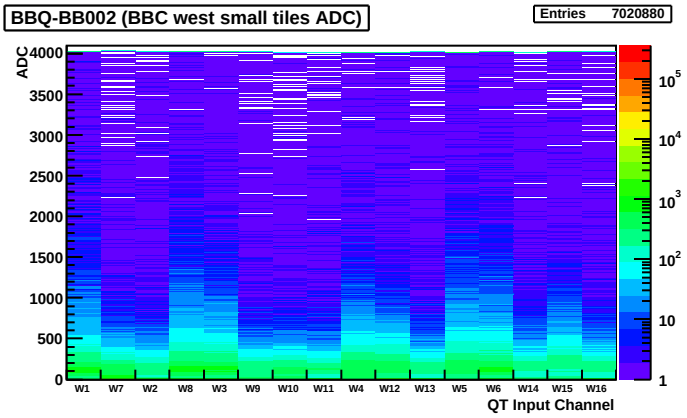

MIX-TF101
Entries 2632830
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

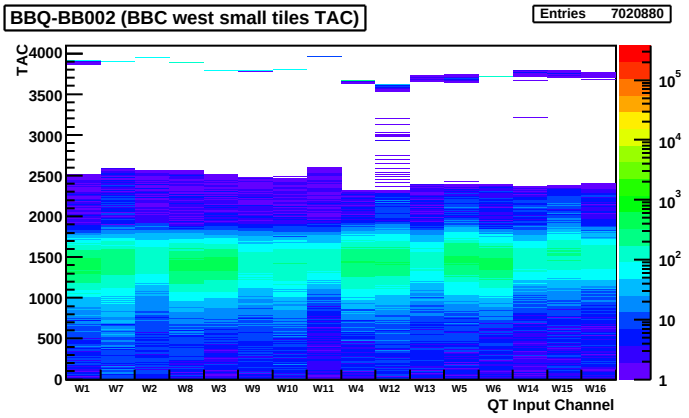

L1-TF201
Entries 438805
TOF total mult

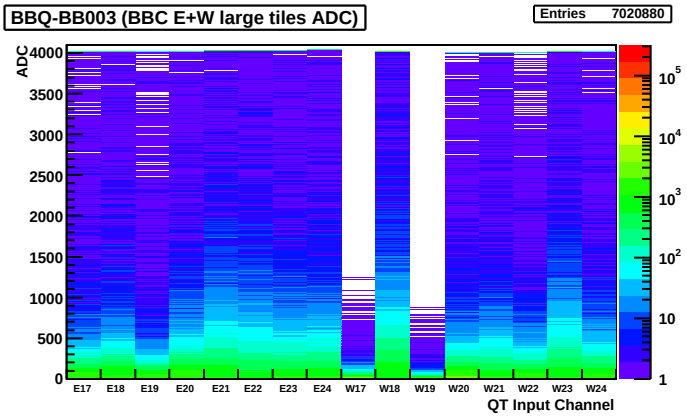

L1-TF201
Entries 2632830
Threshold bits
248703
246965
248150
246611
245061
233160
190102
191840
190655
192194
193744
205645
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

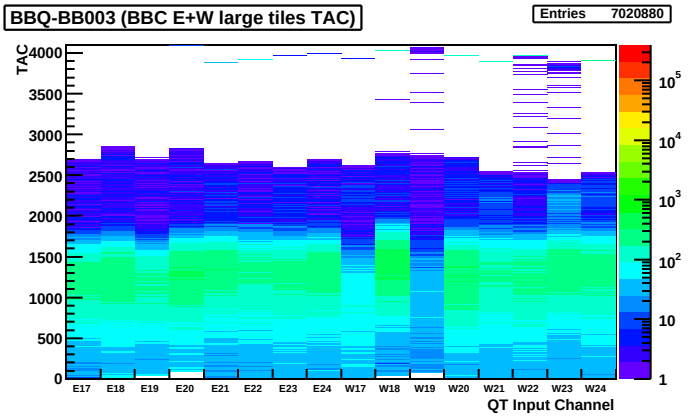

BBQ-BB001 (BBC east small tiles ADC)
Entries 7020880
ADC
QT Input Channel

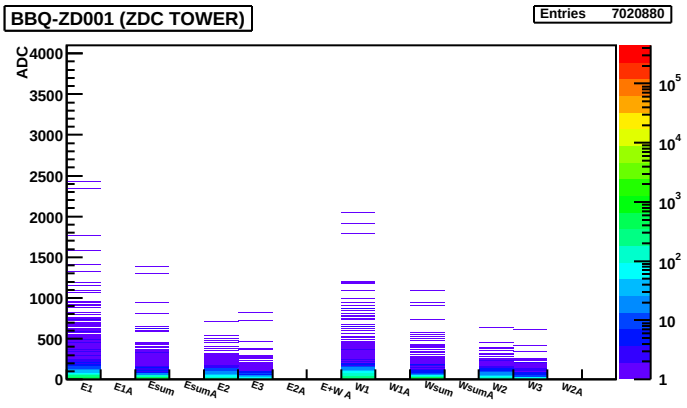

BBQ-BB001 (BBC east small tiles TAC)
Entries 7020880
TAC
QT Input Channel

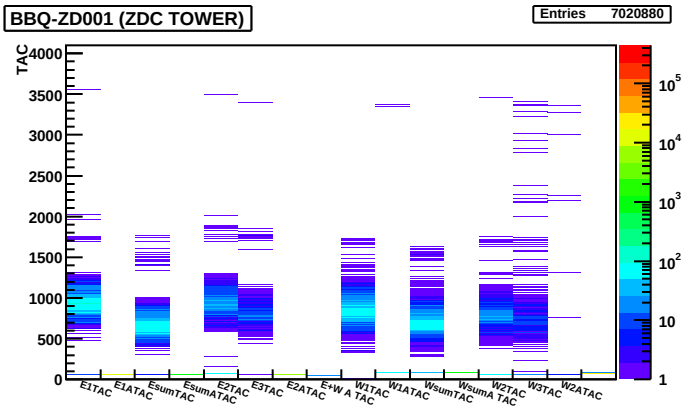

BBQ-BB002 (BBC west small tiles ADC)
Entries 7020880
ADC
QT Input Channel

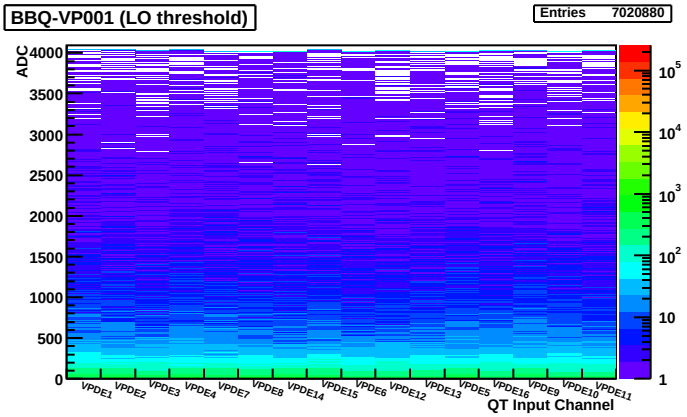

BBQ-BB002 (BBC west small tiles TAC)
Entries 7020880
TAC
QT Input Channel

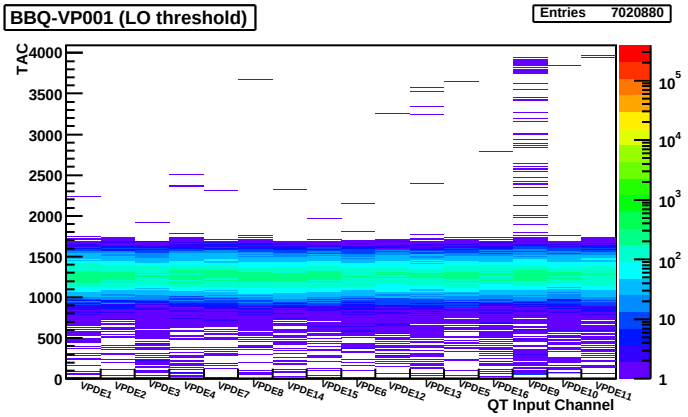

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7020880
ADC
QT Input Channel

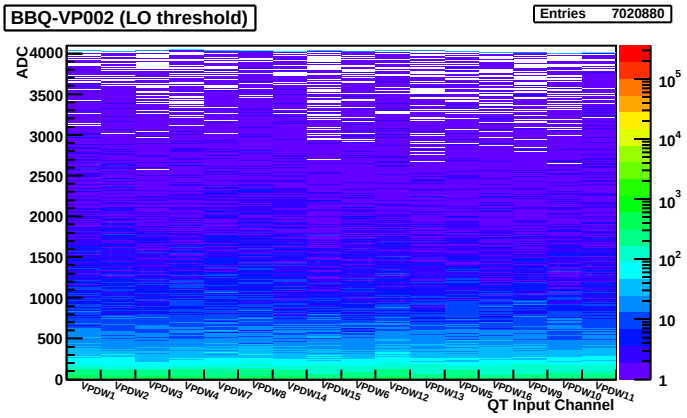

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7020880
TAC
QT Input Channel

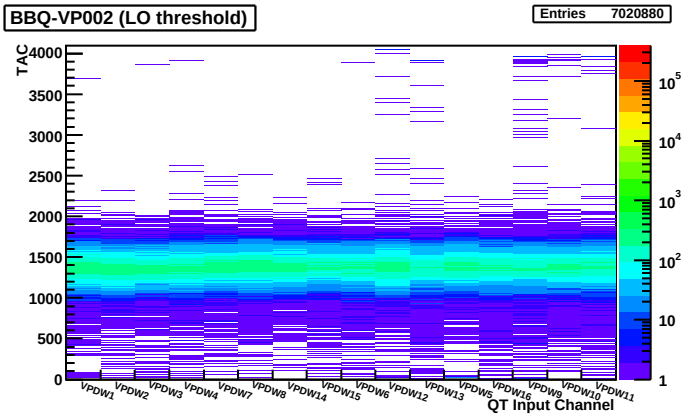

BBQ-ZD001 (ZDC TOWER)
Entries 7020880
ADC

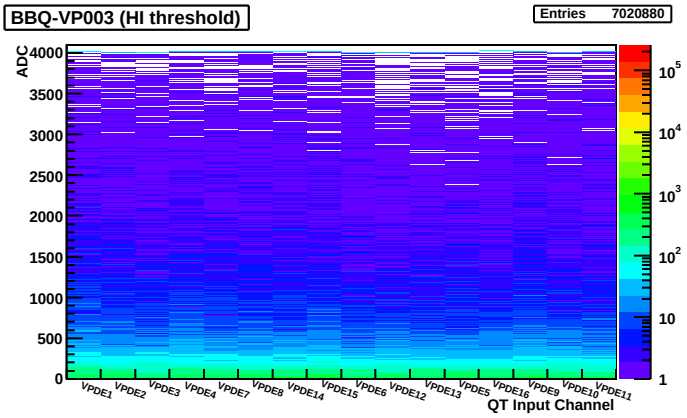

BBQ-ZD001 (ZDC TOWER)
Entries 7020880
TAC

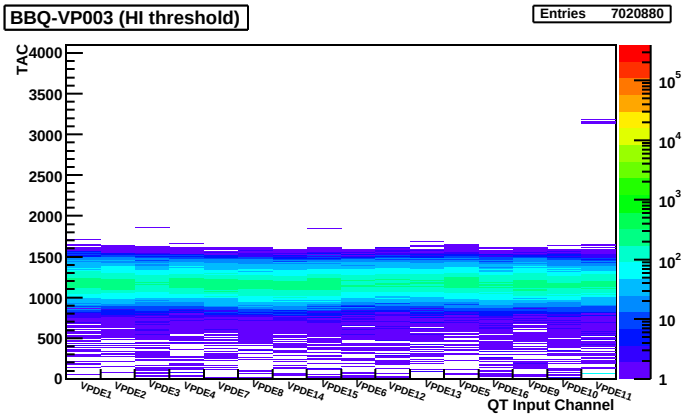

BBQ-VP001 (LO threshold)
Entries 7020880
ADC
QT Input Channel

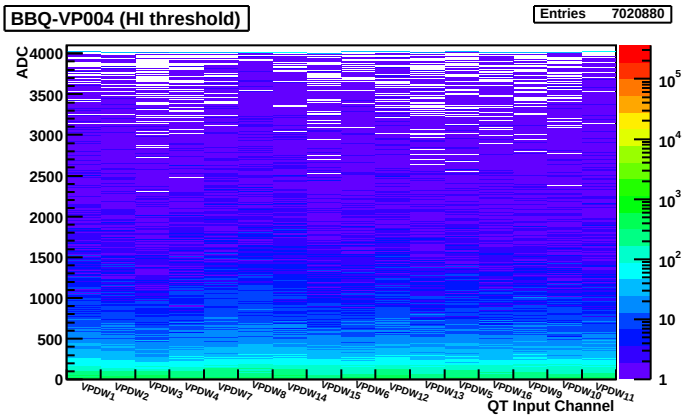

BBQ-VP001 (LO threshold)
Entries 7020880
TAC
QT Input Channel

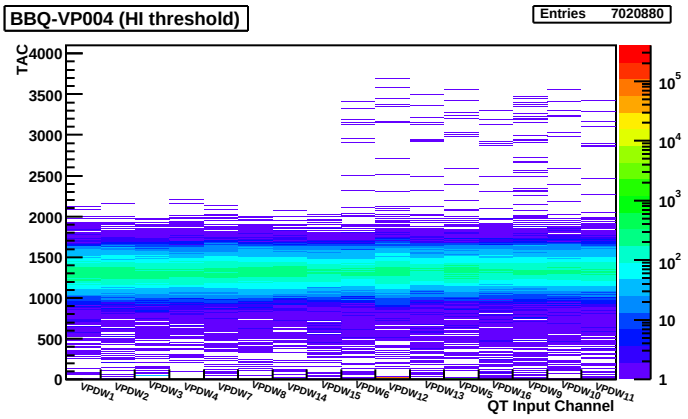

BBQ-VP002 (LO threshold)
Entries 7020880
ADC
QT Input Channel

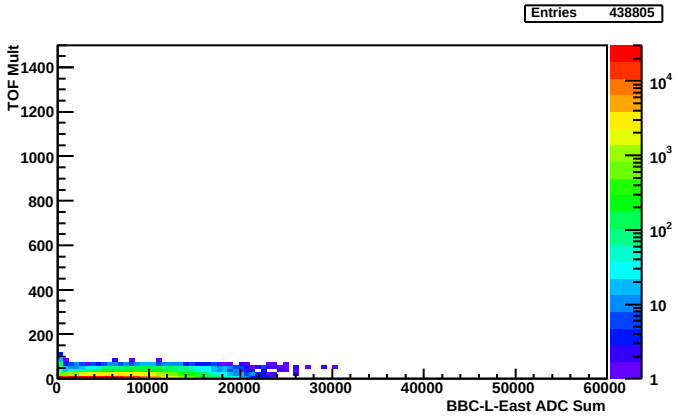

BBQ-VP002 (LO threshold)
Entries 7020880
TAC
QT Input Channel

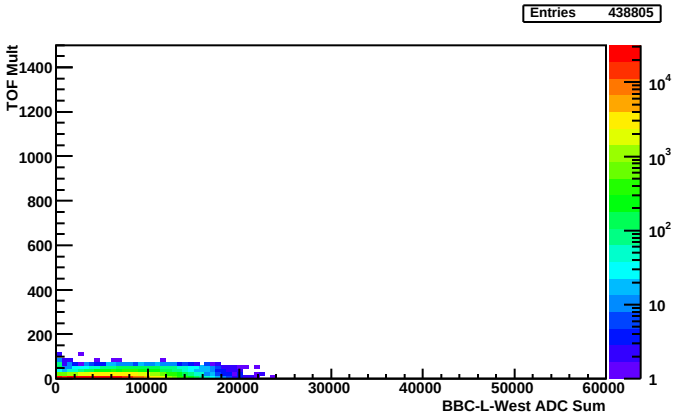

BBQ-VP003 (HI threshold)
Entries 7020880
ADC
QT Input Channel

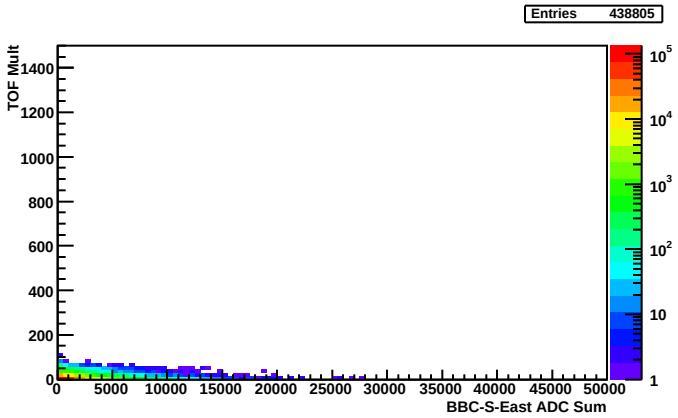

BBQ-VP003 (HI threshold)
Entries 7020880
TAC
QT Input Channel

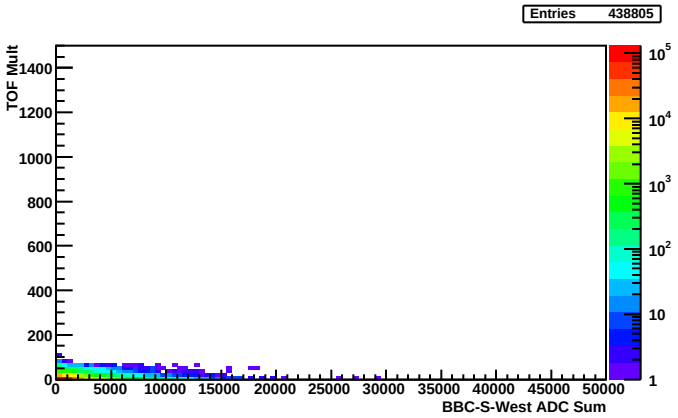

BBQ-VP004 (HI threshold)
Entries 7020880
ADC
QT Input Channel

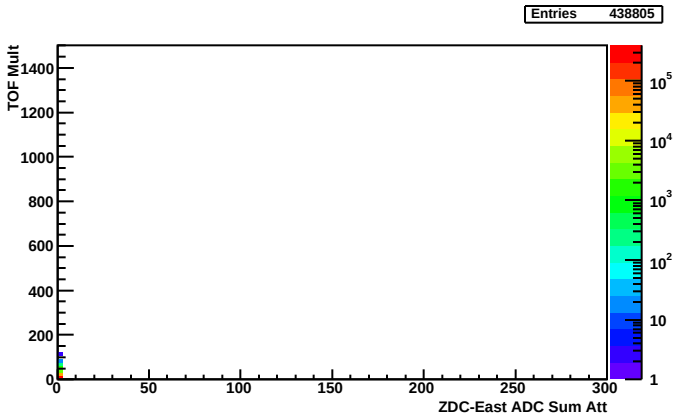

BBQ-VP004 (HI threshold)
Entries 7020880
TAC
QT Input Channel

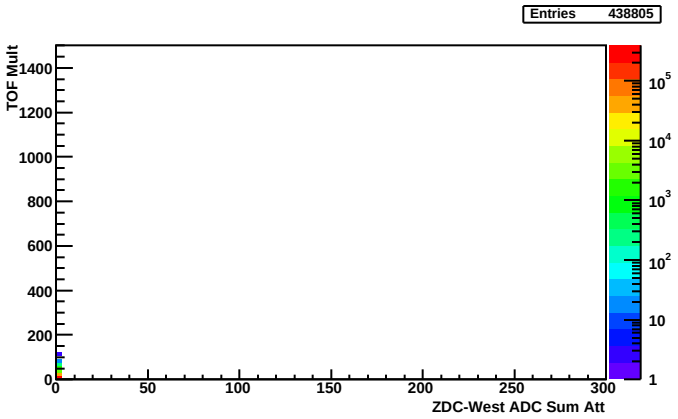

Entries 438805
TOF Mult
BBC-L-East ADC Sum

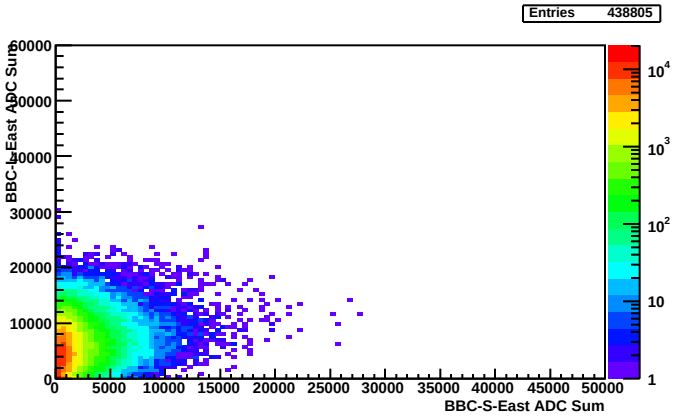

Entries 438805
TOF Mult
BBC-L-West ADC Sum

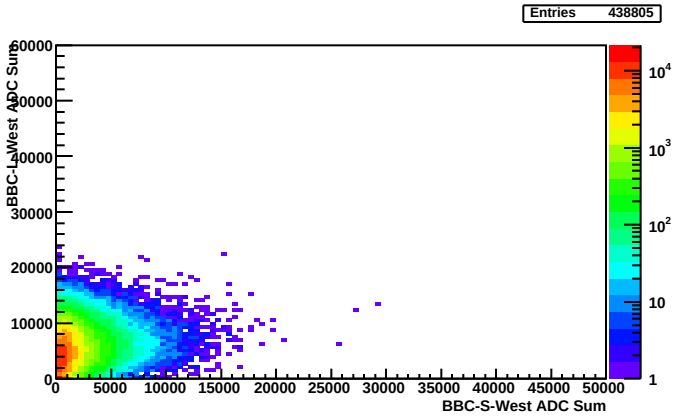

Entries 438805
TOF Mult
BBC-S-East ADC Sum

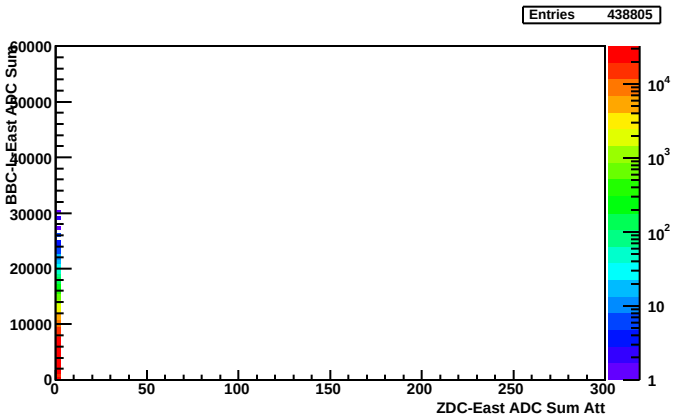

Entries 438805
TOF Mult
BBC-S-West ADC Sum

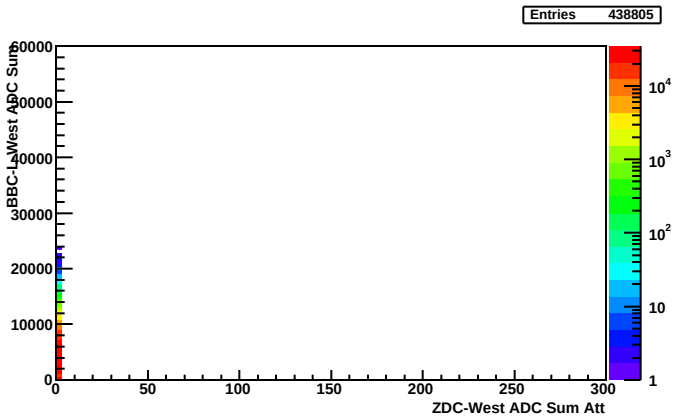

Entries 438805
TOF Mult
ZDC-East ADC Sum Att

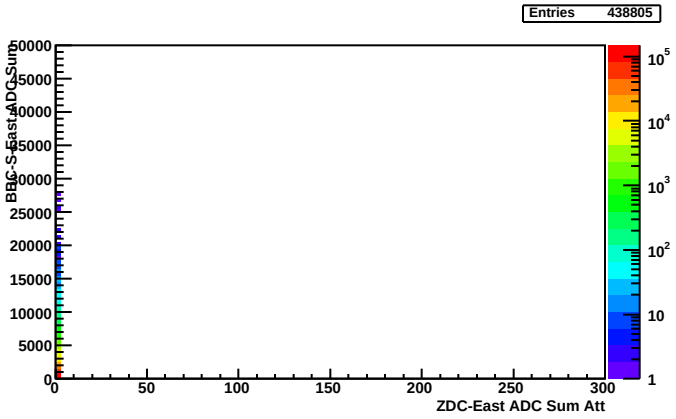

Entries 438805
TOF Mult
ZDC-West ADC Sum Att

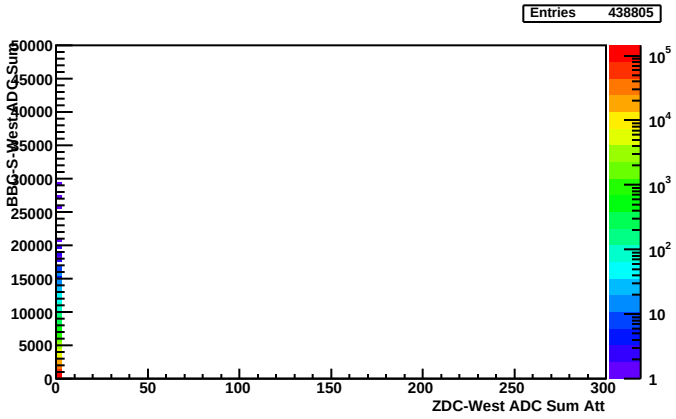

Entries 438805
BBC-L-East ADC Sum
BBC-S-East ADC Sum

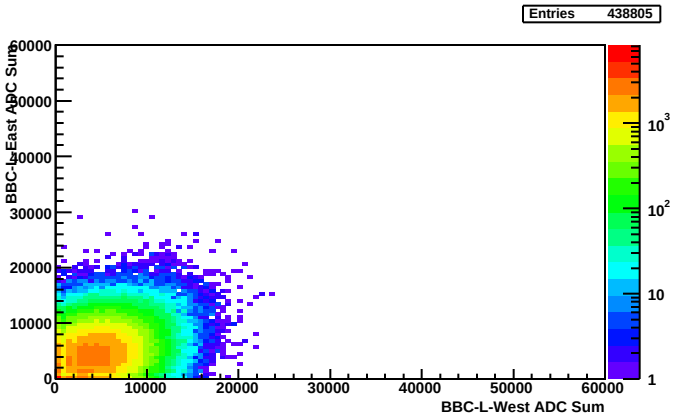

Entries 438805
BBC-L-West ADC Sum
BBC-S-West ADC Sum

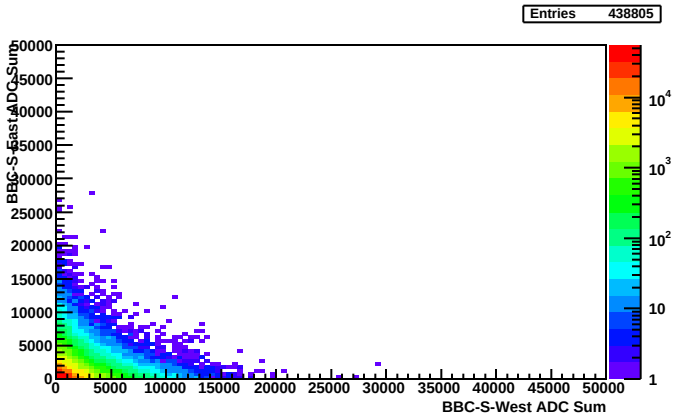

Entries 438805
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

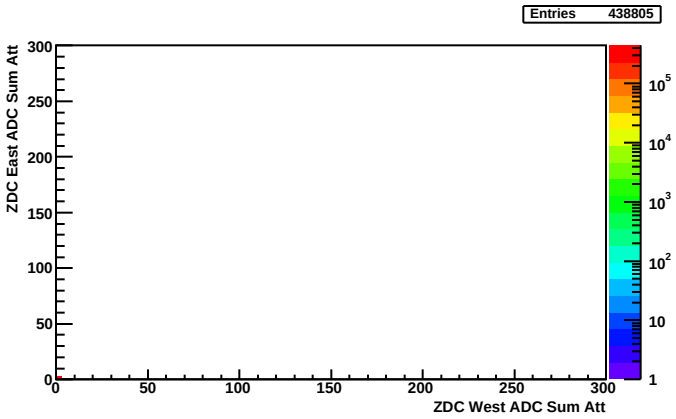

Entries 438805
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

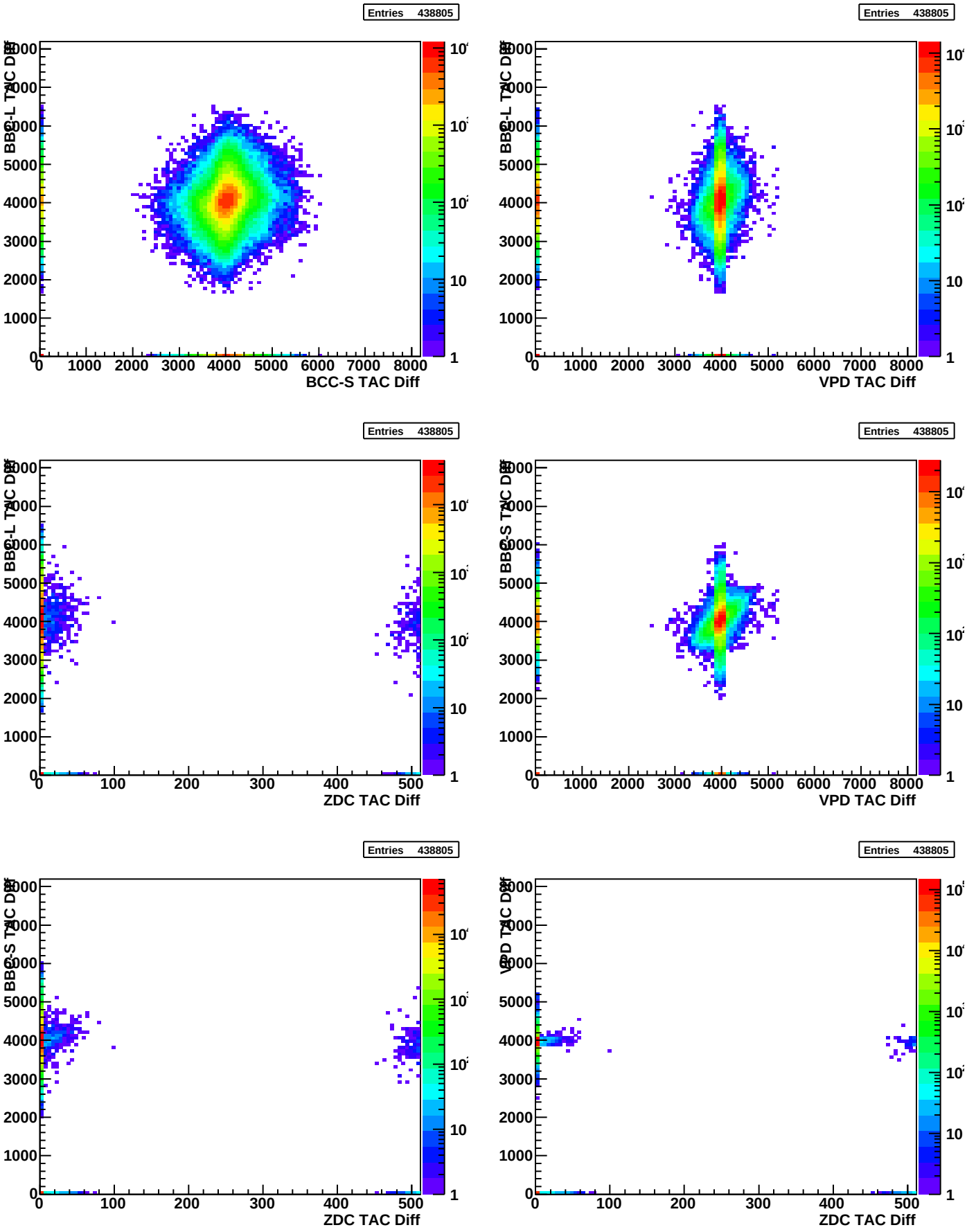

Entries 438805
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

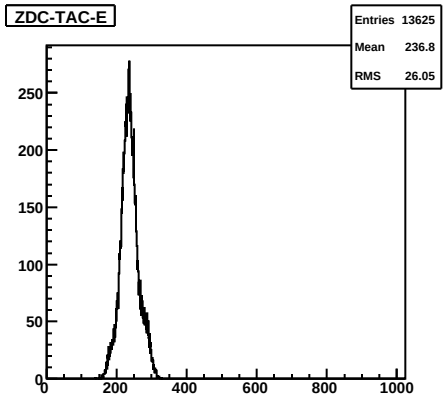

Entries 438805
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

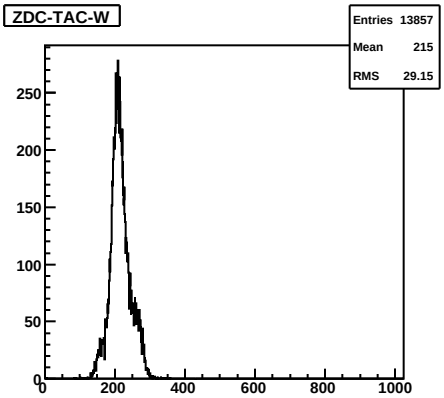

Entries 438805
BBC-L-East ADC Sum
BBC-L-West ADC Sum

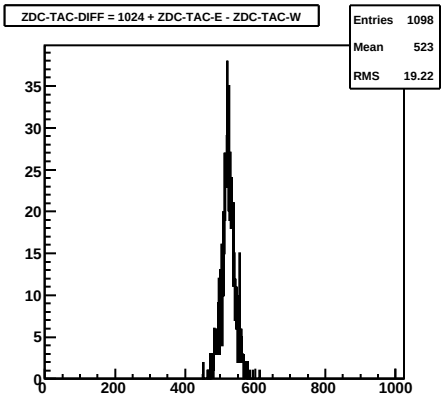

Entries 438805
BBC-S-East ADC Sum
BBC-S-West ADC Sum

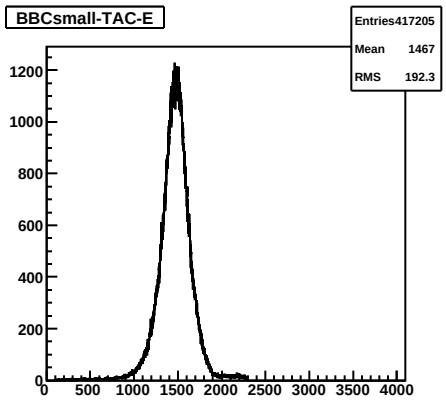

Entries 438805
ZDC East ADC Sum Att
ZDC West ADC Sum Att

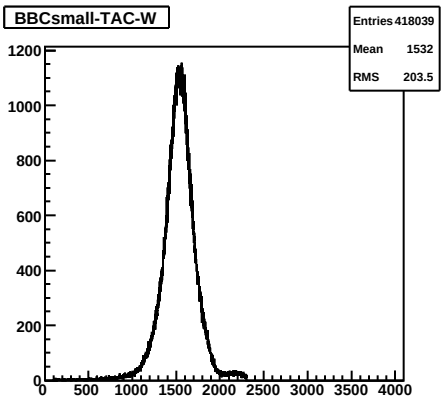

Entries 438805
BBC-L TAC Diff
BCC-S TAC Diff
Entries 438805
BBC-L TAC Diff
VPD TAC Diff
Entries 438805
BBC-L TAC Diff
ZDC TAC Diff
Entries 438805
BBC-S TAC Diff
VPD TAC Diff
Entries 438805
BBC-S TAC Diff
ZDC TAC Diff
Entries 438805
VPD TAC Diff
ZDC TAC Diff

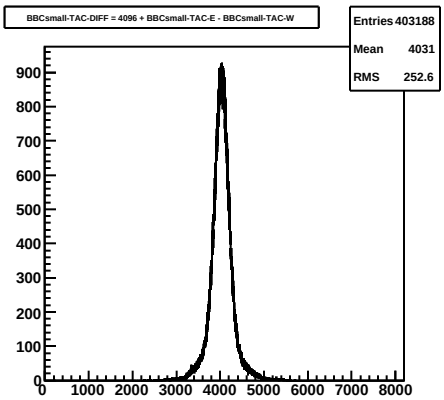

ZDC-TAC-E
Entries 13625
Mean 236.8
RMS 26.05

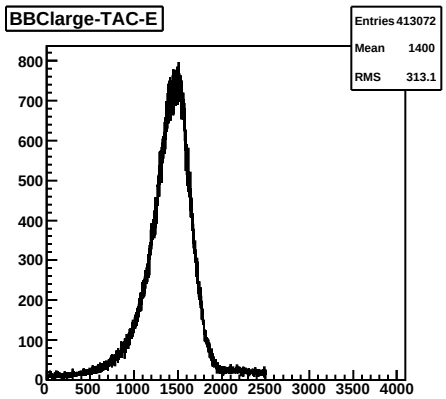

ZDC-TAC-W
Entries 13857
Mean 215
RMS 29.15

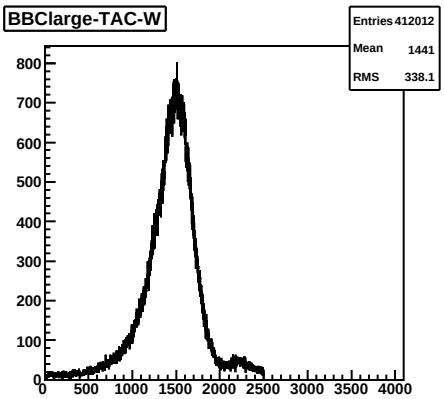

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1098
Mean 523
RMS 19.22

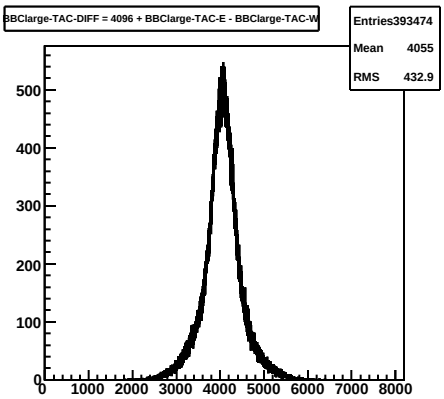

BBCsmall-TAC-E
Entries 417205
Mean 1467
RMS 192.3

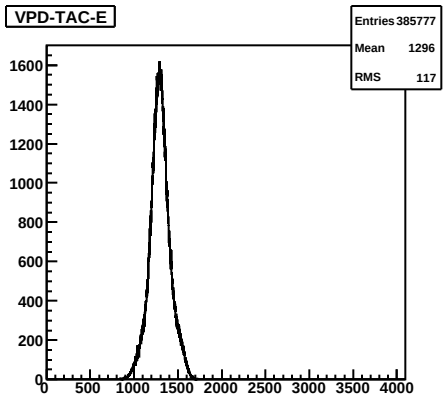

BBCsmall-TAC-W
Entries 418039
Mean 1532
RMS 203.5

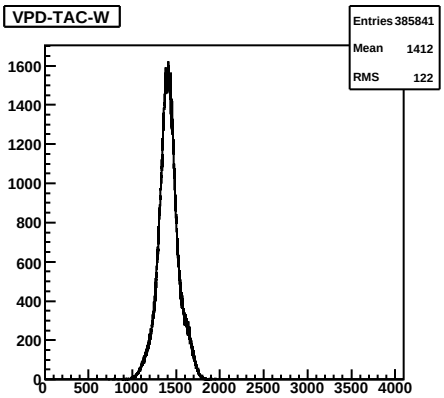

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 403188
Mean 4031
RMS 252.6

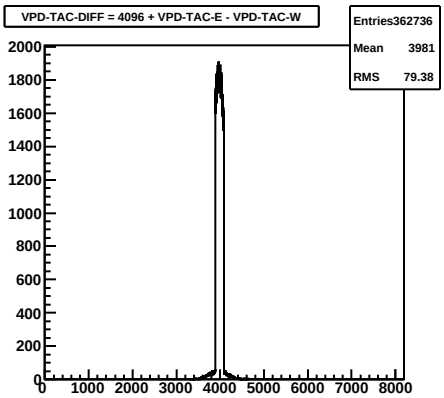

BBClarge-TAC-E
Entries 413072
Mean 1400
RMS 313.1

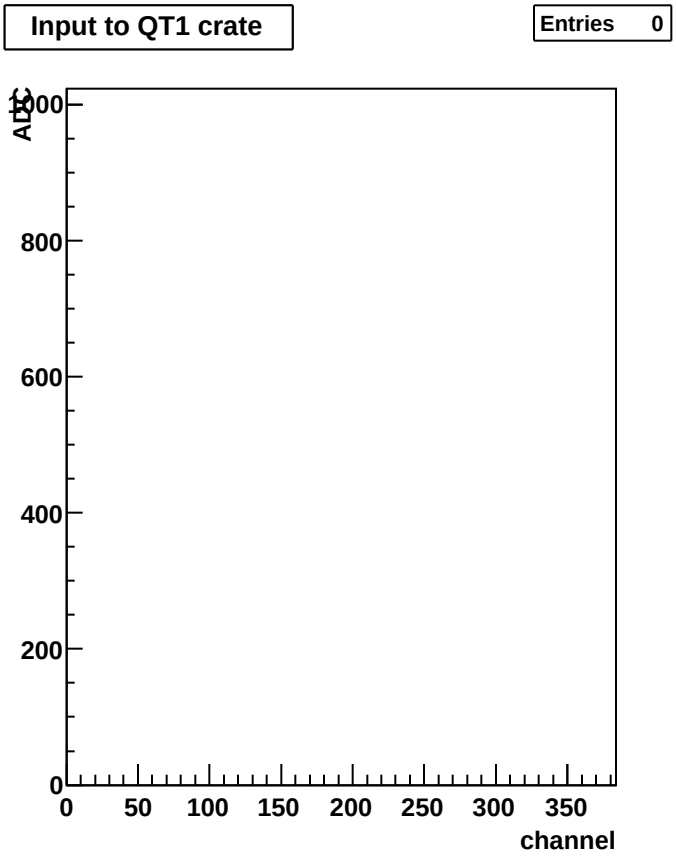

BBClarge-TAC-W
Entries 412012
Mean 1441
RMS 338.1

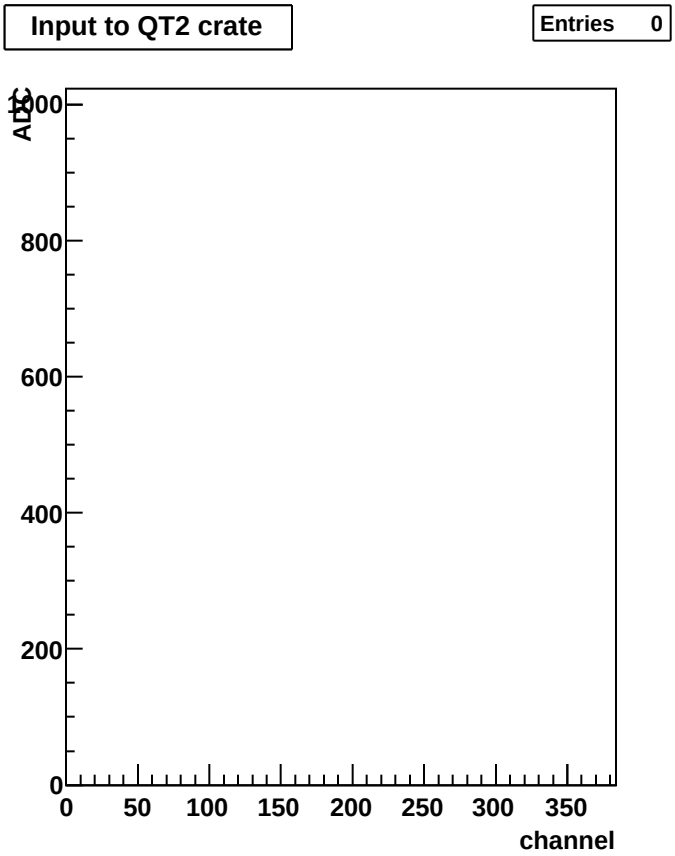

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 393474
Mean 4055
RMS 432.9

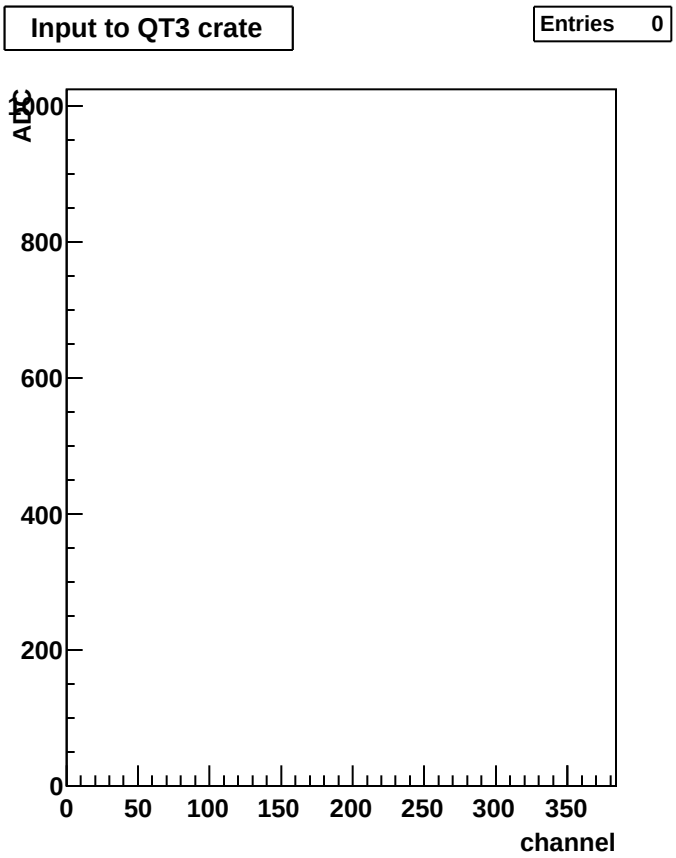

VPD-TAC-E
Entries 385777
Mean 1296
RMS 117

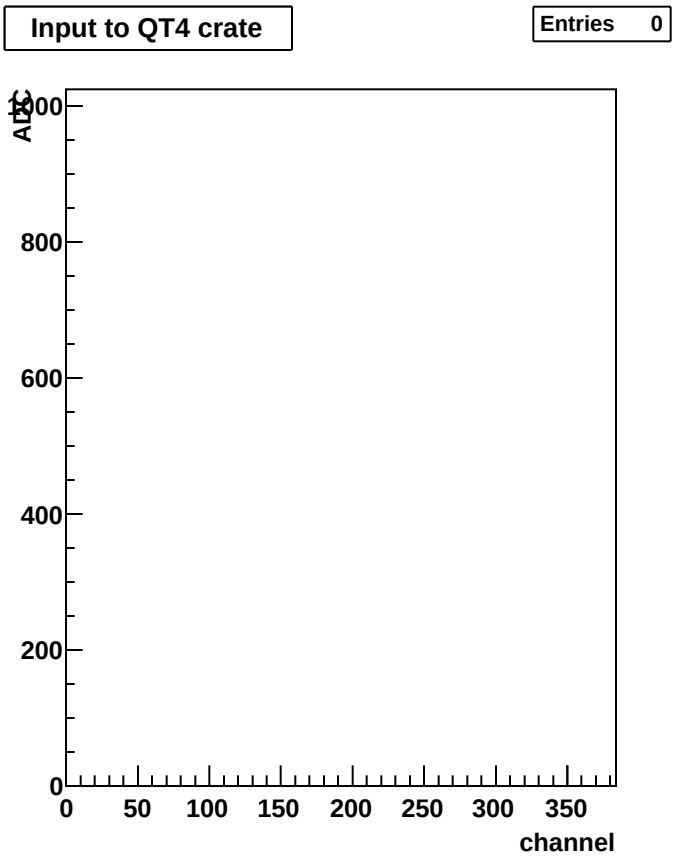

VPD-TAC-W
Entries 385841
Mean 1412
RMS 122

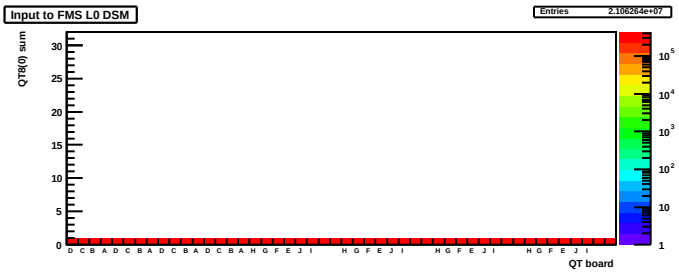

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 362736
Mean 3981
RMS 79.38

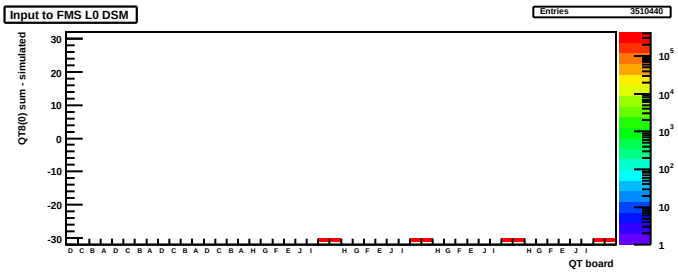

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

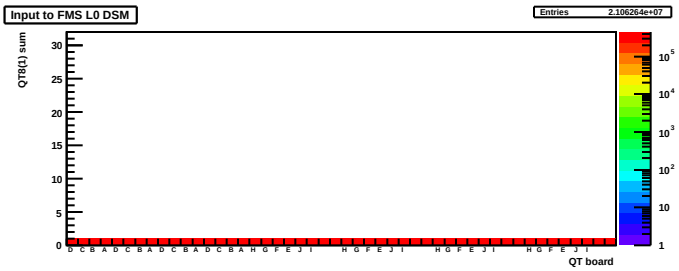

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

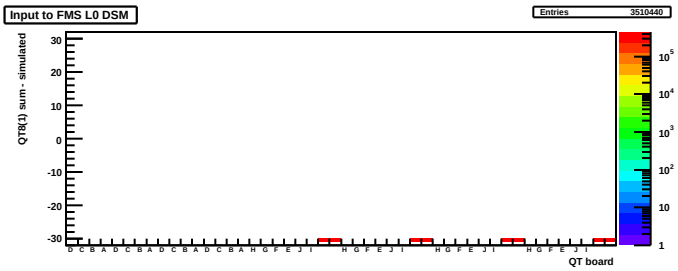

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

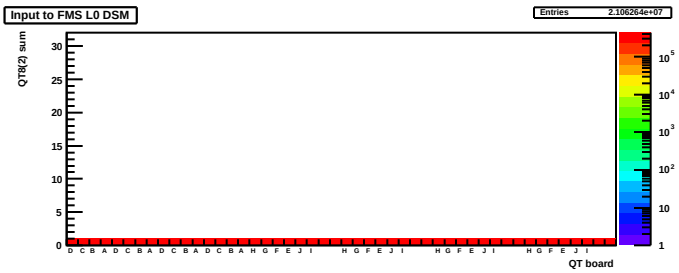

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

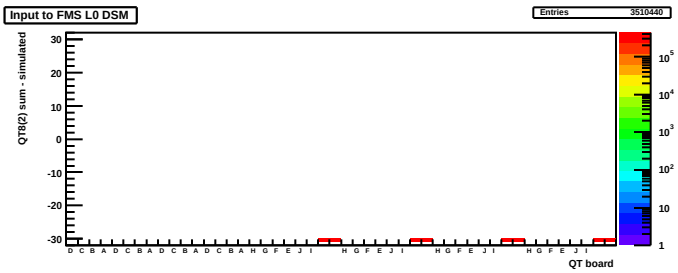

Input to FMS L0 DSM
Entries 2.106264e+07
QT8(0) sum
QT board

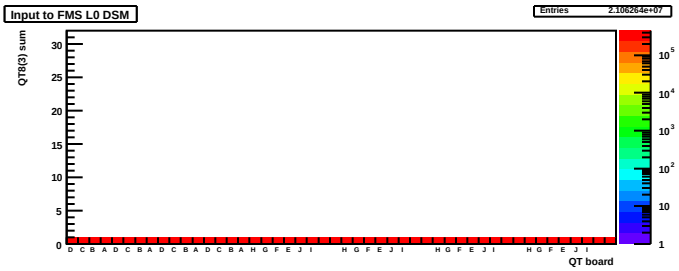

Input to FMS L0 DSM
Entries 3510440
QT8(0) sum - simulated
QT board

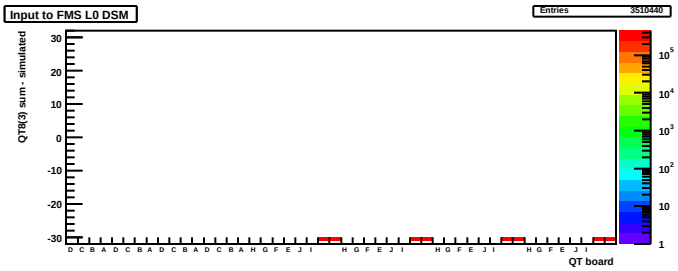

Input to FMS L0 DSM
Entries 2.106264e+07
QT8(1) sum
QT board

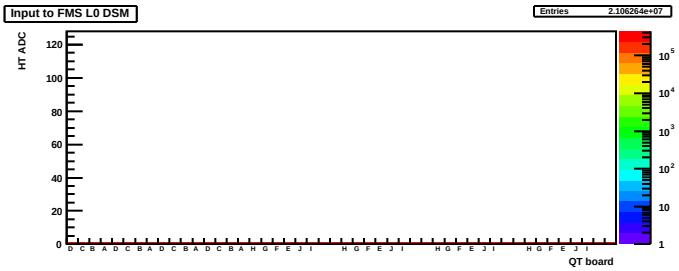

Input to FMS L0 DSM
Entries 3510440
QT8(1) sum - simulated
QT board

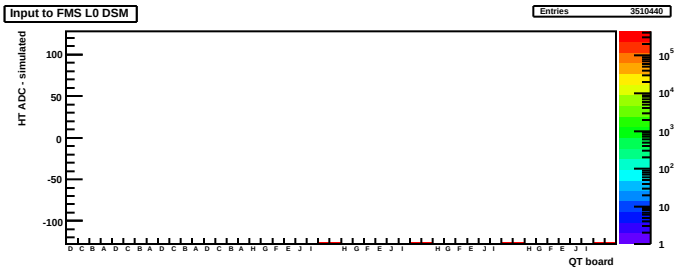

Input to FMS L0 DSM
Entries 2.106264e+07
QT8(2) sum
QT board

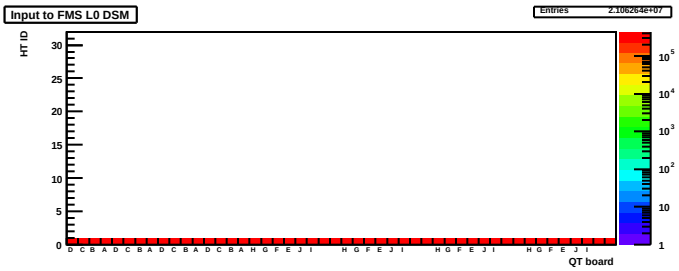

Input to FMS L0 DSM
Entries 3510440
QT8(2) sum - simulated
QT board

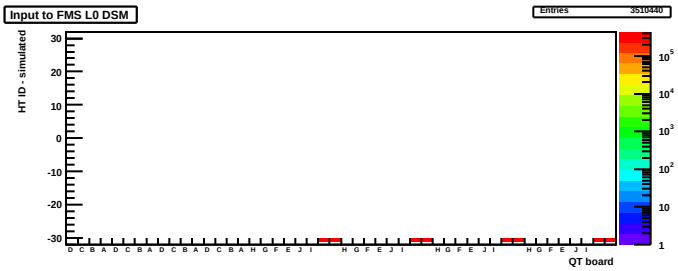

Input to FMS L0 DSM
Entries 2.106264e+07
QT8(3) sum
QT board

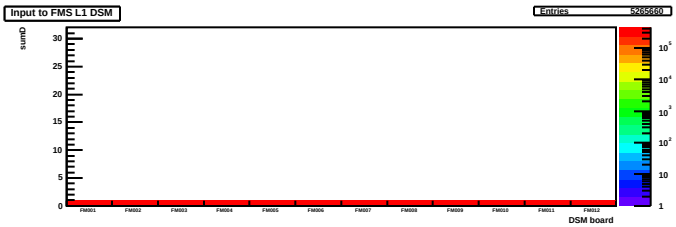

Input to FMS L0 DSM
Entries 3510440
QT8(3) sum - simulated
QT board

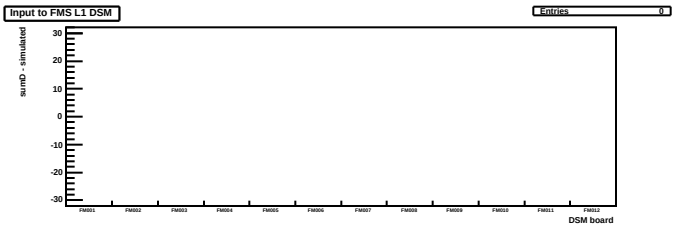

Input to FMS L0 DSM
Entries 2.106264e+07
HT ADC
QT board

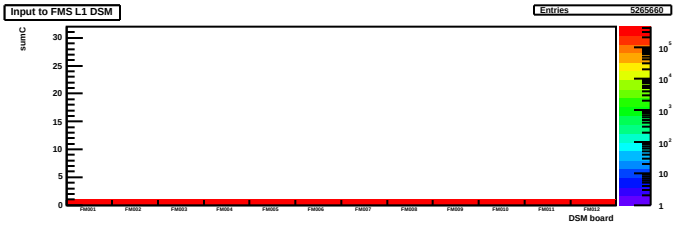

Input to FMS L0 DSM
Entries 3510440
HT ADC - simulated
QT board

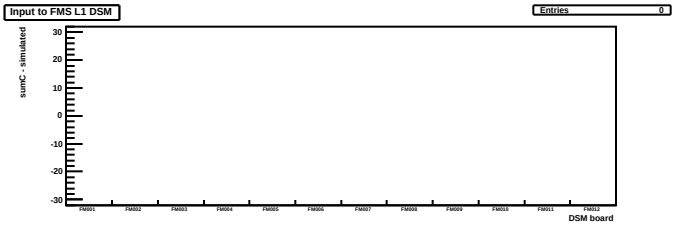

Input to FMS L0 DSM
Entries 2.106264e+07
HT ID
QT board

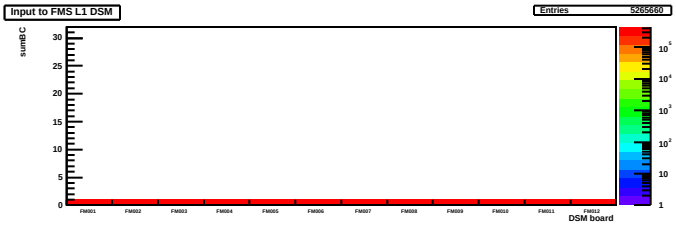

Input to FMS L0 DSM
Entries 3510440
HT ID - simulated
QT board

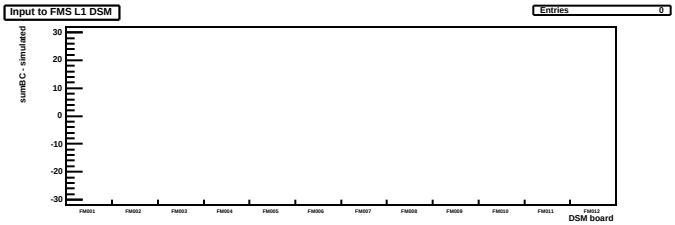

Input to FMS L1 DSM
Entries 5265660
sumD
DSM board

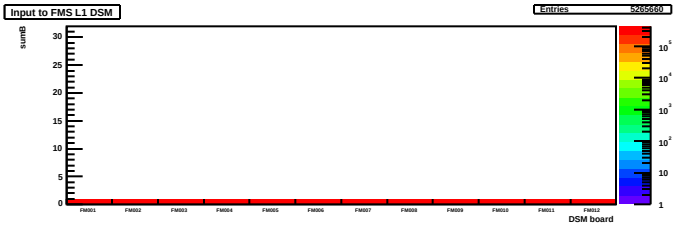

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

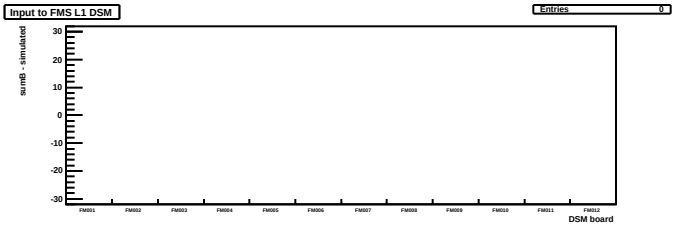

Input to FMS L1 DSM
Entries 5265660
sumC
DSM board

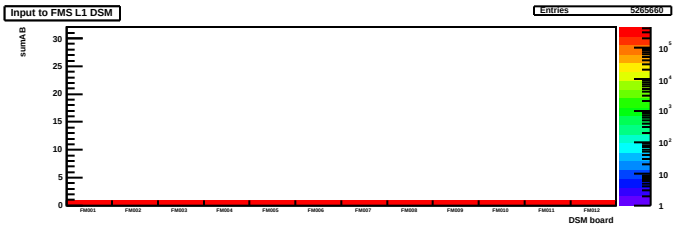

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

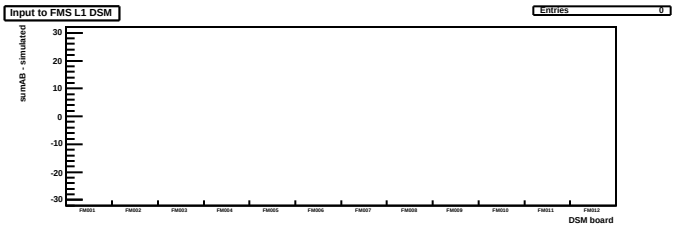

Input to FMS L1 DSM
Entries 5265660
sumBC
DSM board

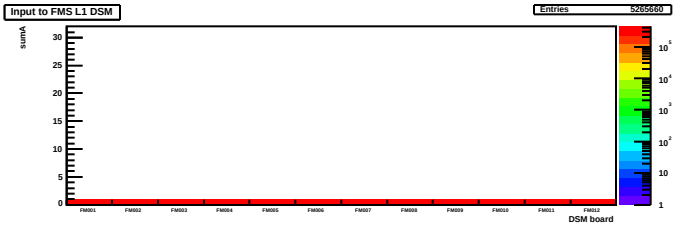

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

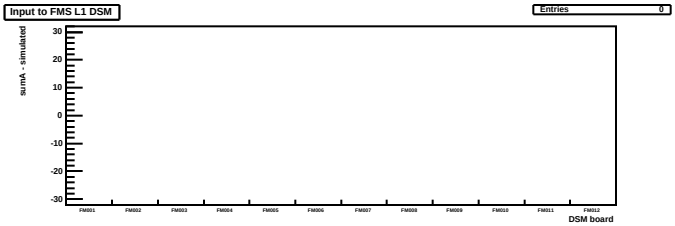

Input to FMS L1 DSM
Entries 5265660
sumB
DSM board

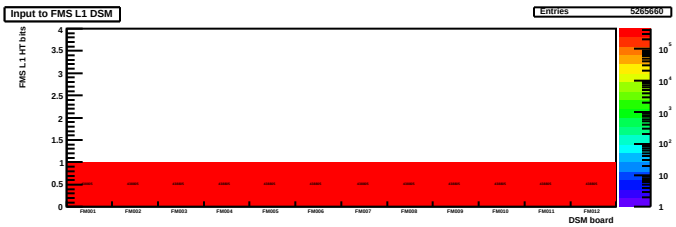

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

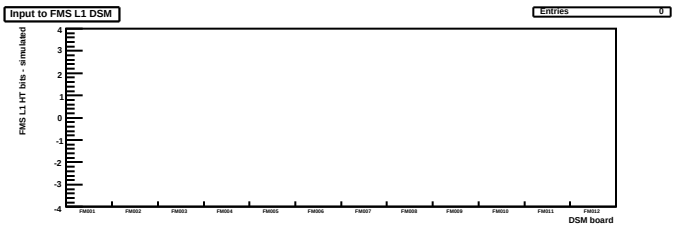

Input to FMS L1 DSM
Entries 5265660
sumAB
DSM board

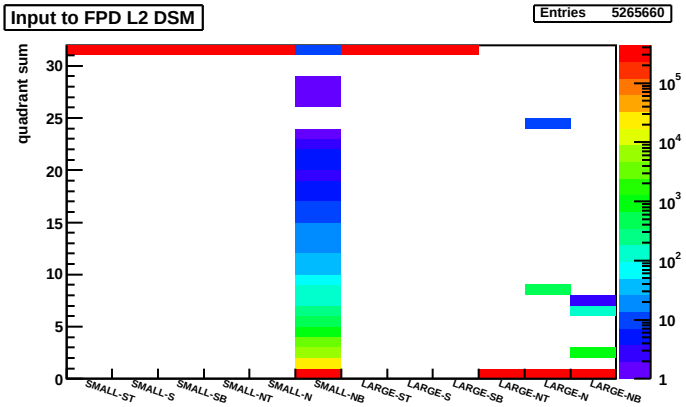

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

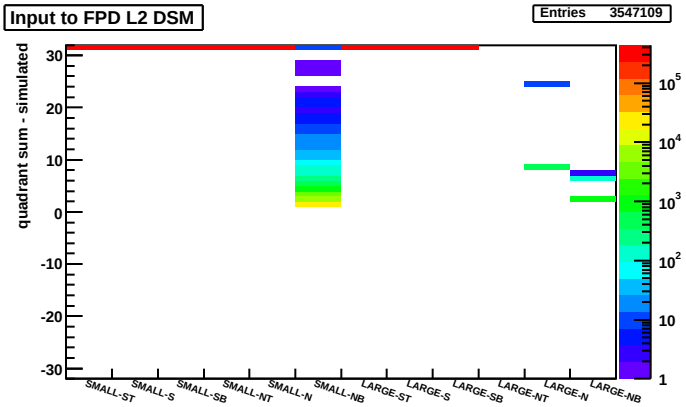

Input to FMS L1 DSM
Entries 5265660
sumA
DSM board

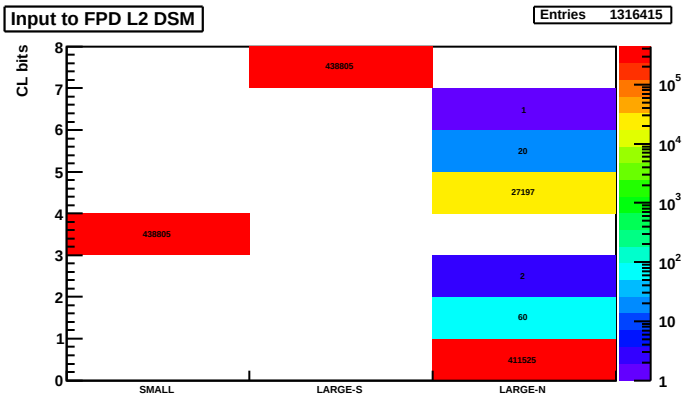

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

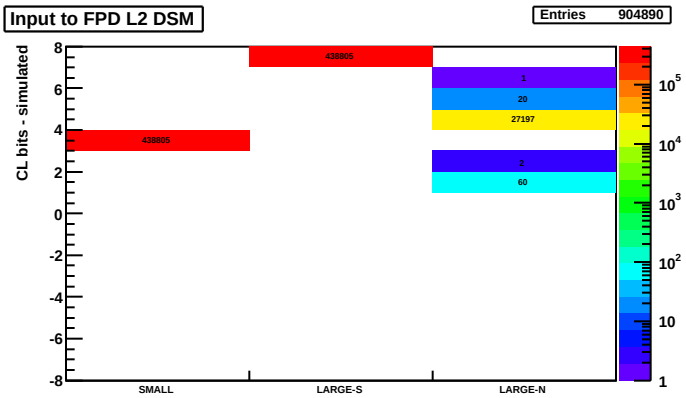

Input to FMS L1 DSM
Entries 5265660
FMS L1 HT bits
DSM board

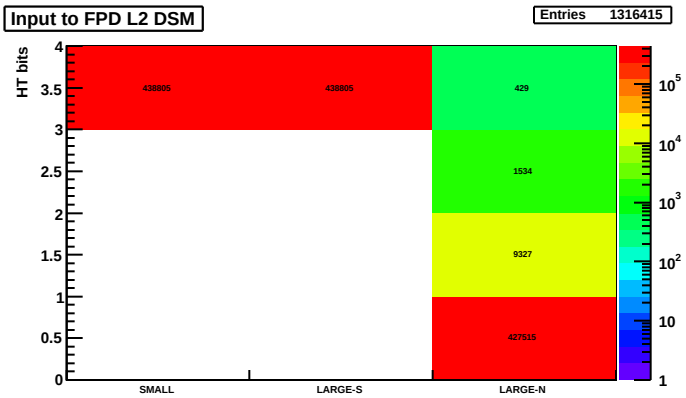

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

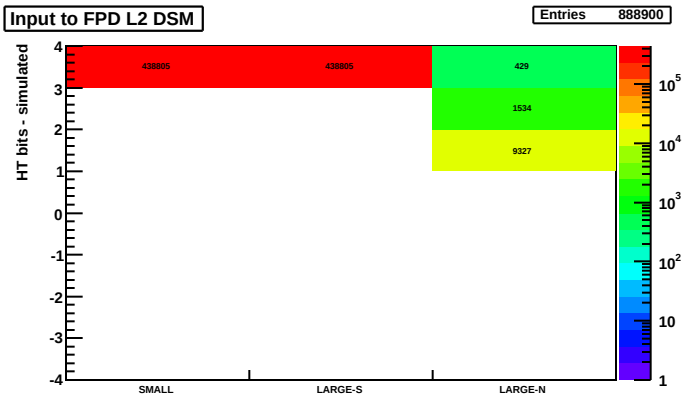

Input to FPD L2 DSM
Entries 5265660
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

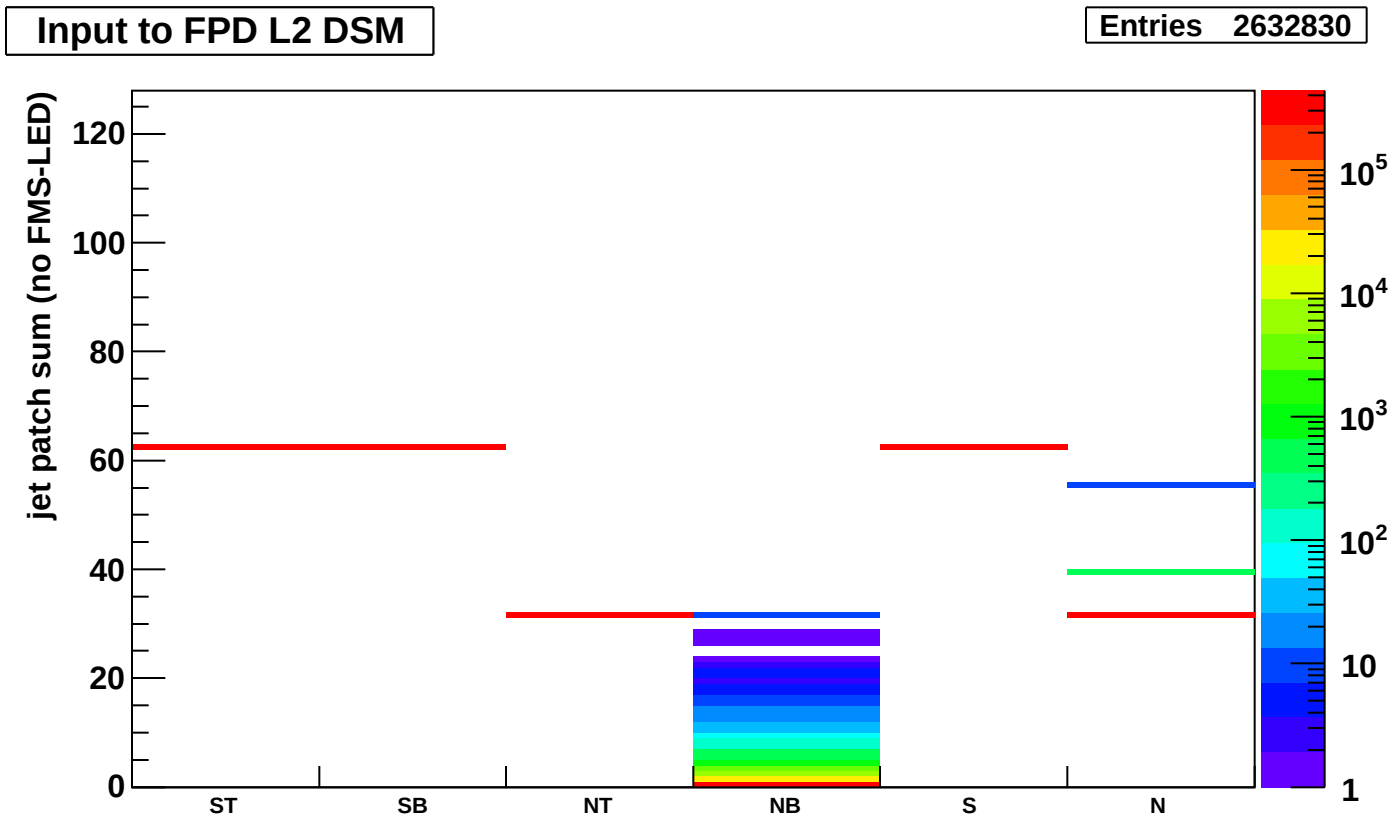

Input to FPD L2 DSM
Entries 3547109
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

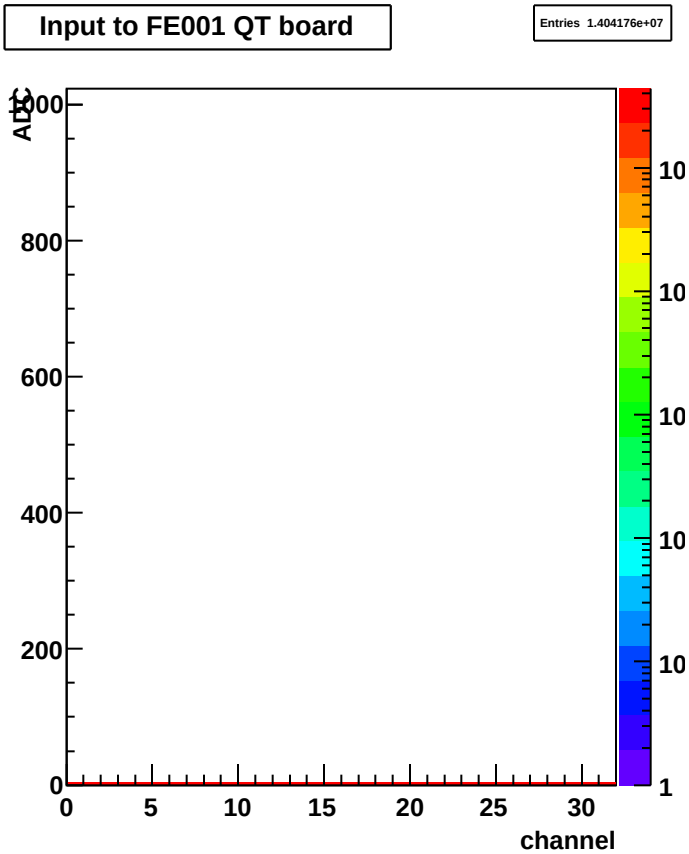

Input to FPD L2 DSM
Entries 1316415
CL bits
438805
438805
1
20
27197
2
60
411525
SMALL
LARGE-S
LARGE-N

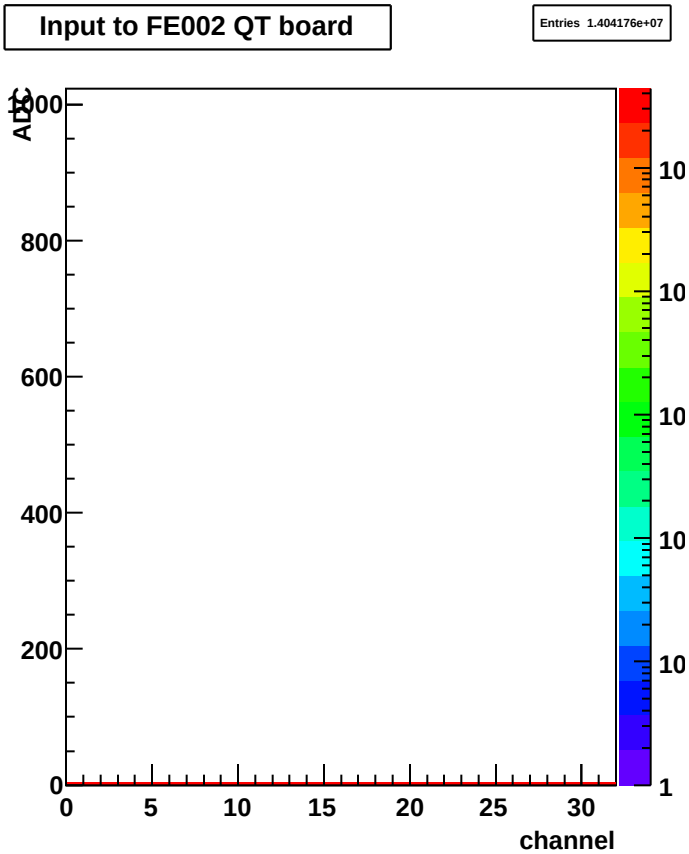

Input to FPD L2 DSM
Entries 904890
CL bits - simulated
438805
438805
1
20
27197
2
60
SMALL
LARGE-S
LARGE-N

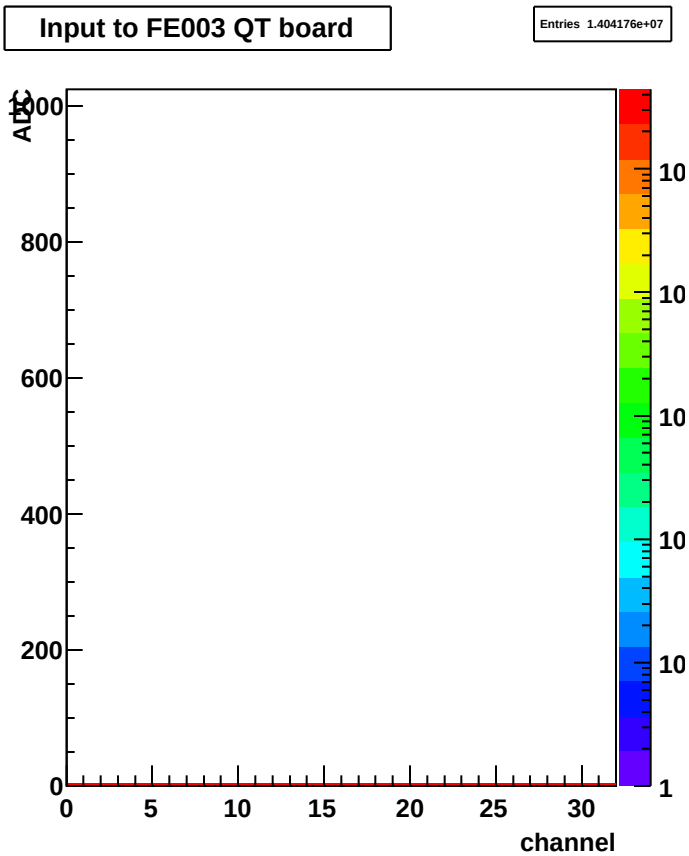

Input to FPD L2 DSM
Entries 1316415
HT bits
438805
438805
429
1534
9327
427515
SMALL
LARGE-S
LARGE-N

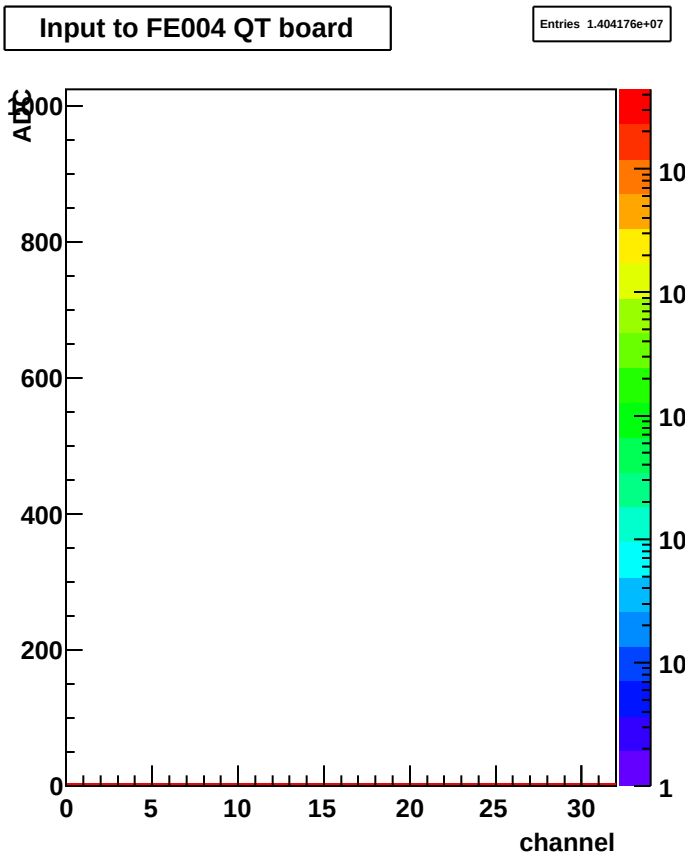

Input to FPD L2 DSM
Entries 888900
HT bits - simulated
438805
438805
429
1534
9327
SMALL
LARGE-S
LARGE-N

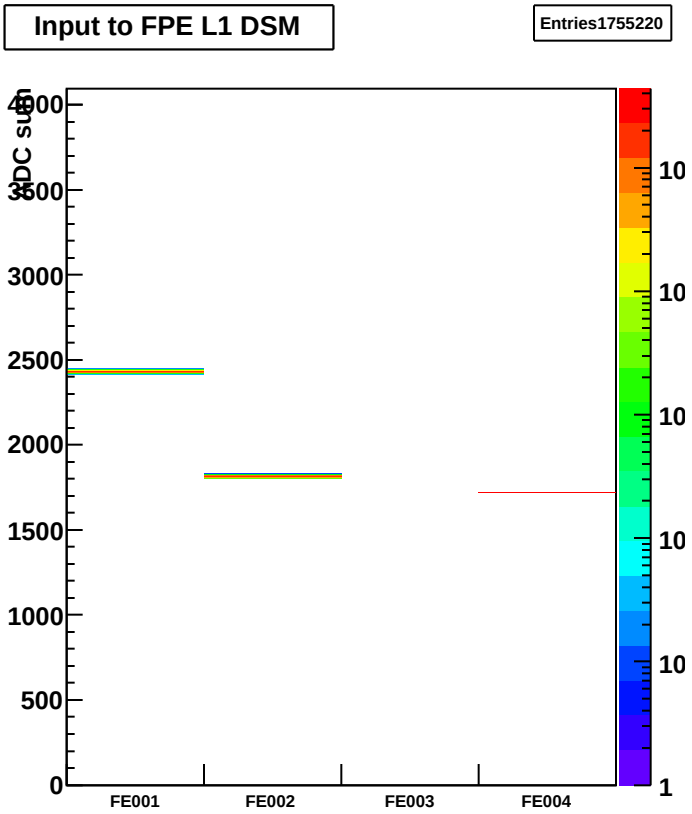

Input to FPD L2 DSM
Entries 2632830
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

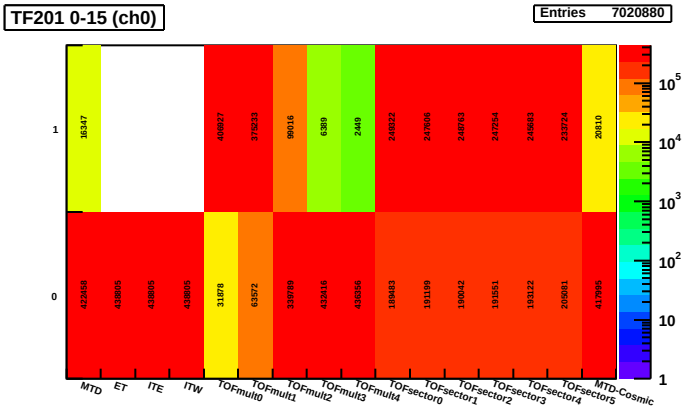

Input to FE001 QT board
Entries 1.404176e+07
ADC
channel

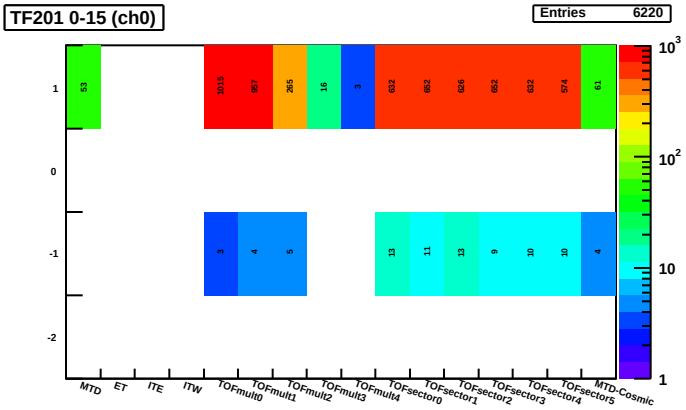

Input to FE002 QT board
Entries 1.404176e+07
ADC
channel

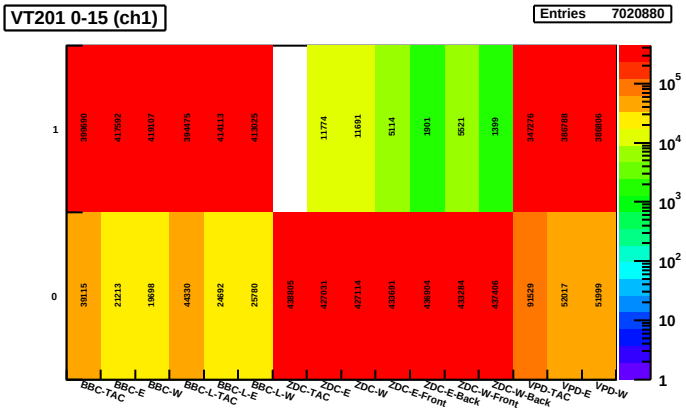

Input to FE003 QT board
Entries 1.404176e+07
ADC
channel

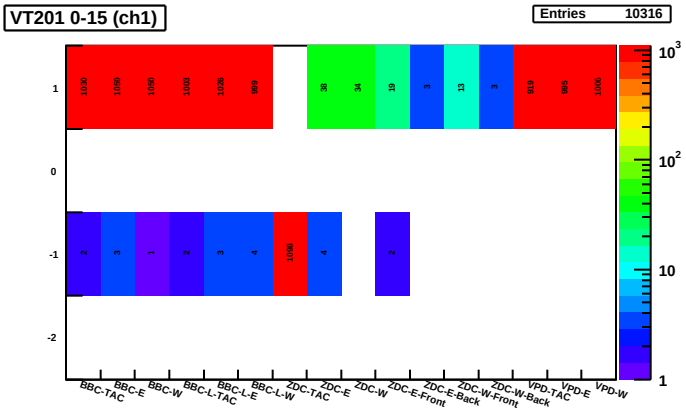

Input to FE004 QT board
Entries 1.404176e+07
ADC
channel

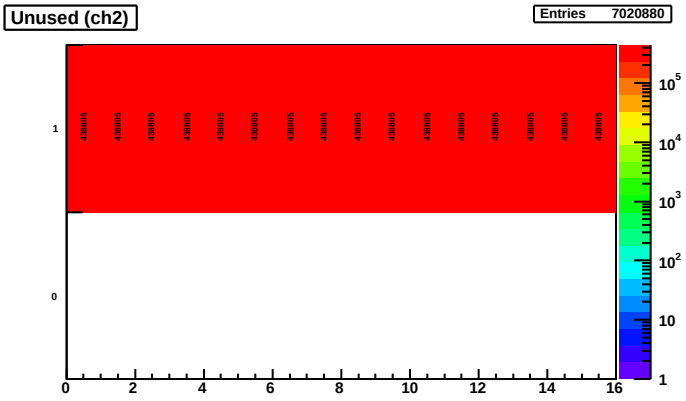

Input to FPE L1 DSM
Entries1755220
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

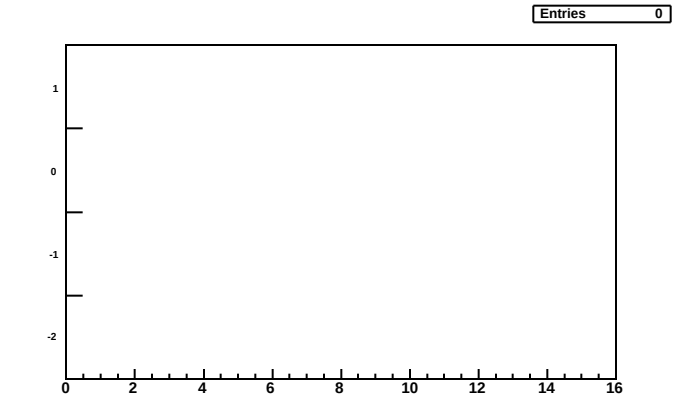

TF201 0-15 (ch0)
Entries 7020880
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

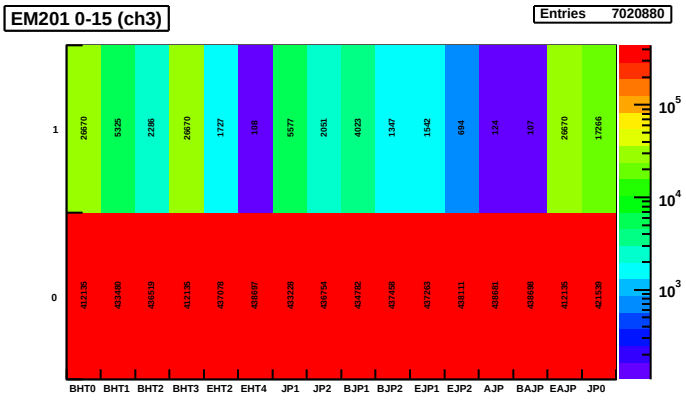

TF201 0-15 (ch0)
Entries 6220
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

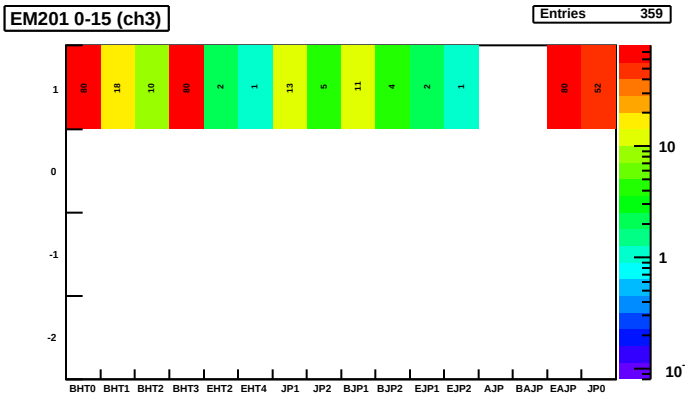

VT201 0-15 (ch1)
Entries 7020880
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 10316
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

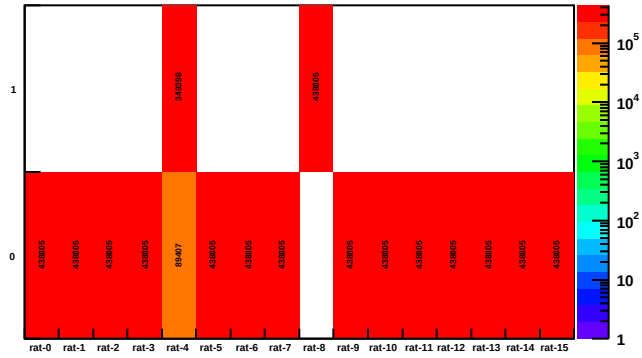

Unused (ch2)
Entries 7020880

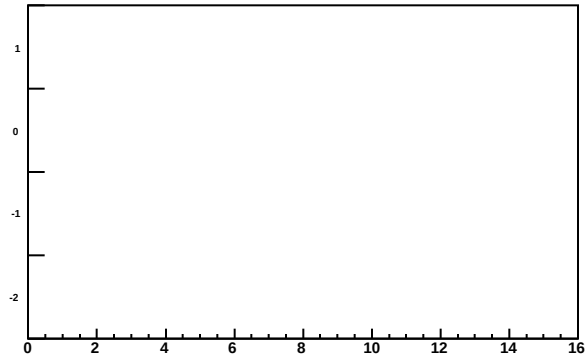

Entries 0


EM201 0-15 (ch3)
Entries 7020880
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

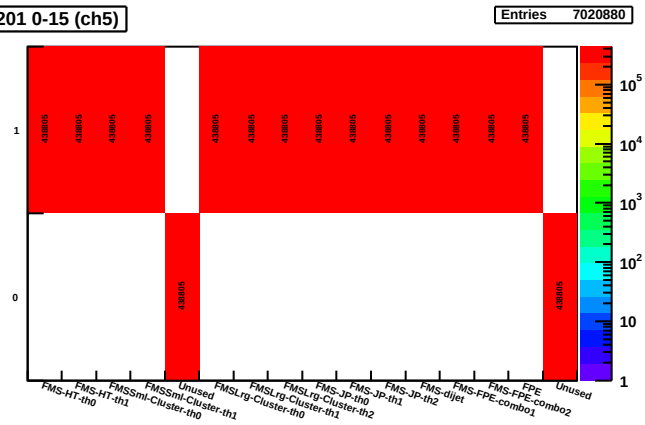

EM201 0-15 (ch3)
Entries 359
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

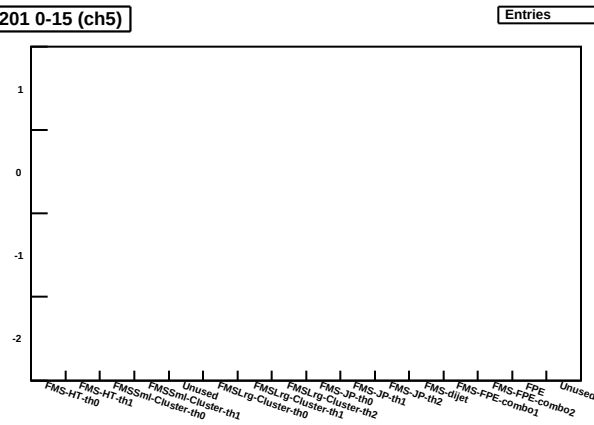




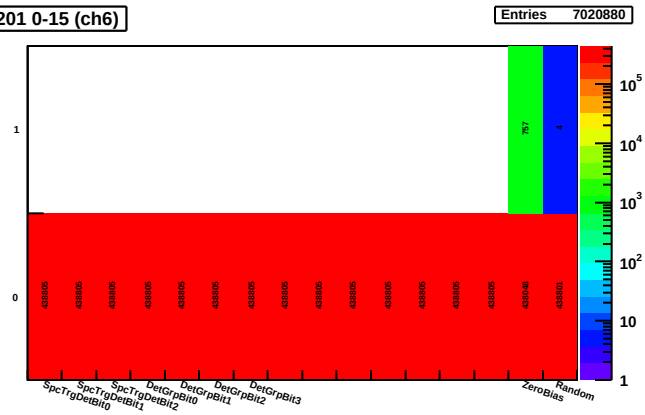
FP201 0-15 (ch5)

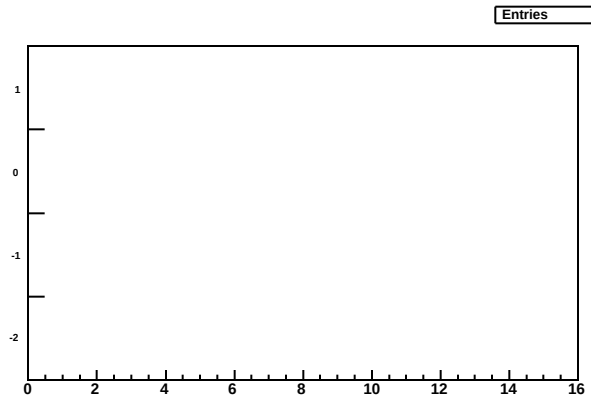
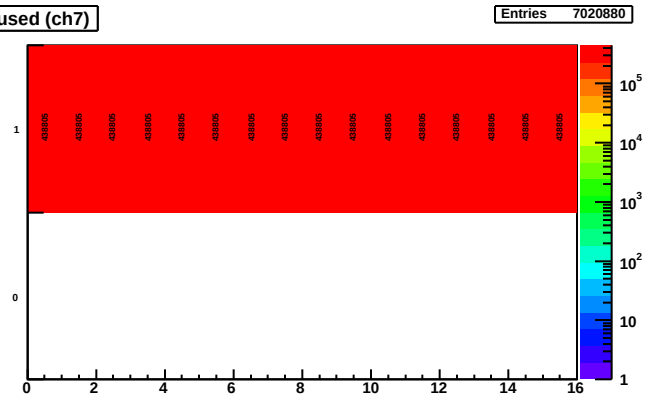
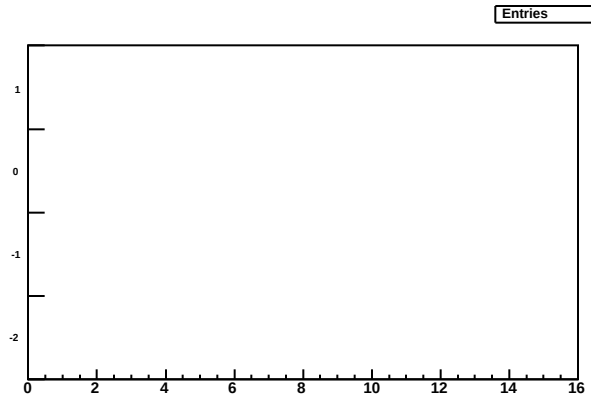


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)