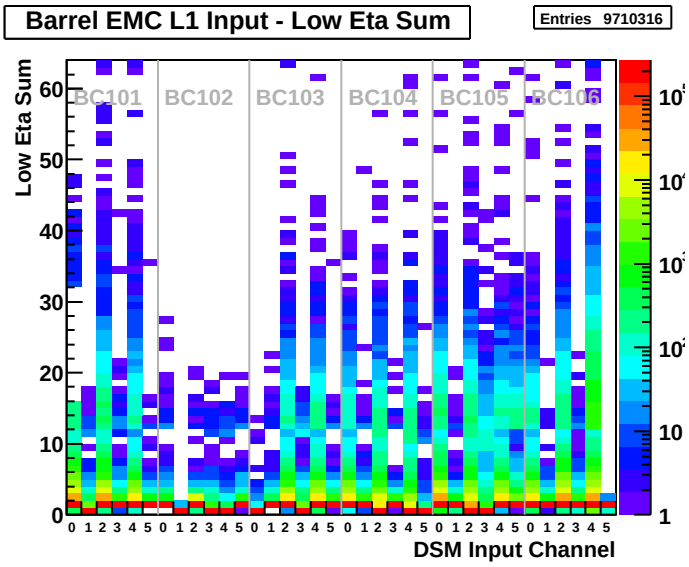
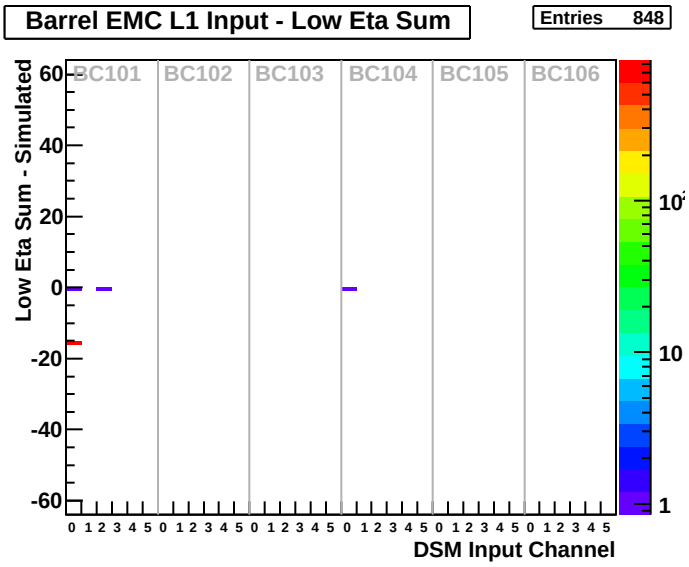
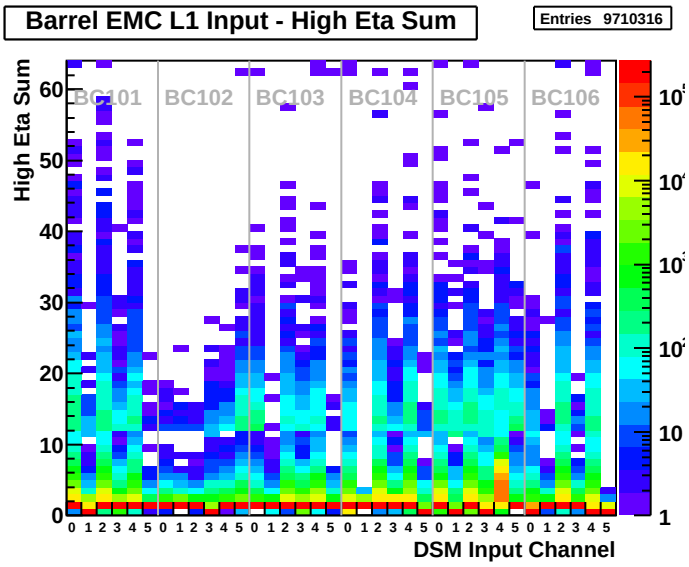
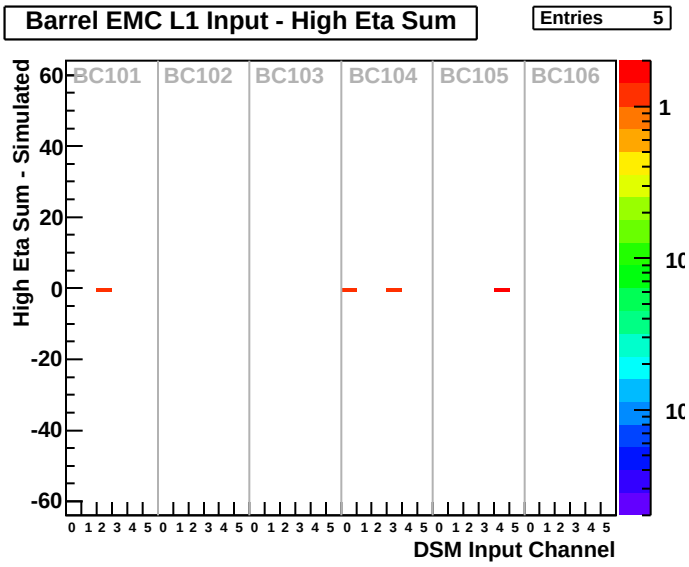
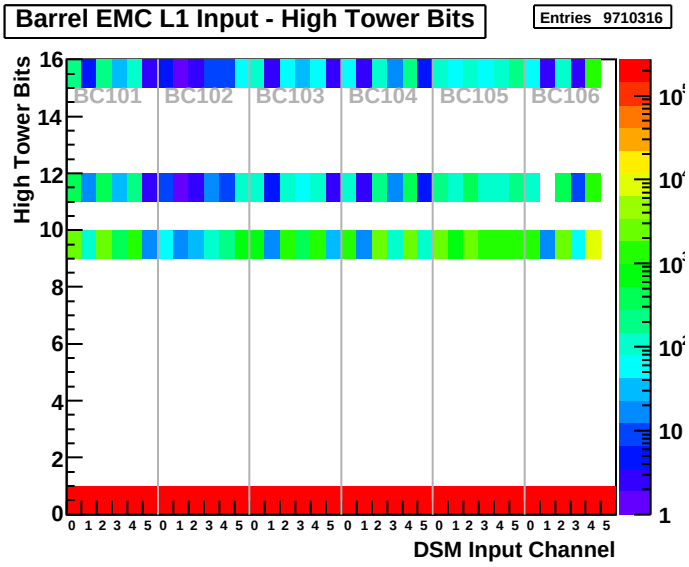
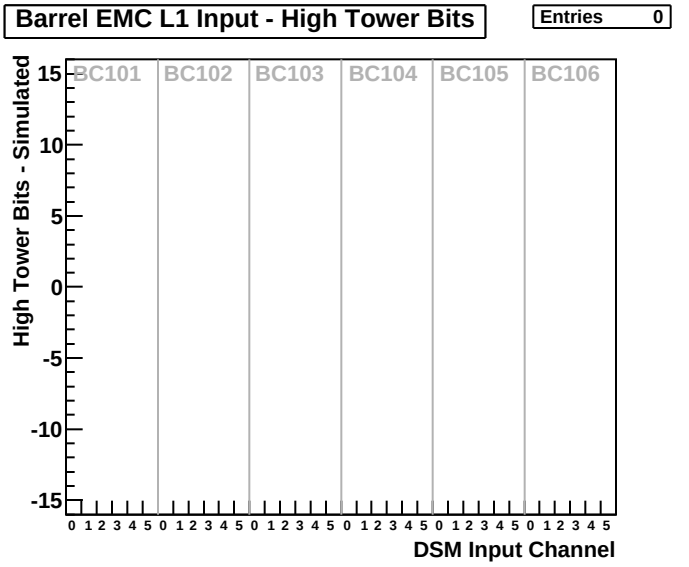

Barrel EMC L1 Input - Low Eta Sum
Entries 9710316
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

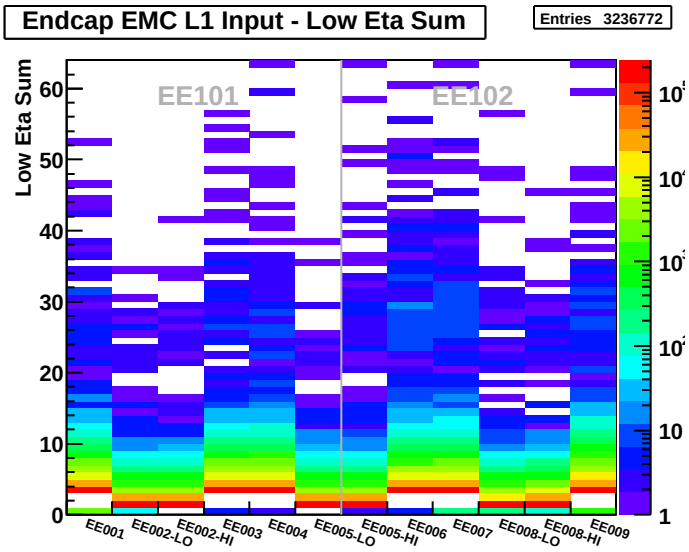

Barrel EMC L1 Input - Low Eta Sum
Entries 848
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

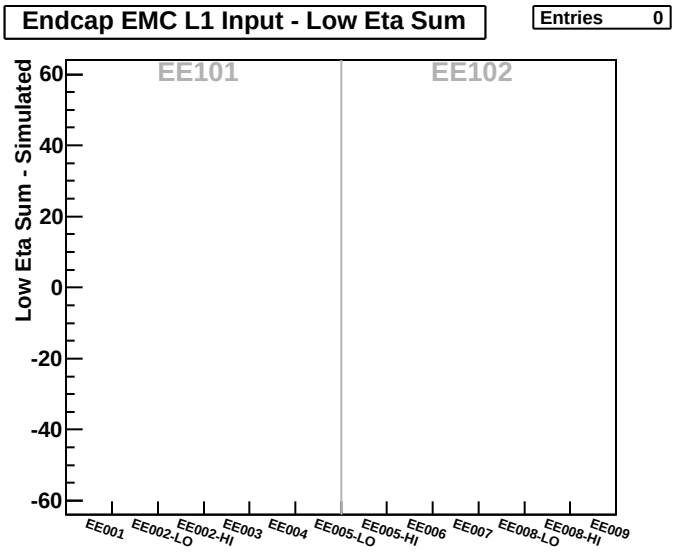

Barrel EMC L1 Input - High Eta Sum
Entries 9710316
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

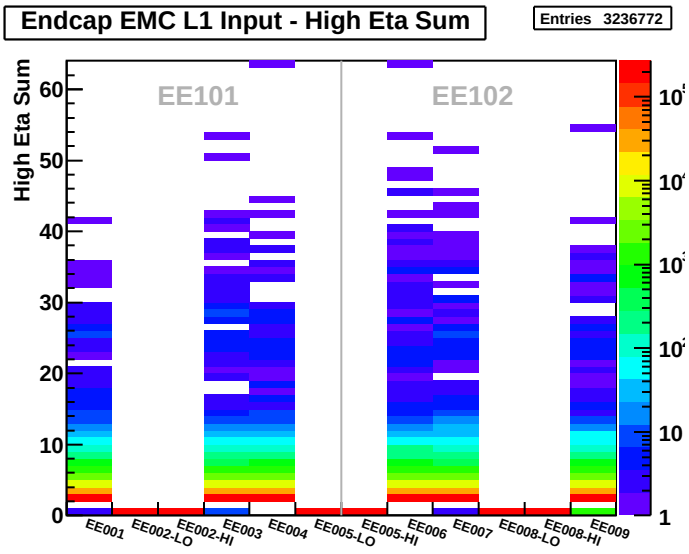

Barrel EMC L1 Input - High Eta Sum
Entries 5
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

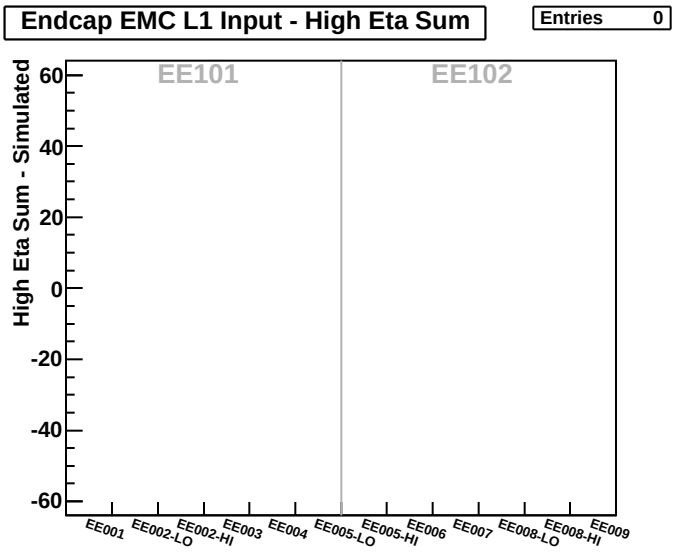

Barrel EMC L1 Input - High Tower Bits
Entries 9710316
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

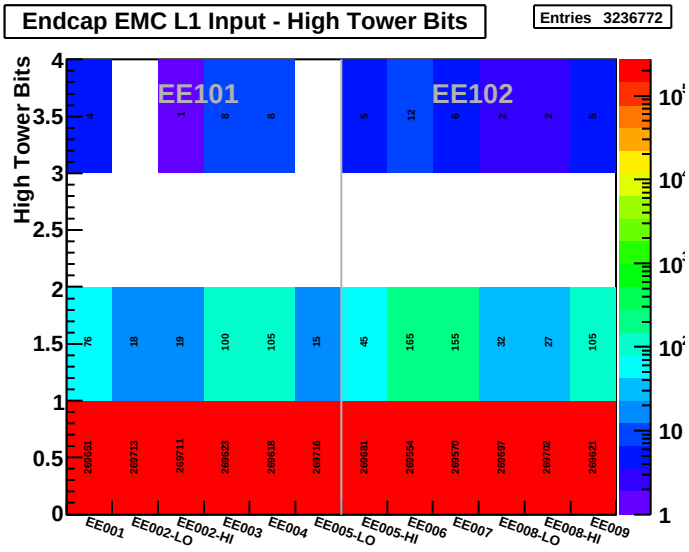

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

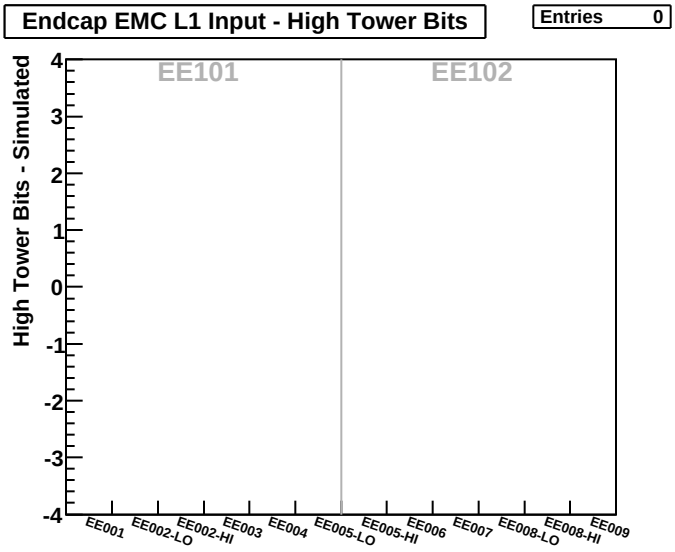

Endcap EMC L1 Input - Low Eta Sum
Entries 3236772
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

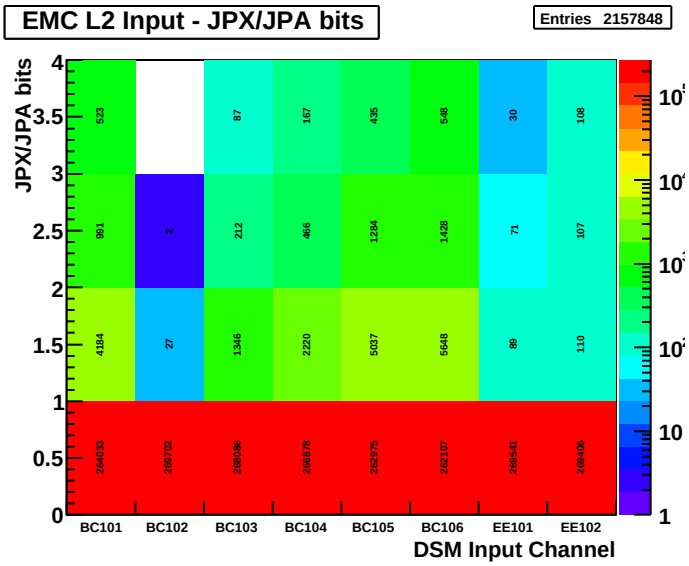

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

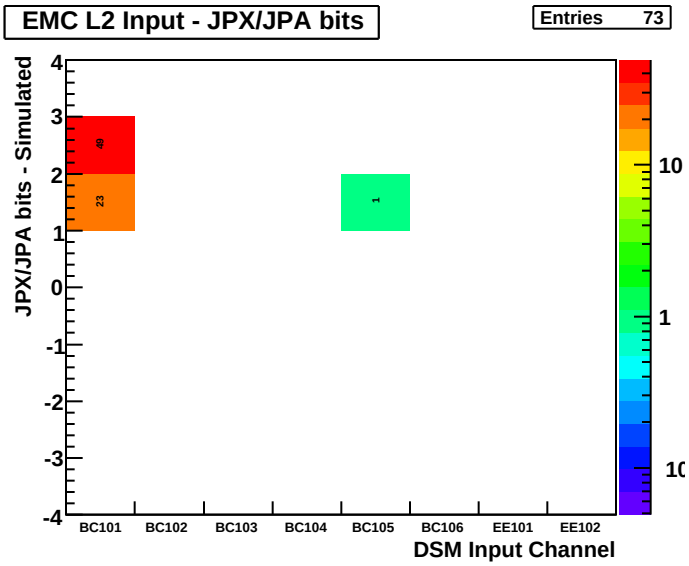

Endcap EMC L1 Input - High Eta Sum
Entries 3236772
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

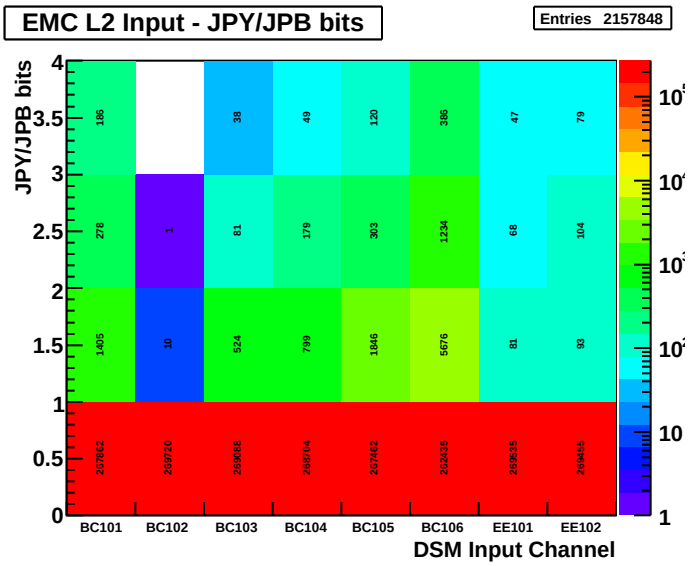

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

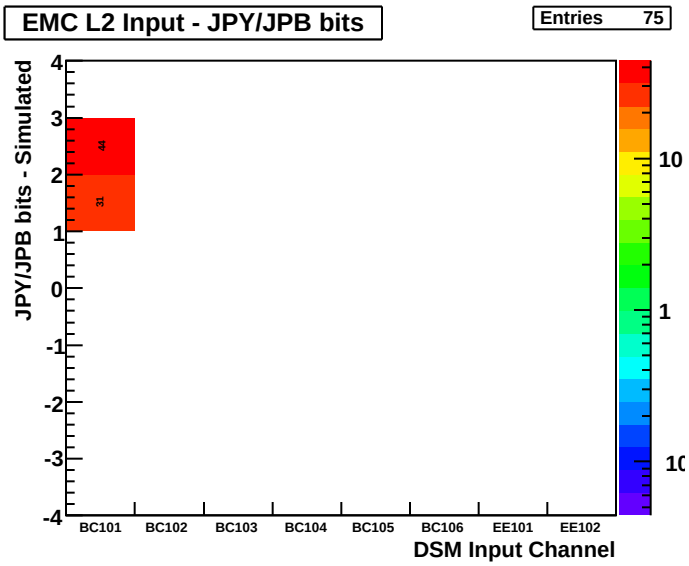

Endcap EMC L1 Input - High Tower Bits
Entries 3236772
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

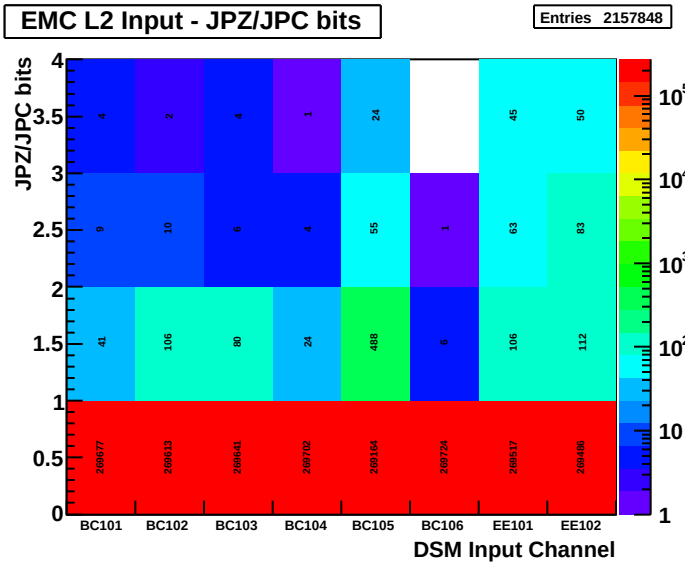

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

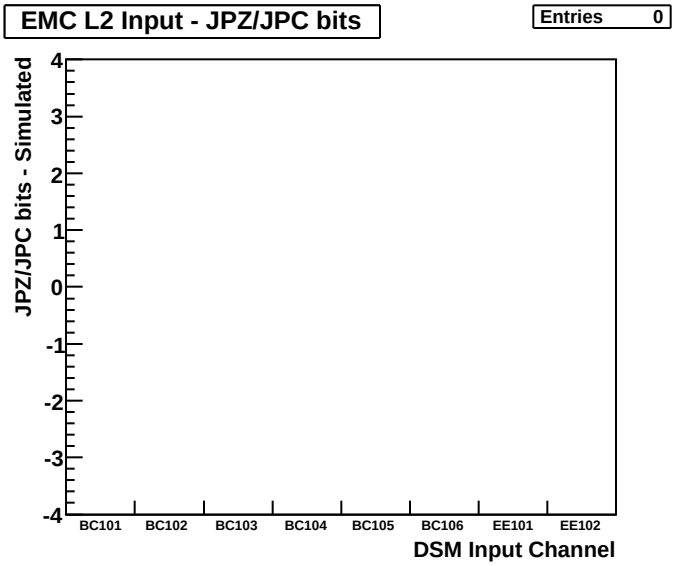

EMC L2 Input - JPX/JPA bits
Entries 2157848
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

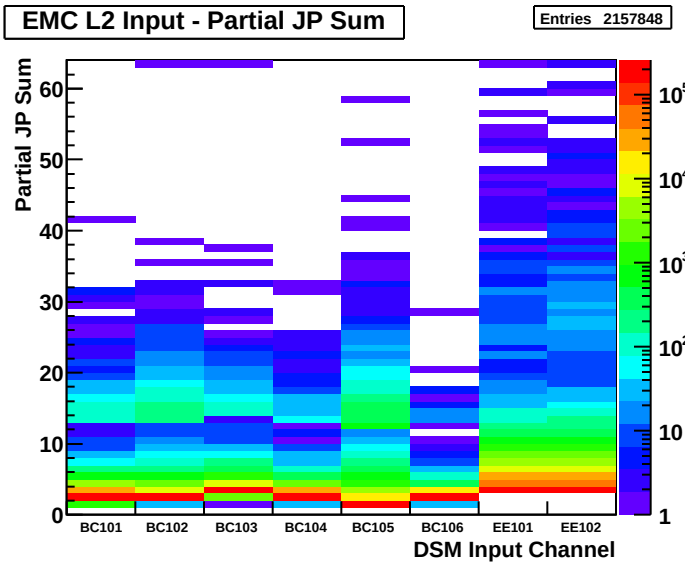

EMC L2 Input - JPX/JPA bits
Entries 73
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

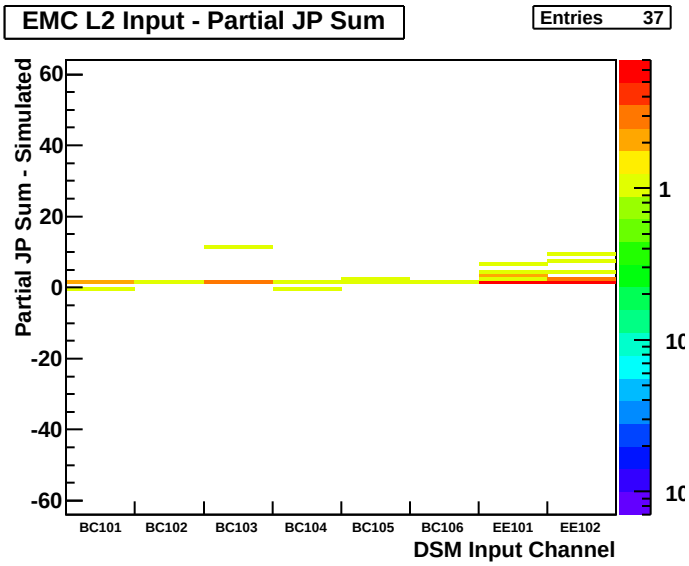

EMC L2 Input - JPY/JPB bits
Entries 2157848
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

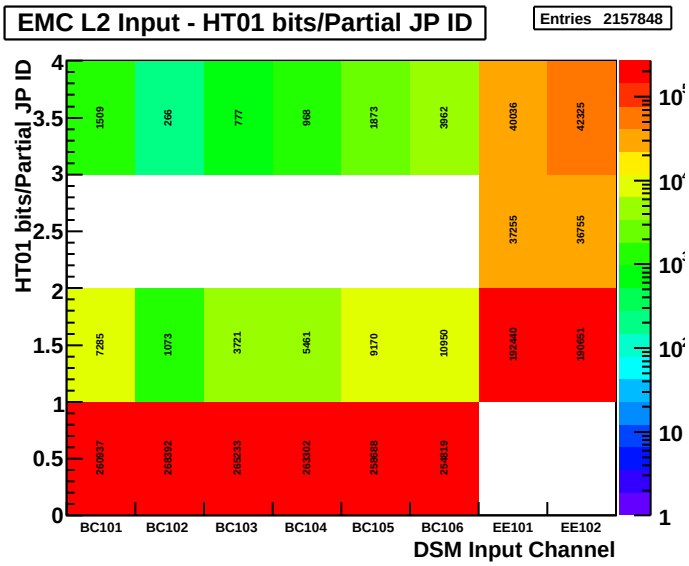

EMC L2 Input - JPY/JPB bits
Entries 75
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

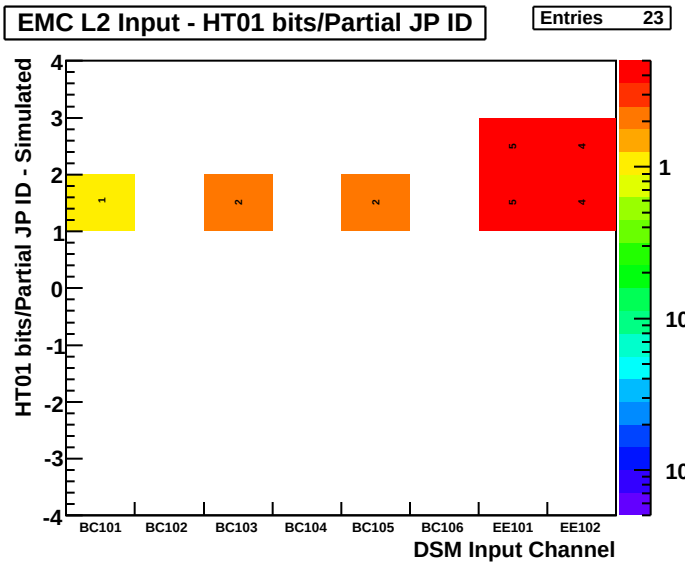

EMC L2 Input - JPZ/JPC bits
Entries 2157848
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

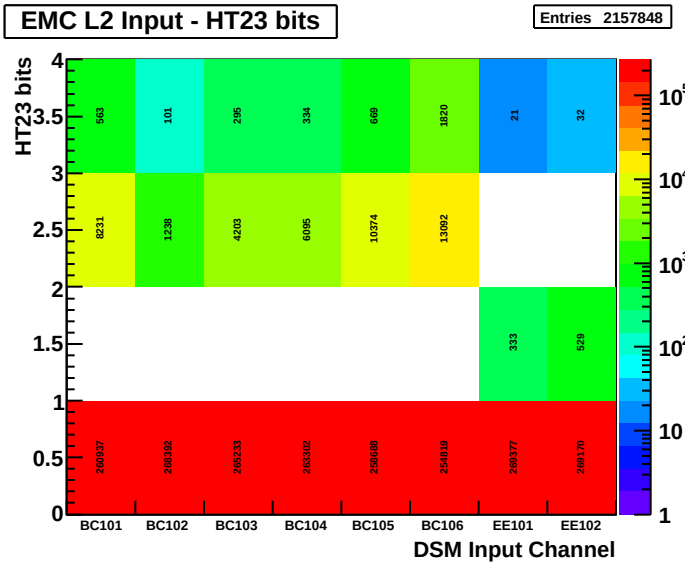

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

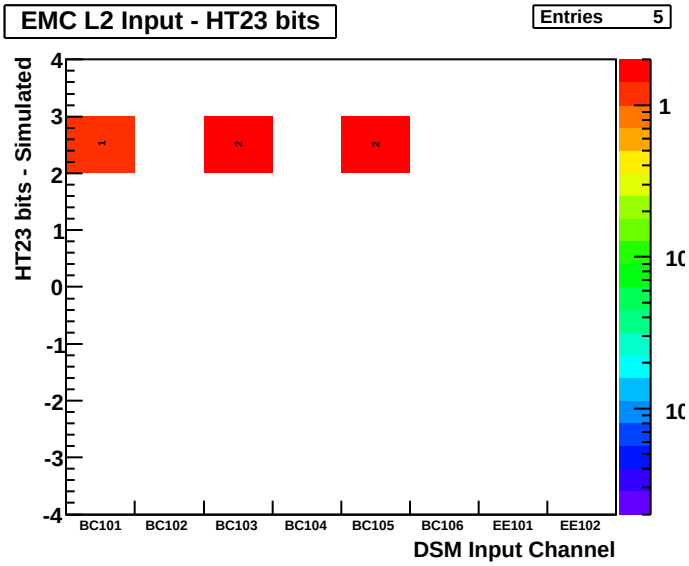

EMC L2 Input - Partial JP Sum
Entries 2157848
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

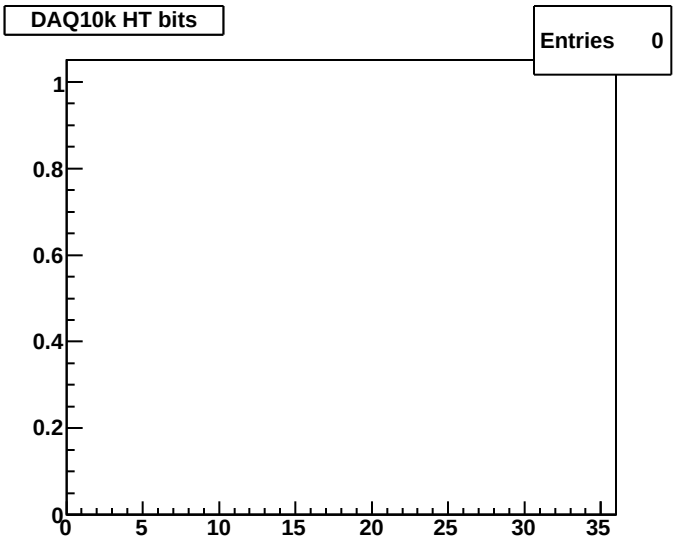

EMC L2 Input - Partial JP Sum
Entries 37
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

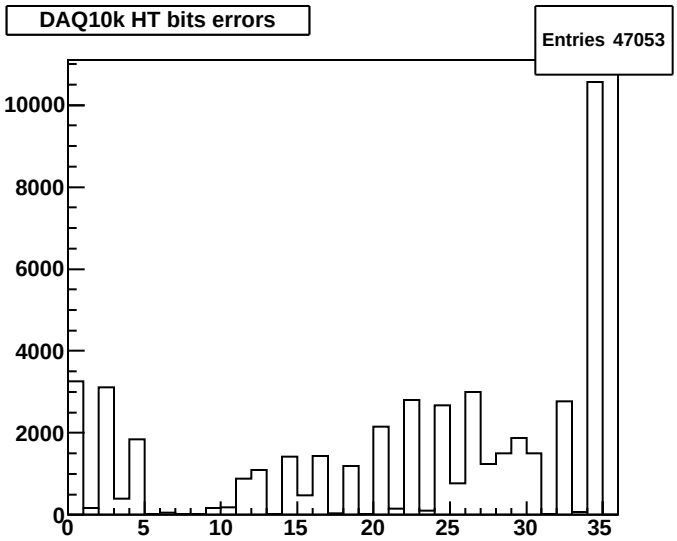

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2157848
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

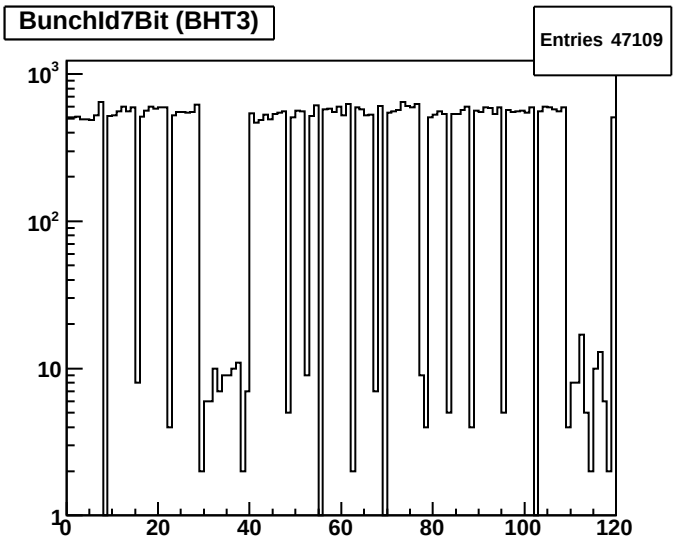

EMC L2 Input - HT01 bits/Partial JP ID
Entries 23
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

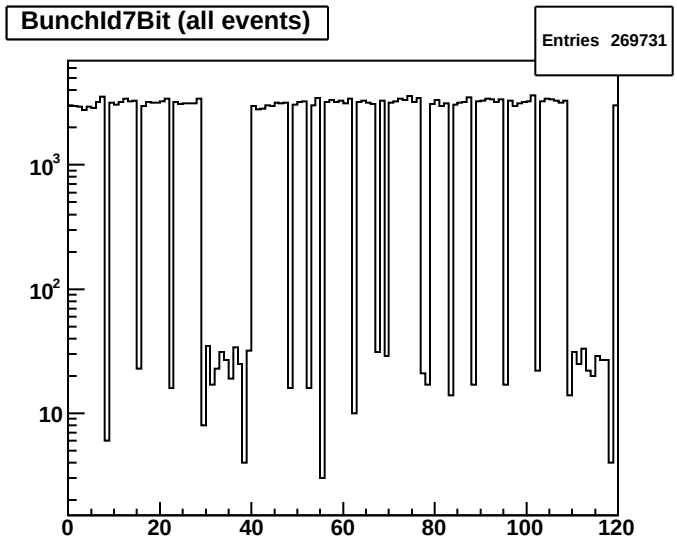

EMC L2 Input - HT23 bits
Entries 2157848
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

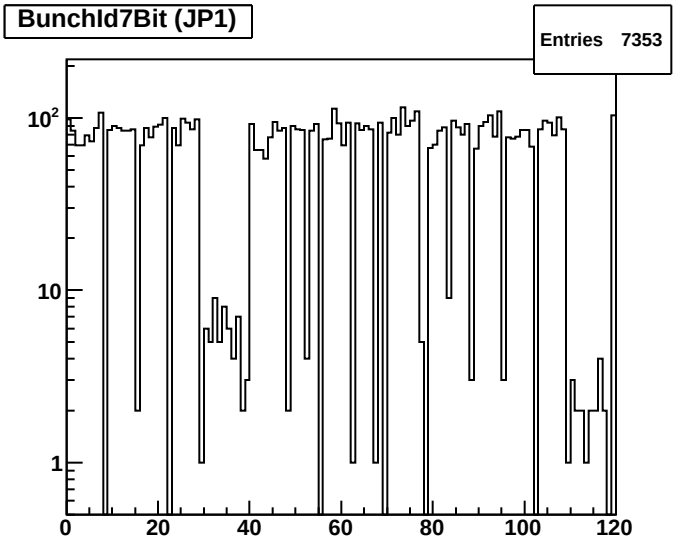

EMC L2 Input - HT23 bits
Entries 5
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

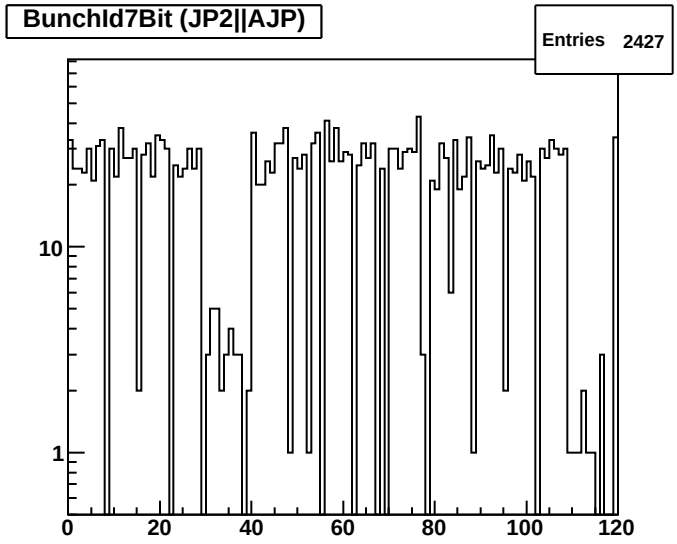

DAQ10k HT bits
Entries 0

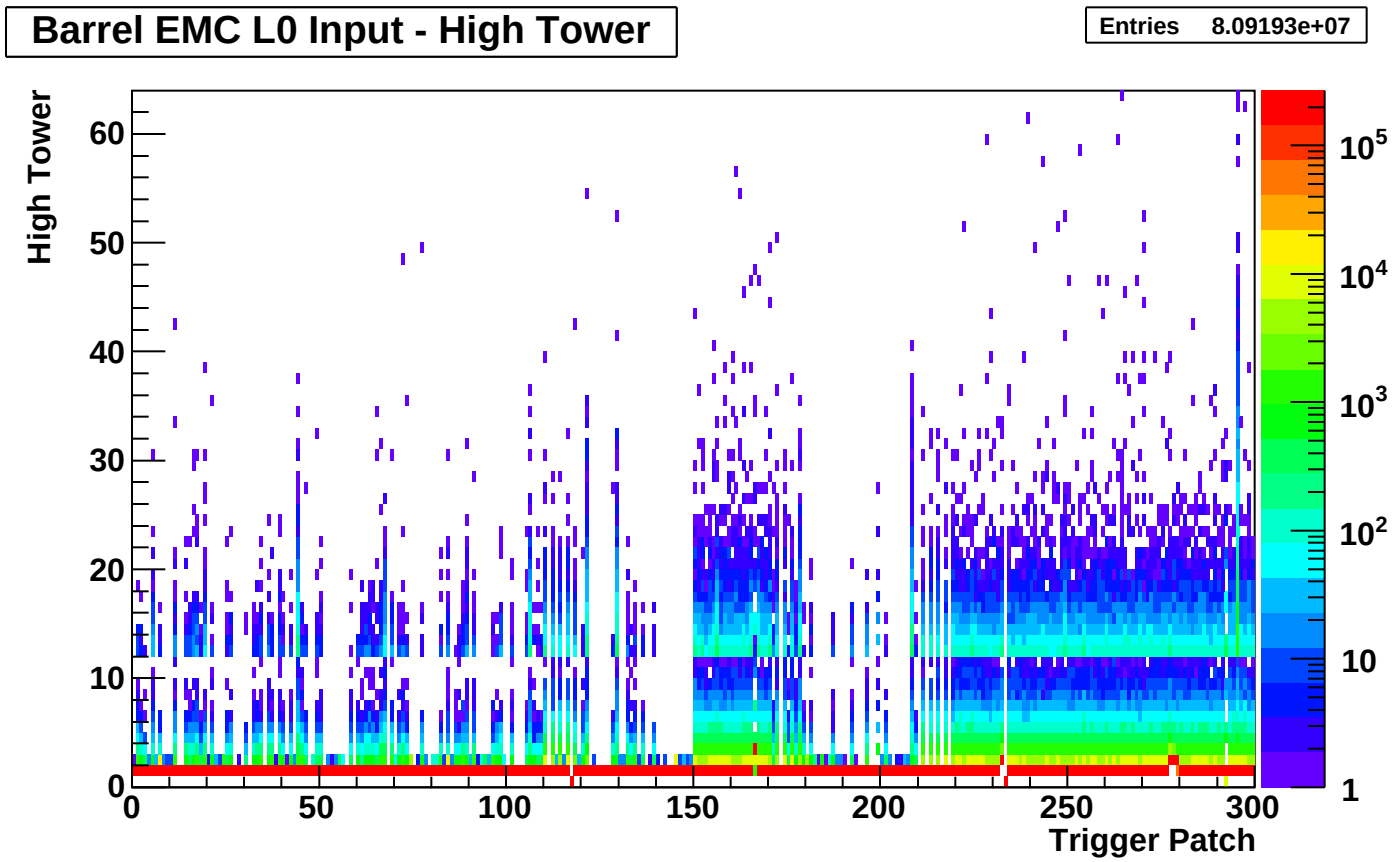

DAQ10k HT bits errors
Entries 47053

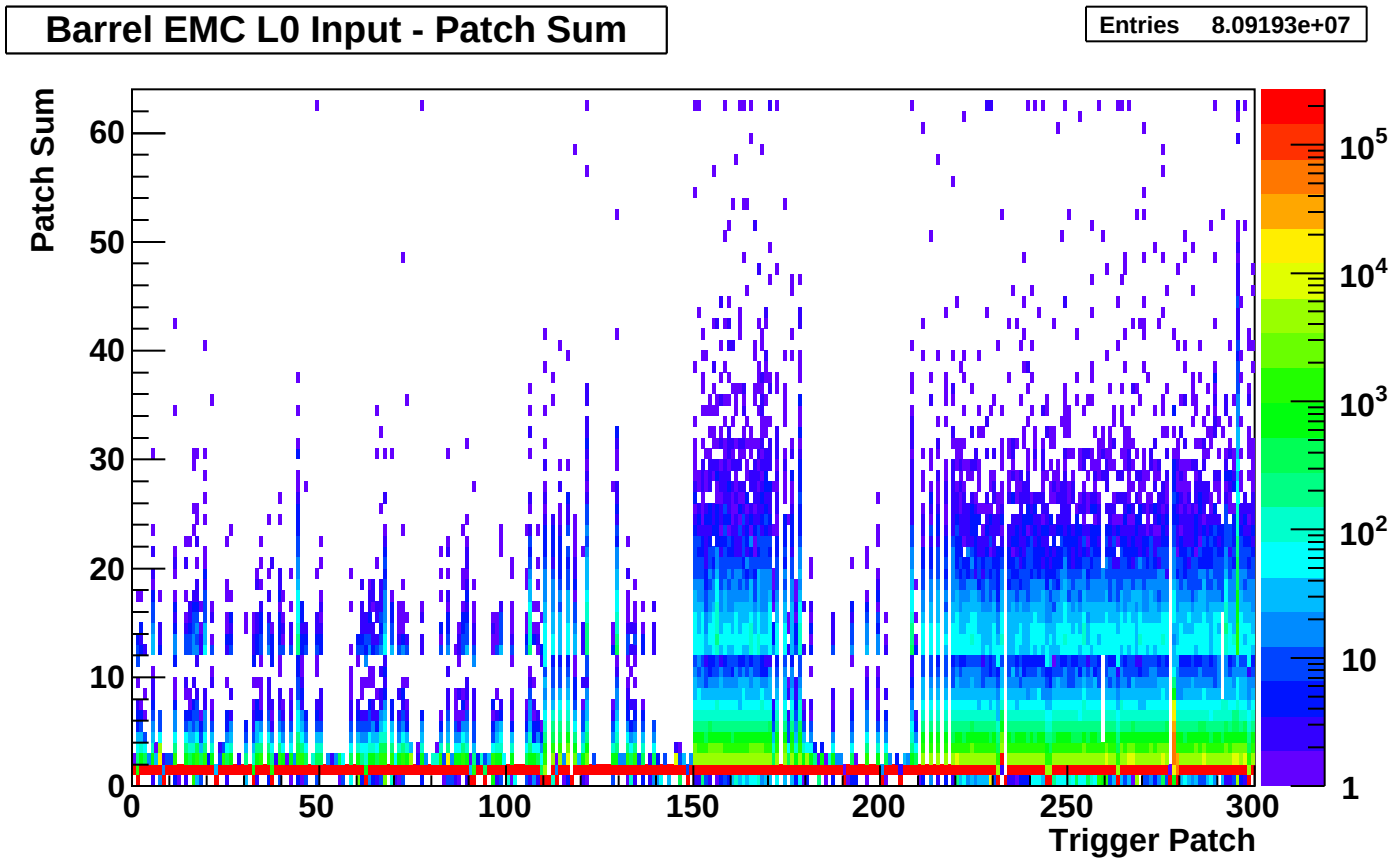

BunchId7Bit (BHT3)
Entries 47109

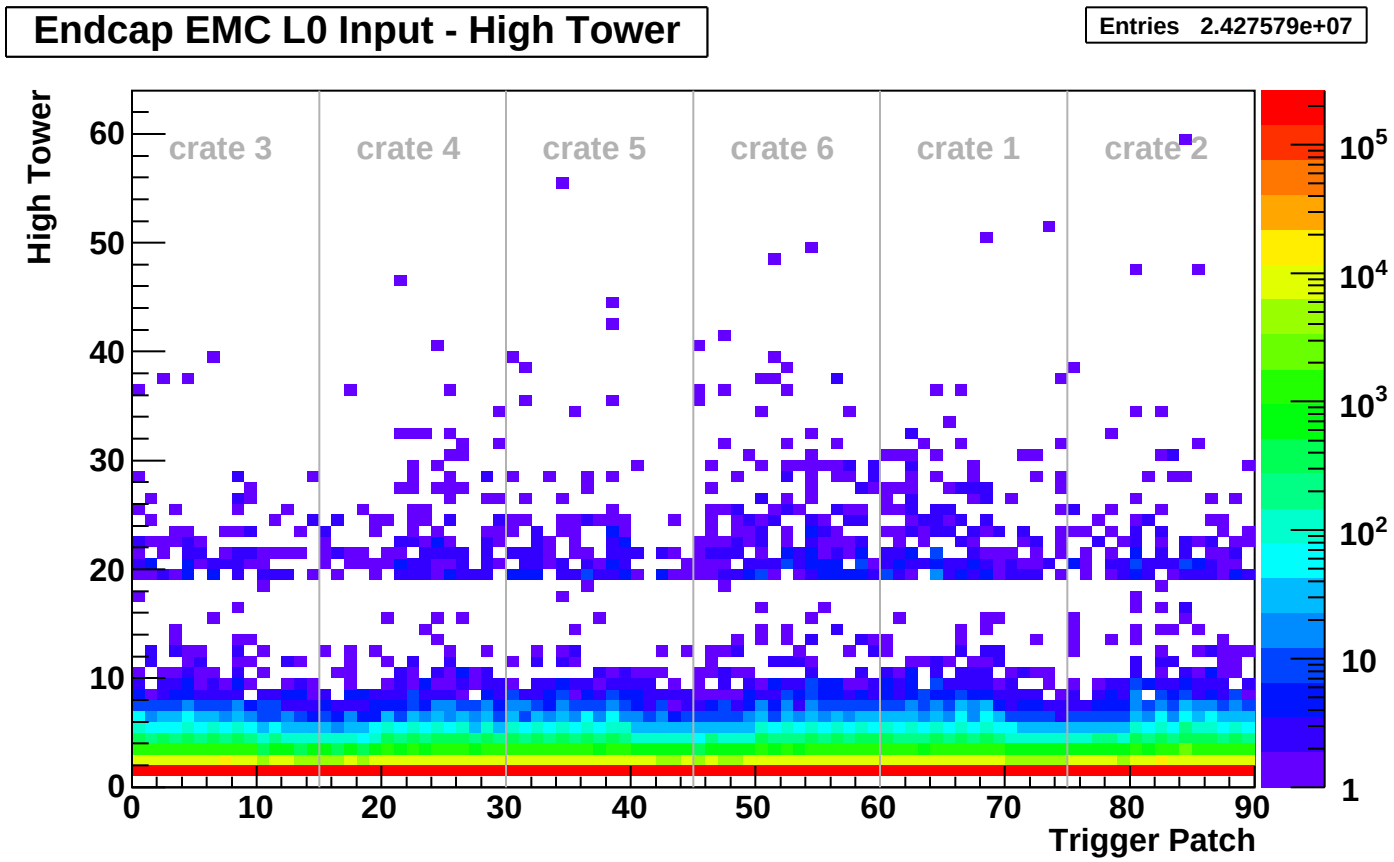

BunchId7Bit (all events)
Entries 269731

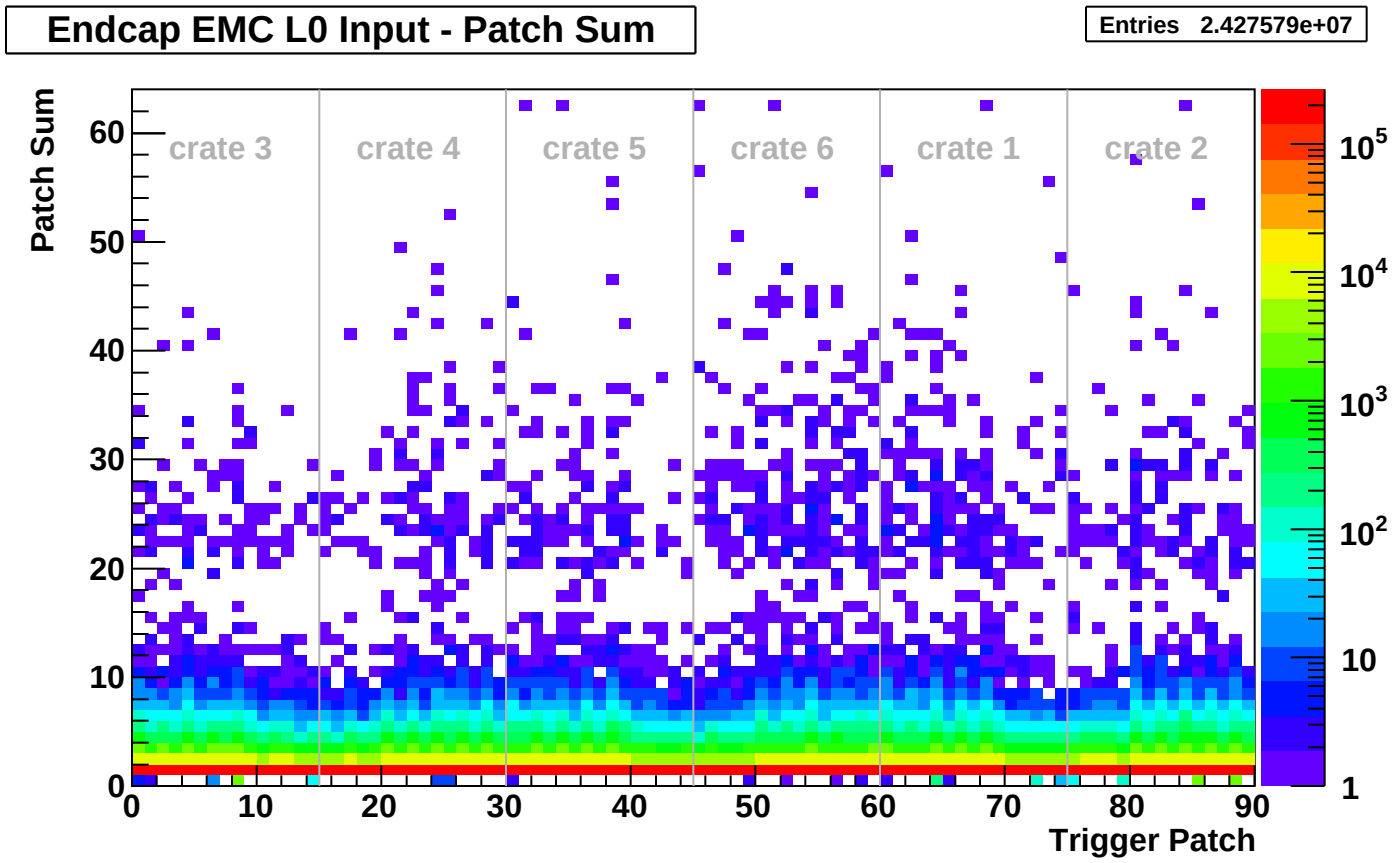

BunchId7Bit (JP1)
Entries 7353

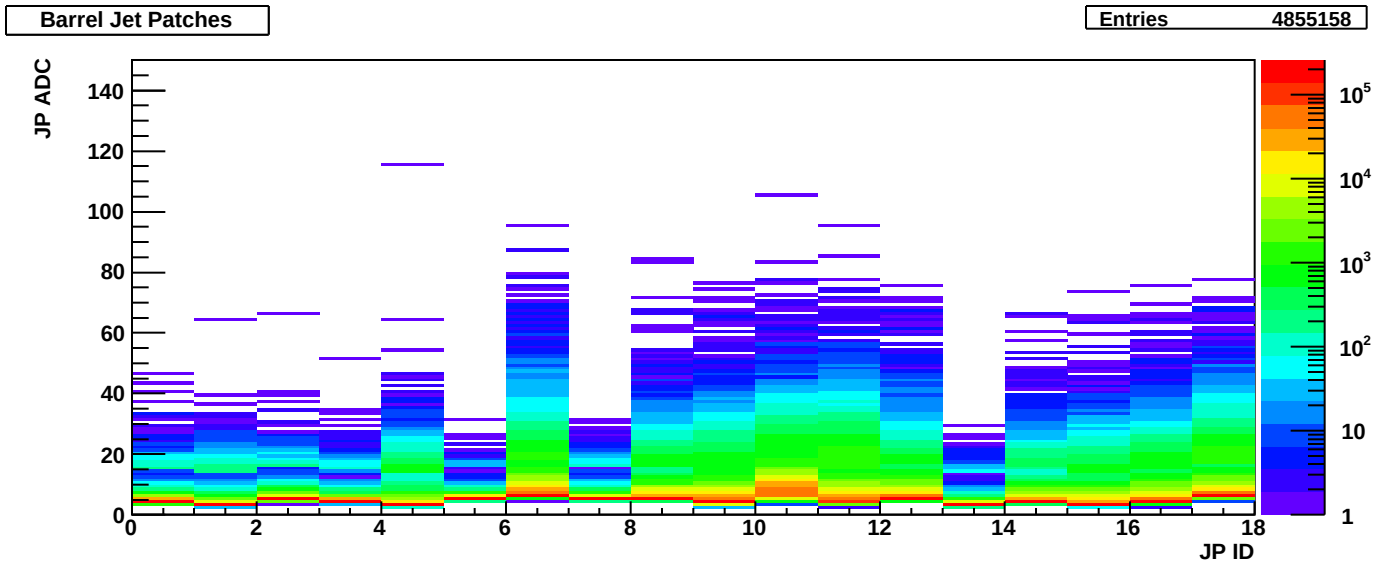

BunchId7Bit (JP2||AJP)
Entries 2427

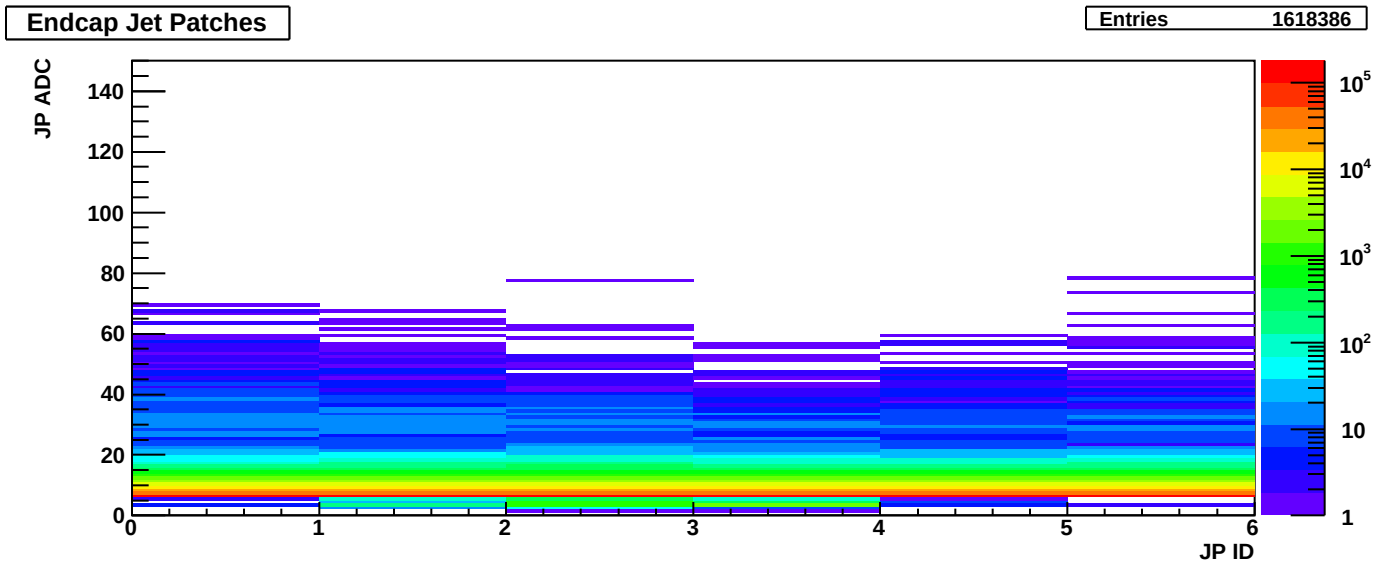

Barrel EMC L0 Input - High Tower
Entries 8.09193e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

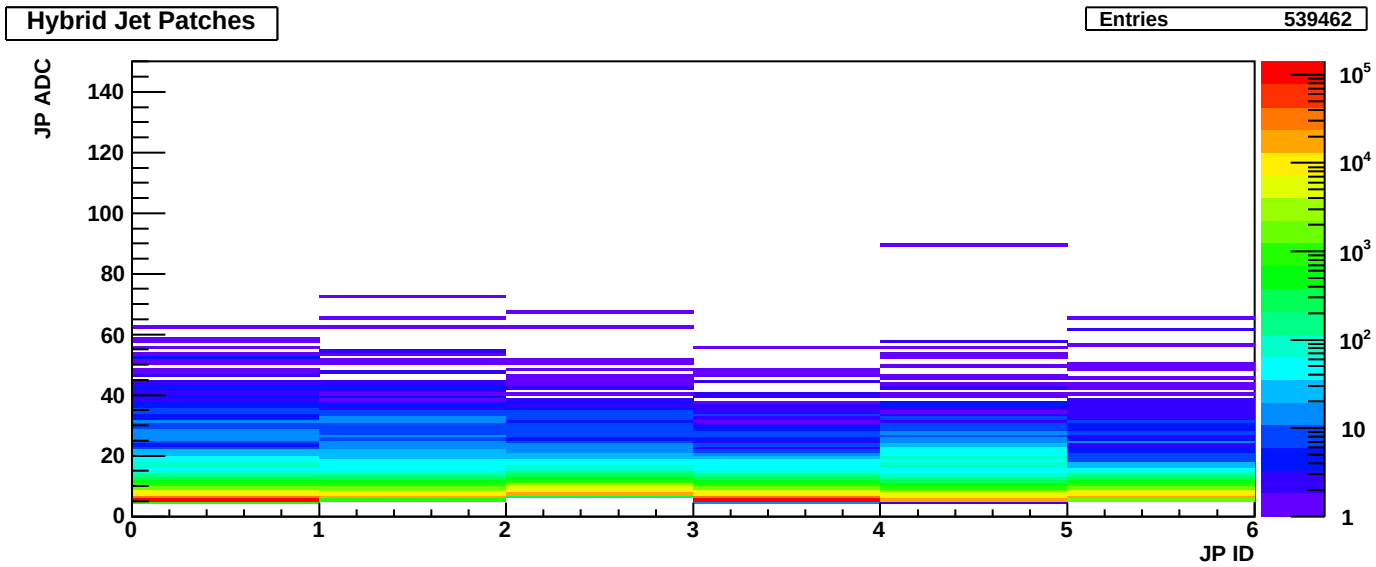

Barrel EMC L0 Input - Patch Sum
Entries 8.09193e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

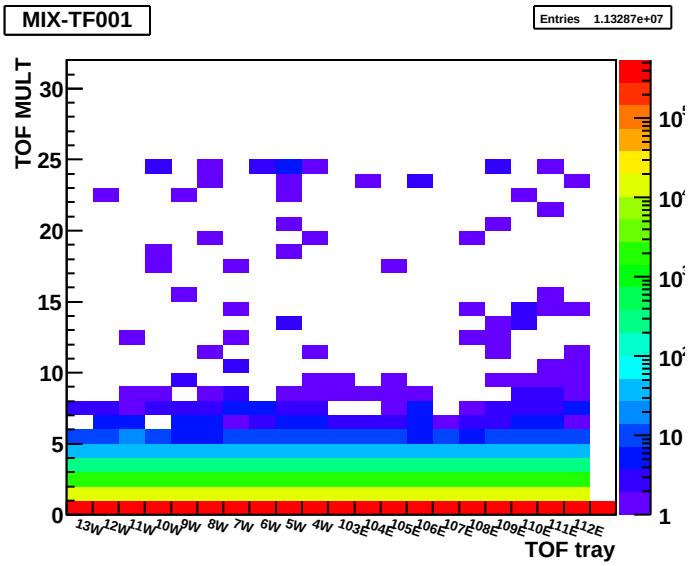

Endcap EMC L0 Input - High Tower
Entries 2.427579e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

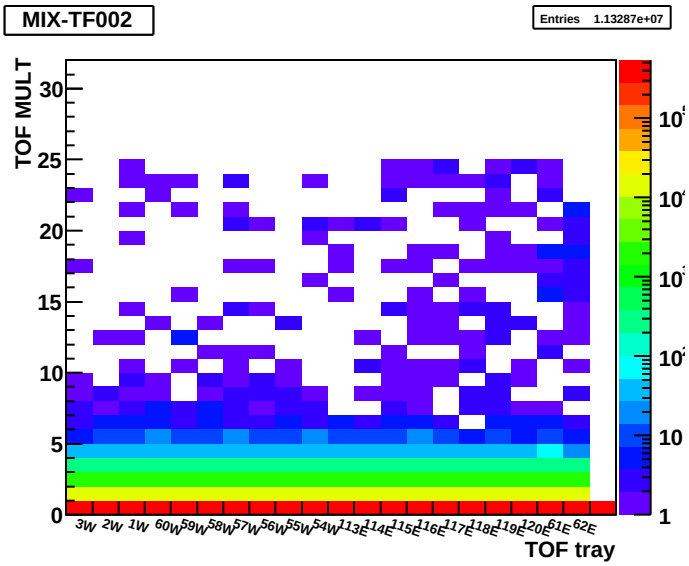

Endcap EMC L0 Input - Patch Sum
Entries 2.427579e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

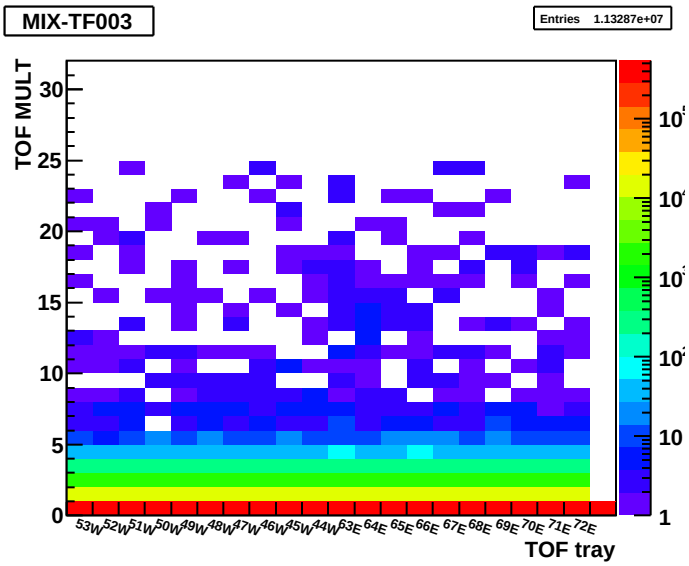

Barrel Jet Patches
Entries 4855158
JP ADC
JP ID

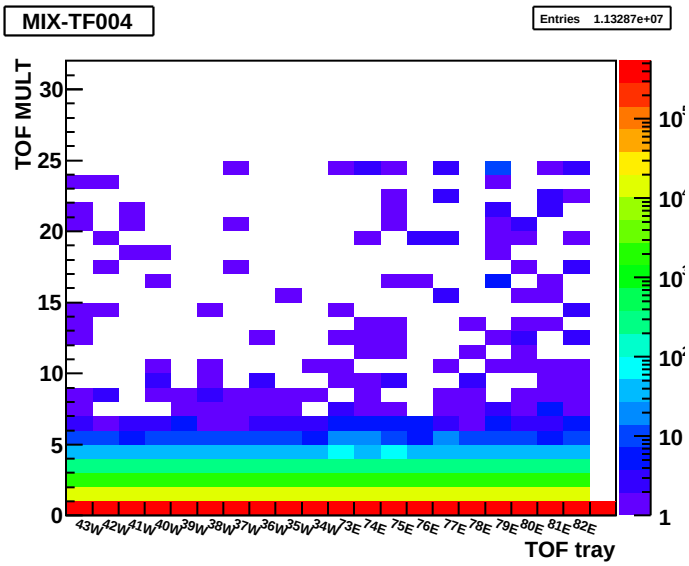

Endcap Jet Patches
Entries 1618386
JP ADC
JP ID

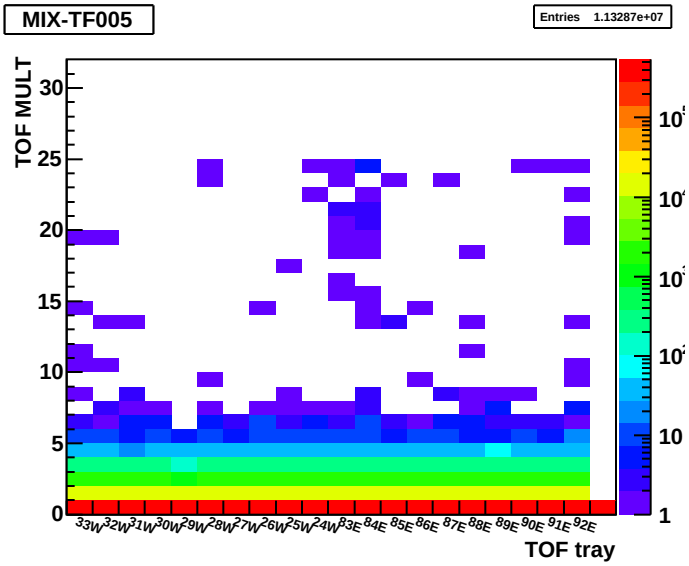

Hybrid Jet Patches
Entries 539462
JP ADC
JP ID

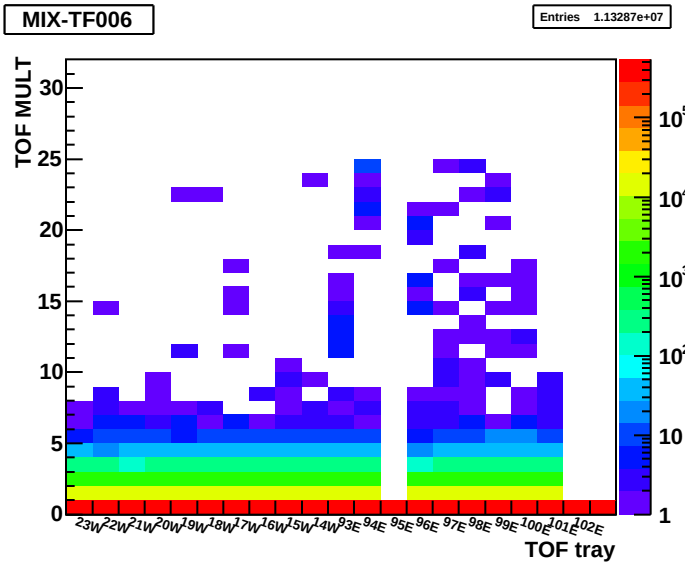

MIX-TF001
Entries 1.13287e+07
TOF MULT
TOF tray

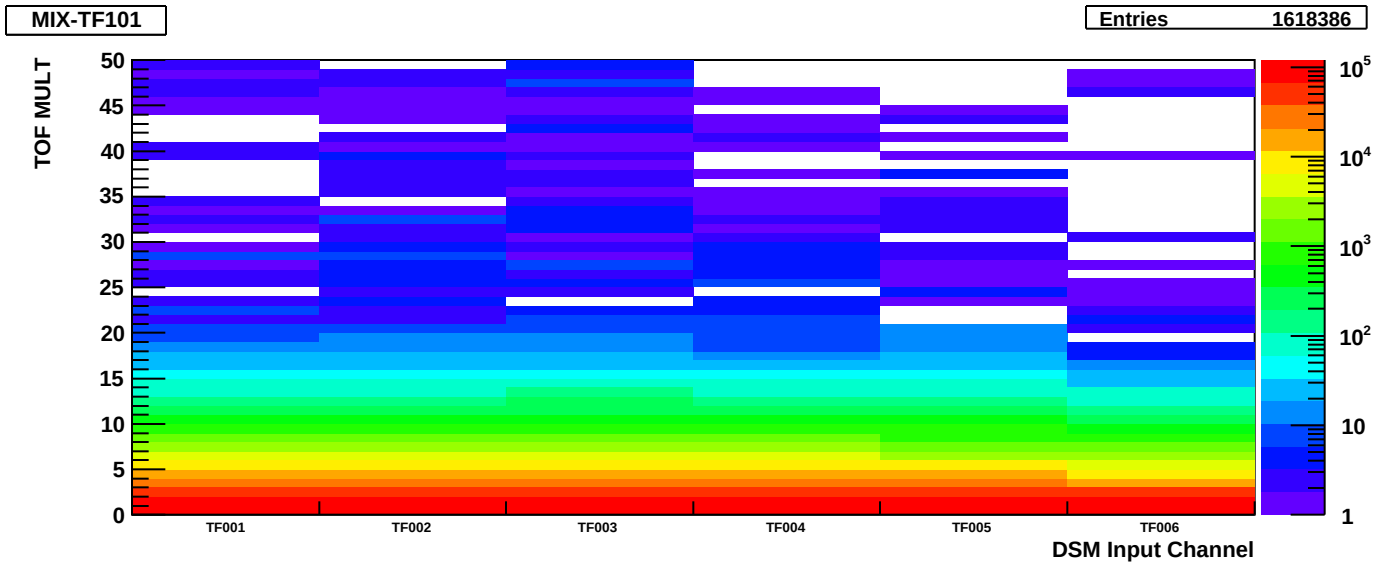

MIX-TF002
Entries 1.13287e+07
TOF MULT
TOF tray

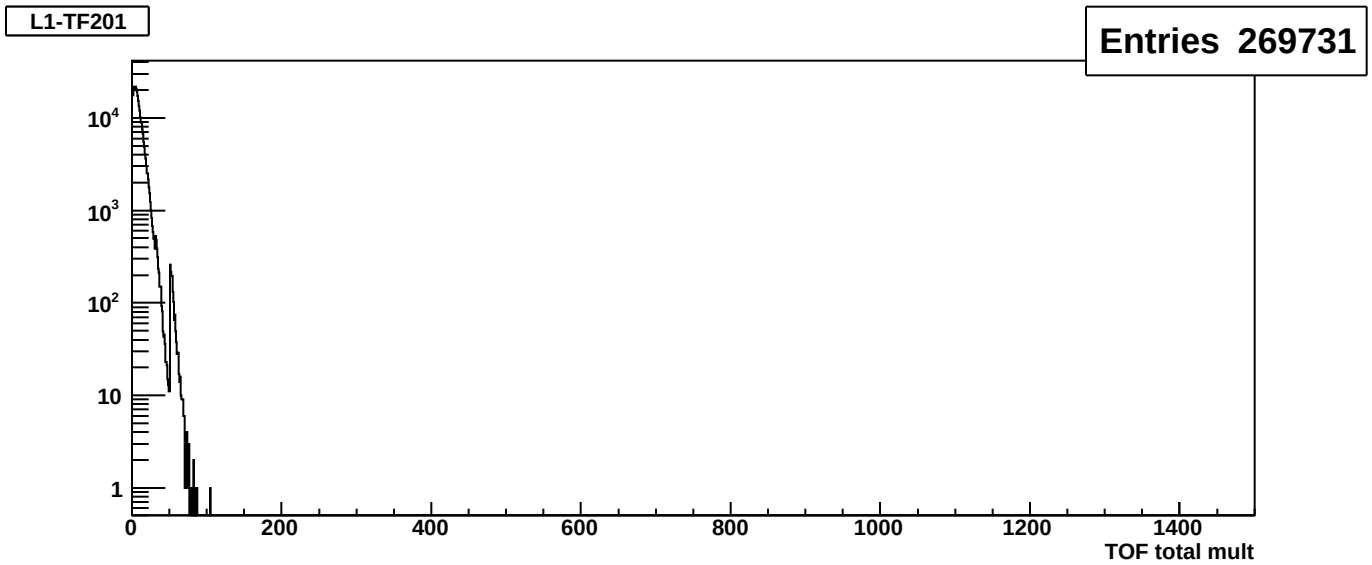

MIX-TF003
Entries 1.13287e+07
TOF MULT
TOF tray

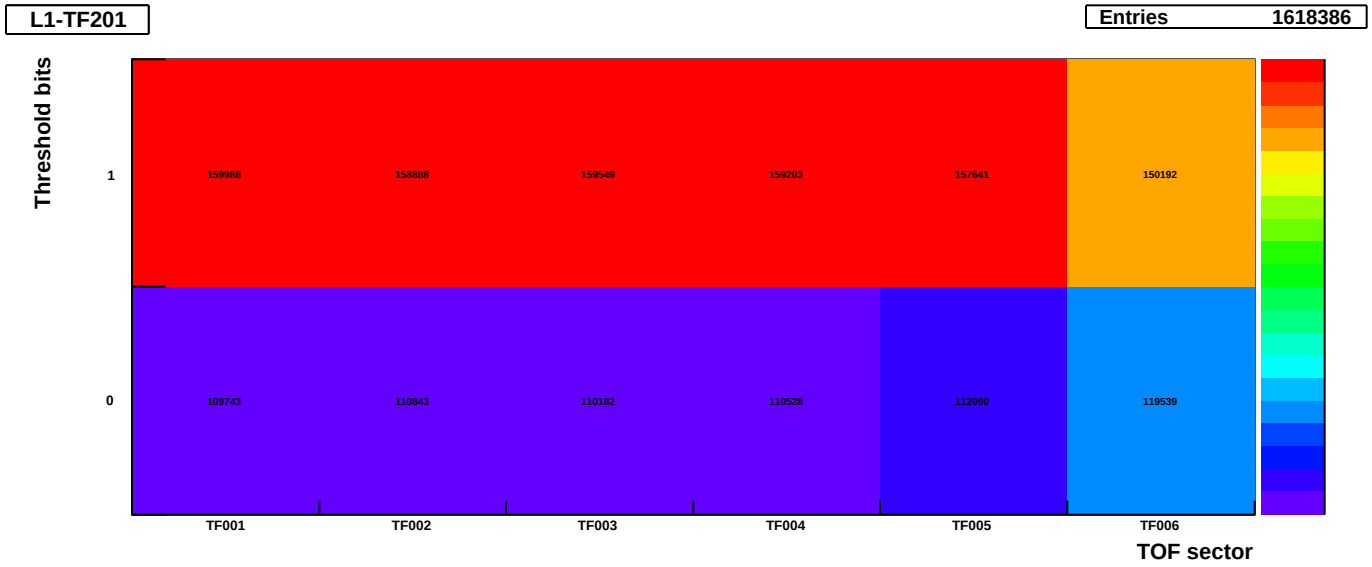

MIX-TF004
Entries 1.13287e+07
TOF MULT
TOF tray

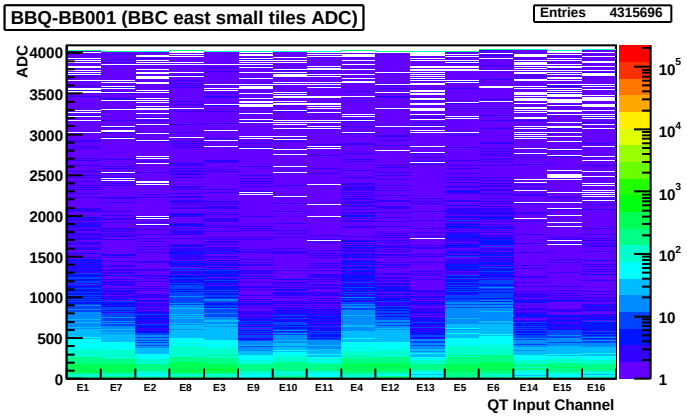

MIX-TF005
Entries 1.13287e+07
TOF MULT
TOF tray

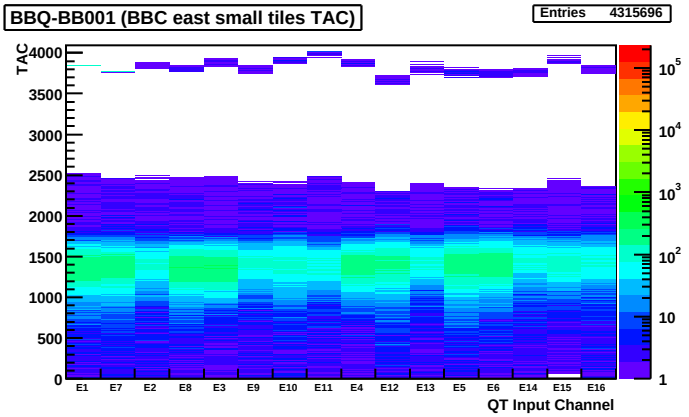

MIX-TF006
Entries 1.13287e+07
TOF MULT
TOF tray

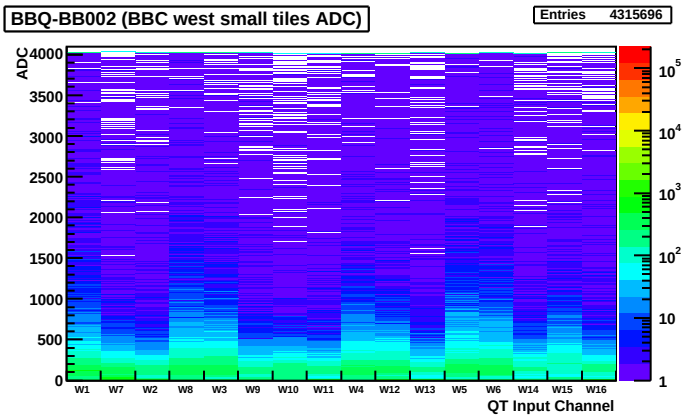

MIX-TF101
Entries 1618386
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

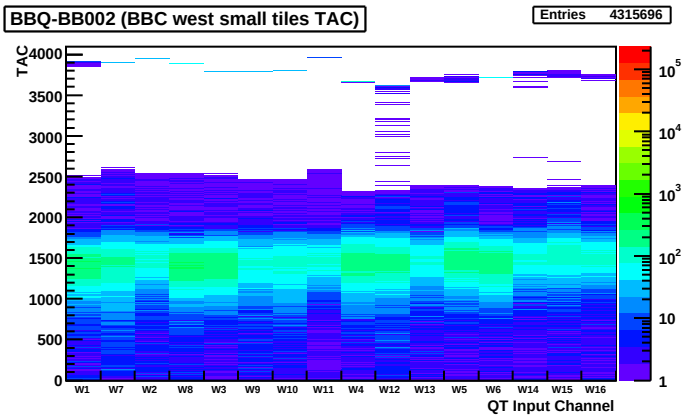

L1-TF201
Entries 269731
TOF total mult

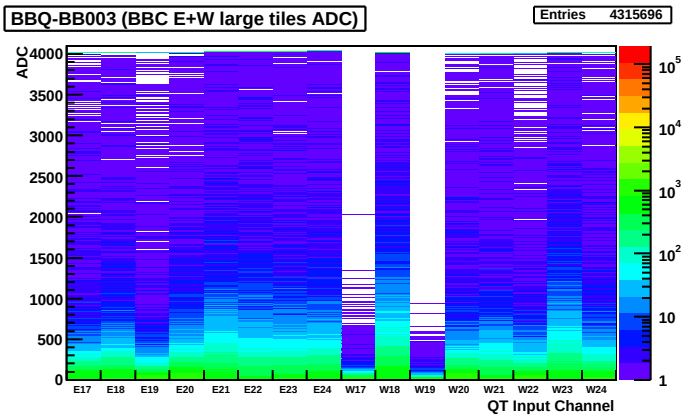

L1-TF201
Entries 1618386
Threshold bits
159988
158888
159549
159203
157641
150192
109743
110843
110182
110528
112090
119539
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

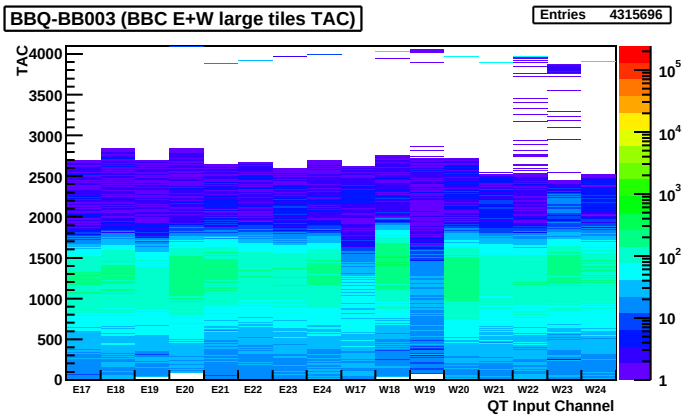

BBQ-BB001 (BBC east small tiles ADC)
Entries 4315696
ADC
QT Input Channel

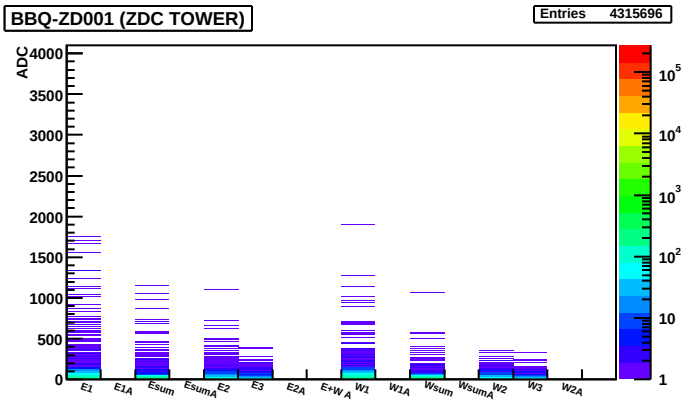

BBQ-BB001 (BBC east small tiles TAC)
Entries 4315696
TAC
QT Input Channel

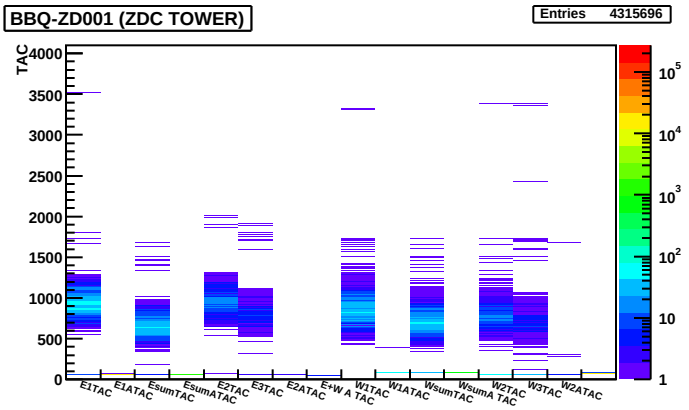

BBQ-BB002 (BBC west small tiles ADC)
Entries 4315696
ADC
QT Input Channel

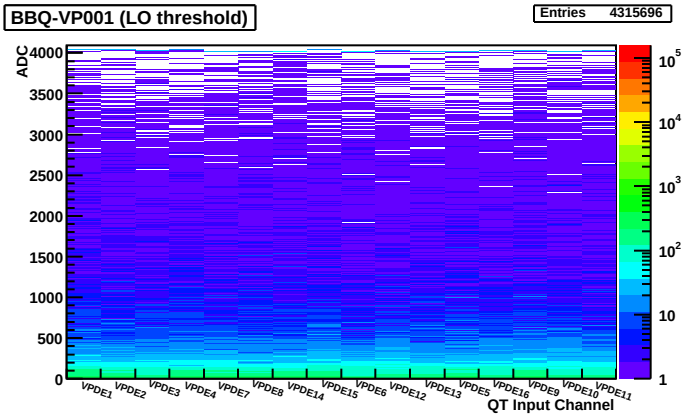

BBQ-BB002 (BBC west small tiles TAC)
Entries 4315696
TAC
QT Input Channel

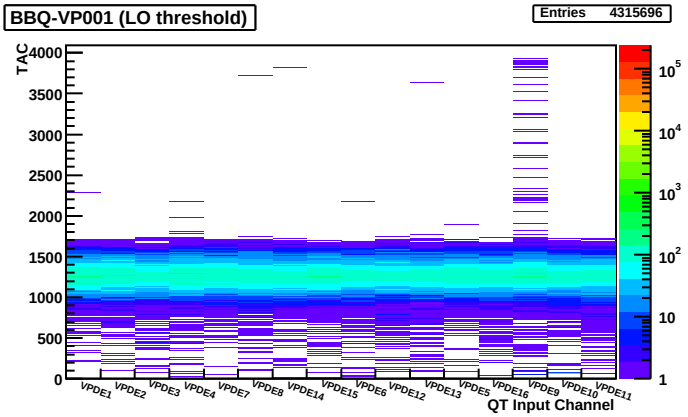

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4315696
ADC
QT Input Channel

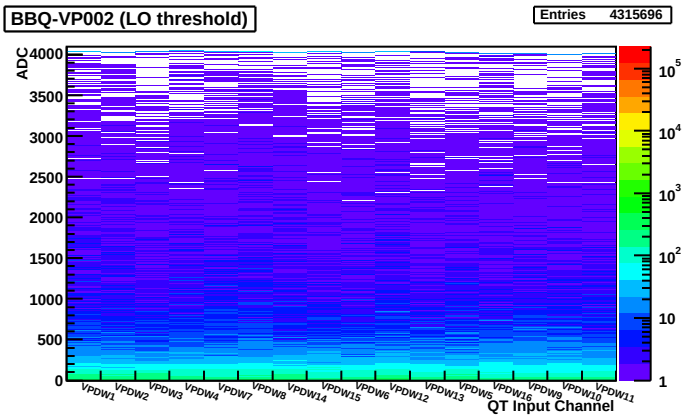

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4315696
TAC
QT Input Channel

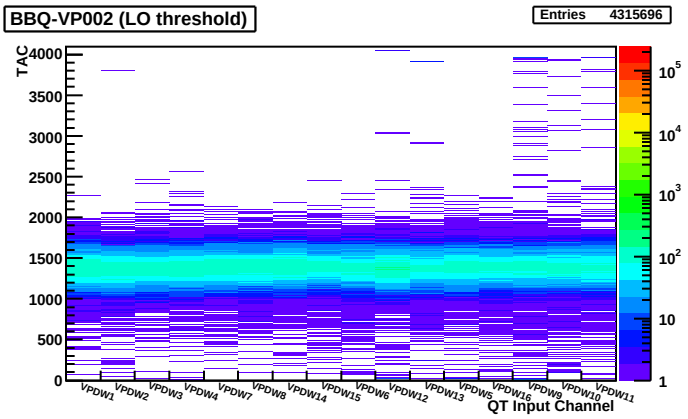

BBQ-ZD001 (ZDC TOWER)
Entries 4315696
ADC

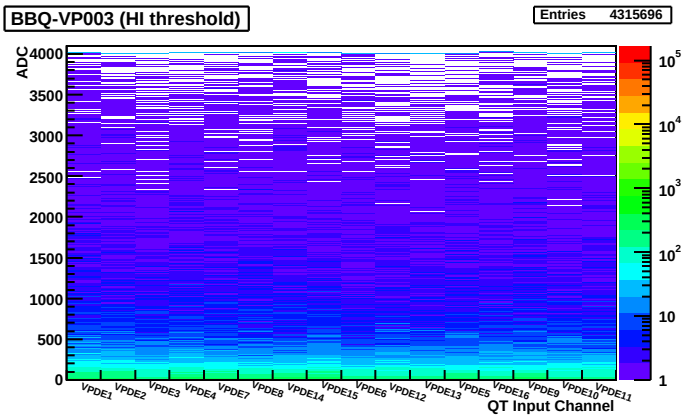

BBQ-ZD001 (ZDC TOWER)
Entries 4315696
TAC

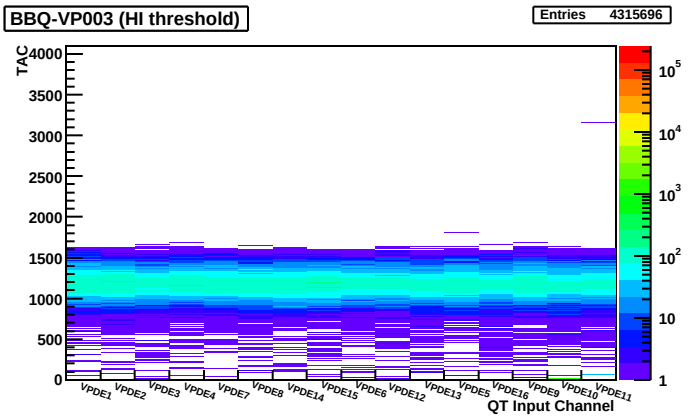

BBQ-VP001 (LO threshold)
Entries 4315696
ADC
QT Input Channel

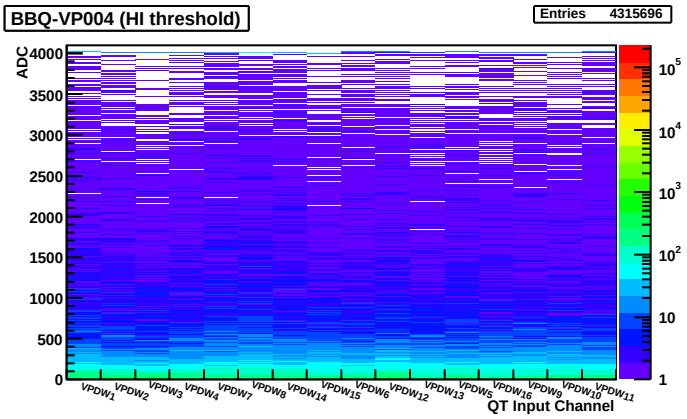

BBQ-VP001 (LO threshold)
Entries 4315696
TAC
QT Input Channel

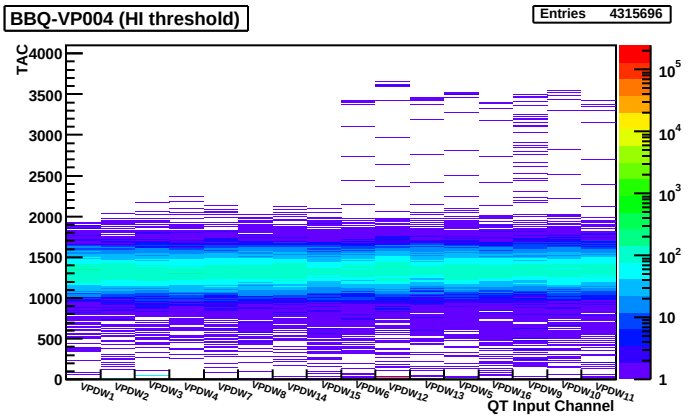

BBQ-VP002 (LO threshold)
Entries 4315696
ADC
QT Input Channel

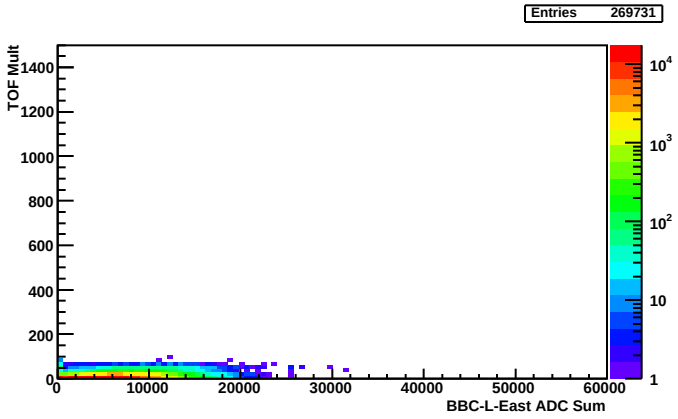

BBQ-VP002 (LO threshold)
Entries 4315696
TAC
QT Input Channel

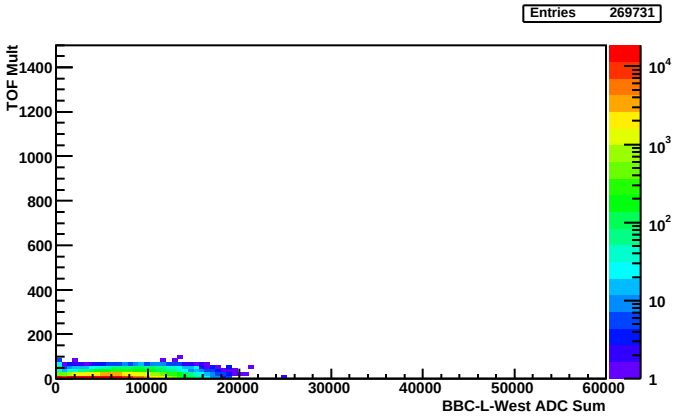

BBQ-VP003 (HI threshold)
Entries 4315696
ADC
QT Input Channel

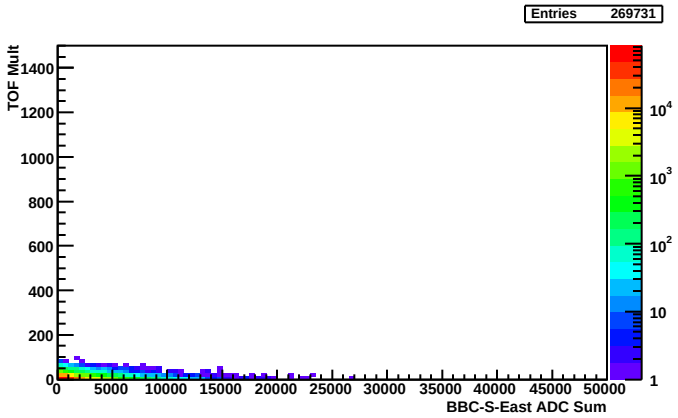

BBQ-VP003 (HI threshold)
Entries 4315696
TAC
QT Input Channel

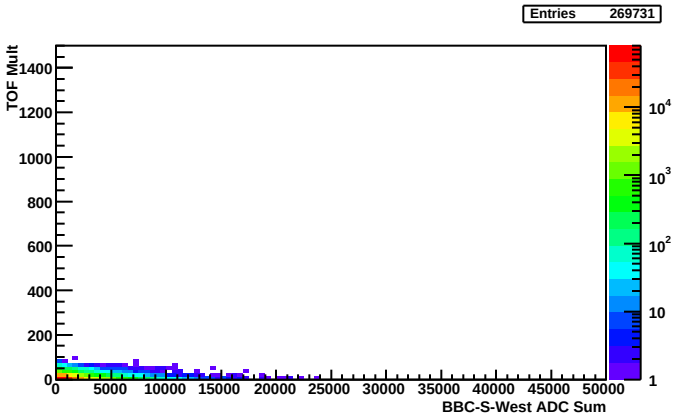

BBQ-VP004 (HI threshold)
Entries 4315696
ADC
QT Input Channel

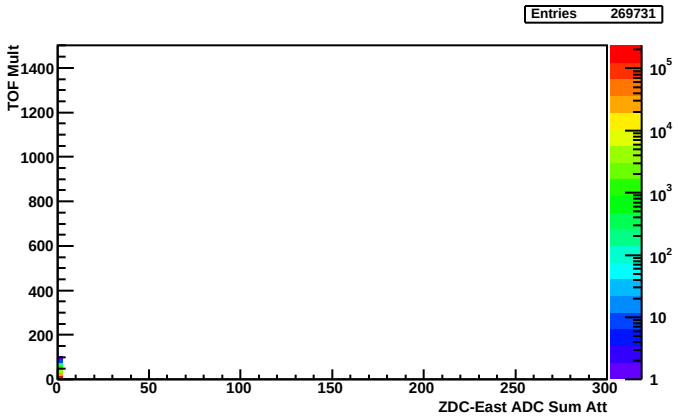

BBQ-VP004 (HI threshold)
Entries 4315696
TAC
QT Input Channel

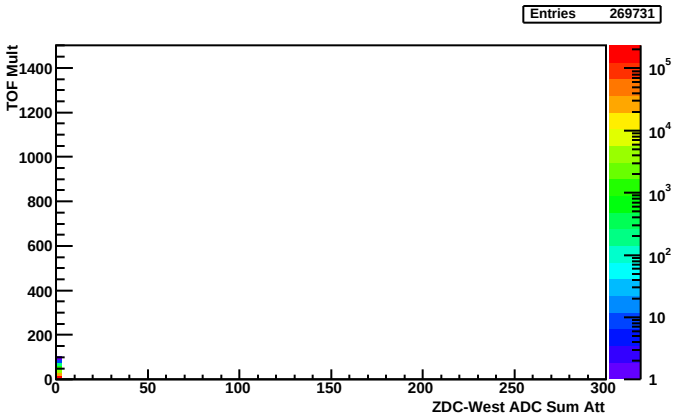

Entries 269731
TOF Mult
BBC-L-East ADC Sum

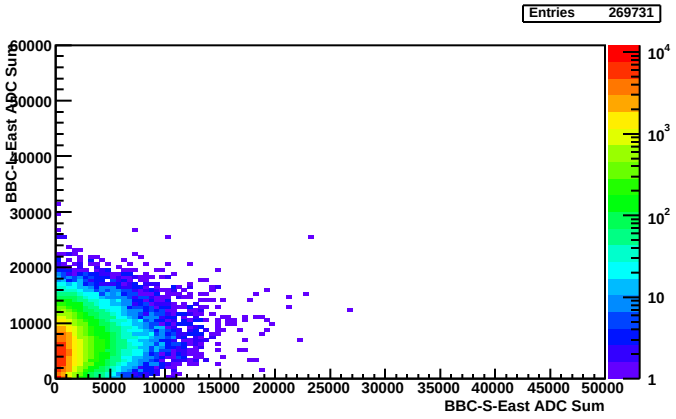

Entries 269731
TOF Mult
BBC-L-West ADC Sum

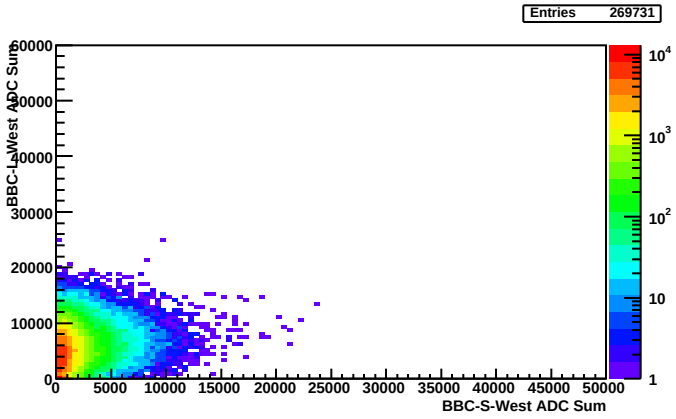

Entries 269731
TOF Mult
BBC-S-East ADC Sum

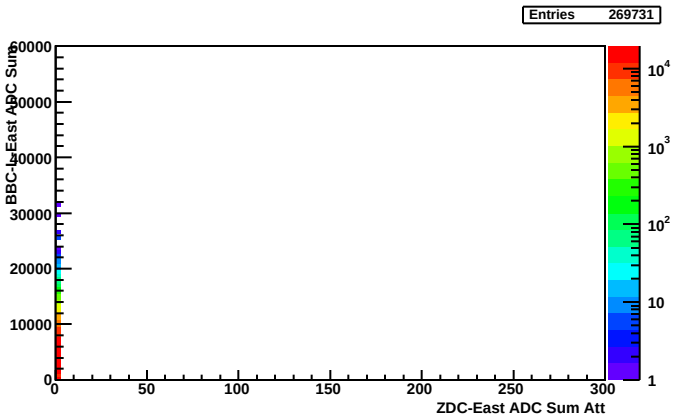

Entries 269731
TOF Mult
BBC-S-West ADC Sum

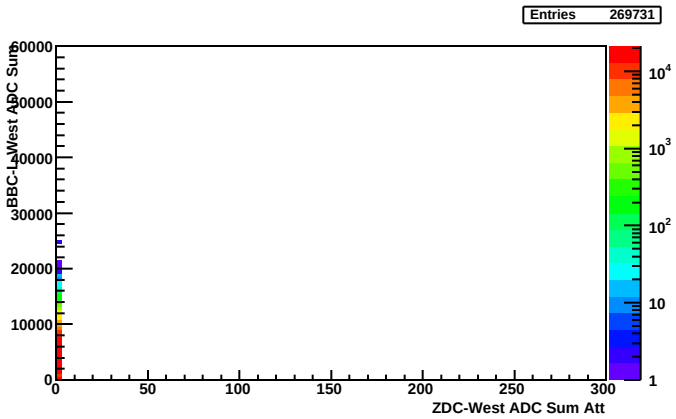

Entries 269731
TOF Mult
ZDC-East ADC Sum Att

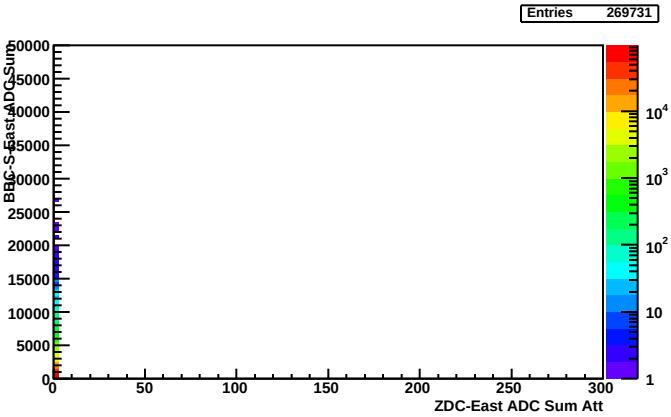

Entries 269731
TOF Mult
ZDC-West ADC Sum Att

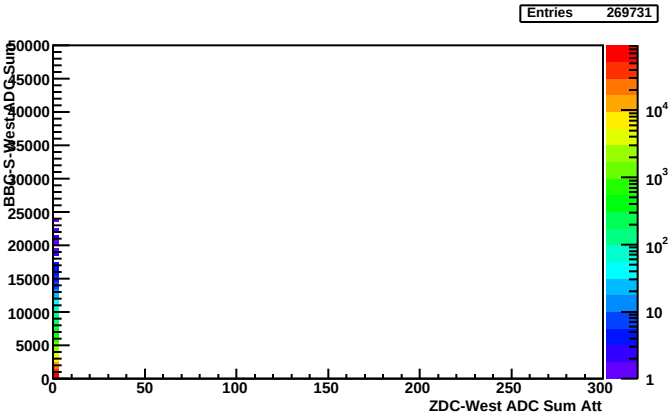

Entries 269731
BBC-L-East ADC Sum
BBC-S-East ADC Sum

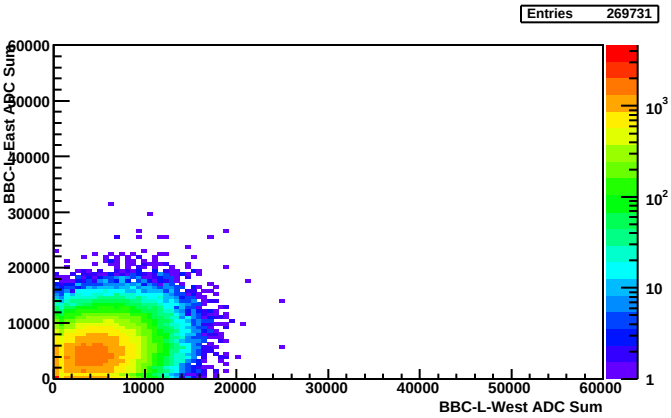

Entries 269731
BBC-L-West ADC Sum
BBC-S-West ADC Sum

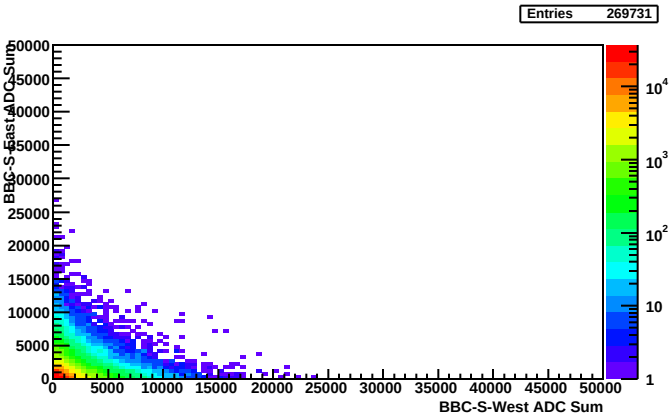

Entries 269731
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

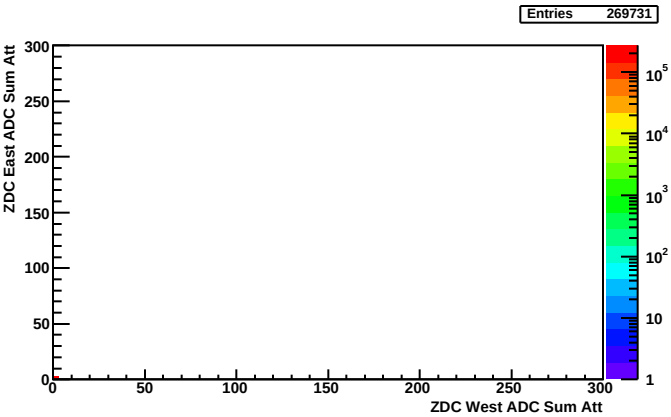

Entries 269731
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

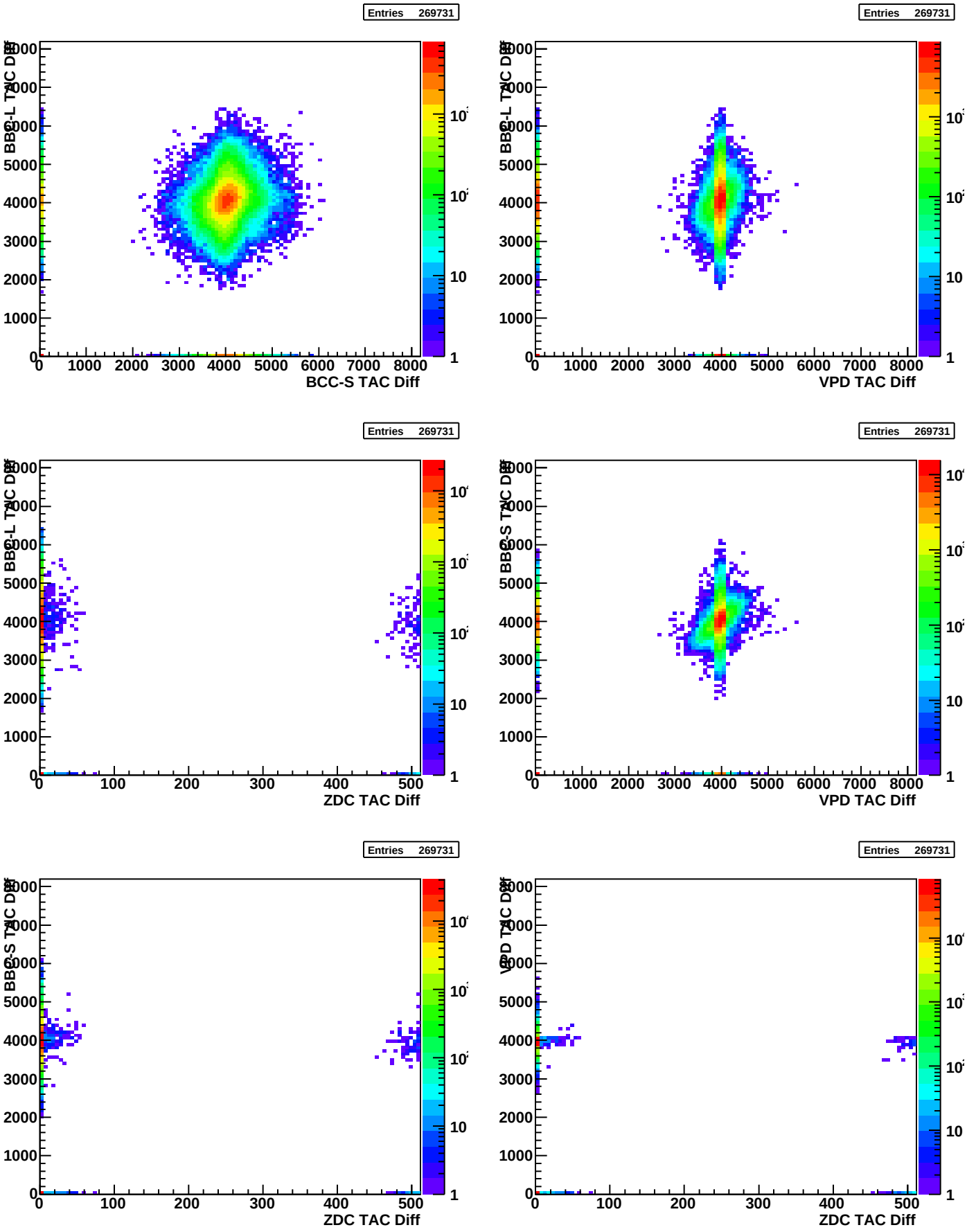

Entries 269731
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

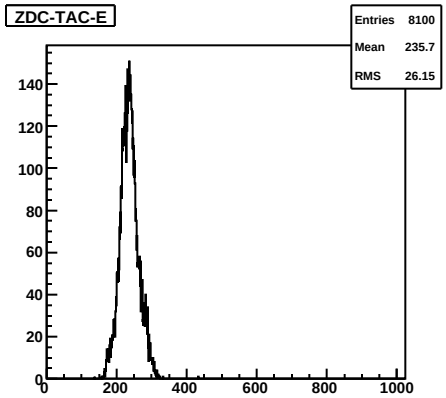

Entries 269731
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

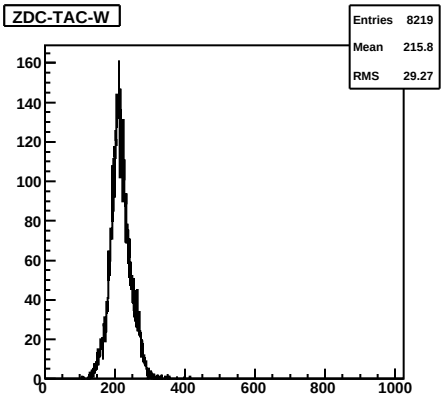

Entries 269731
BBC-L-East ADC Sum
BBC-L-West ADC Sum

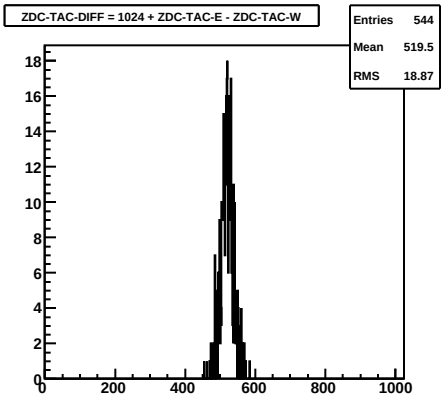

Entries 269731
BBC-S-East ADC Sum
BBC-S-West ADC Sum

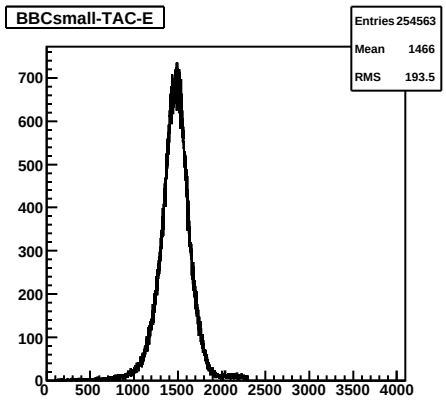

Entries 269731
ZDC East ADC Sum Att
ZDC West ADC Sum Att

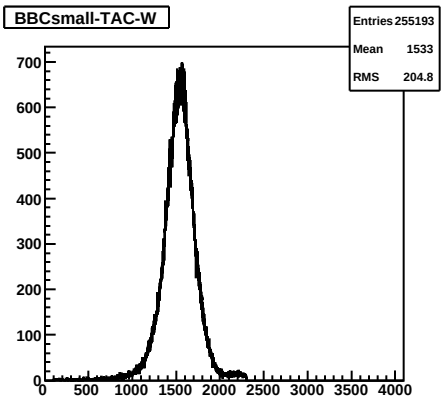

Entries 269731
BBC-L TAC Diff
BCC-S TAC Diff
Entries 269731
BBC-L TAC Diff
VPD TAC Diff
Entries 269731
BBC-L TAC Diff
ZDC TAC Diff
Entries 269731
BBC-S TAC Diff
VPD TAC Diff
Entries 269731
BBC-S TAC Diff
ZDC TAC Diff
Entries 269731
VPD TAC Diff
ZDC TAC Diff

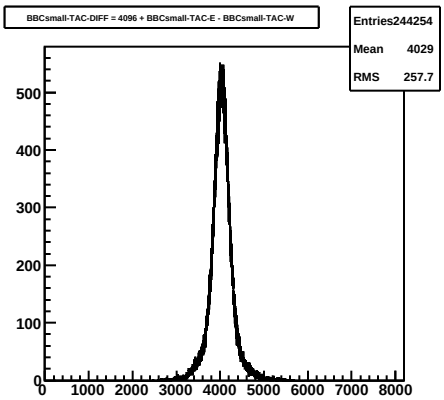

ZDC-TAC-E
Entries 8100
Mean 235.7
RMS 26.15

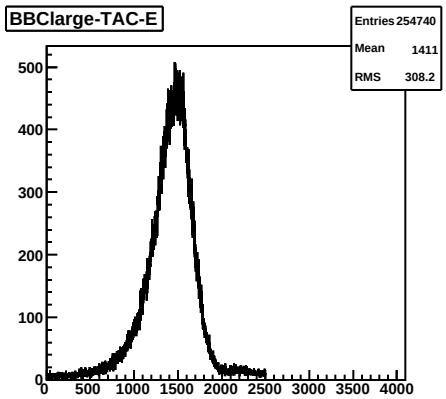

ZDC-TAC-W
Entries 8219
Mean 215.8
RMS 29.27

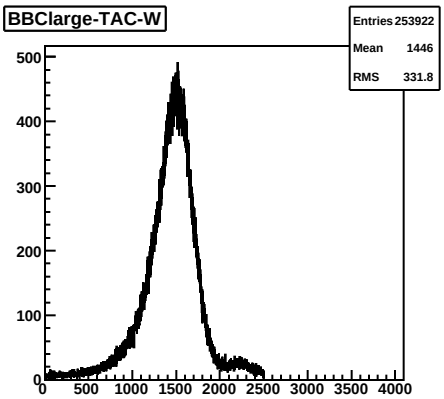

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 544
Mean 519.5
RMS 18.87

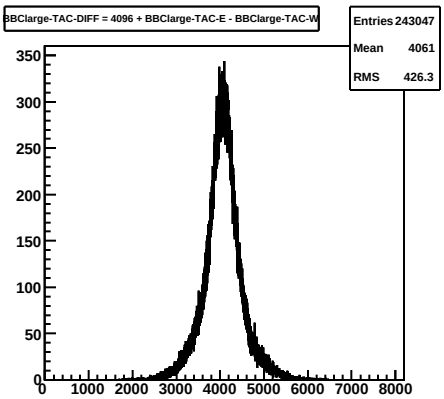

BBCsmall-TAC-E
Entries 254563
Mean 1466
RMS 193.5

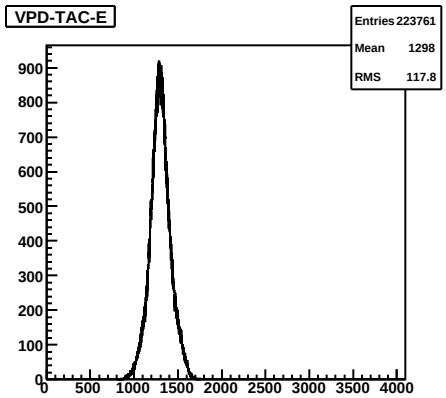

BBCsmall-TAC-W
Entries 255193
Mean 1533
RMS 204.8

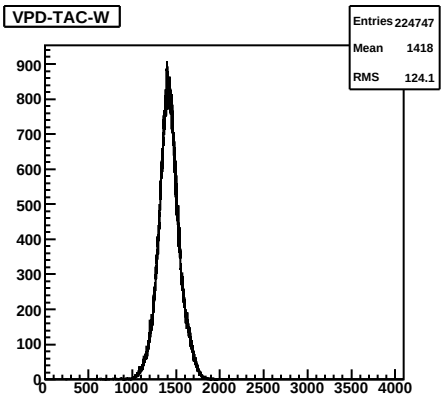

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 244254
Mean 4029
RMS 257.7

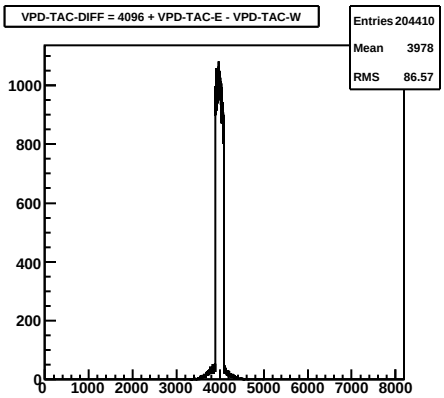

BBClarge-TAC-E
Entries 254740
Mean 1411
RMS 308.2

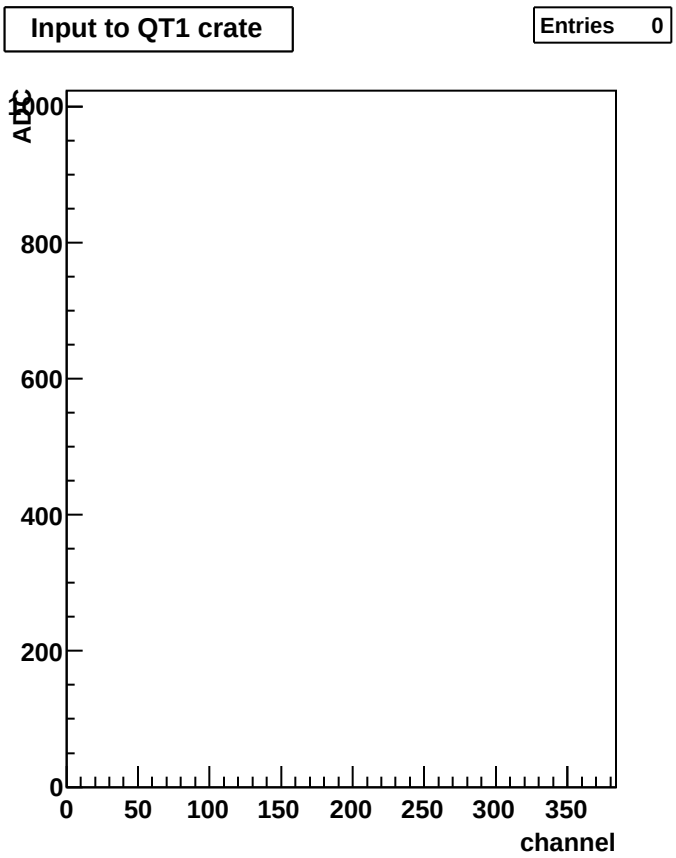

BBClarge-TAC-W
Entries 253922
Mean 1446
RMS 331.8

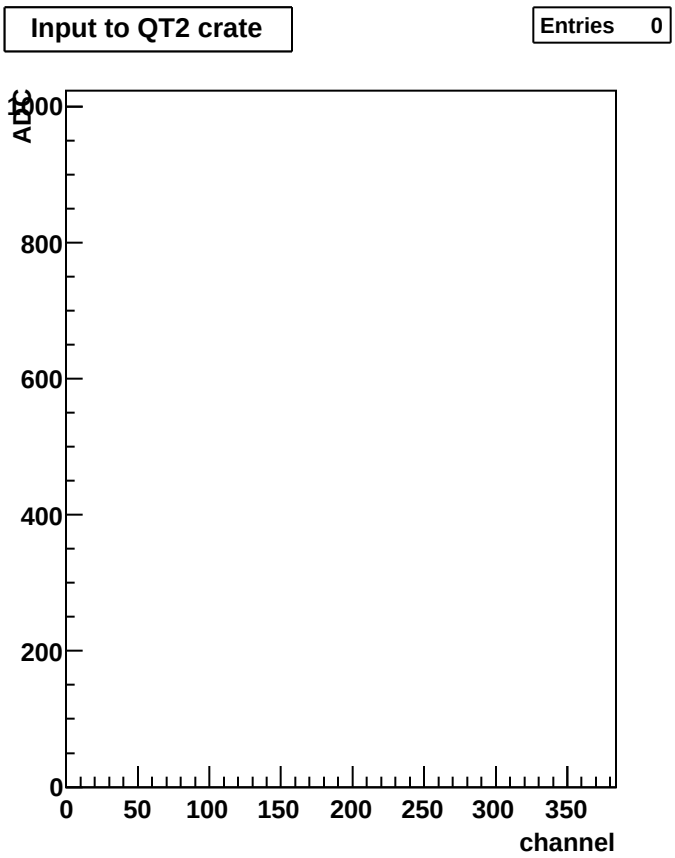

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 243047
Mean 4061
RMS 426.3

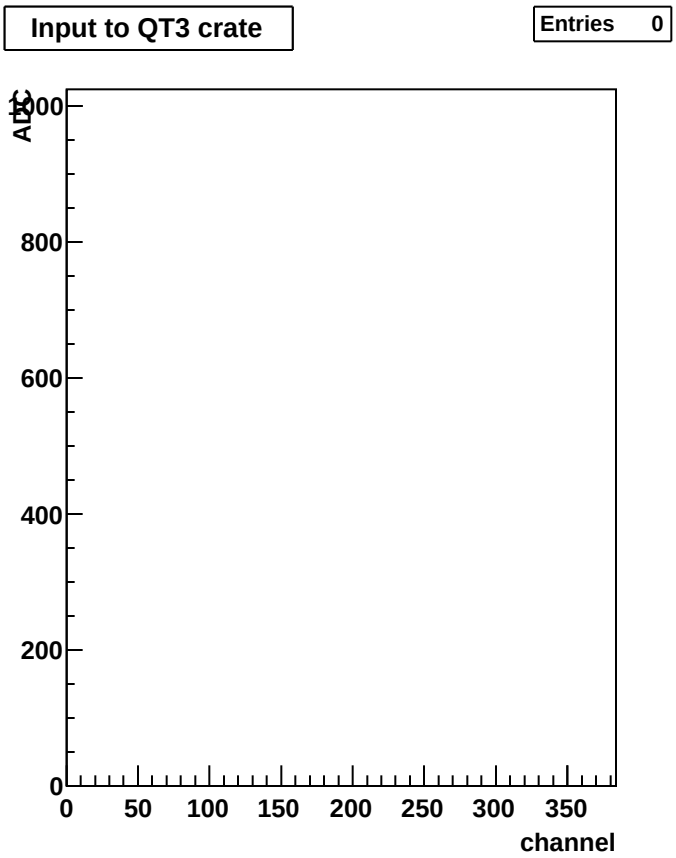

VPD-TAC-E
Entries 223761
Mean 1298
RMS 117.8

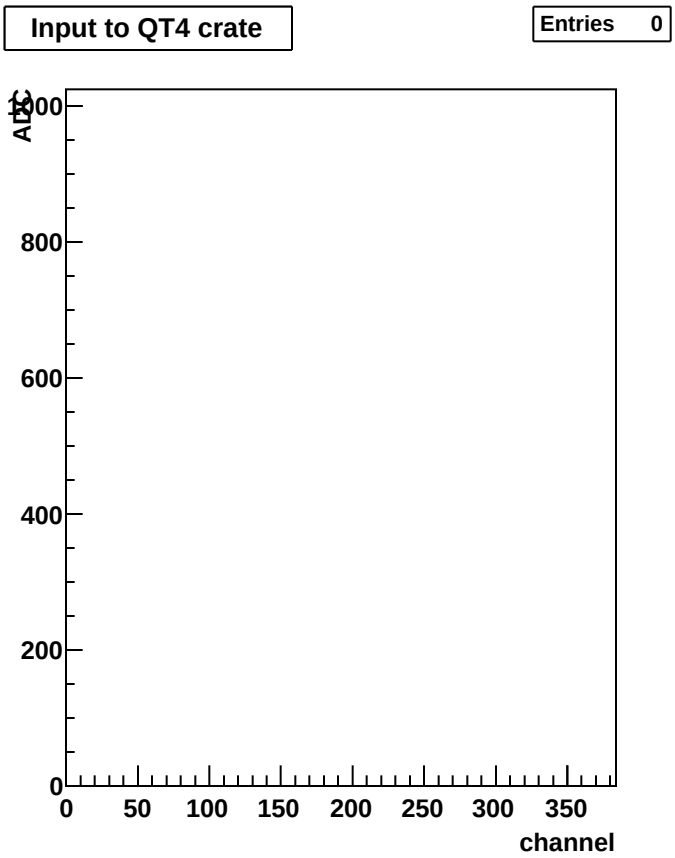

VPD-TAC-W
Entries 224747
Mean 1418
RMS 124.1

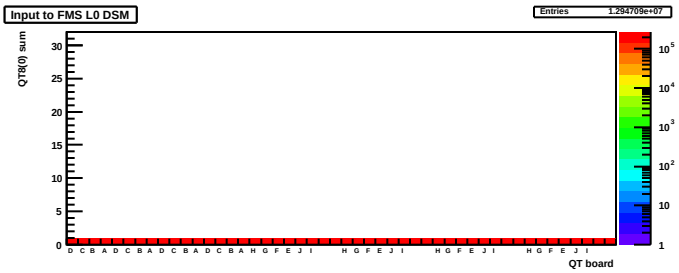

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 204410
Mean 3978
RMS 86.57

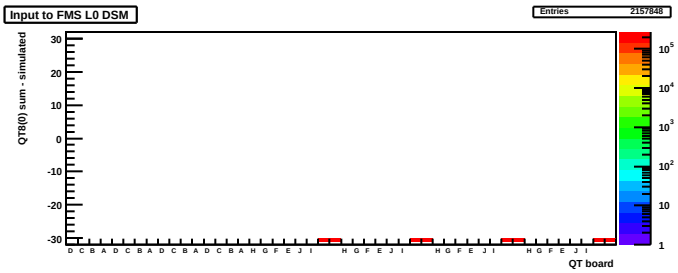

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

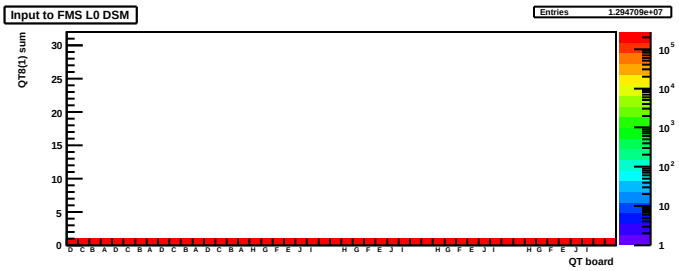

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

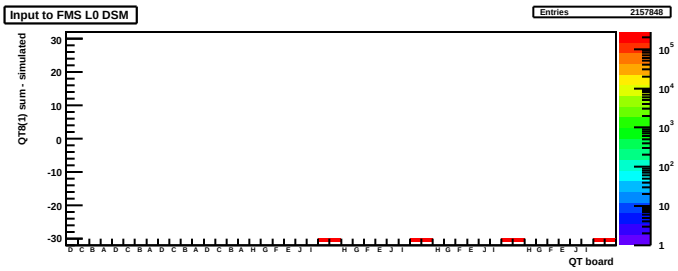

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

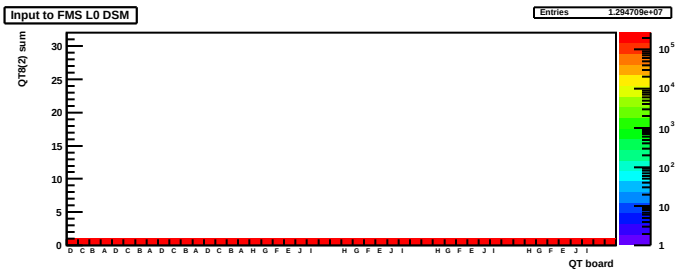

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

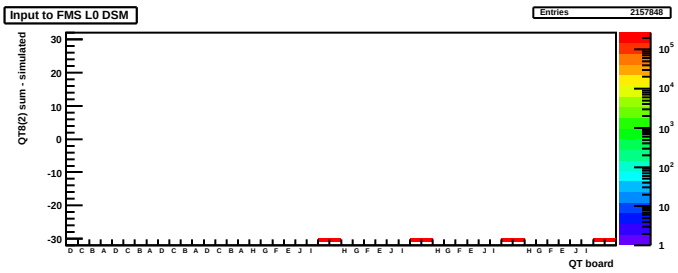

Input to FMS L0 DSM
Entries 1.294709e+07
QT8(0) sum
QT board

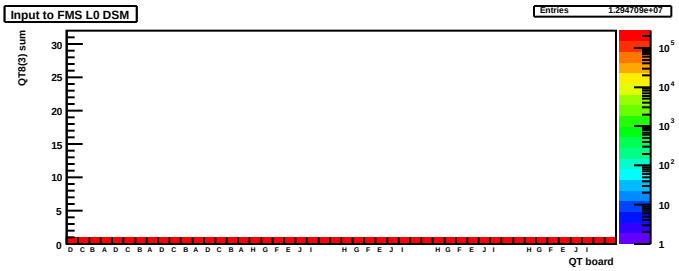

Input to FMS L0 DSM
Entries 2157848
QT8(0) sum - simulated
QT board

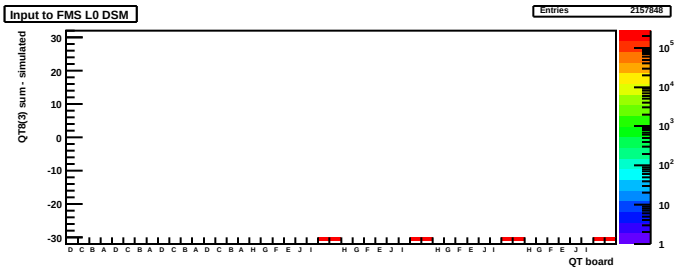

Input to FMS L0 DSM
Entries 1.294709e+07
QT8(1) sum
QT board

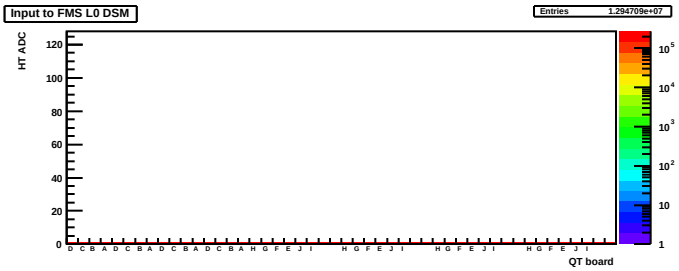

Input to FMS L0 DSM
Entries 2157848
QT8(1) sum - simulated
QT board

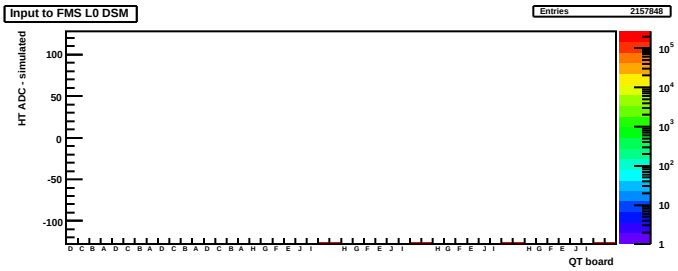

Input to FMS L0 DSM
Entries 1.294709e+07
QT8(2) sum
QT board

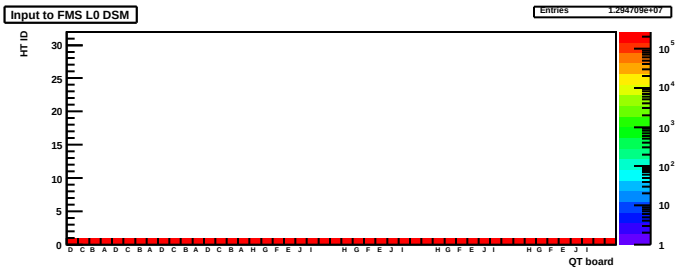

Input to FMS L0 DSM
Entries 2157848
QT8(2) sum - simulated
QT board

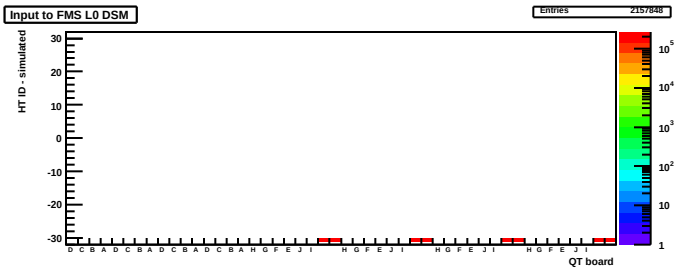

Input to FMS L0 DSM
Entries 1.294709e+07
QT8(3) sum
QT board

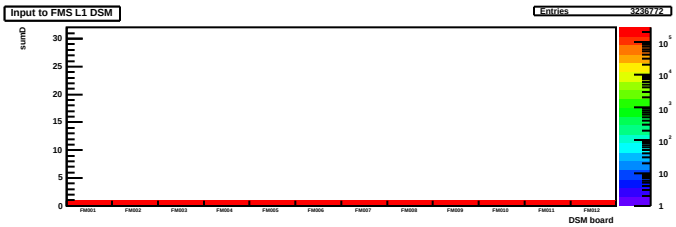

Input to FMS L0 DSM
Entries 2157848
QT8(3) sum - simulated
QT board

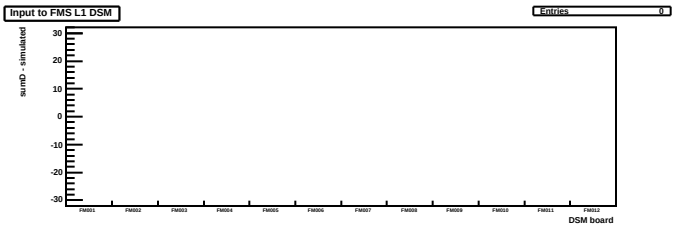

Input to FMS L0 DSM
Entries 1.294709e+07
HT ADC
QT board

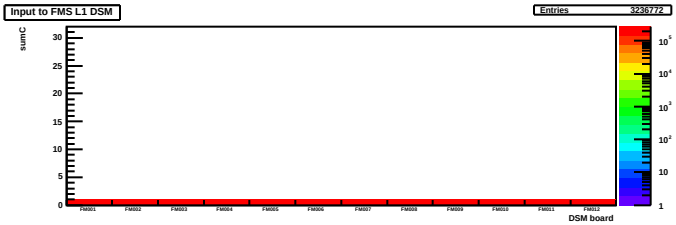

Input to FMS L0 DSM
Entries 2157848
HT ADC - simulated
QT board

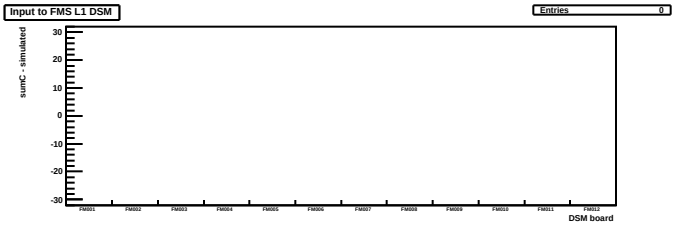

Input to FMS L0 DSM
Entries 1.294709e+07
HT ID
QT board

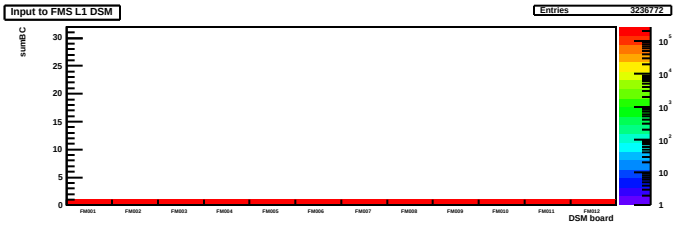

Input to FMS L0 DSM
Entries 2157848
HT ID - simulated
QT board

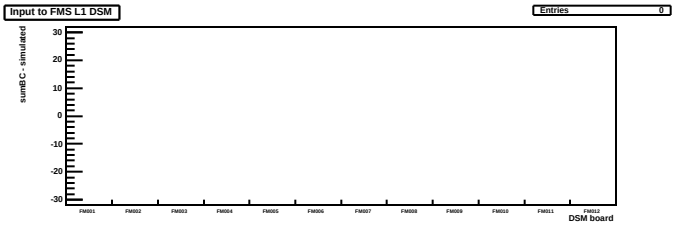

Input to FMS L1 DSM
Entries 3236772
sumD
DSM board

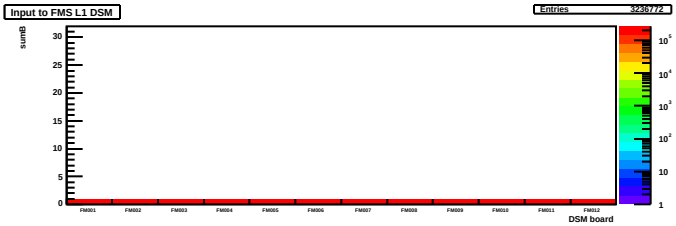

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

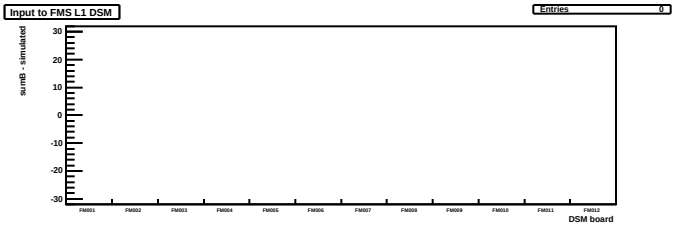

Input to FMS L1 DSM
Entries 3236772
sumC
DSM board

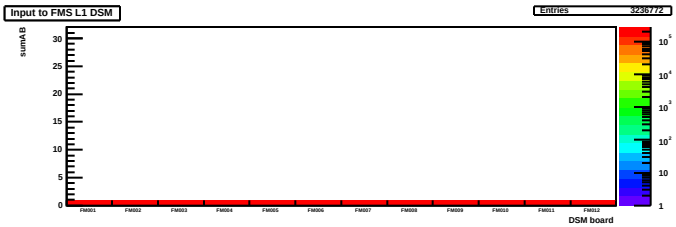

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

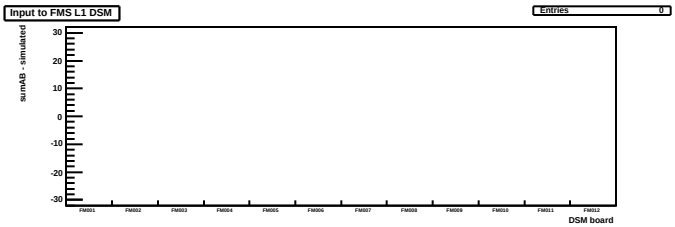

Input to FMS L1 DSM
Entries 3236772
sumBC
DSM board

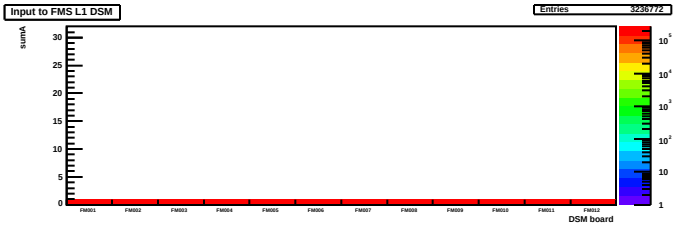

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

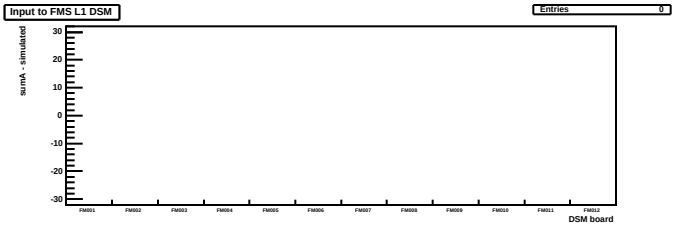

Input to FMS L1 DSM
Entries 3236772
sumB
DSM board

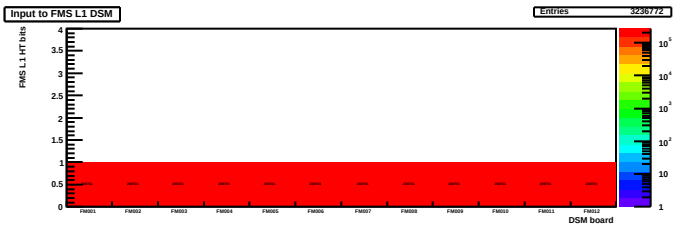

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

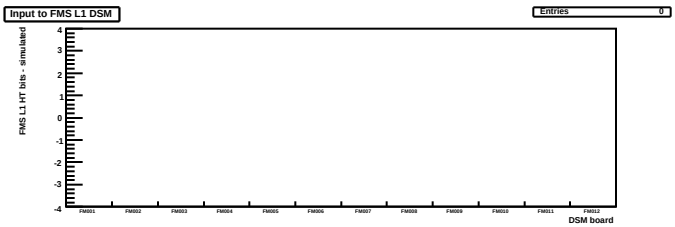

Input to FMS L1 DSM
Entries 3236772
sumAB
DSM board

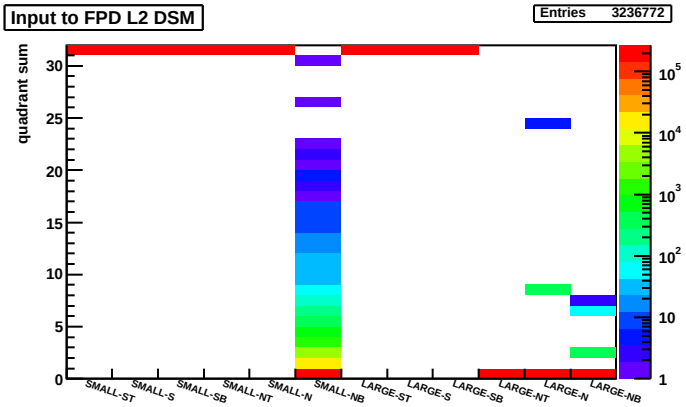

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

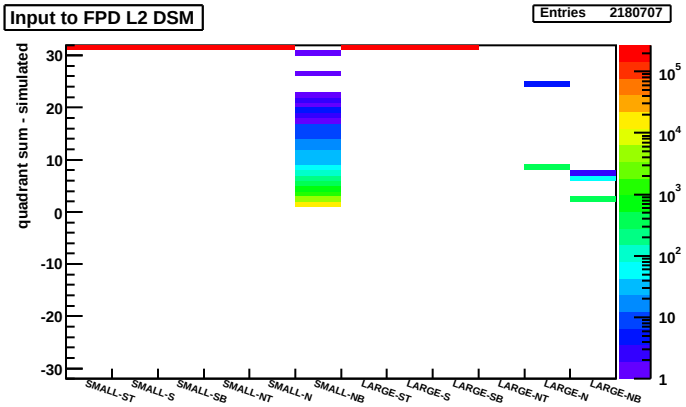

Input to FMS L1 DSM
Entries 3236772
sumA
DSM board

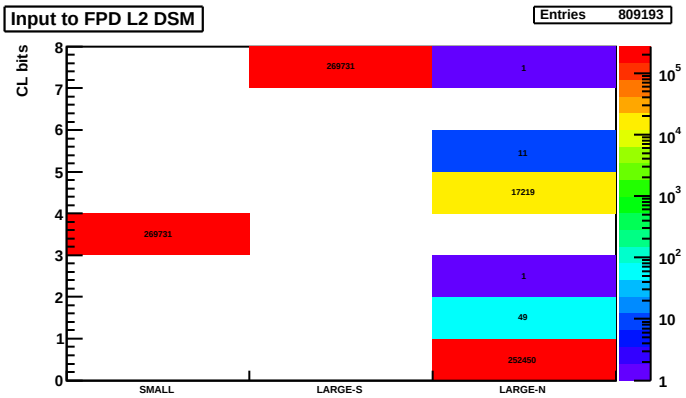

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

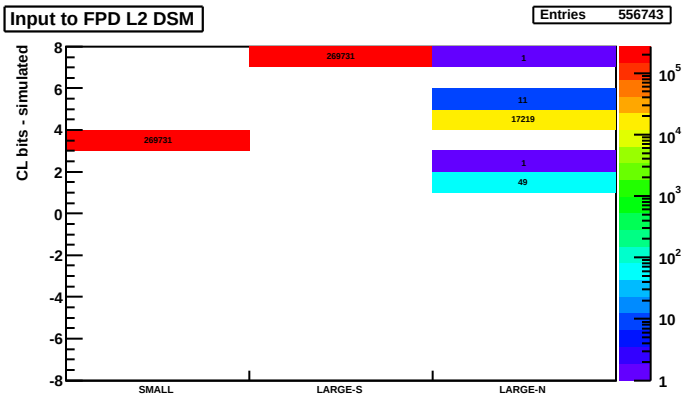

Input to FMS L1 DSM
Entries 3236772
FMS L1 HT bits
DSM board

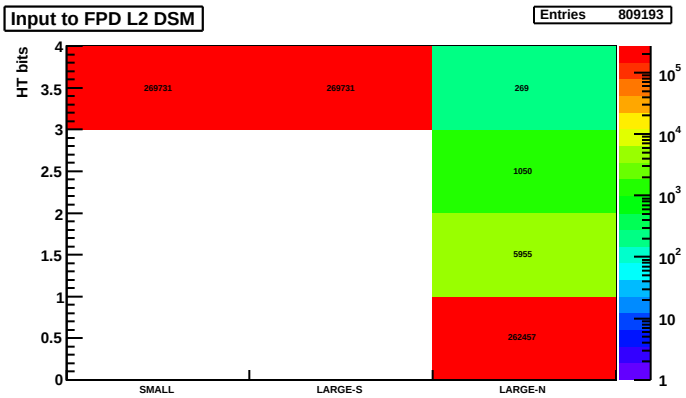

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

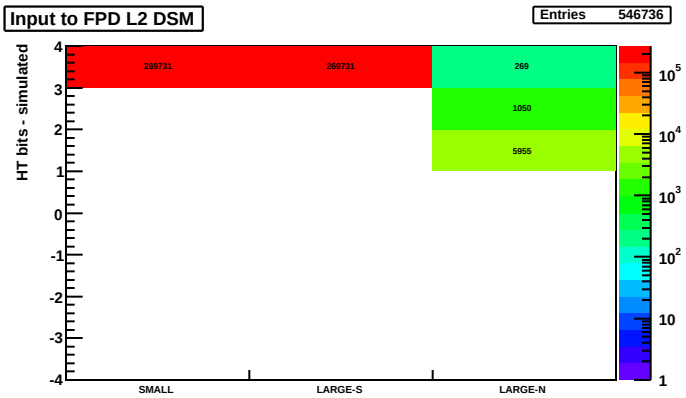

Input to FPD L2 DSM
Entries 3236772
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

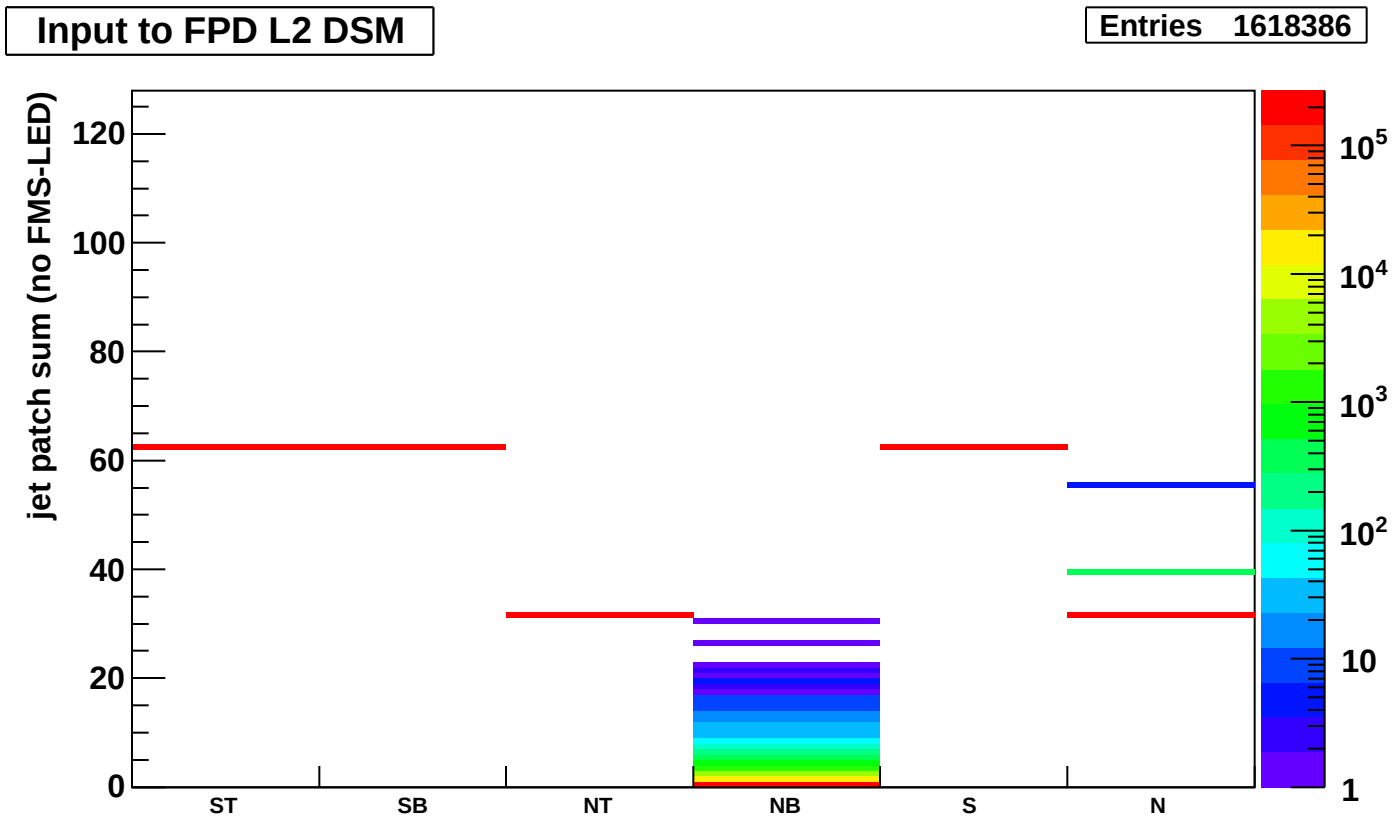

Input to FPD L2 DSM
Entries 2180707
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

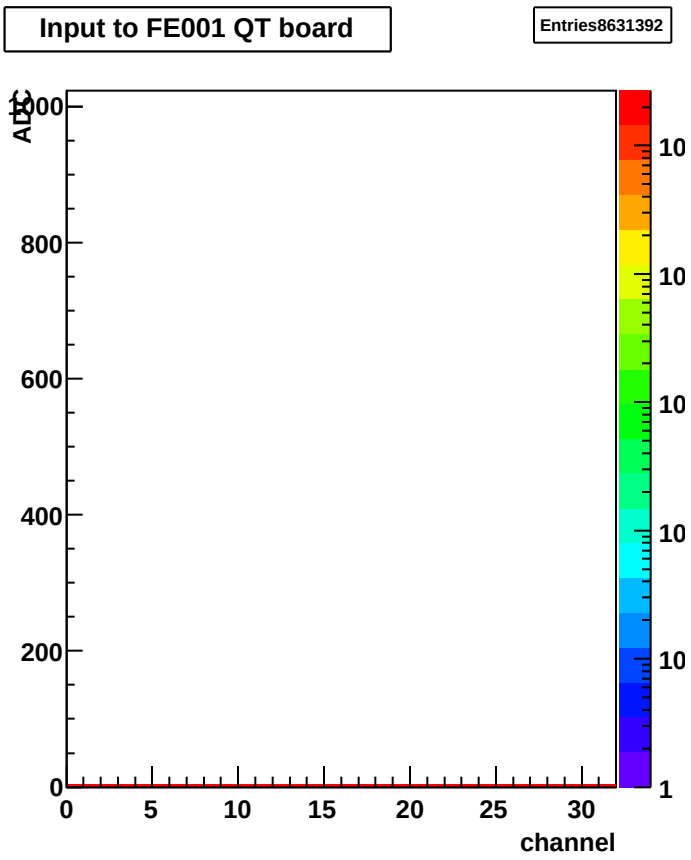

Input to FPD L2 DSM
Entries 809193
CL bits
269731
1
11
17219
269731
1
49
252450
SMALL
LARGE-S
LARGE-N

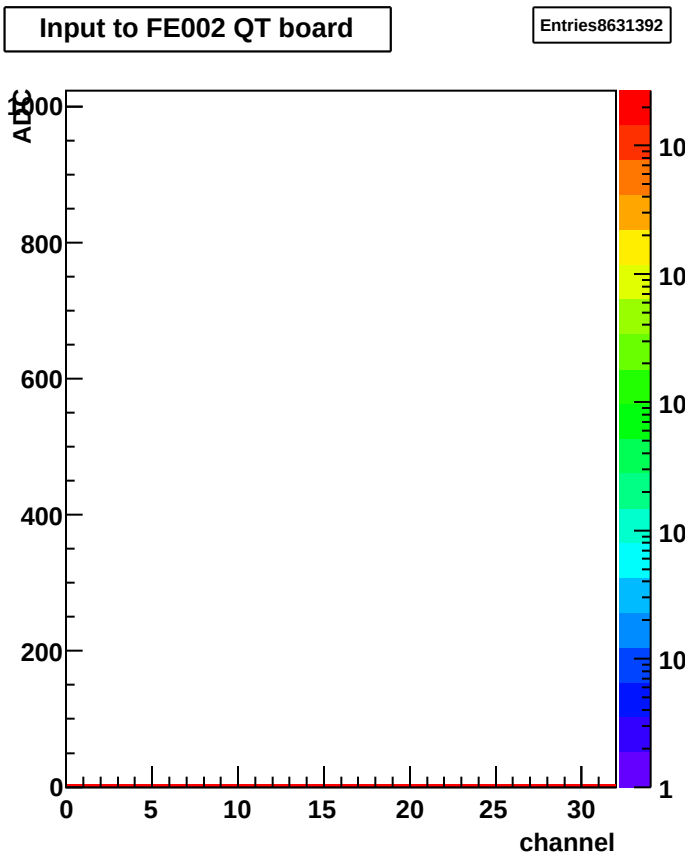

Input to FPD L2 DSM
Entries 556743
CL bits - simulated
269731
1
11
17219
269731
1
49
SMALL
LARGE-S
LARGE-N

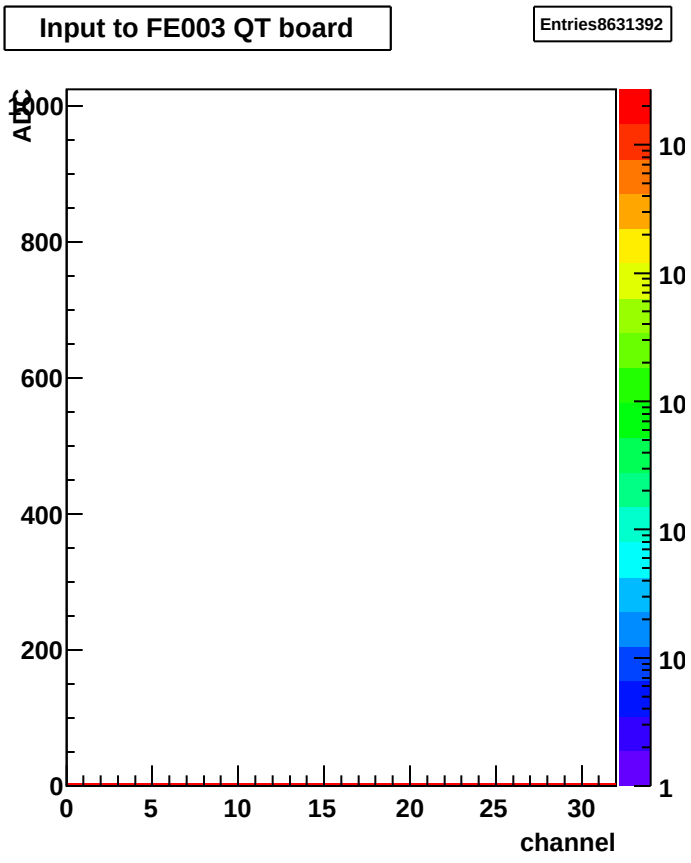

Input to FPD L2 DSM
Entries 809193
HT bits
269731
269731
269
1050
5955
262457
SMALL
LARGE-S
LARGE-N

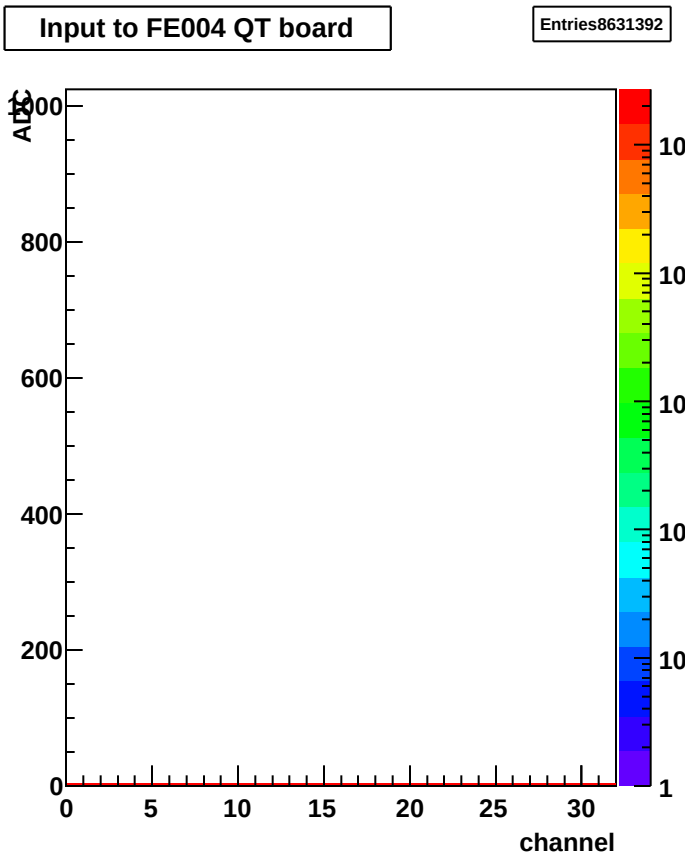

Input to FPD L2 DSM
Entries 546736
HT bits - simulated
269731
269731
269
1050
5955
SMALL
LARGE-S
LARGE-N

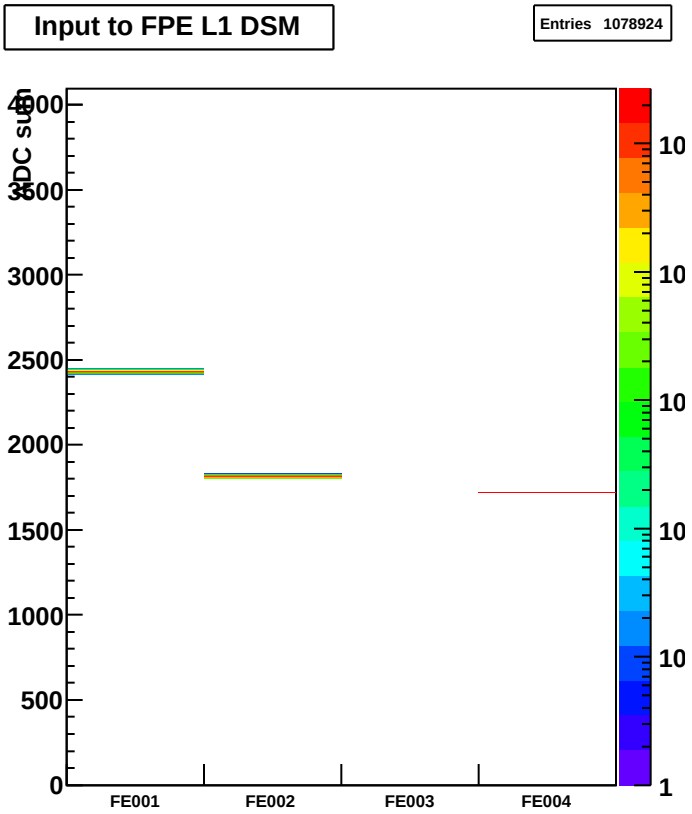

Input to FPD L2 DSM
Entries 1618386
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries8631392
ADC
channel

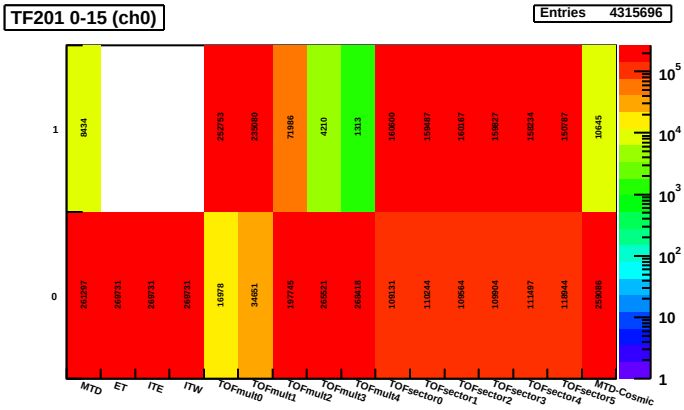

Input to FE002 QT board
Entries8631392
ADC
channel


Input to FE003 QT board
Entries8631392
ADC
channel

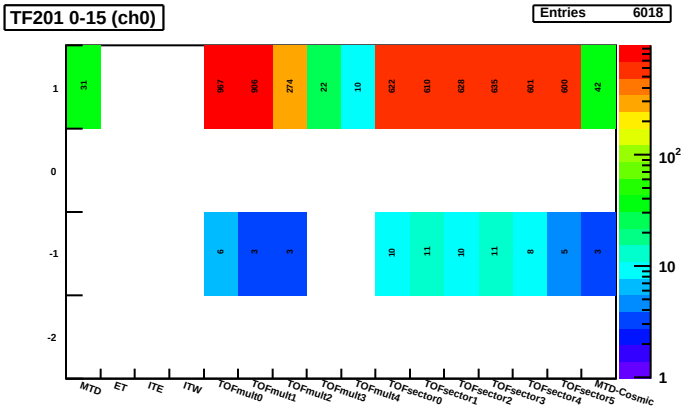

Input to FE004 QT board
Entries8631392
ADC
channel


Input to FPE L1 DSM
Entries 1078924
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

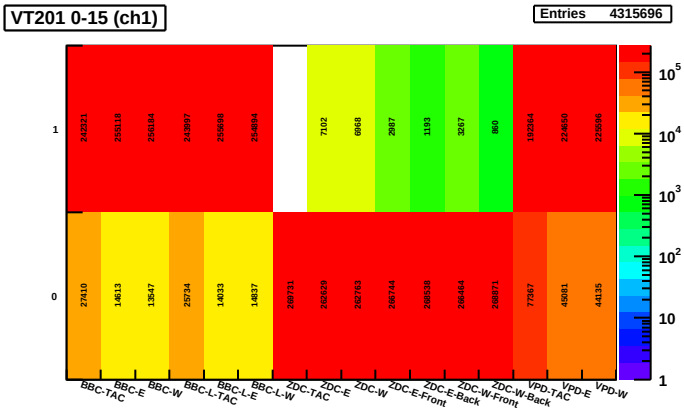
TF201 0-15 (ch0)



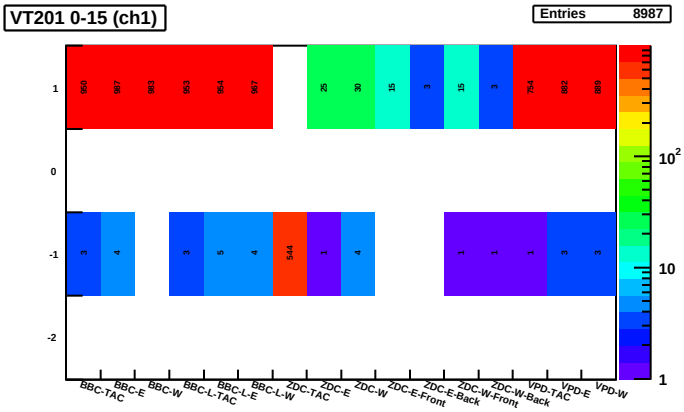
TF201 0-15 (ch0)



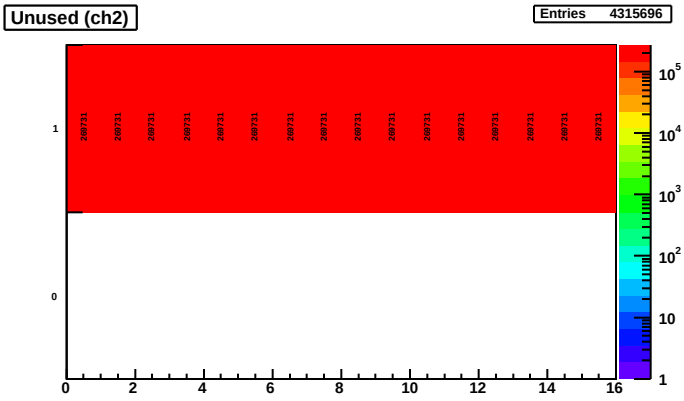
VT201 0-15 (ch1)

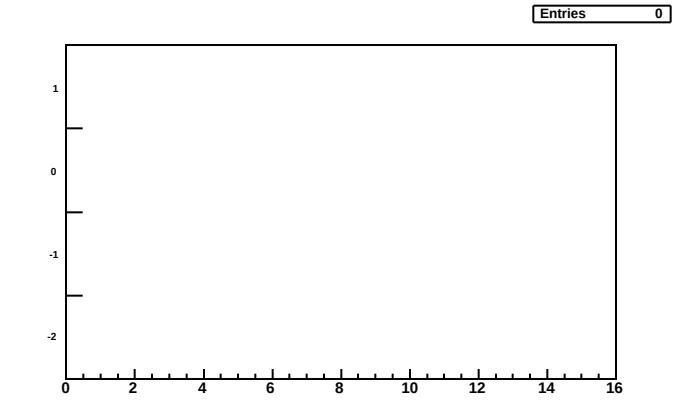


VT201 0-15 (ch1)

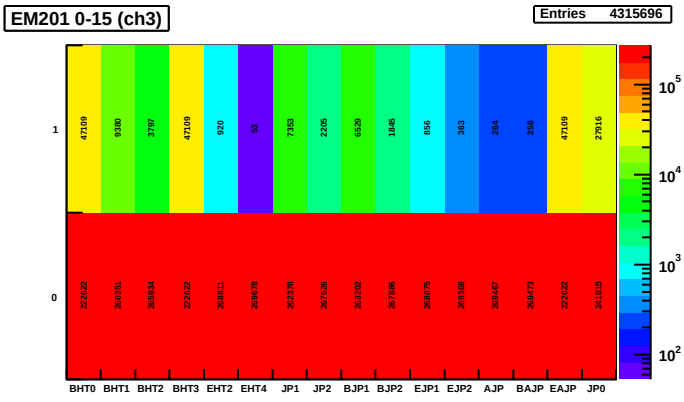


Unused (ch2)

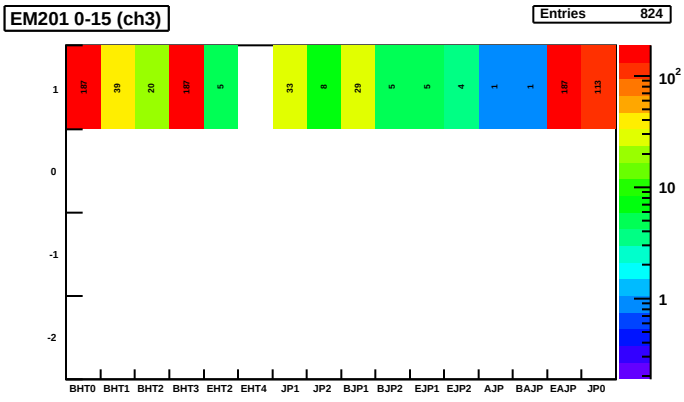




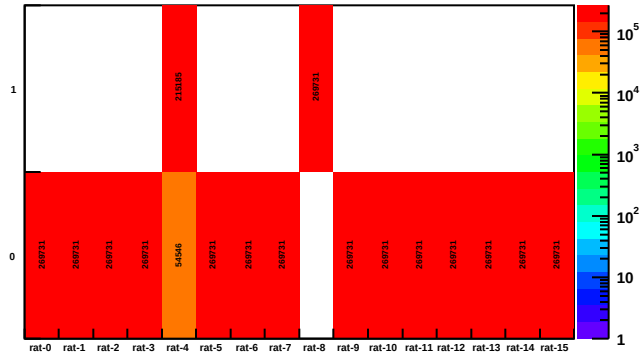
EM201 0-15 (ch3)

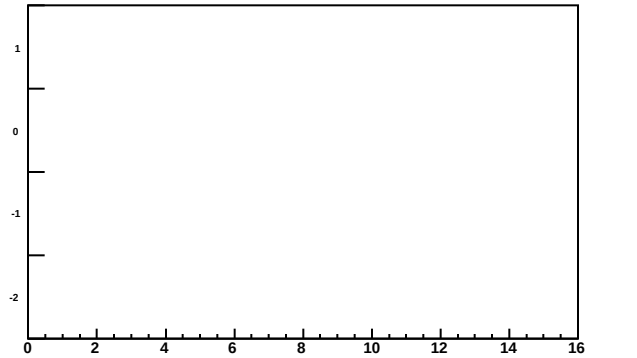


EM201 0-15 (ch3)

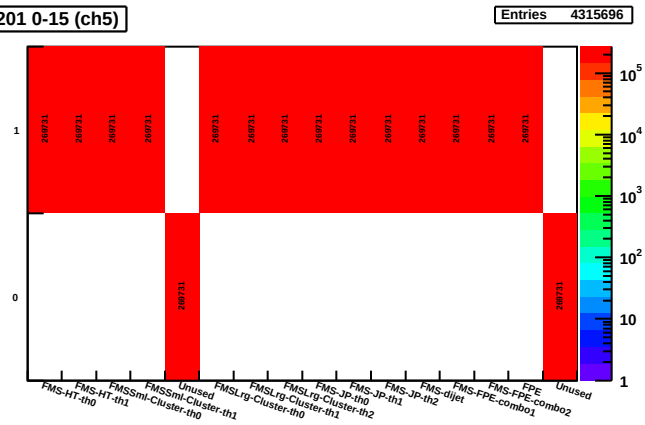


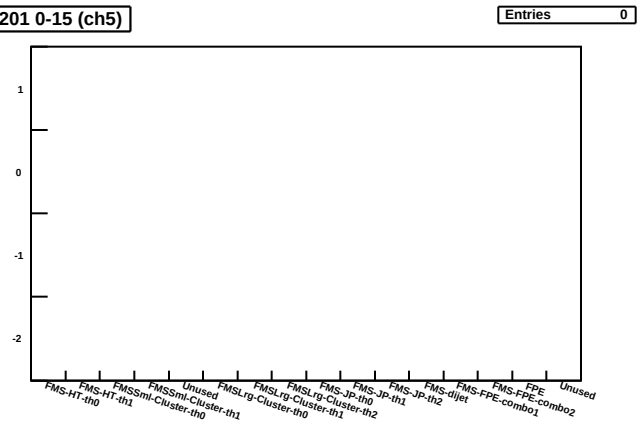
RAT board (ch4)



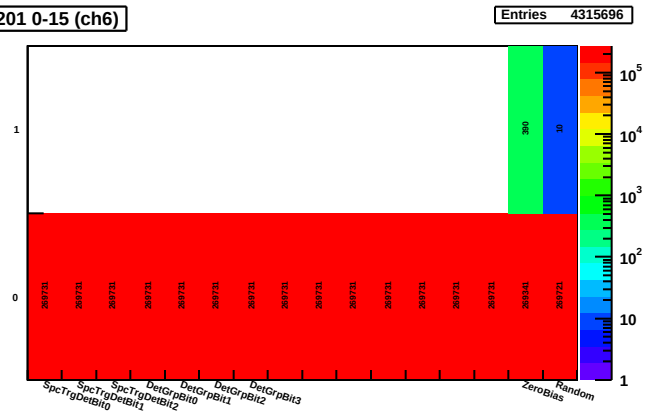


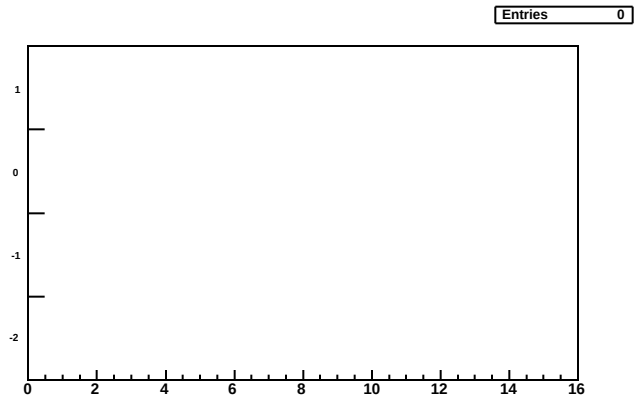
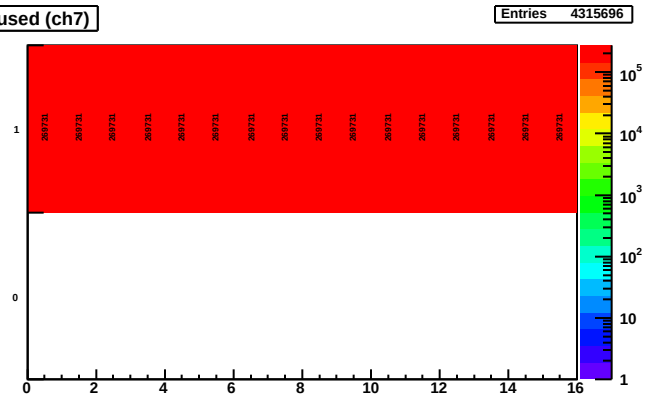
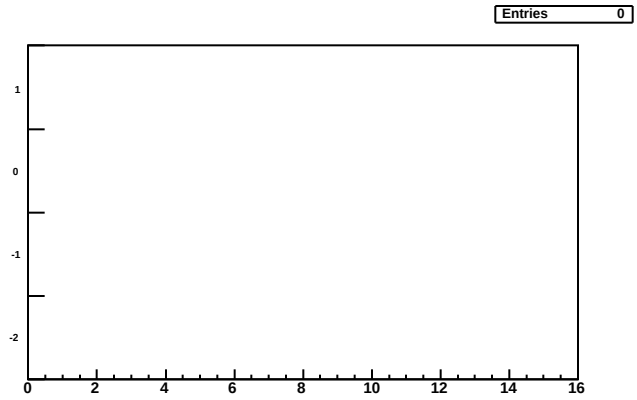
FP201 0-15 (ch5)





ST201 0-15 (ch6)





Unused (ch7)