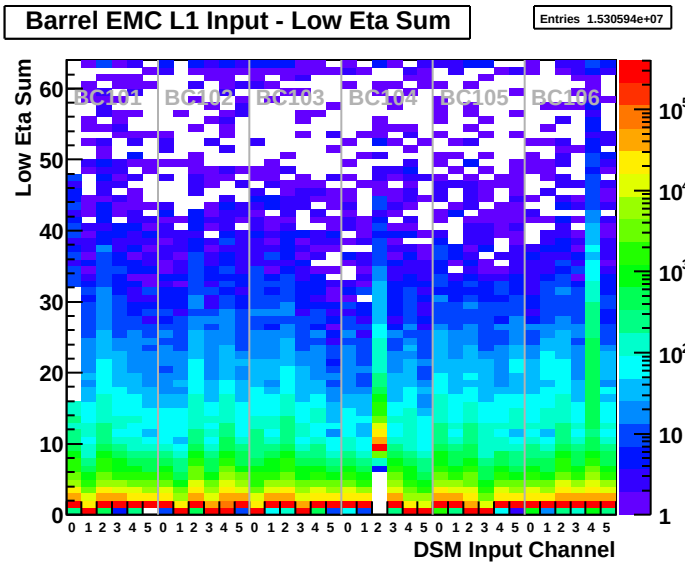
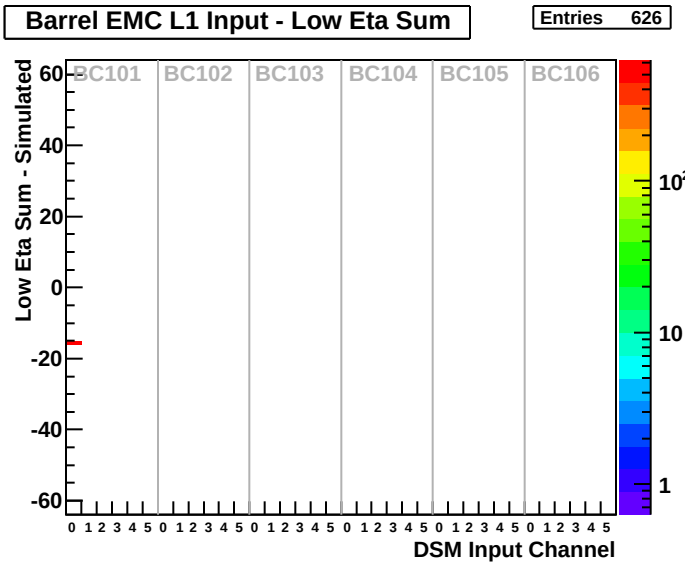
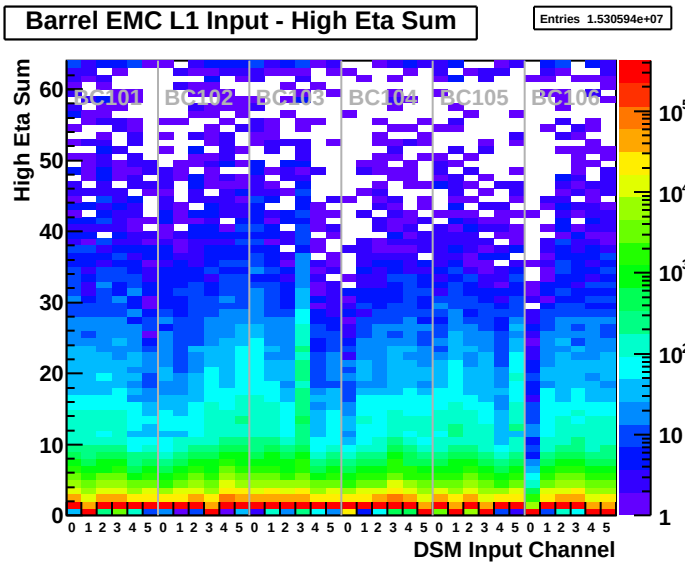
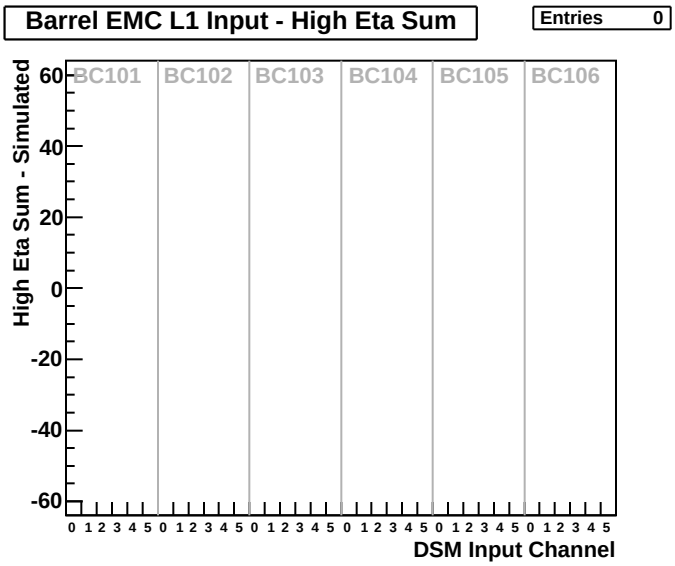
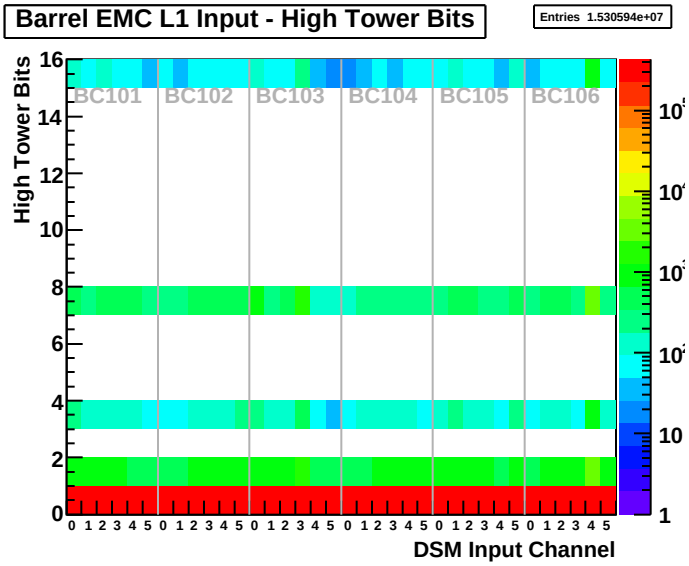
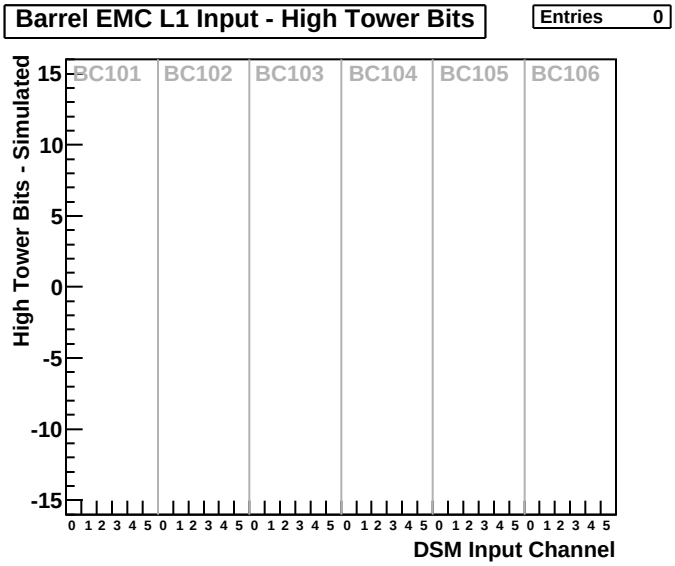

Barrel EMC L1 Input - Low Eta Sum
Entries 1.530594e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

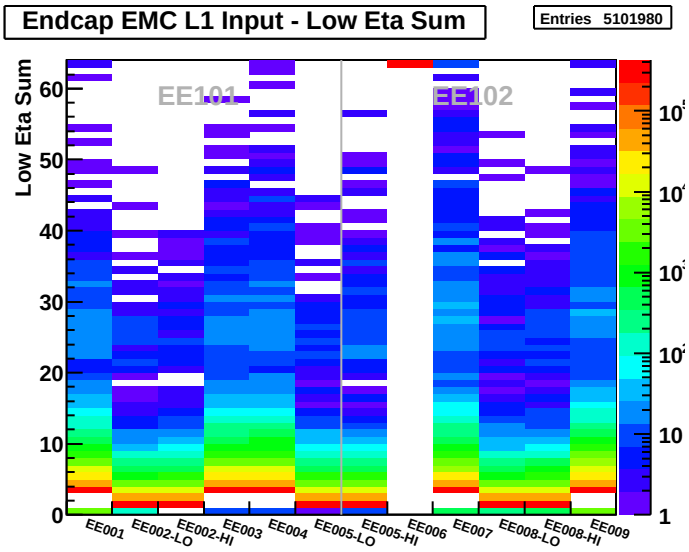

Barrel EMC L1 Input - Low Eta Sum
Entries 626
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

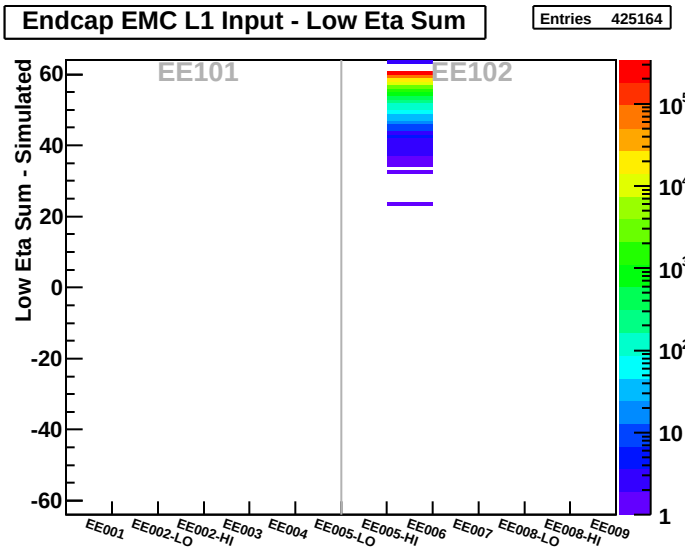

Barrel EMC L1 Input - High Eta Sum
Entries 1.530594e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

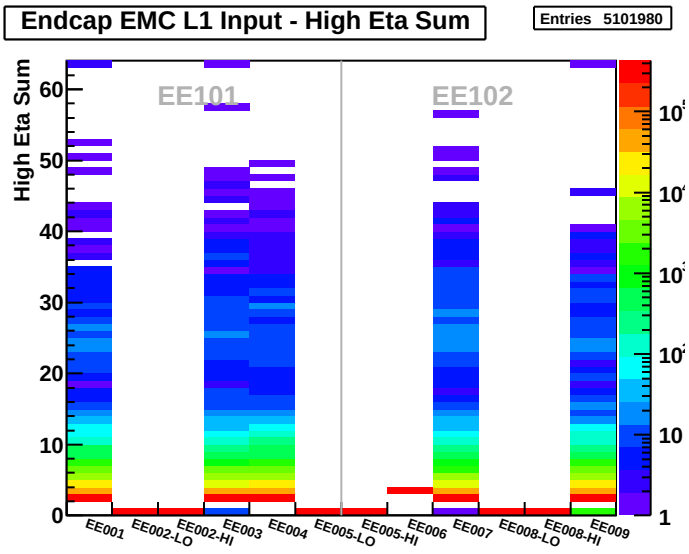

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

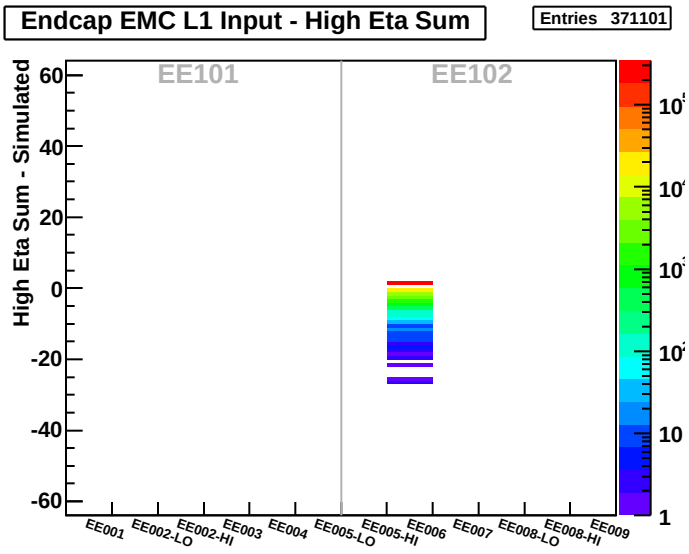

Barrel EMC L1 Input - High Tower Bits
Entries 1.530594e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

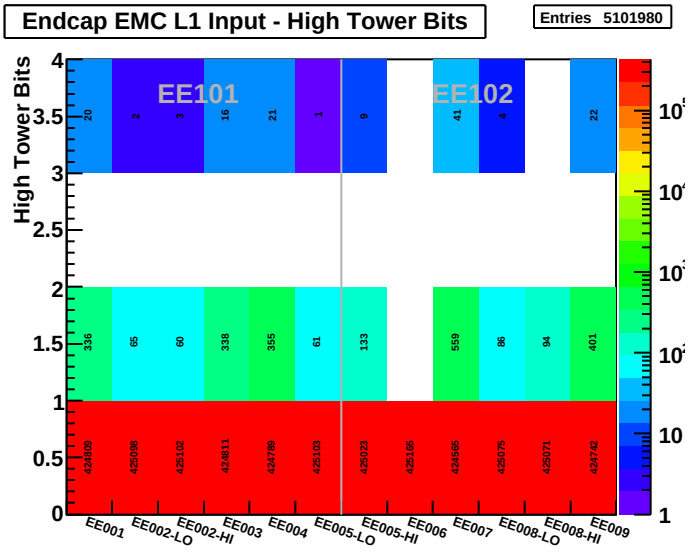

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

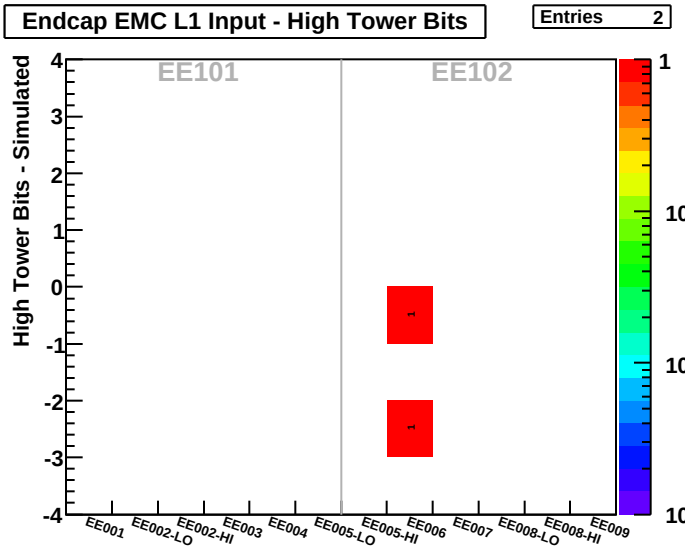

Endcap EMC L1 Input - Low Eta Sum
Entries 5101980
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

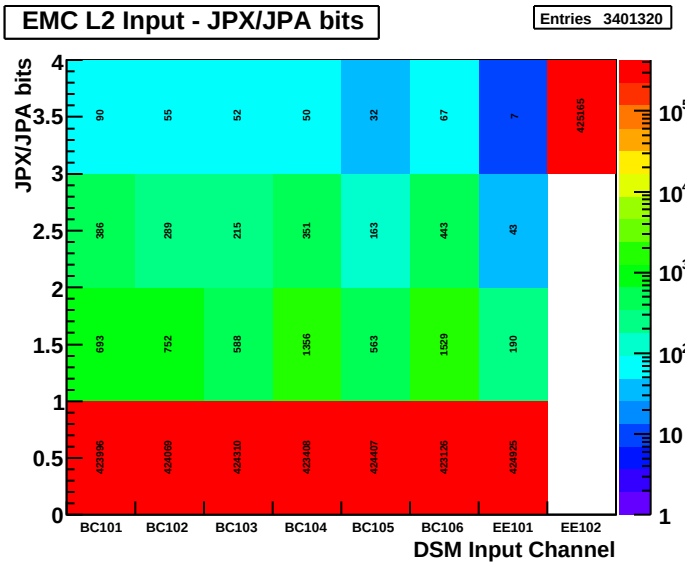

Endcap EMC L1 Input - Low Eta Sum
Entries 425164
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

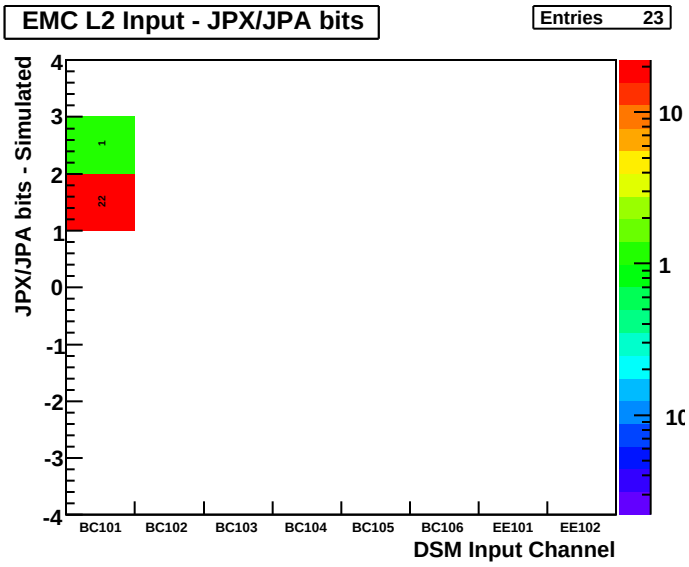

Endcap EMC L1 Input - High Eta Sum
Entries 5101980
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

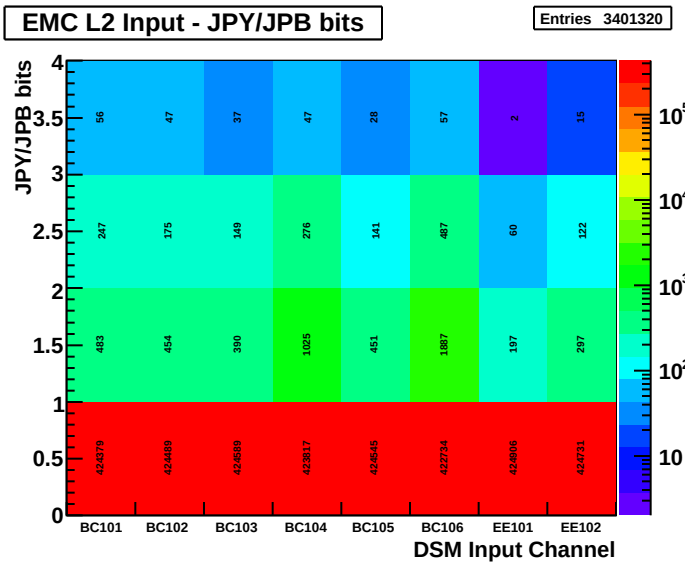

Endcap EMC L1 Input - High Eta Sum
Entries 371101
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

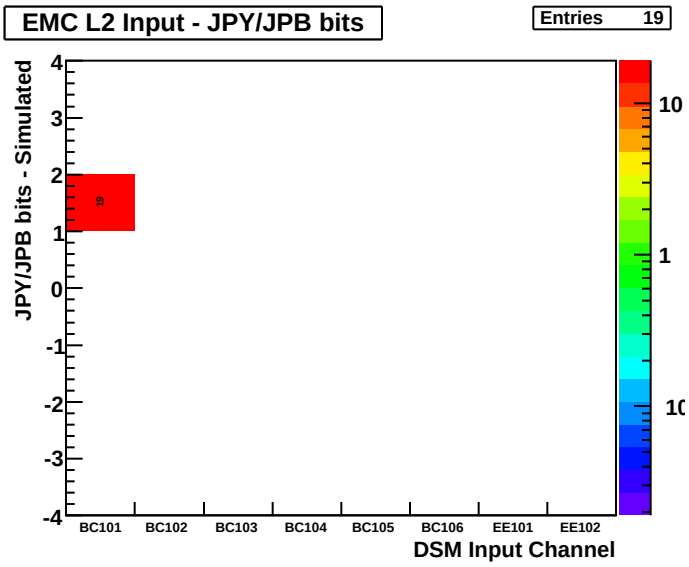

Endcap EMC L1 Input - High Tower Bits
Entries 5101980
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

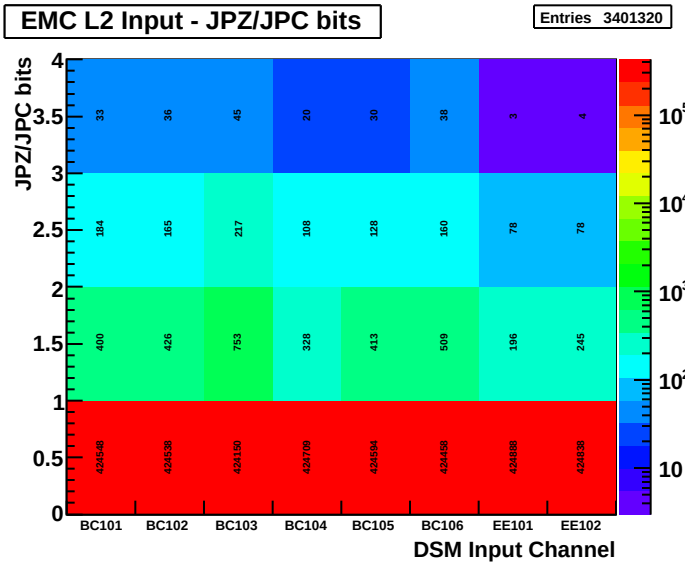

Endcap EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

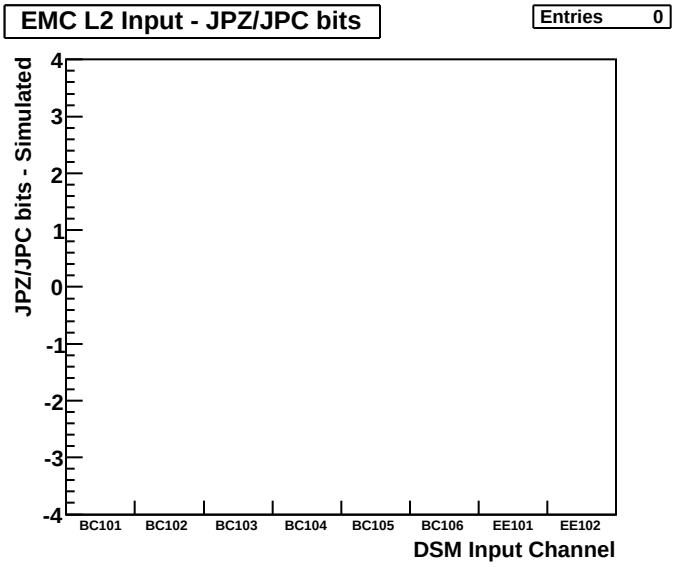

EMC L2 Input - JPX/JPA bits
Entries 3401320
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

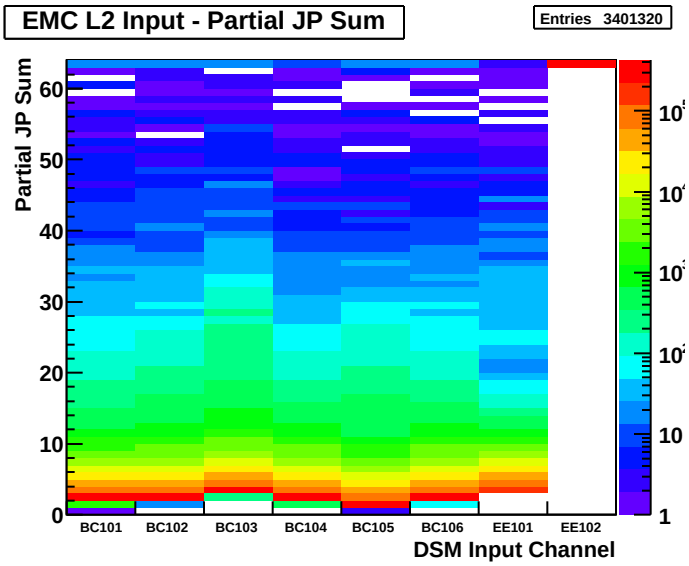

EMC L2 Input - JPX/JPA bits
Entries 23
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

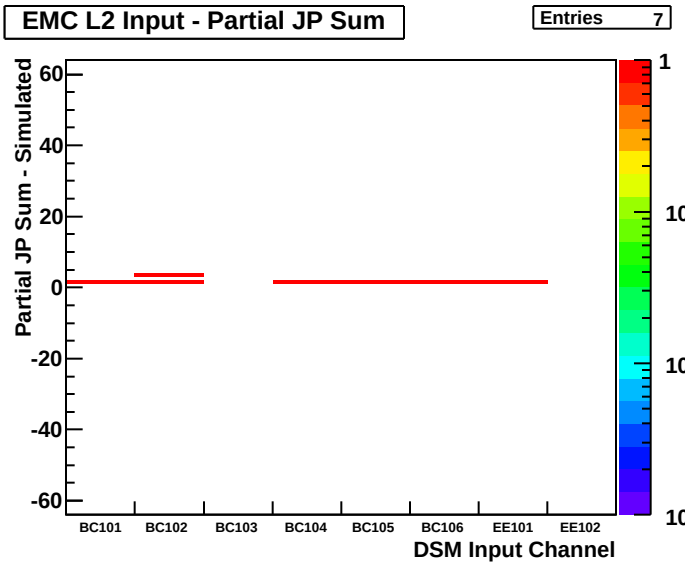

EMC L2 Input - JPY/JPB bits
Entries 3401320
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

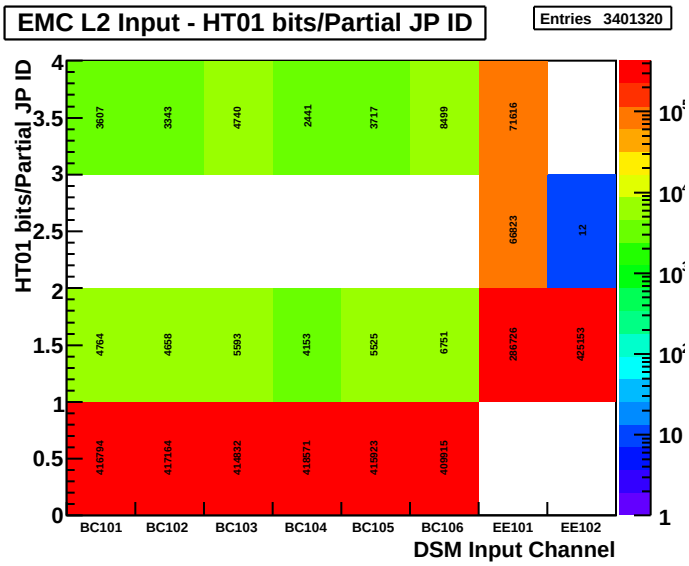

EMC L2 Input - JPY/JPB bits
Entries 19
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

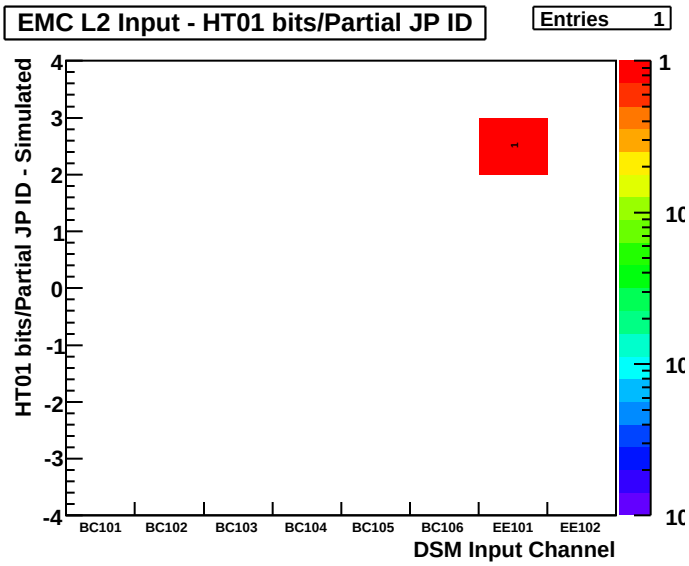

EMC L2 Input - JPZ/JPC bits
Entries 3401320
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

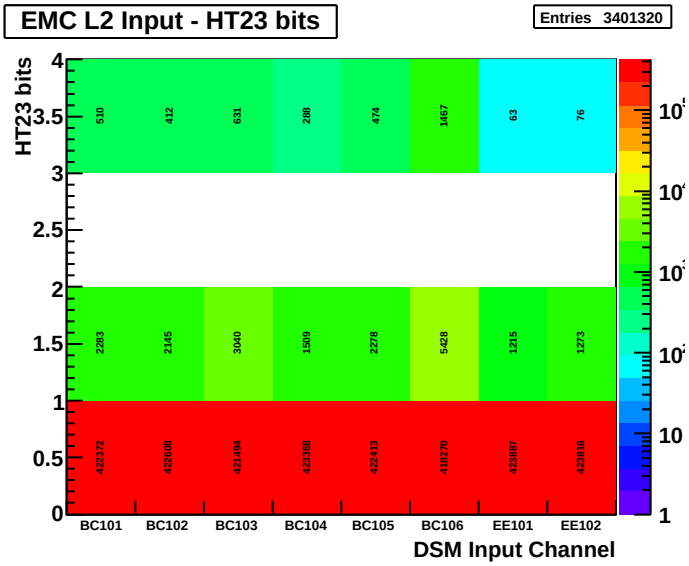

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

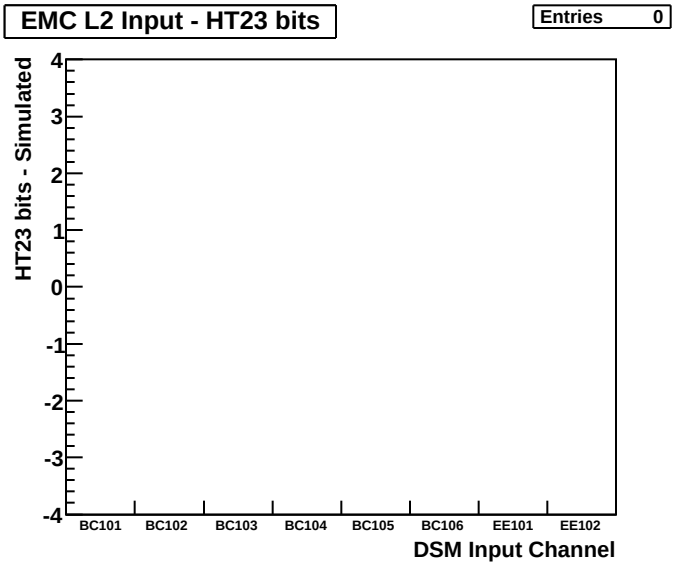

EMC L2 Input - Partial JP Sum
Entries 3401320
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

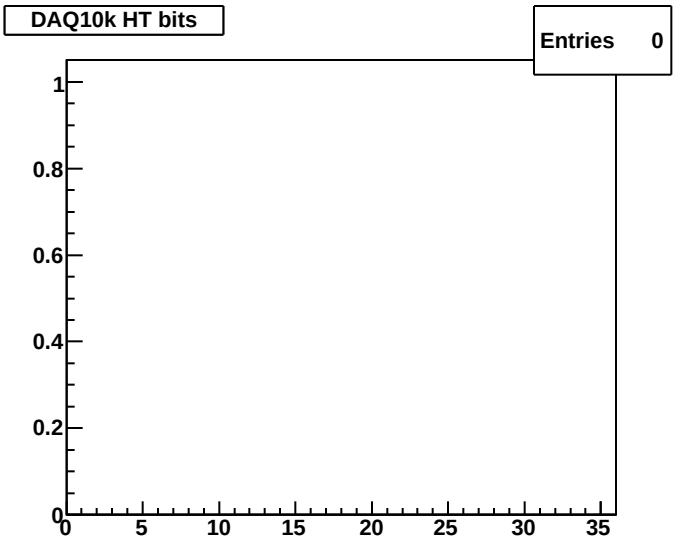

EMC L2 Input - Partial JP Sum
Entries 7
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

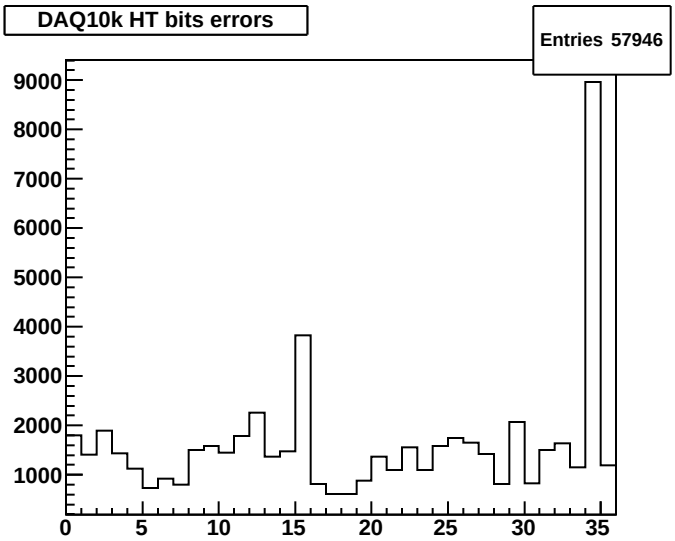

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3401320
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

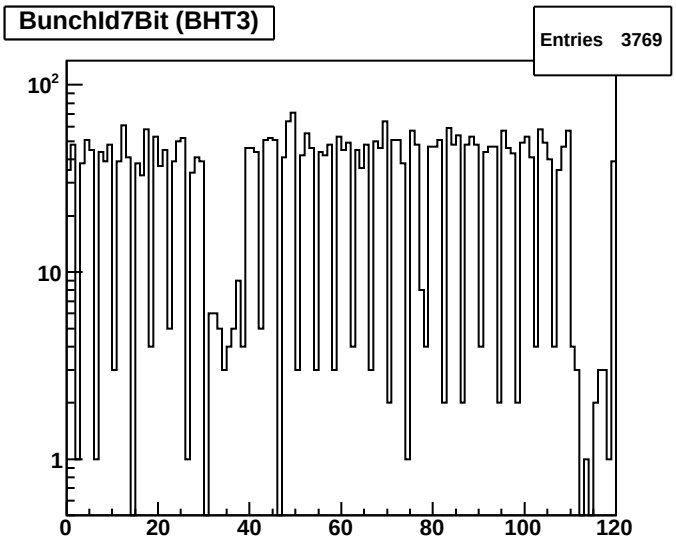

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

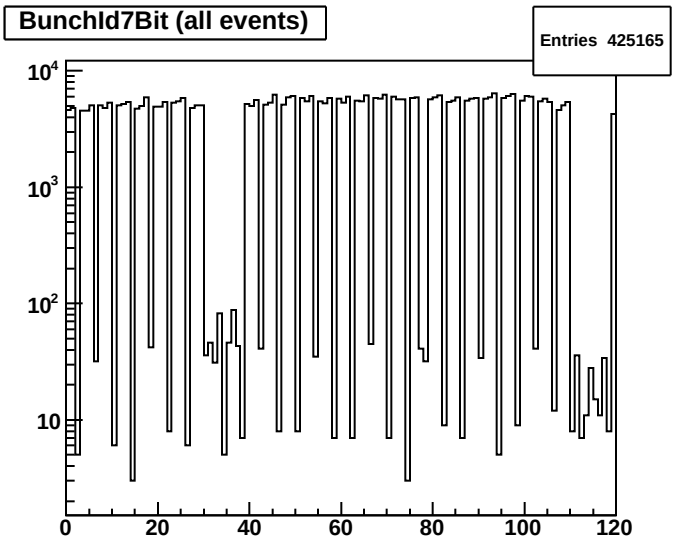

EMC L2 Input - HT23 bits
Entries 3401320
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

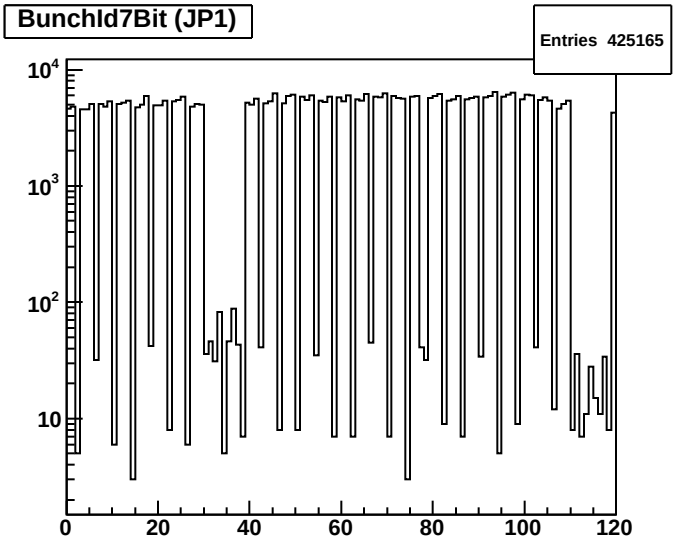

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

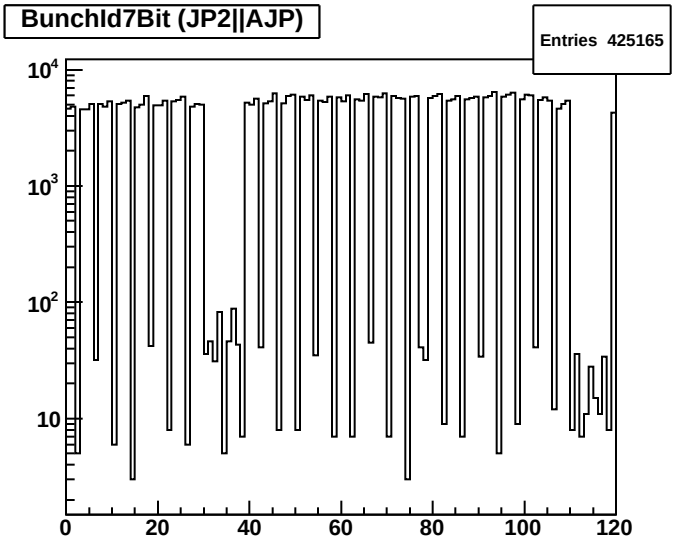

DAQ10k HT bits
Entries 0

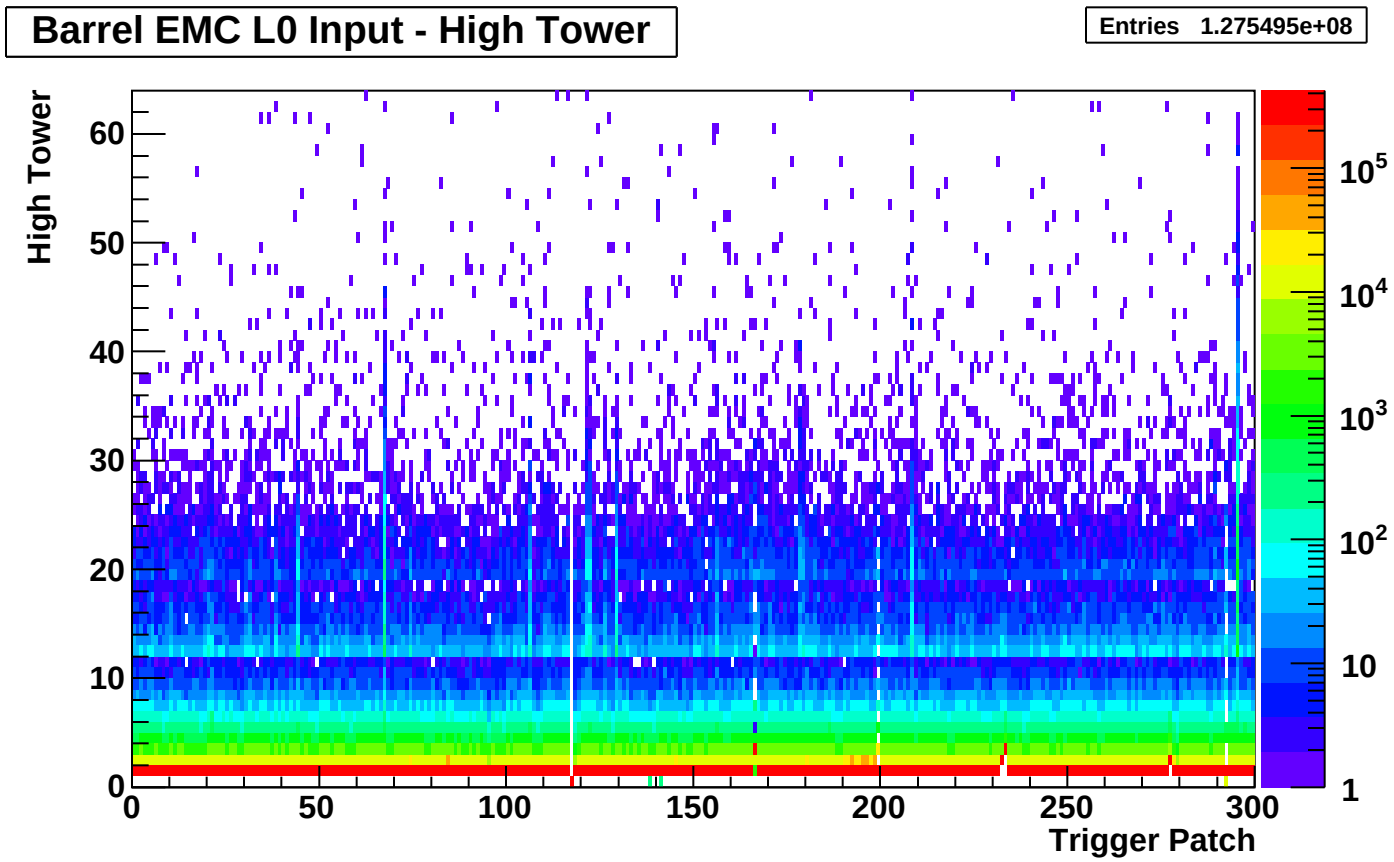

DAQ10k HT bits errors
Entries 57946

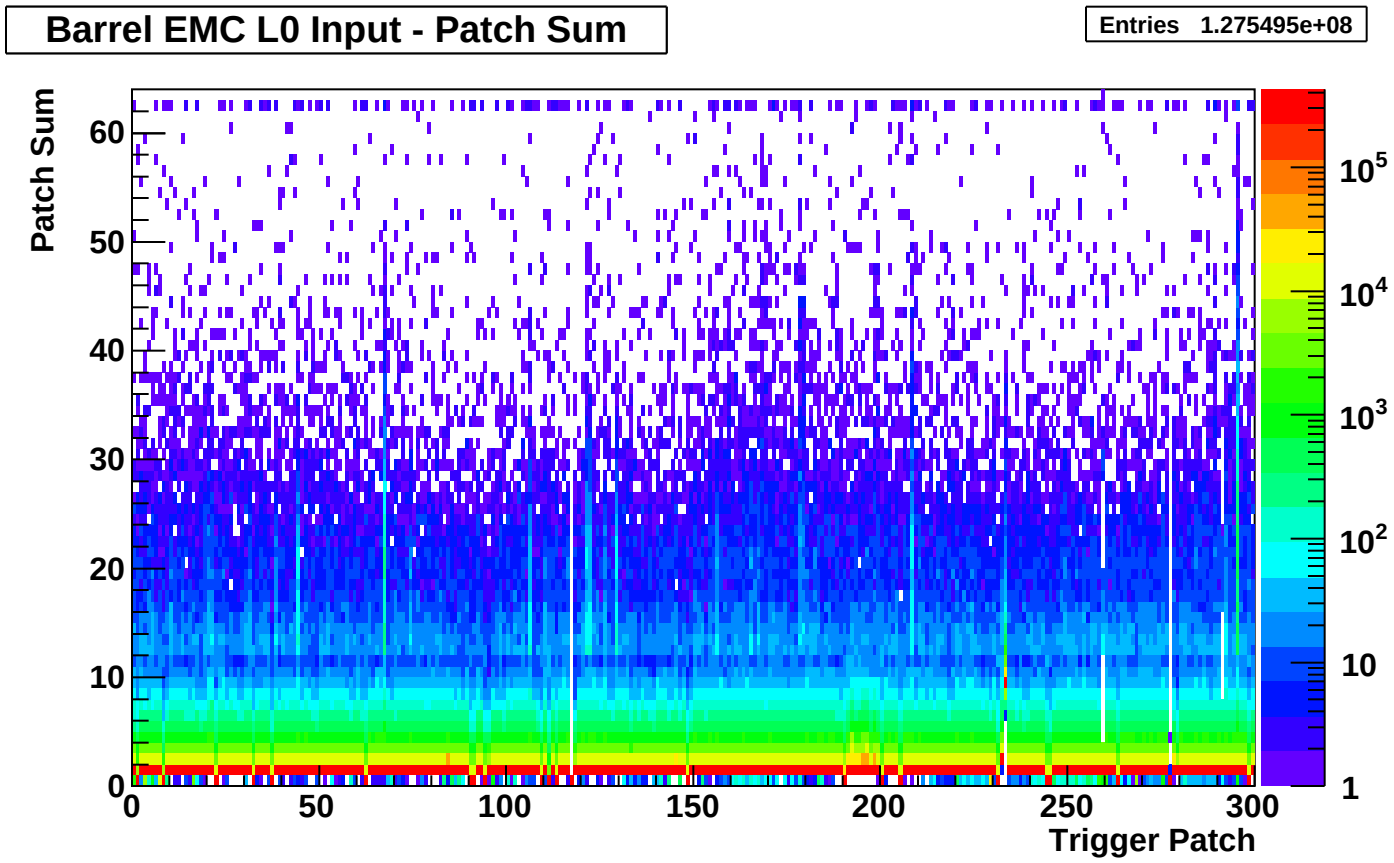

BunchId7Bit (BHT3)
Entries 3769

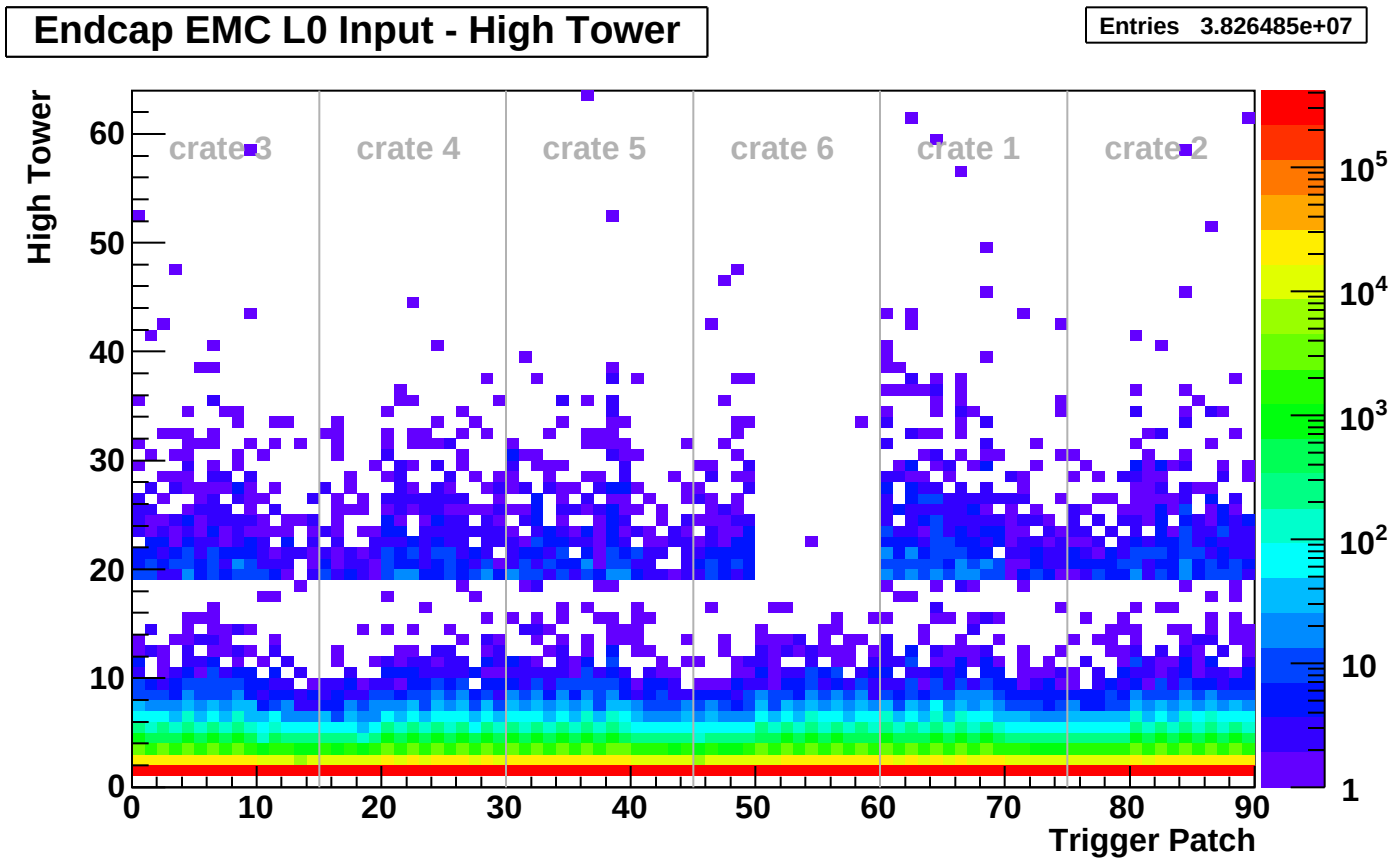

BunchId7Bit (all events)
Entries 425165

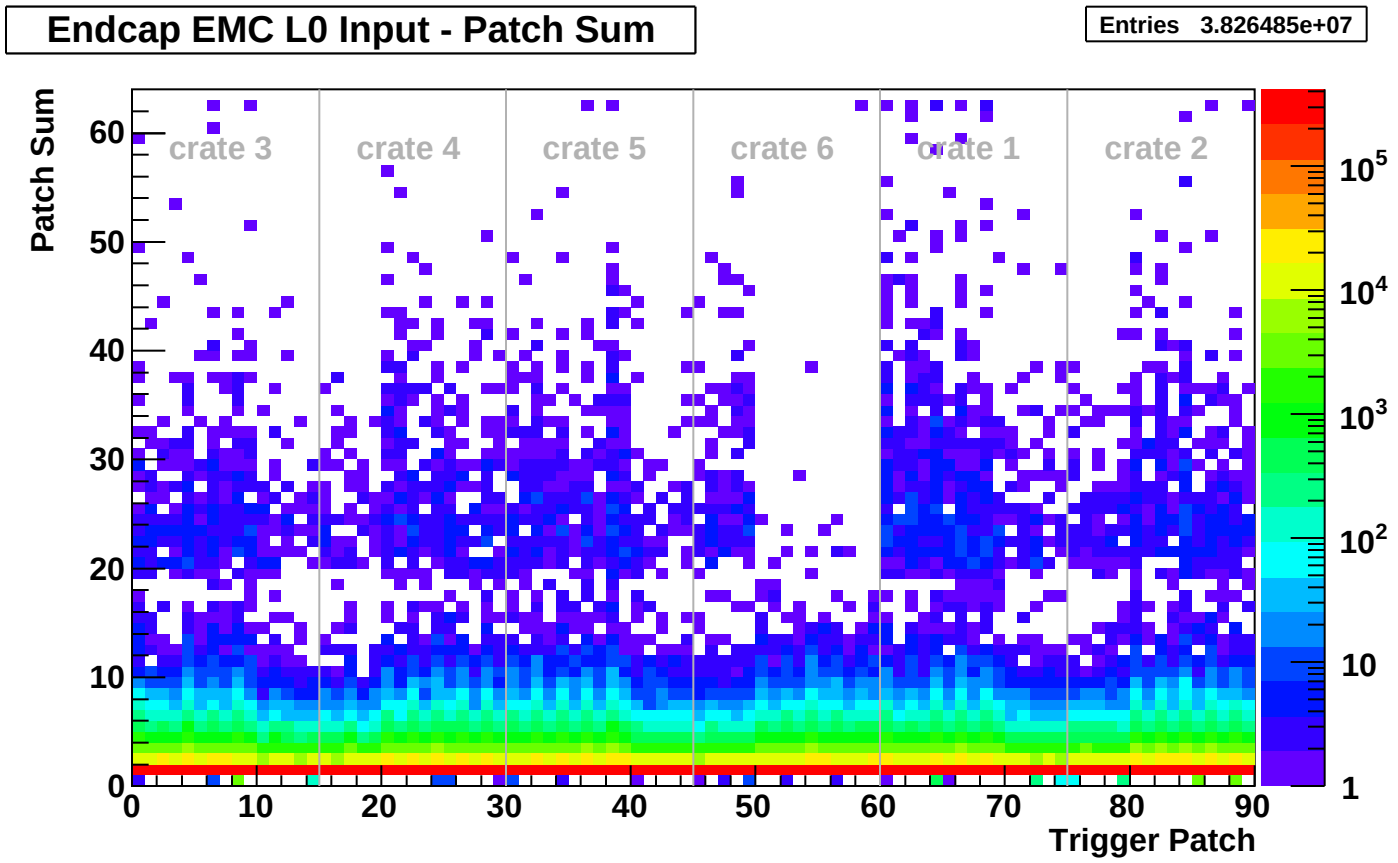

BunchId7Bit (JP1)
Entries 425165

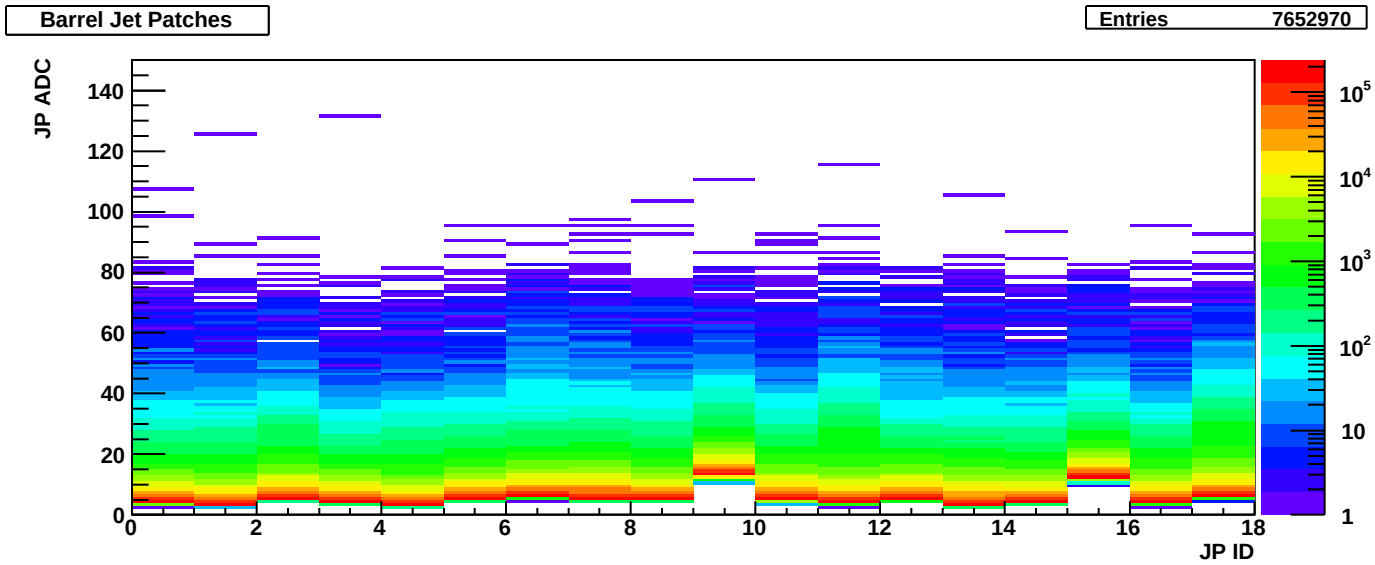

BunchId7Bit (JP2||AJP)
Entries 425165

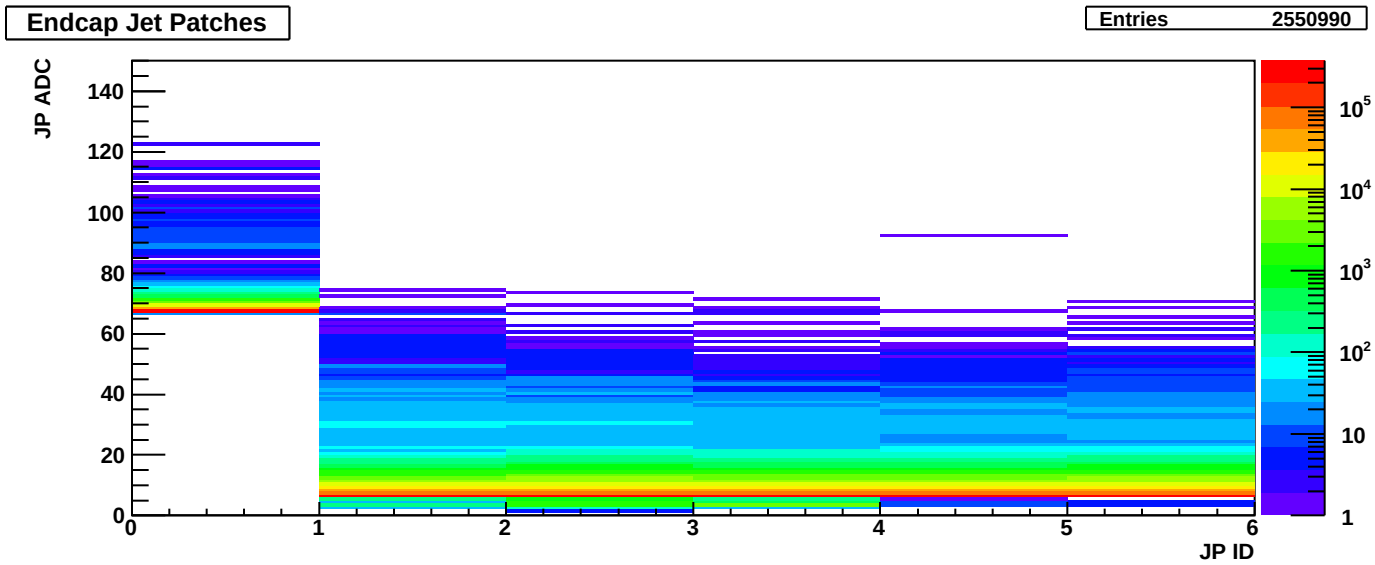

Barrel EMC L0 Input - High Tower
Entries 1.275495e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

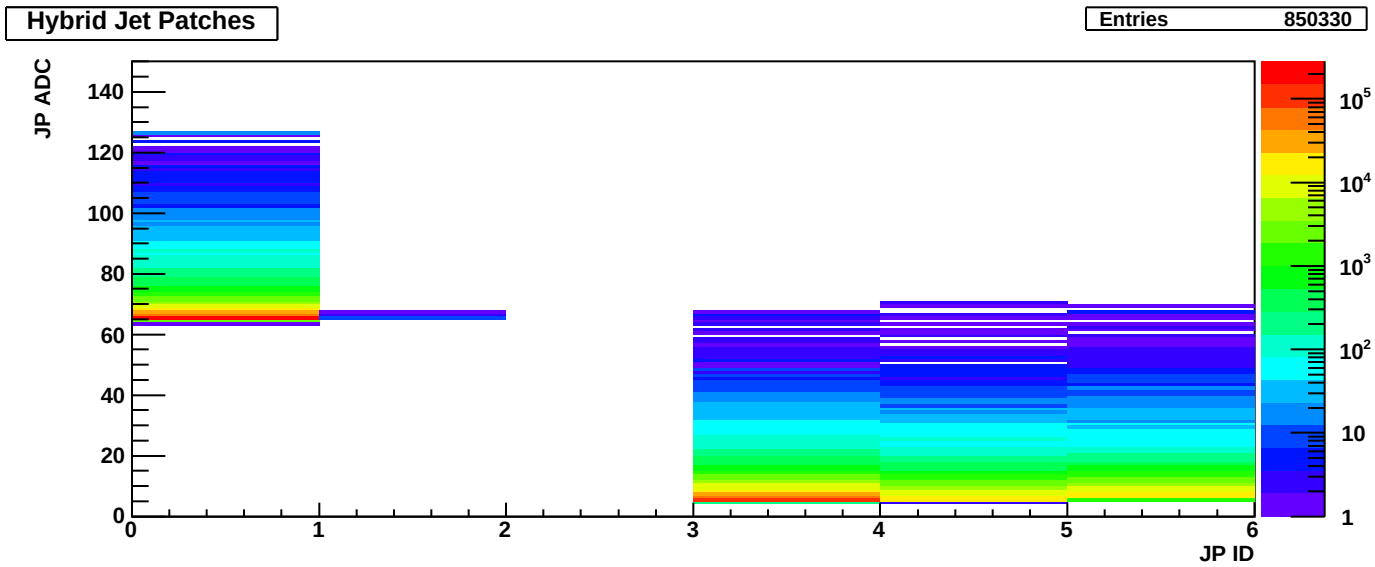

Barrel EMC L0 Input - Patch Sum
Entries 1.275495e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

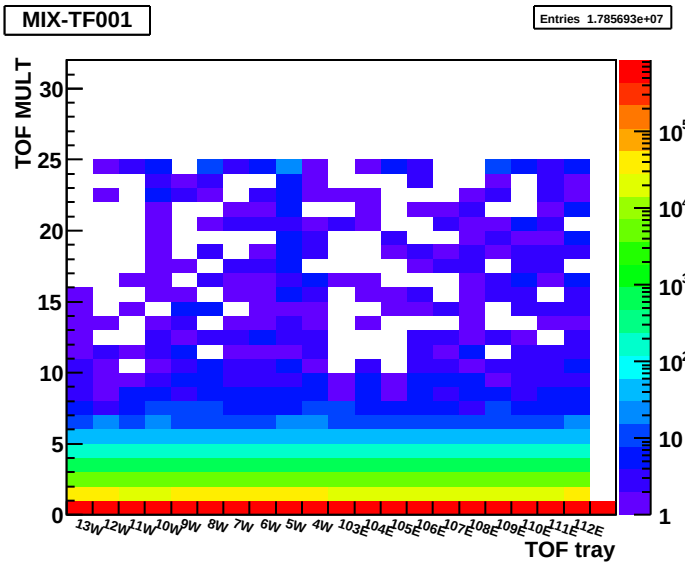

Endcap EMC L0 Input - High Tower
Entries 3.826485e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

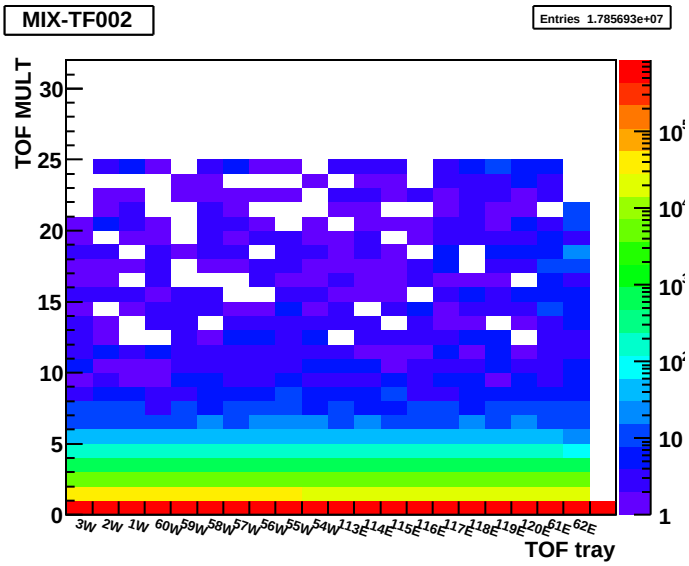

Endcap EMC L0 Input - Patch Sum
Entries 3.826485e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

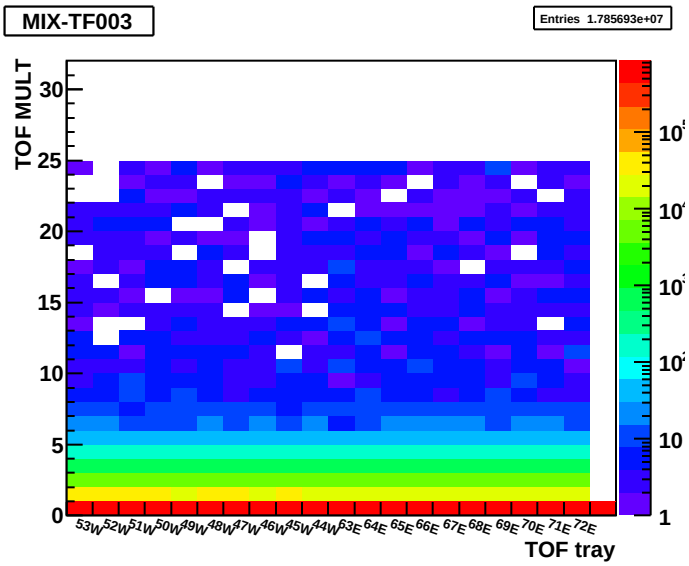

Barrel Jet Patches
Entries 7652970
JP ADC
JP ID

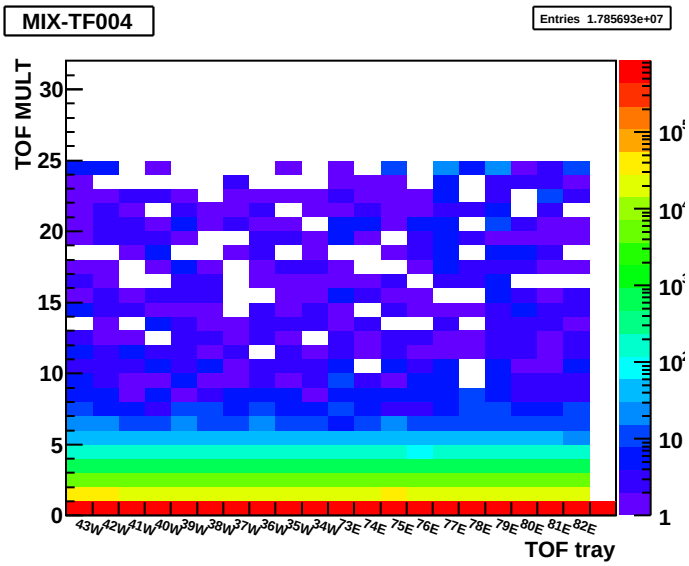

Endcap Jet Patches
Entries 2550990
JP ADC
JP ID

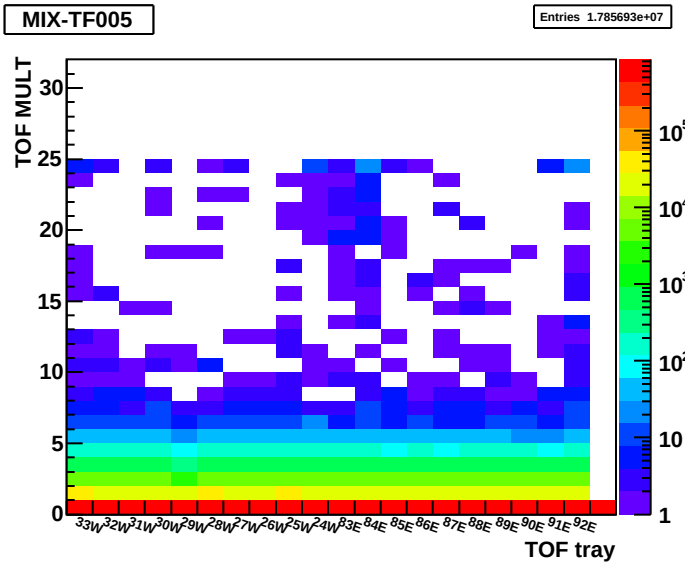

Hybrid Jet Patches
Entries 850330
JP ADC
JP ID

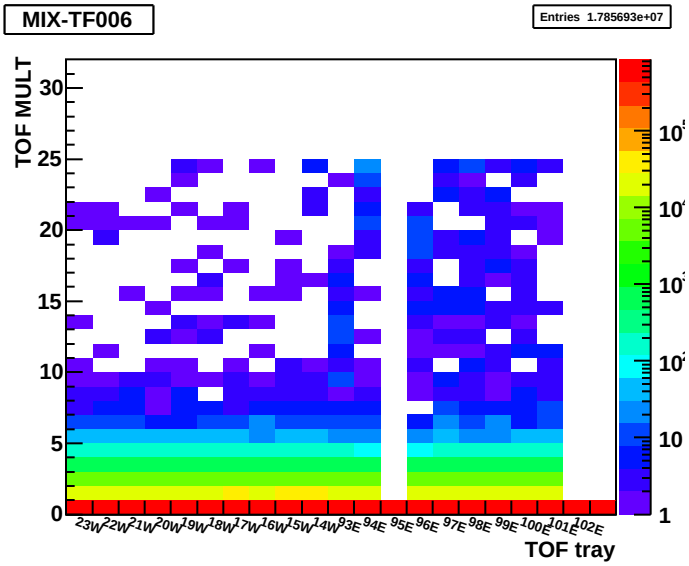

MIX-TF001
Entries 1.785693e+07
TOF MULT
TOF tray

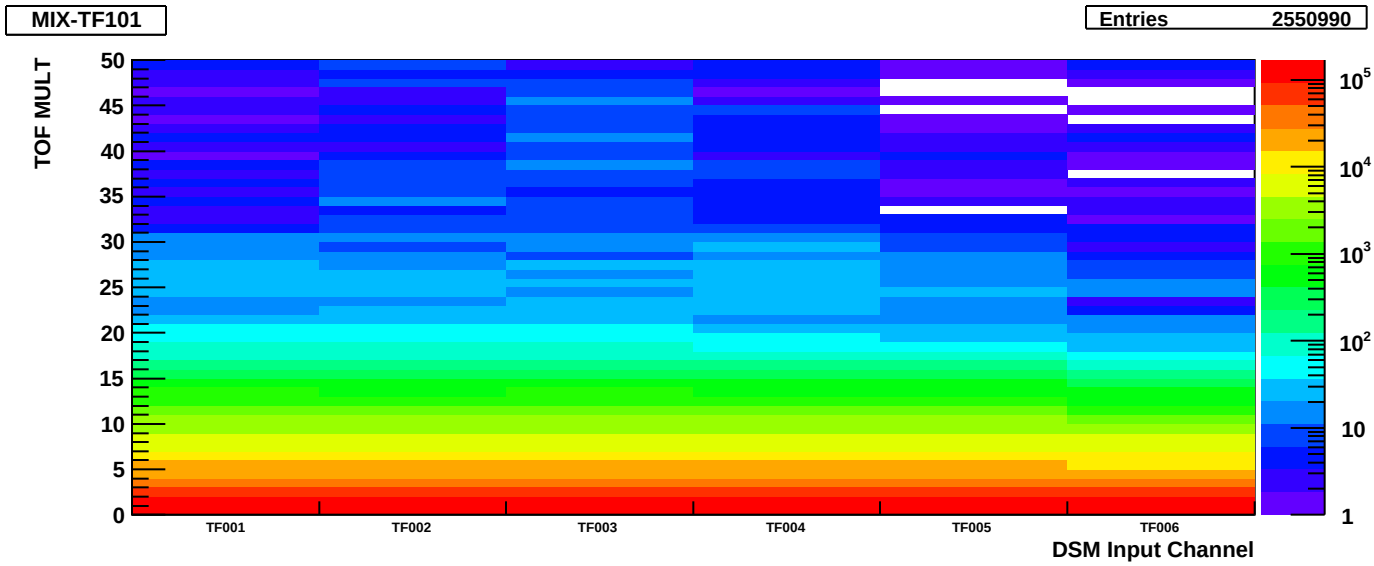

MIX-TF002
Entries 1.785693e+07
TOF MULT
TOF tray

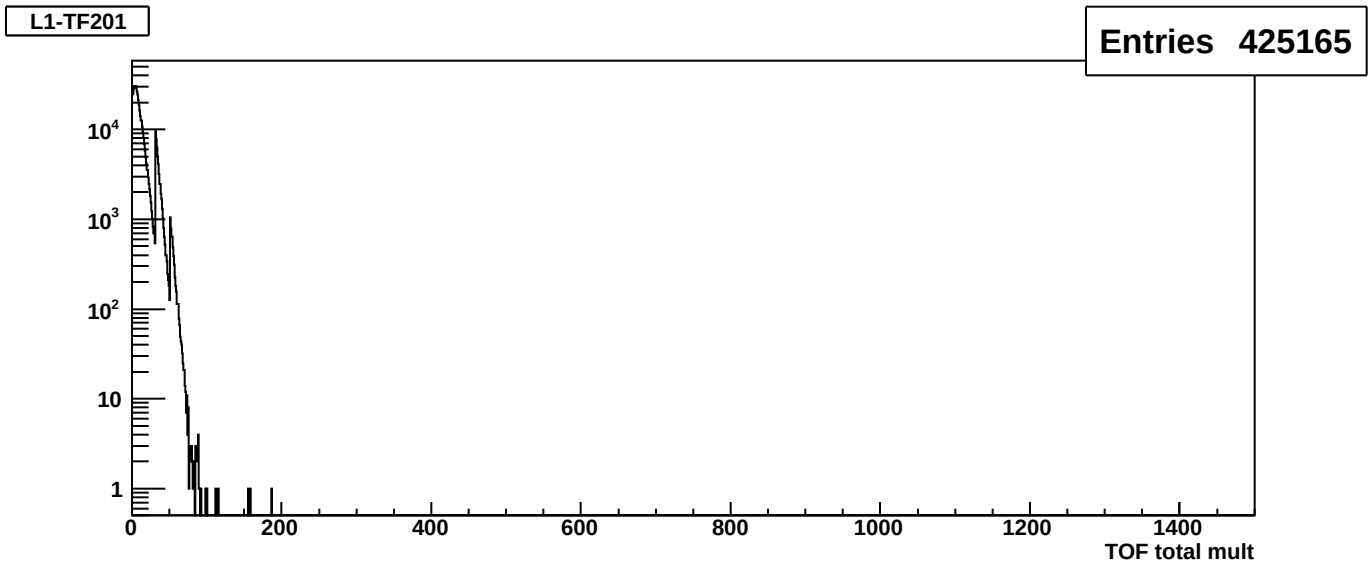

MIX-TF003
Entries 1.785693e+07
TOF MULT
TOF tray

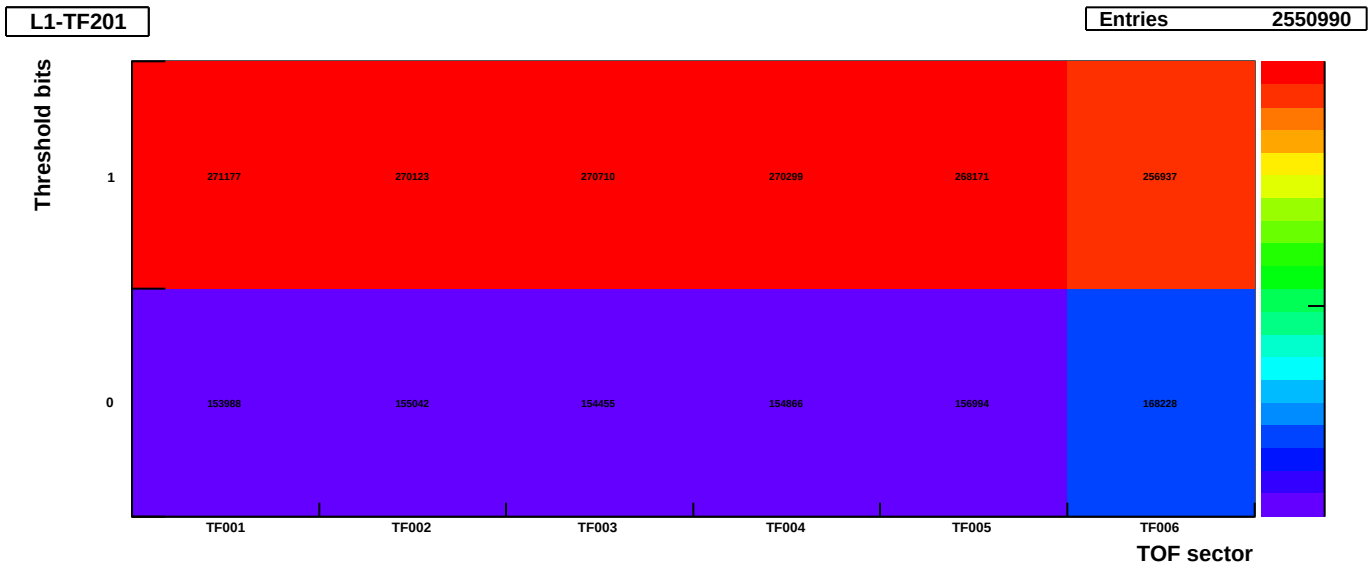

MIX-TF004
Entries 1.785693e+07
TOF MULT
TOF tray

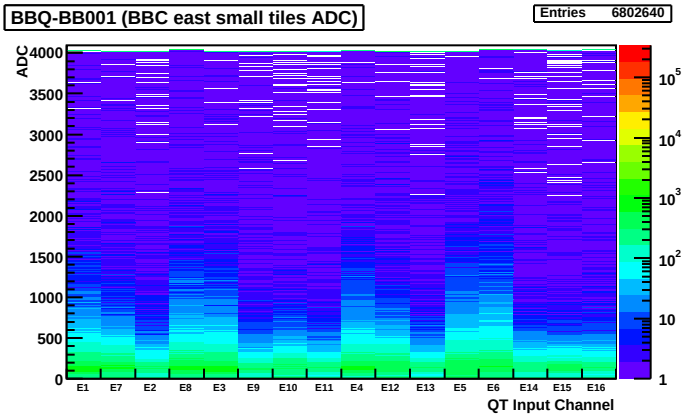

MIX-TF005
Entries 1.785693e+07
TOF MULT
TOF tray

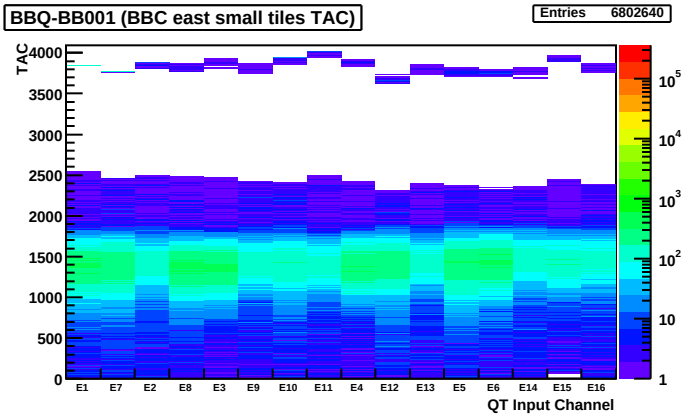

MIX-TF006
Entries 1.785693e+07
TOF MULT
TOF tray

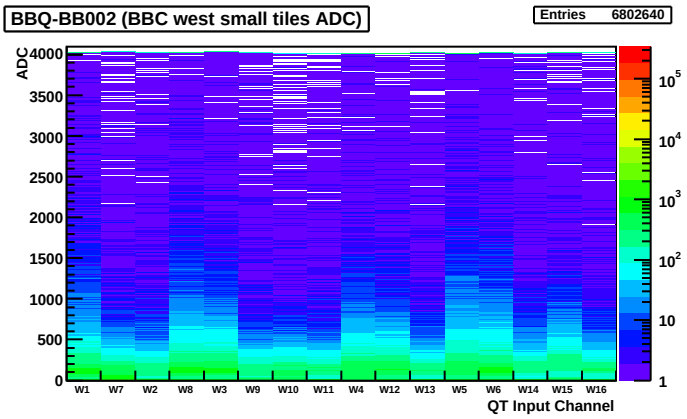

MIX-TF101
Entries 2550990
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

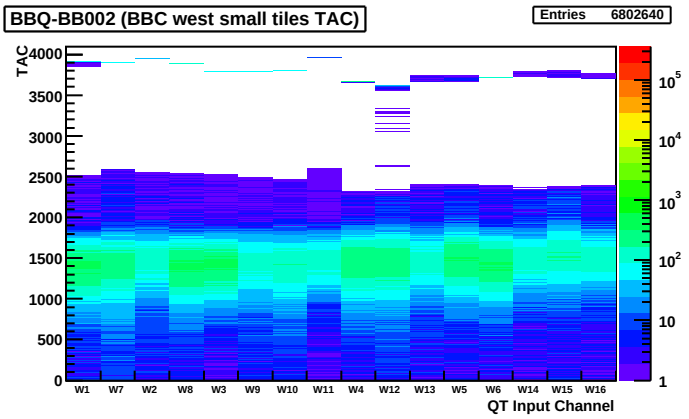

L1-TF201
Entries 425165
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

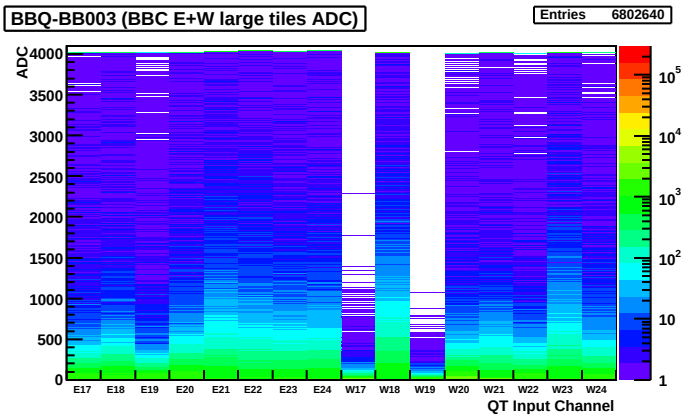

L1-TF201
Entries 2550990
Threshold bits
1
0
271177
270123
270710
270299
268171
256937
153988
155042
154455
154866
156994
168228
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

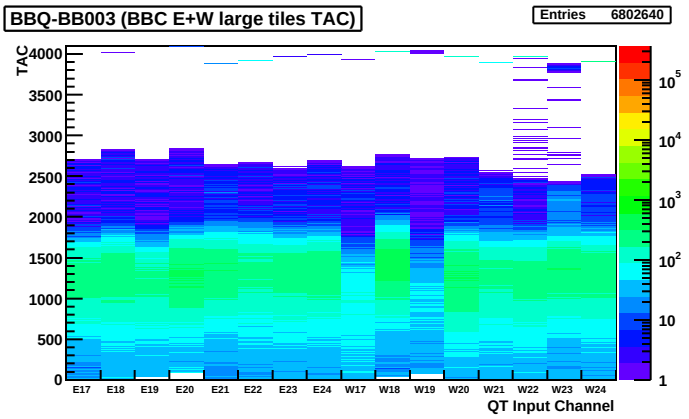

BBQ-BB001 (BBC east small tiles ADC)
Entries 6802640
ADC
QT Input Channel

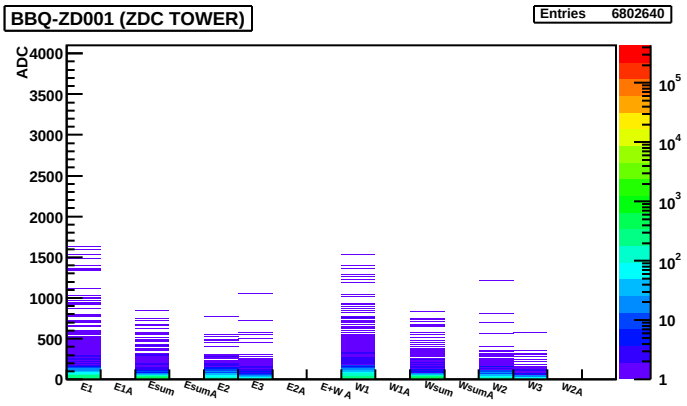

BBQ-BB001 (BBC east small tiles TAC)
Entries 6802640
TAC
QT Input Channel

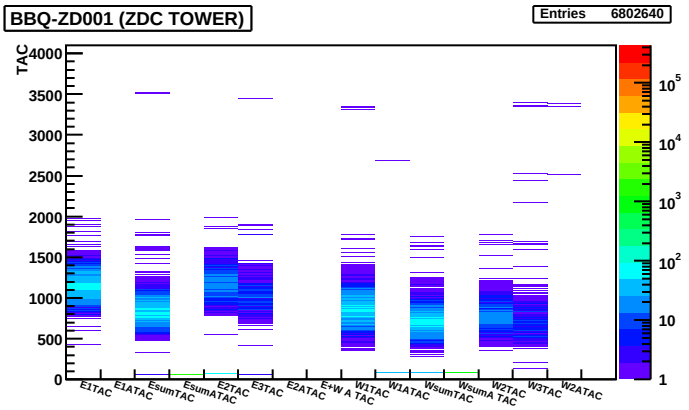

BBQ-BB002 (BBC west small tiles ADC)
Entries 6802640
ADC
QT Input Channel

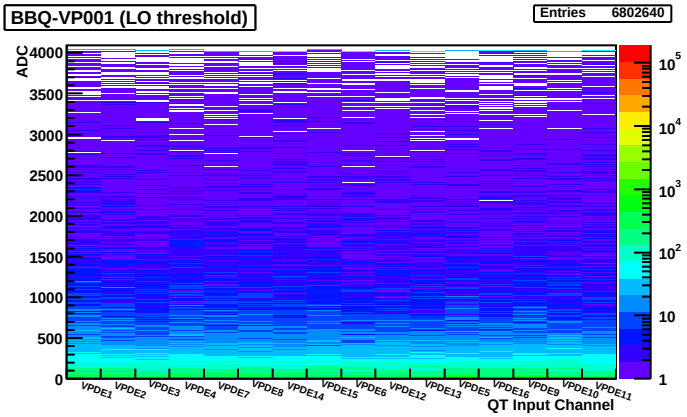

BBQ-BB002 (BBC west small tiles TAC)
Entries 6802640
TAC
QT Input Channel

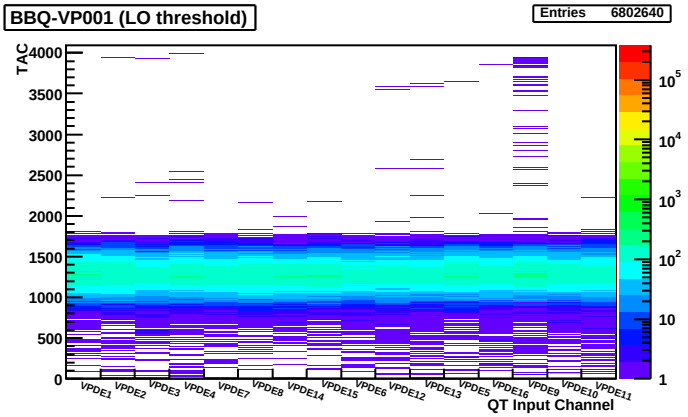

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6802640
ADC
QT Input Channel

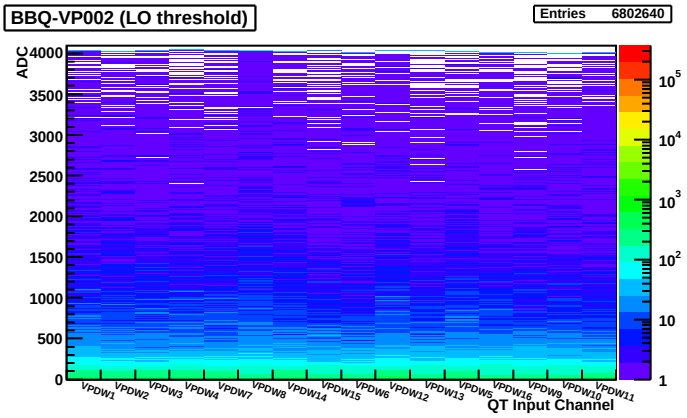

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6802640
TAC
QT Input Channel

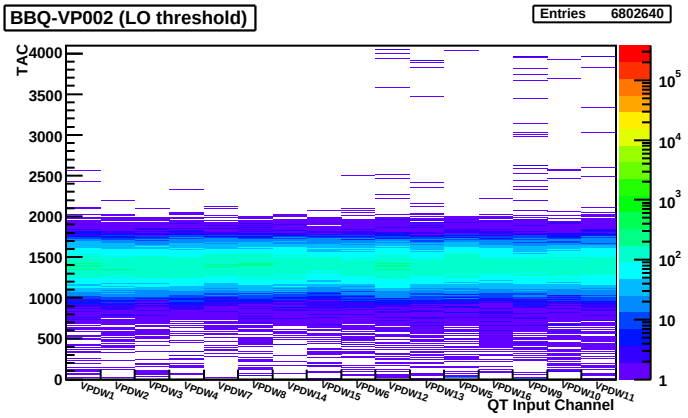

BBQ-ZD001 (ZDC TOWER)
Entries 6802640
ADC

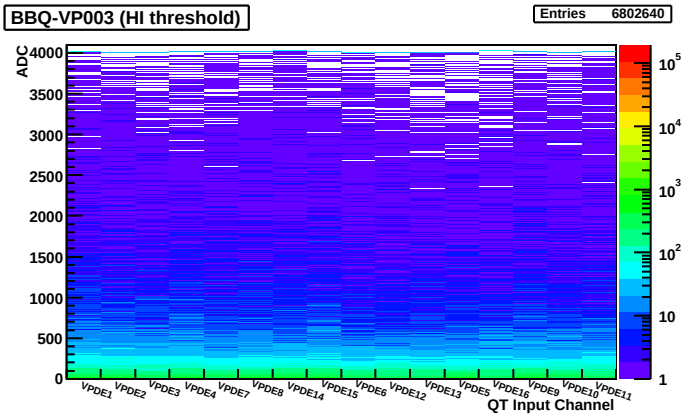

BBQ-ZD001 (ZDC TOWER)
Entries 6802640
TAC

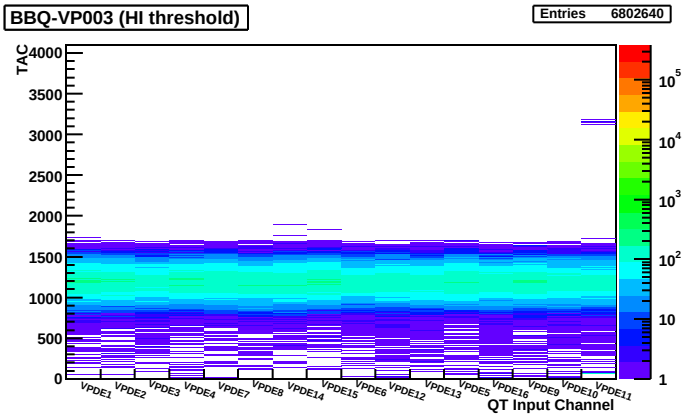

BBQ-VP001 (LO threshold)
Entries 6802640
ADC
QT Input Channel

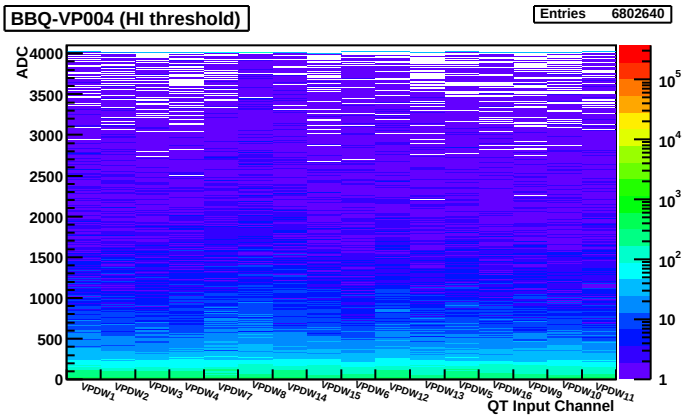

BBQ-VP001 (LO threshold)
Entries 6802640
TAC
QT Input Channel

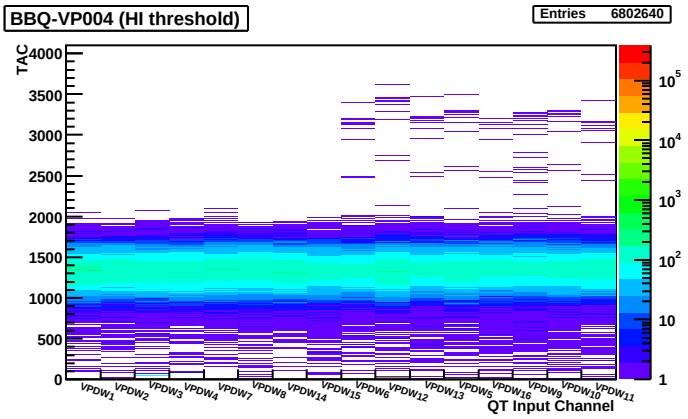

BBQ-VP002 (LO threshold)
Entries 6802640
ADC
QT Input Channel

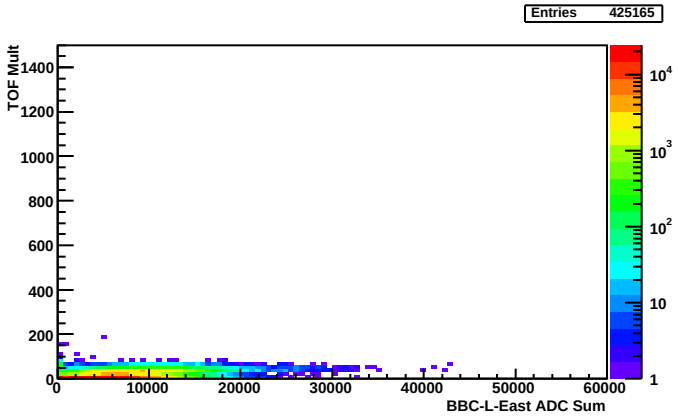

BBQ-VP002 (LO threshold)
Entries 6802640
TAC
QT Input Channel

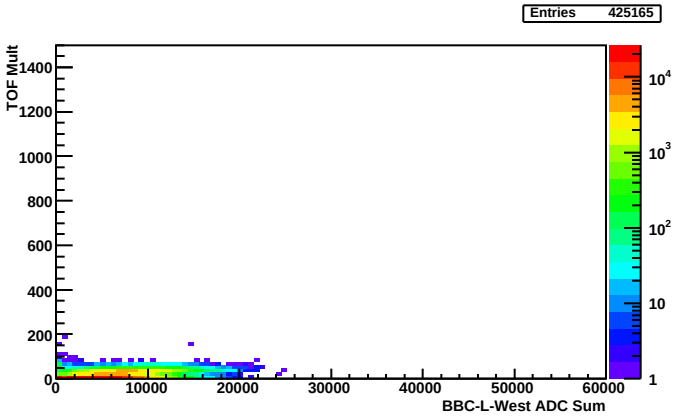

BBQ-VP003 (HI threshold)
Entries 6802640
ADC
QT Input Channel

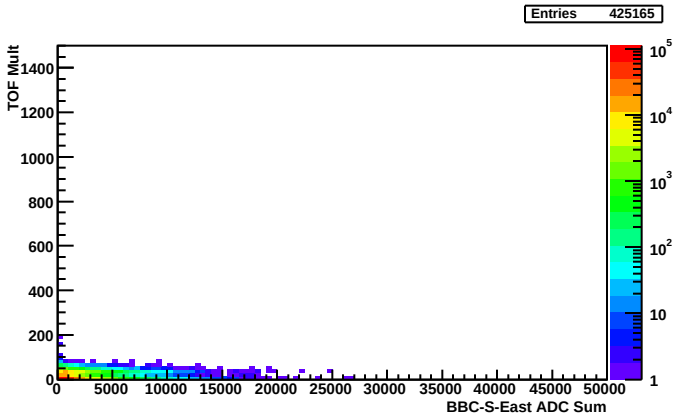

BBQ-VP003 (HI threshold)
Entries 6802640
TAC
QT Input Channel

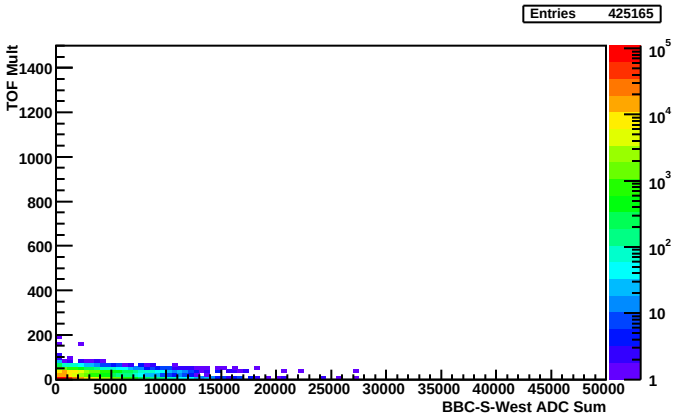

BBQ-VP004 (HI threshold)
Entries 6802640
ADC
QT Input Channel

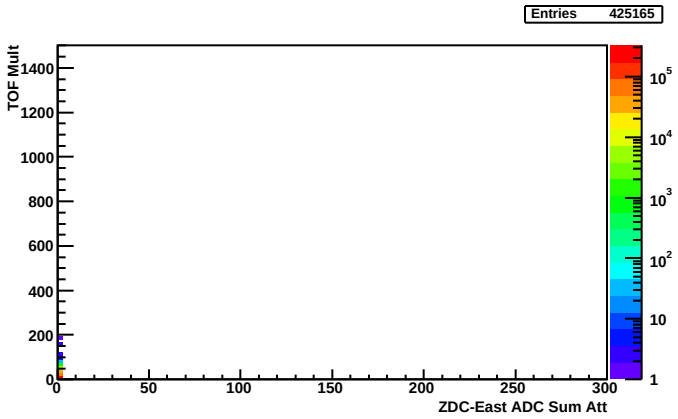

BBQ-VP004 (HI threshold)
Entries 6802640
TAC
QT Input Channel

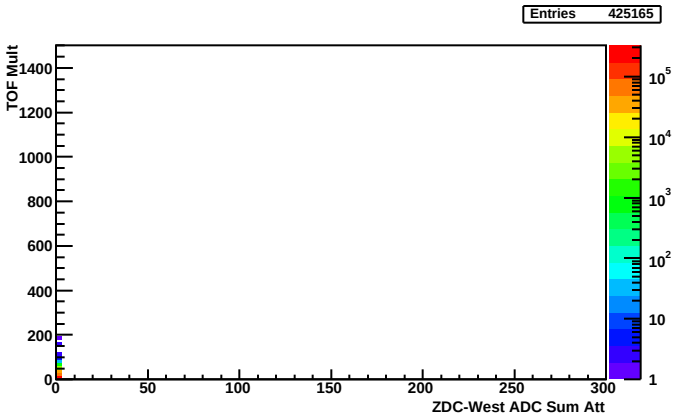

Entries 425165
TOF Mult
BBC-L-East ADC Sum

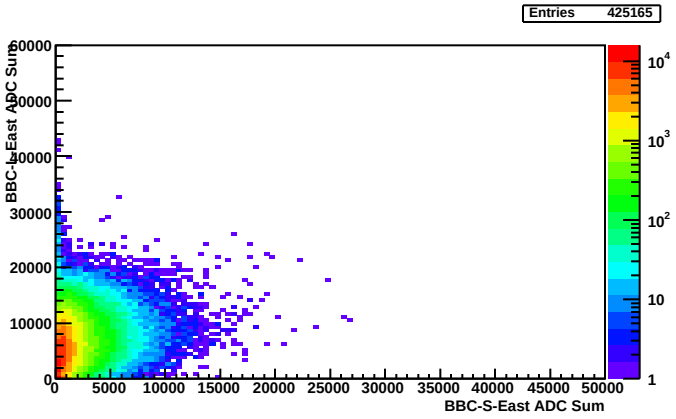

Entries 425165
TOF Mult
BBC-L-West ADC Sum

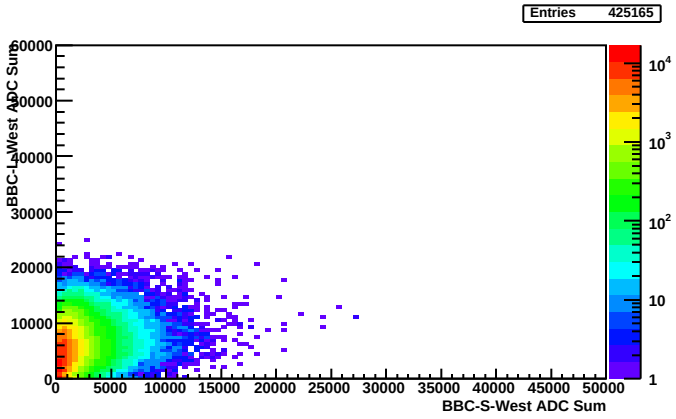

Entries 425165
TOF Mult
BBC-S-East ADC Sum

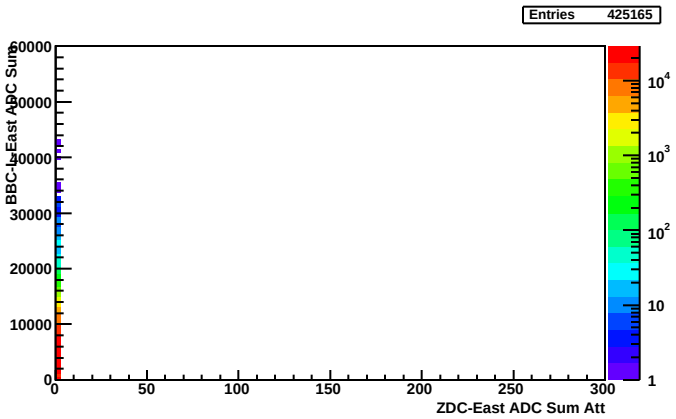

Entries 425165
TOF Mult
BBC-S-West ADC Sum

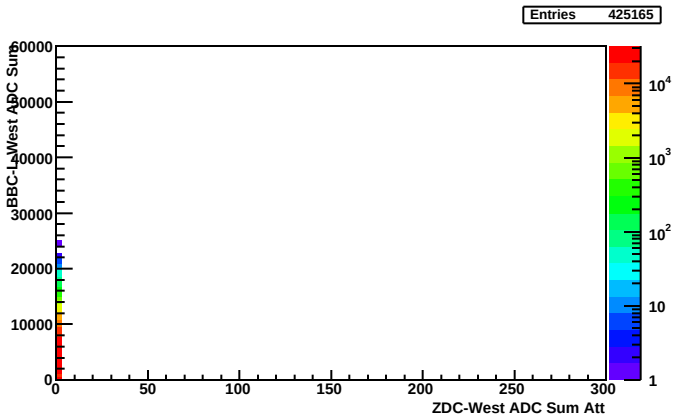

Entries 425165
TOF Mult
ZDC-East ADC Sum Att

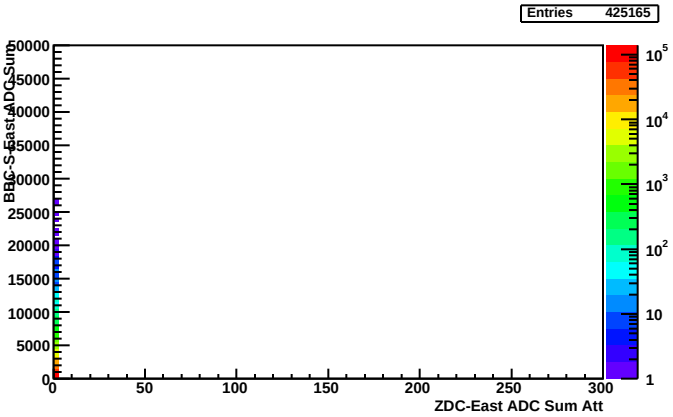

Entries 425165
TOF Mult
ZDC-West ADC Sum Att

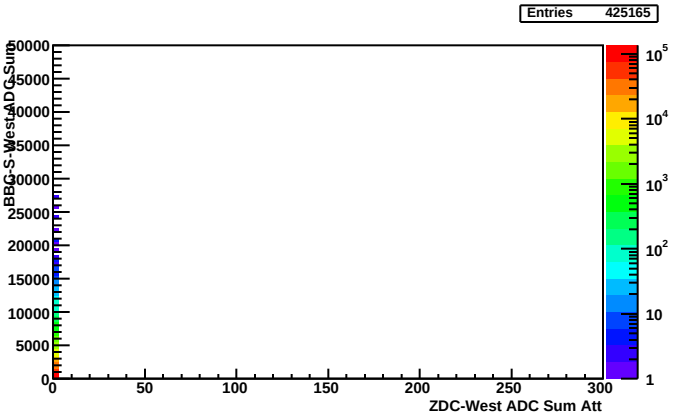

Entries 425165
BBC-L-East ADC Sum
BBC-S-East ADC Sum

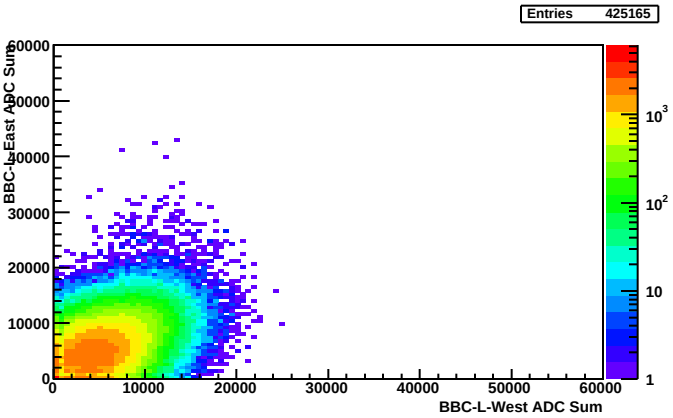

Entries 425165
BBC-L-West ADC Sum
BBC-S-West ADC Sum

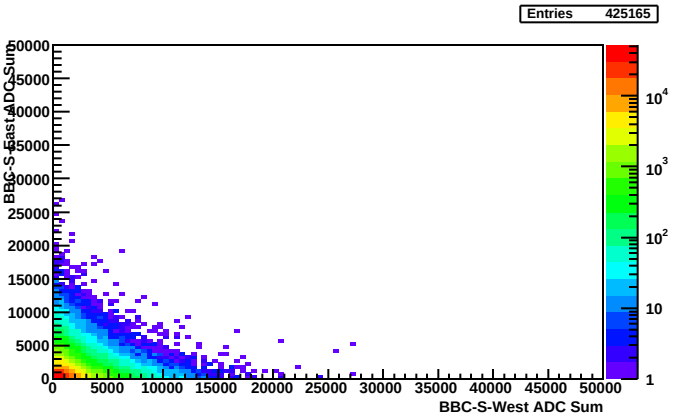

Entries 425165
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

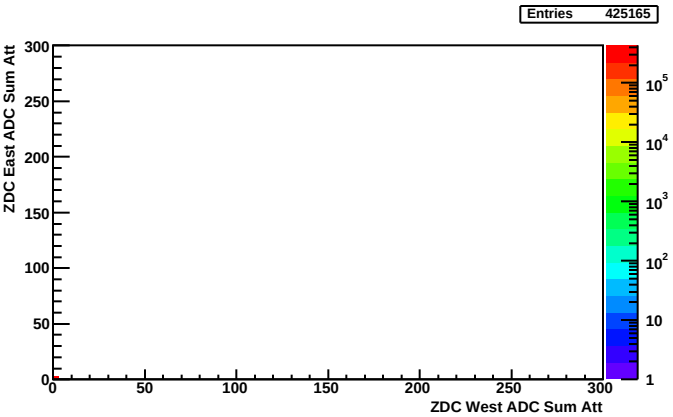

Entries 425165
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

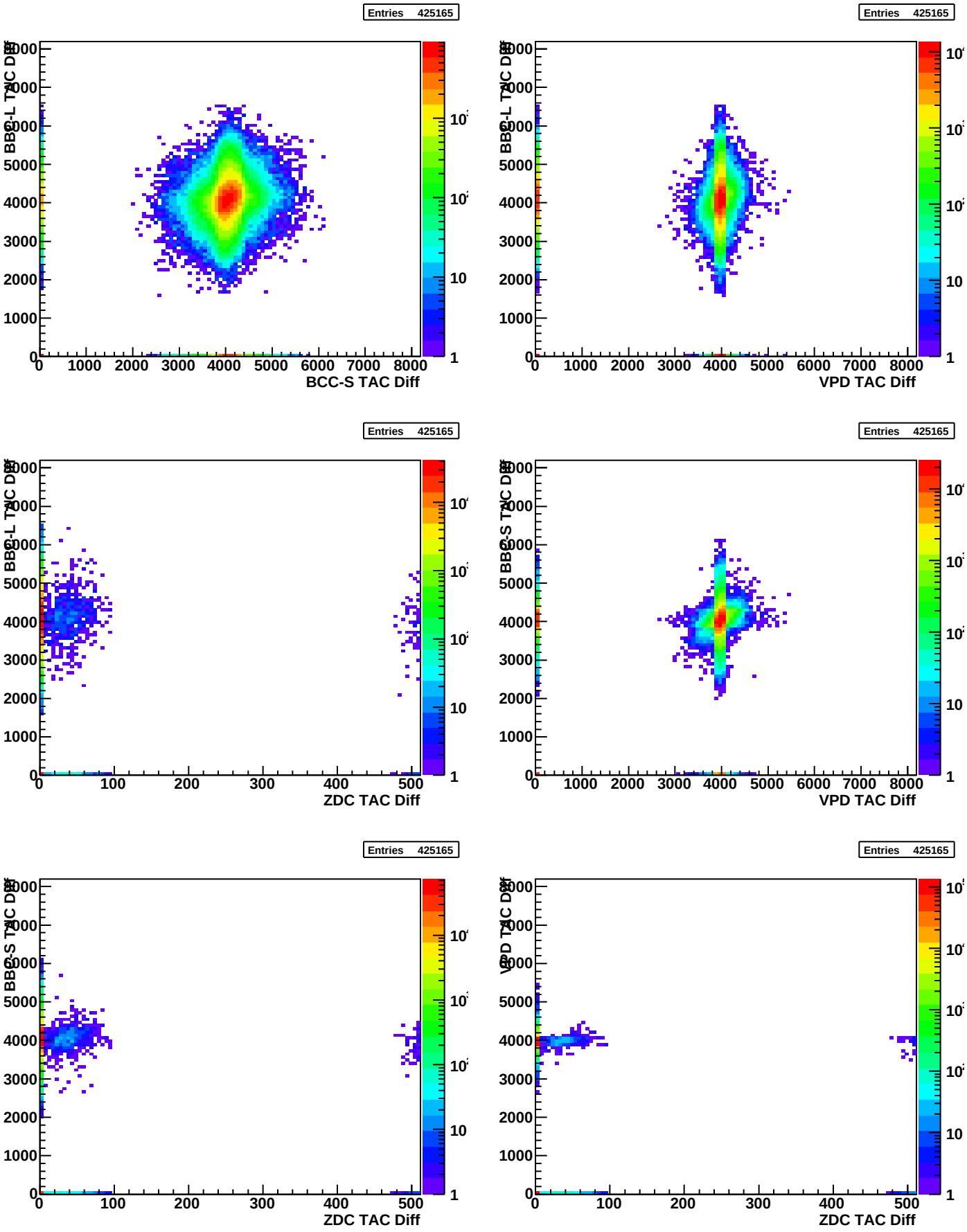

Entries 425165
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

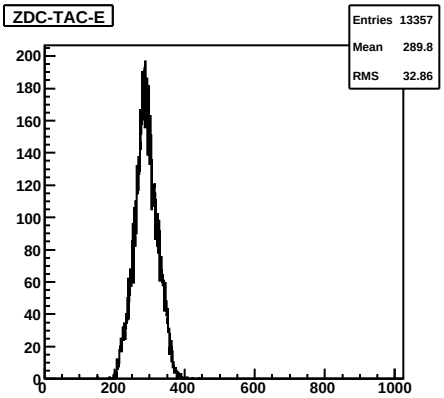

Entries 425165
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

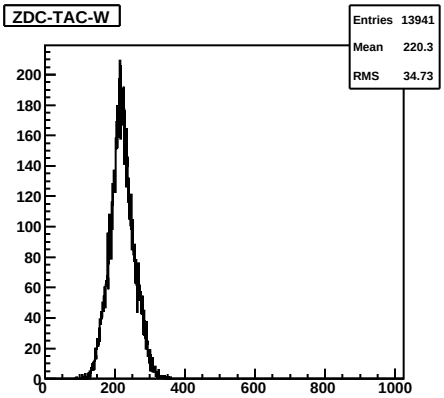

Entries 425165
BBC-L-East ADC Sum
BBC-L-West ADC Sum

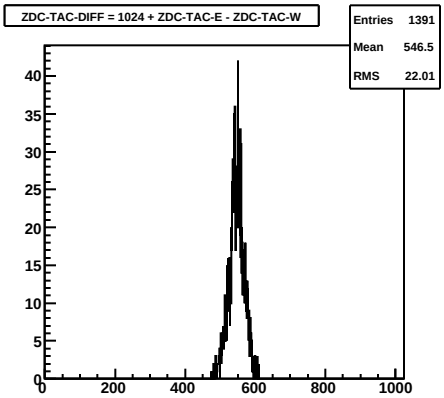

Entries 425165
BBC-S-East ADC Sum
BBC-S-West ADC Sum

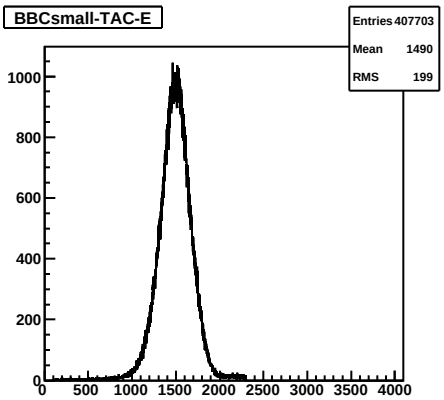

Entries 425165
ZDC East ADC Sum Att
ZDC West ADC Sum Att

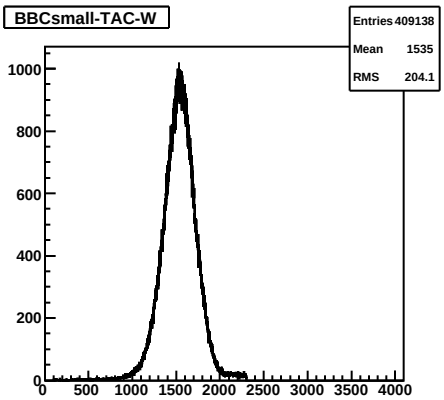

Entries 425165
BBC-L TAC Diff
BCC-S TAC Diff
Entries 425165
BBC-L TAC Diff
VPD TAC Diff
Entries 425165
BBC-L TAC Diff
ZDC TAC Diff
Entries 425165
BBC-S TAC Diff
VPD TAC Diff
Entries 425165
BBC-S TAC Diff
ZDC TAC Diff
Entries 425165
VPD TAC Diff
ZDC TAC Diff

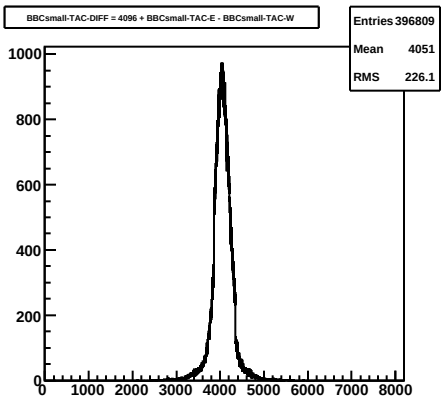

ZDC-TAC-E
Entries 13357
Mean 289.8
RMS 32.86

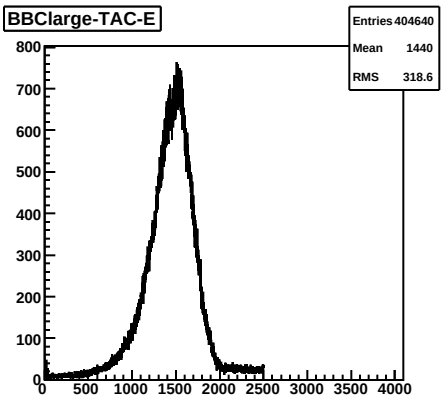

ZDC-TAC-W
Entries 13941
Mean 220.3
RMS 34.73

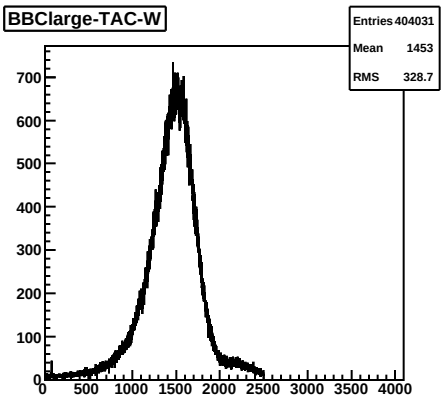

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1391
Mean 546.5
RMS 22.01

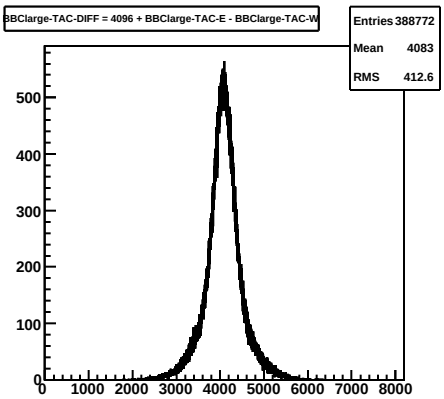

BBCsmall-TAC-E
Entries 407703
Mean 1490
RMS 199

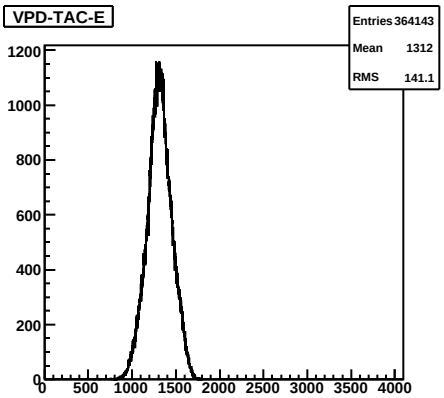

BBCsmall-TAC-W
Entries 409138
Mean 1535
RMS 204.1

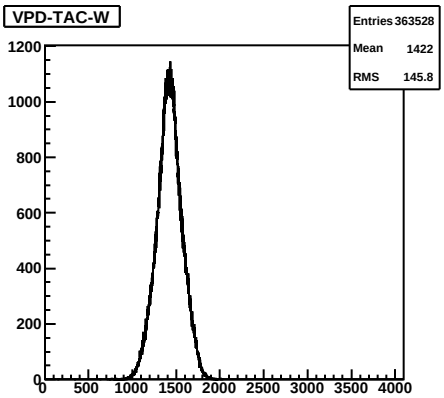

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 396809
Mean 4051
RMS 226.1

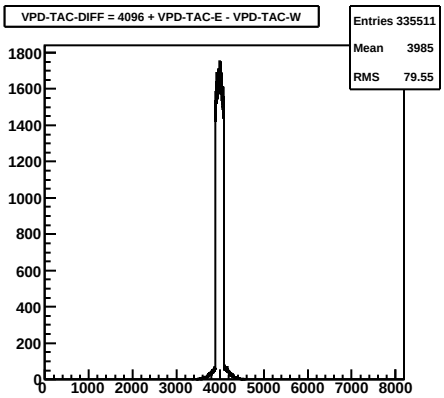

BBClarge-TAC-E
Entries 404640
Mean 1440
RMS 318.6

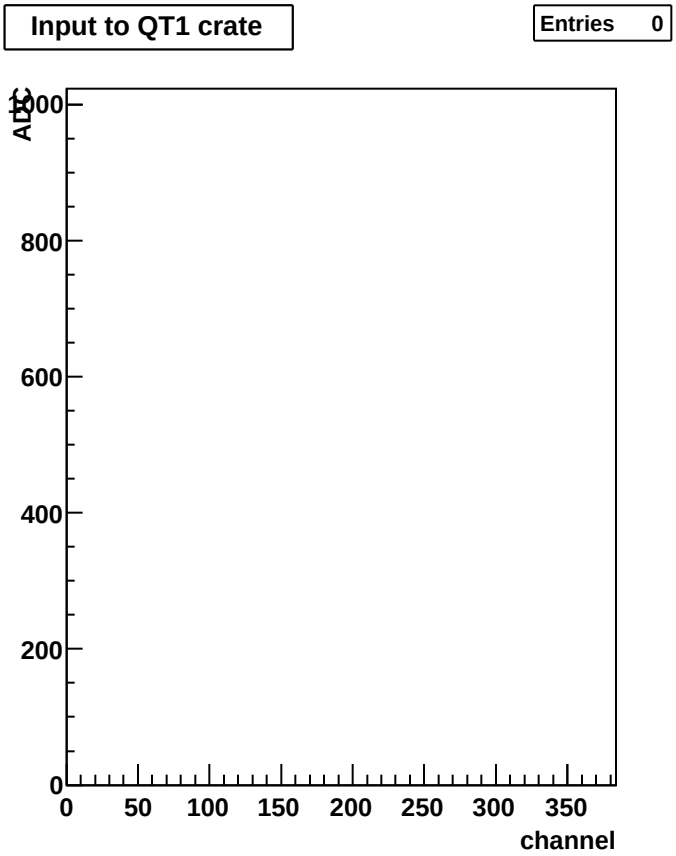

BBClarge-TAC-W
Entries 404031
Mean 1453
RMS 328.7

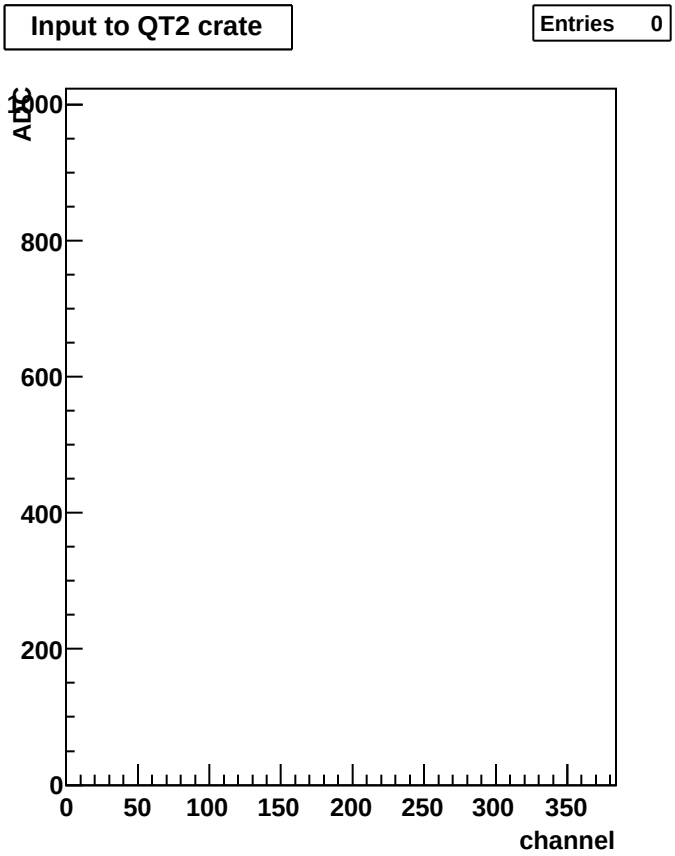

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 388772
Mean 4083
RMS 412.6

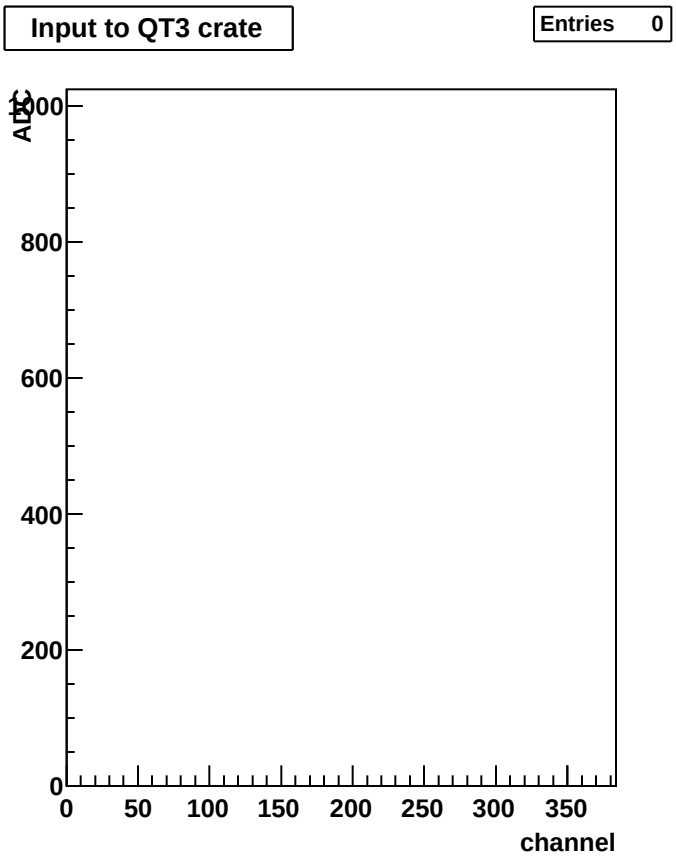

VPD-TAC-E
Entries 364143
Mean 1312
RMS 141.1

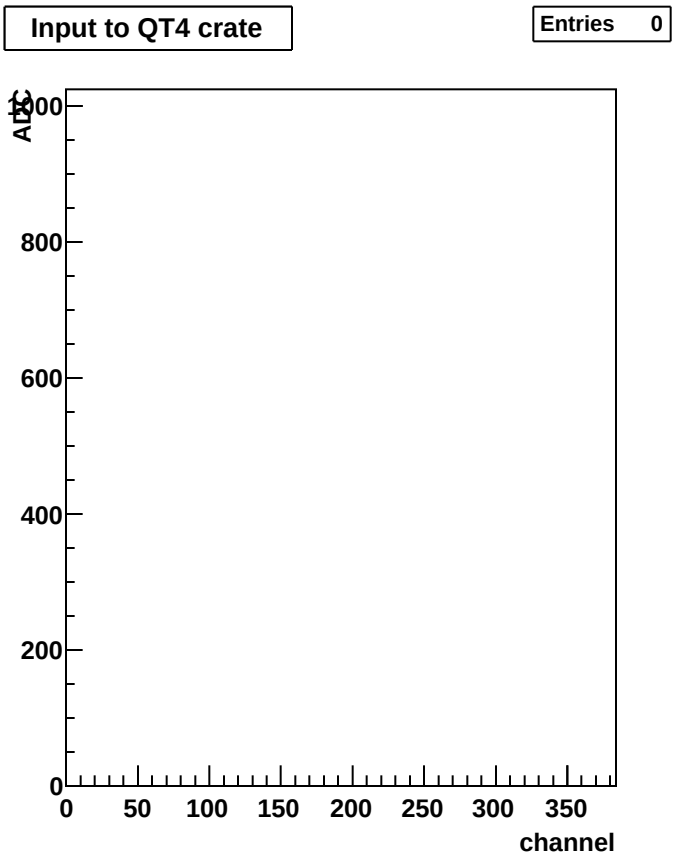

VPD-TAC-W
Entries 363528
Mean 1422
RMS 145.8

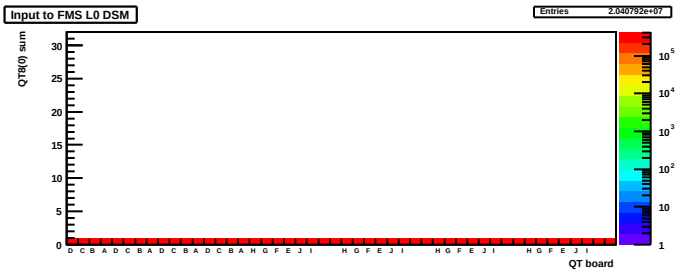

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 335511
Mean 3985
RMS 79.55

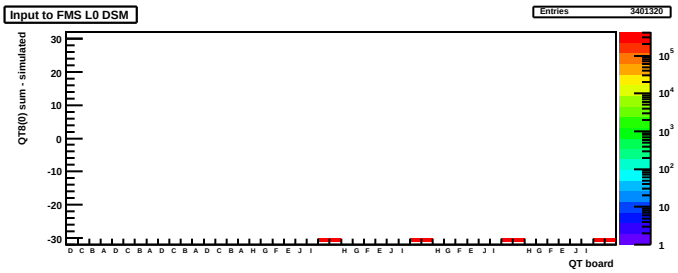

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

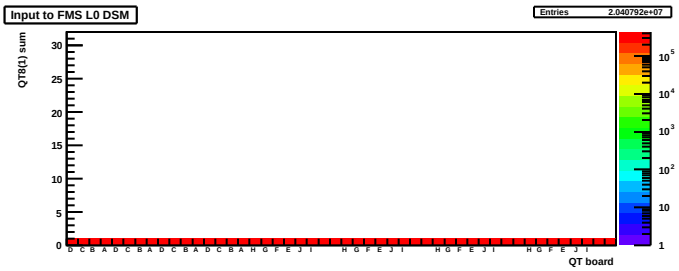

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

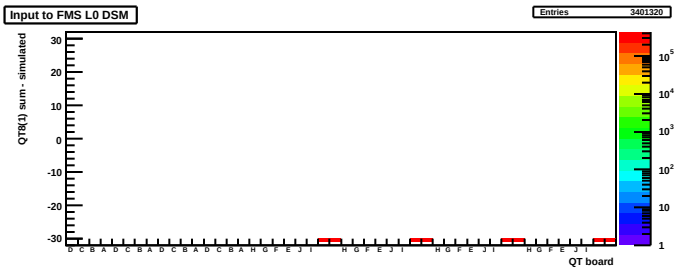

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

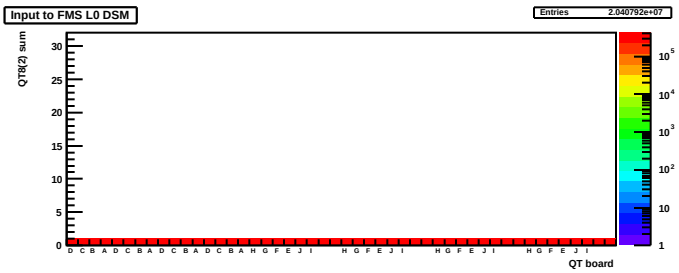

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

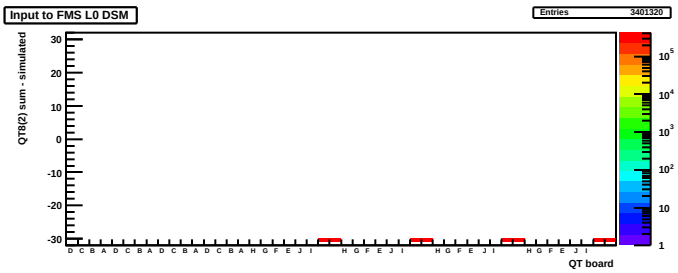

Input to FMS L0 DSM
Entries 2.040792e+07
QT8(0) sum
QT board

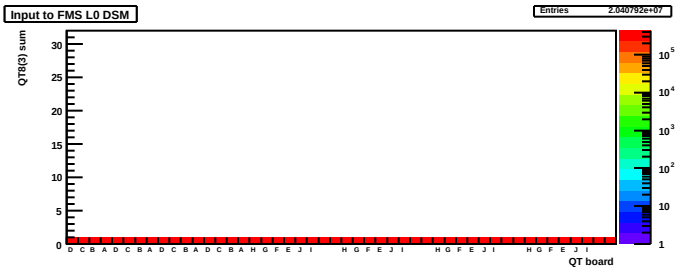

Input to FMS L0 DSM
Entries 3401320
QT8(0) sum - simulated
QT board

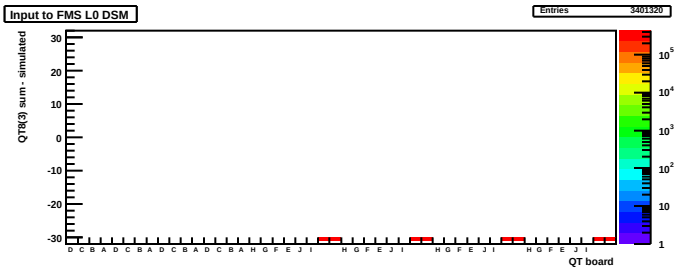

Input to FMS L0 DSM
Entries 2.040792e+07
QT8(1) sum
QT board

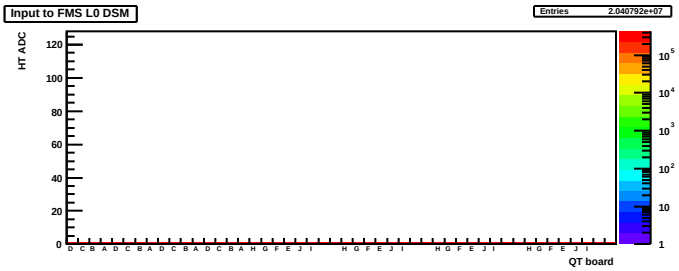

Input to FMS L0 DSM
Entries 3401320
QT8(1) sum - simulated
QT board

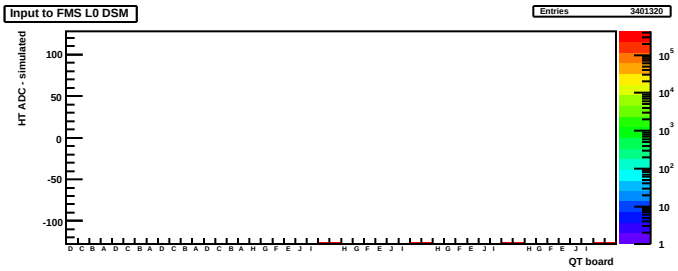

Input to FMS L0 DSM
Entries 2.040792e+07
QT8(2) sum
QT board

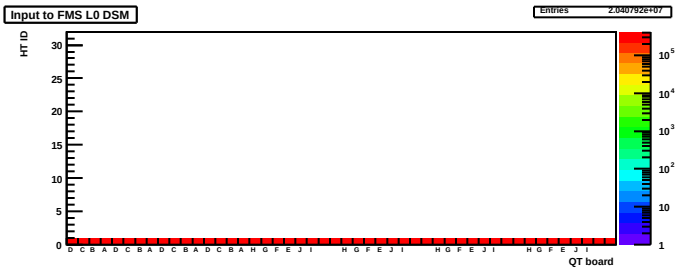

Input to FMS L0 DSM
Entries 3401320
QT8(2) sum - simulated
QT board

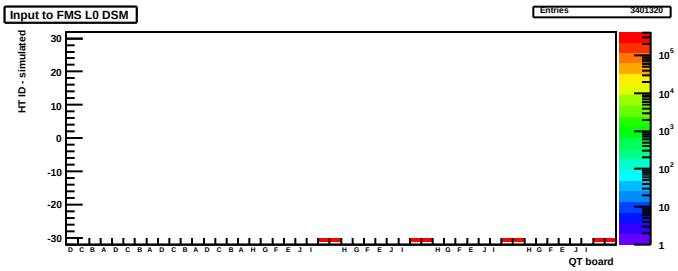

Input to FMS L0 DSM
Entries 2.040792e+07
QT8(3) sum
QT board

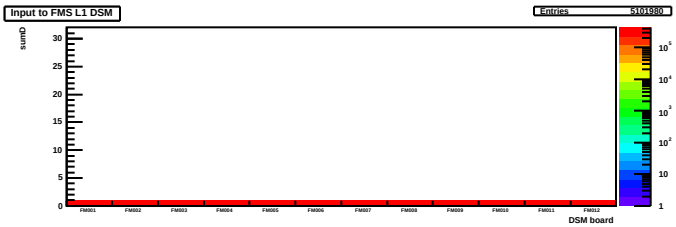

Input to FMS L0 DSM
Entries 3401320
QT8(3) sum - simulated
QT board

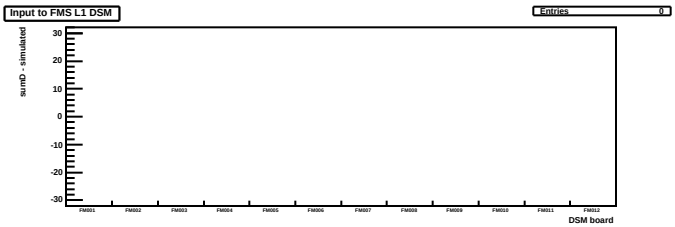

Input to FMS L0 DSM
Entries 2.040792e+07
HT ADC
QT board

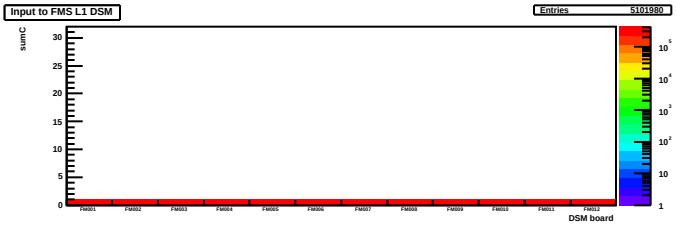

Input to FMS L0 DSM
Entries 3401320
HT ADC - simulated
QT board

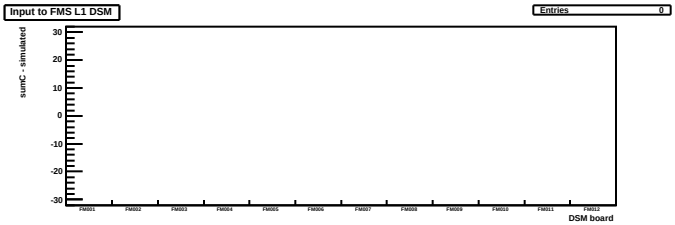

Input to FMS L0 DSM
Entries 2.040792e+07
HT ID
QT board

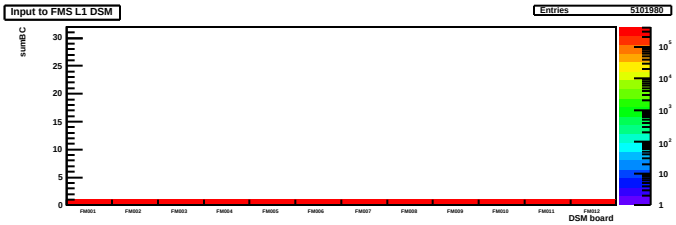

Input to FMS L0 DSM
Entries 3401320
HT ID - simulated
QT board

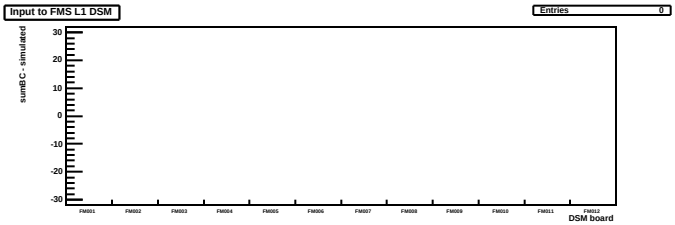

Input to FMS L1 DSM
Entries 5101980
sumD
DSM board

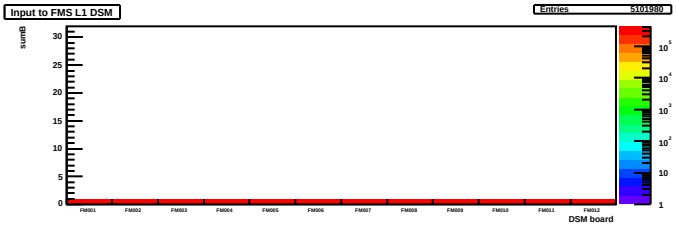

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

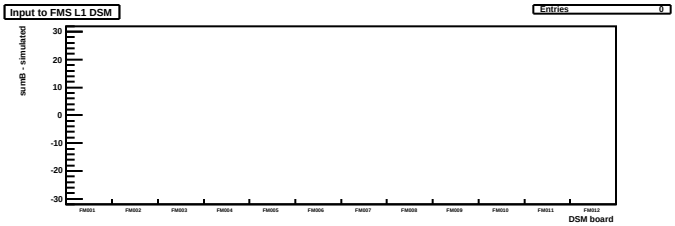

Input to FMS L1 DSM
Entries 5101980
sumC
DSM board

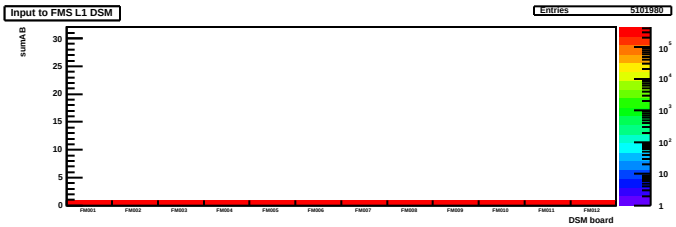

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

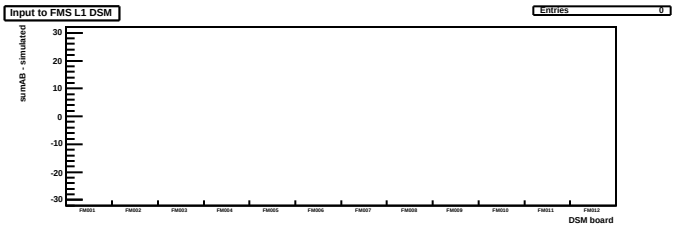

Input to FMS L1 DSM
Entries 5101980
sumBC
DSM board

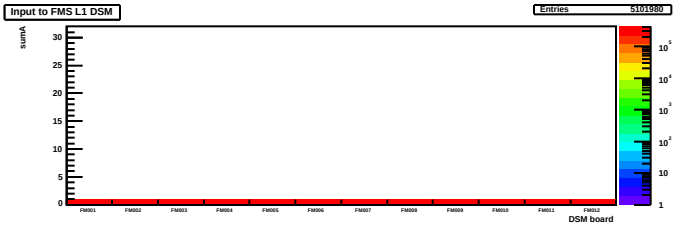

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

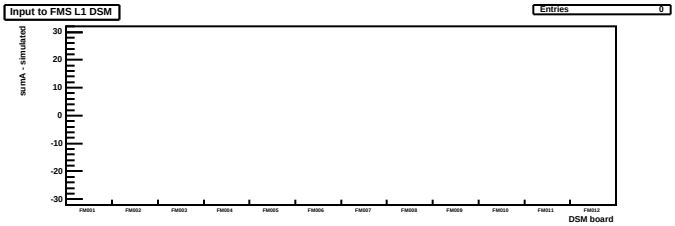

Input to FMS L1 DSM
Entries 5101980
sumB
DSM board

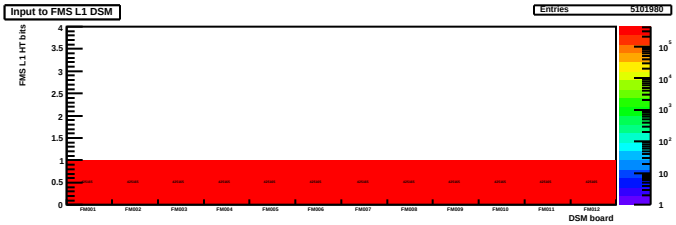

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

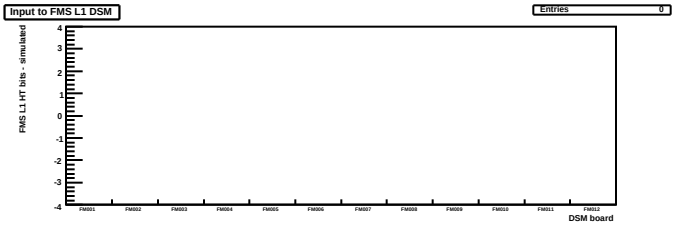

Input to FMS L1 DSM
Entries 5101980
sumAB
DSM board

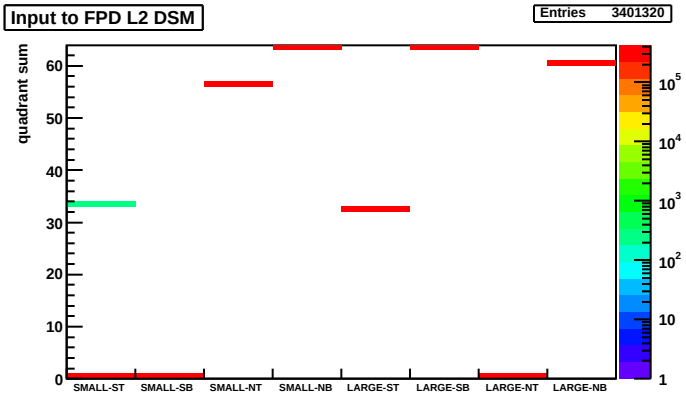

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

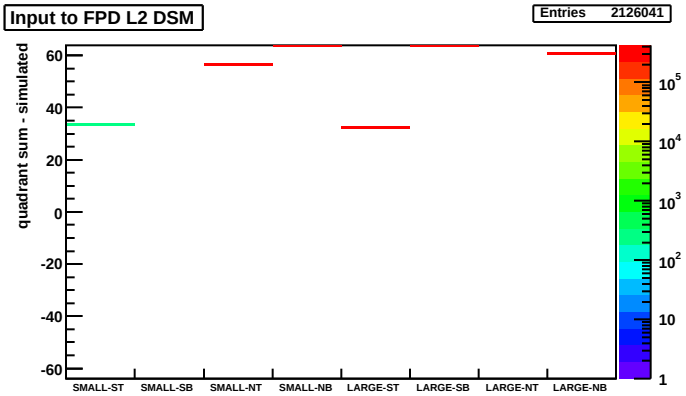

Input to FMS L1 DSM
Entries 5101980
sumA
DSM board

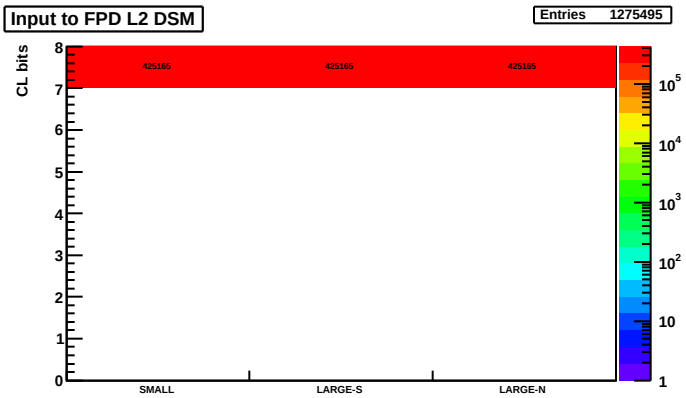

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

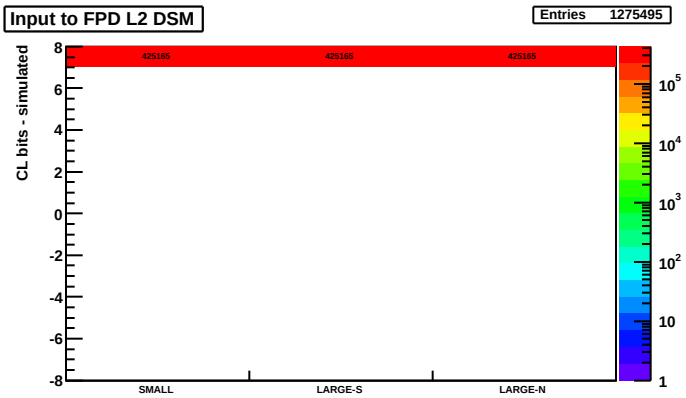

Input to FMS L1 DSM
Entries 5101980
FMS L1 HT bits
DSM board

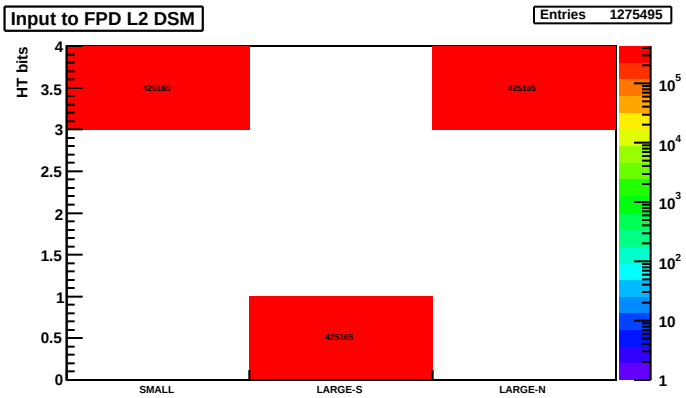

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

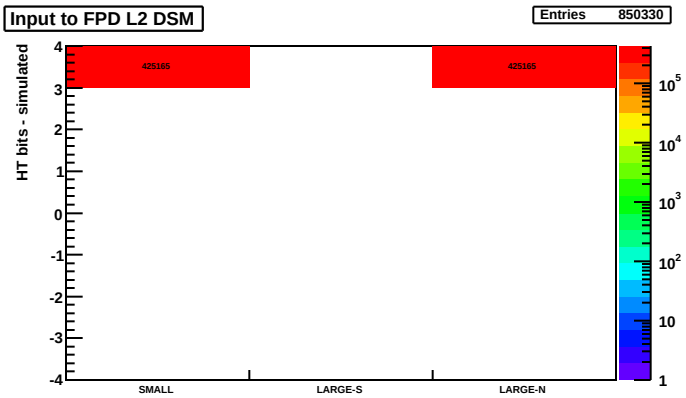

Input to FPD L2 DSM
Entries 3401320
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

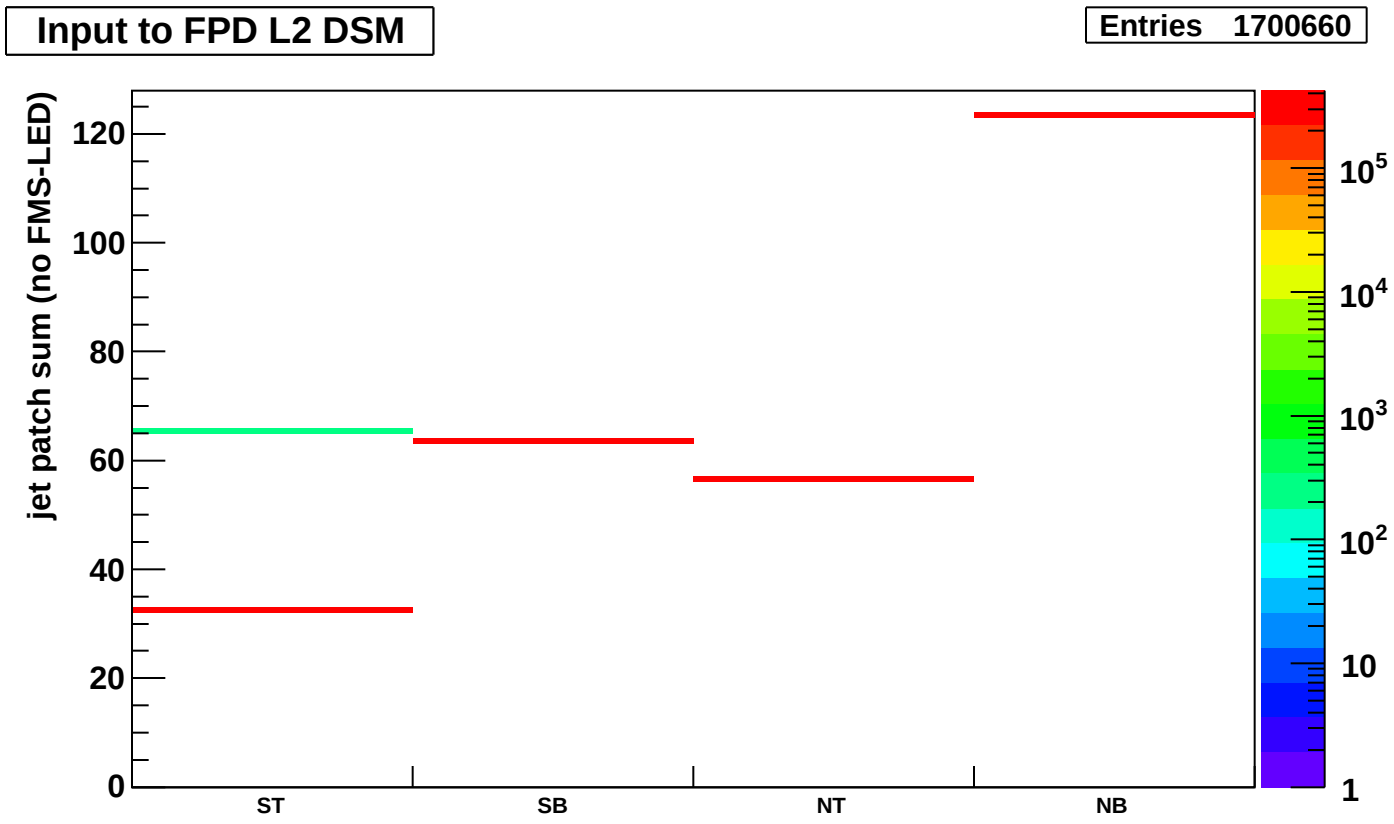

Input to FPD L2 DSM
Entries 2126041
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

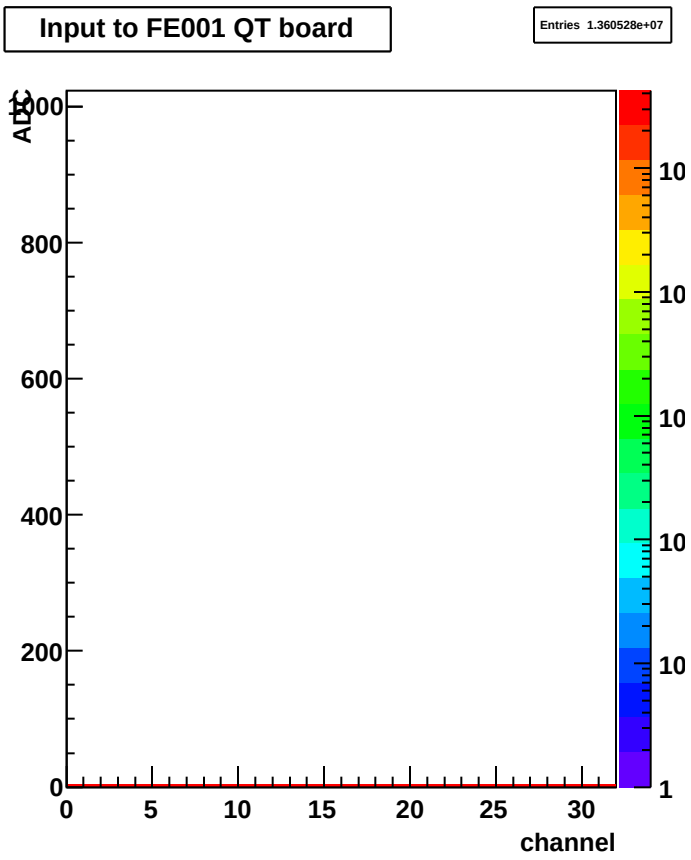

Input to FPD L2 DSM
Entries 1275495
CL bits
425165
425165
425165
SMALL
LARGE-S
LARGE-N

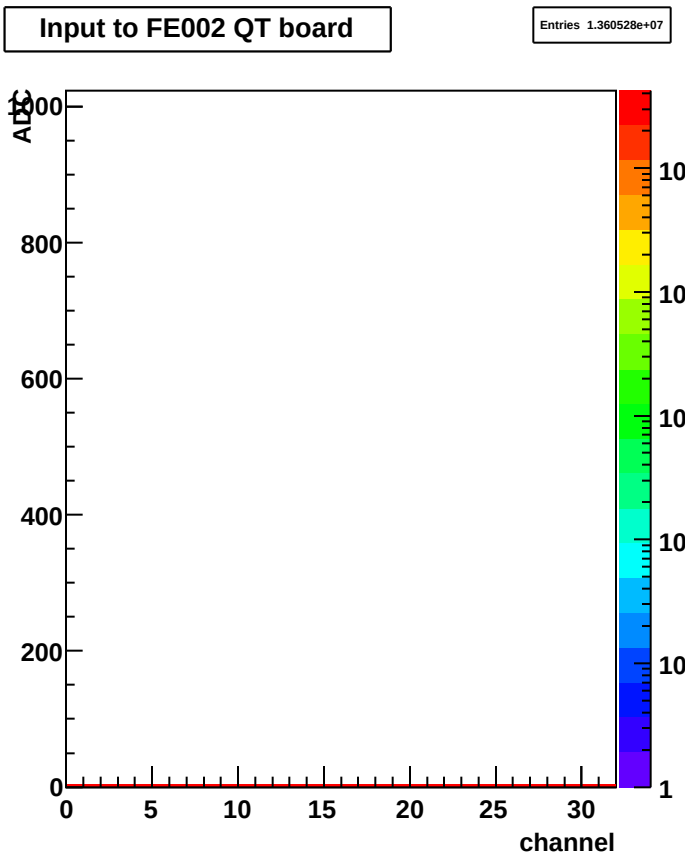

Input to FPD L2 DSM
Entries 1275495
CL bits - simulated
425165
425165
425165
SMALL
LARGE-S
LARGE-N

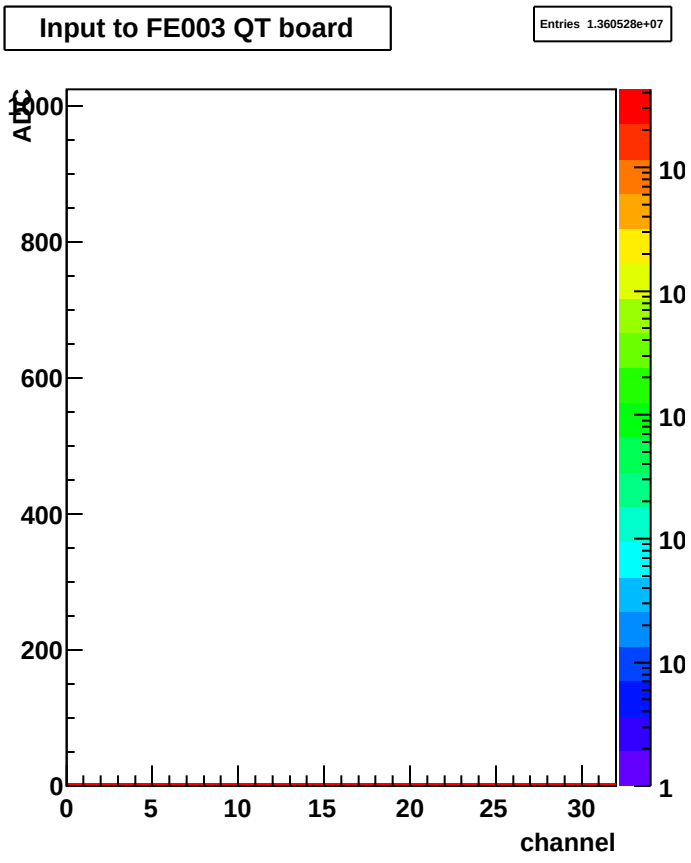

Input to FPD L2 DSM
Entries 1275495
HT bits
425165
425165
425165
SMALL
LARGE-S
LARGE-N

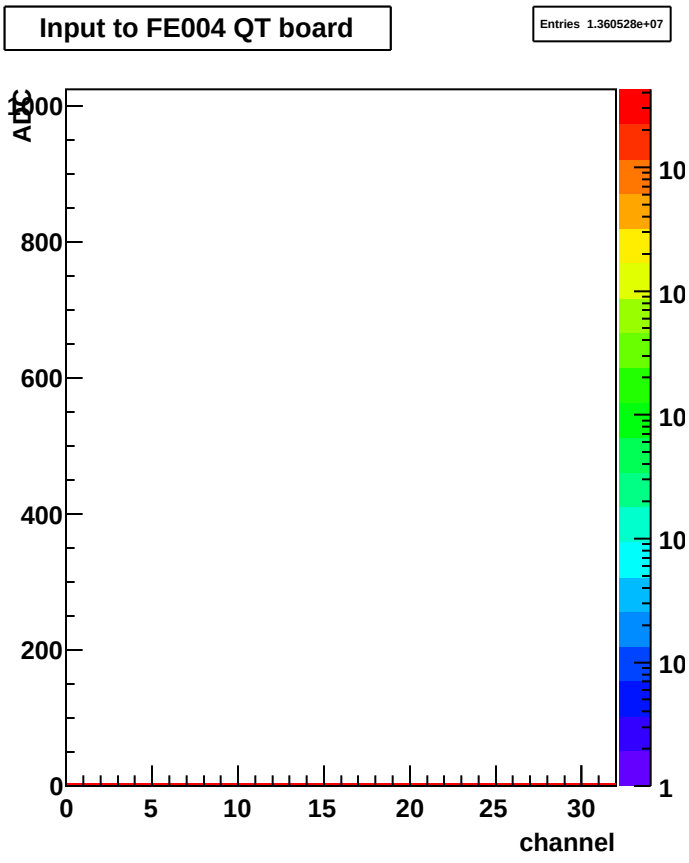

Input to FPD L2 DSM
Entries 850330
HT bits - simulated
425165
425165
SMALL
LARGE-S
LARGE-N

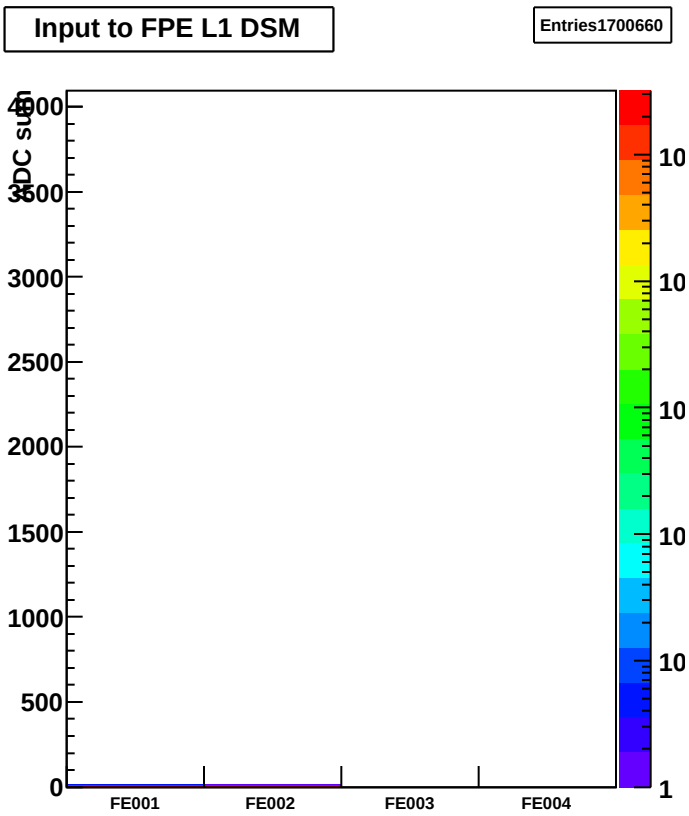

Input to FPD L2 DSM
Entries 1700660
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

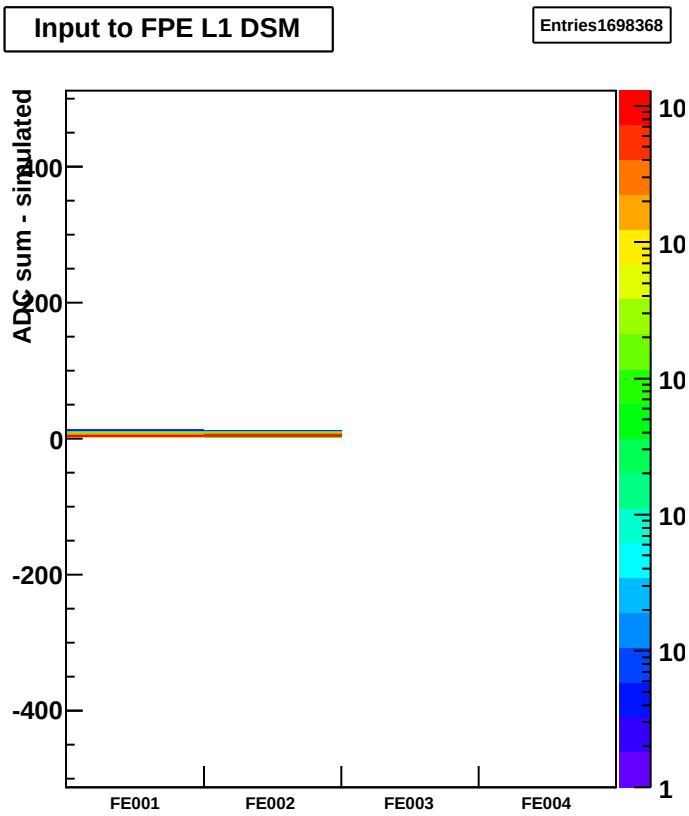

Input to FE001 QT board
Entries 1.360528e+07
ADC
channel

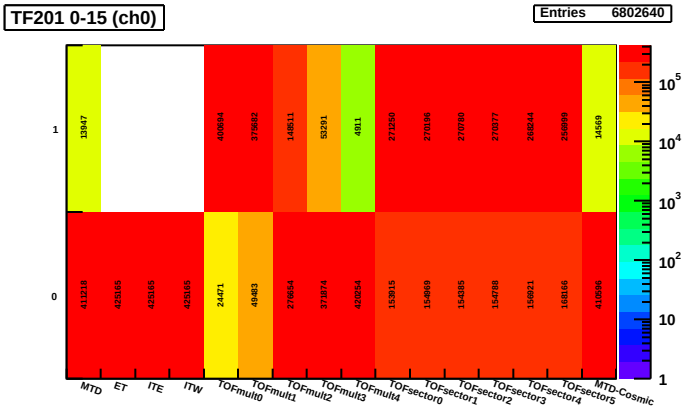

Input to FE002 QT board
Entries 1.360528e+07
ADC
channel

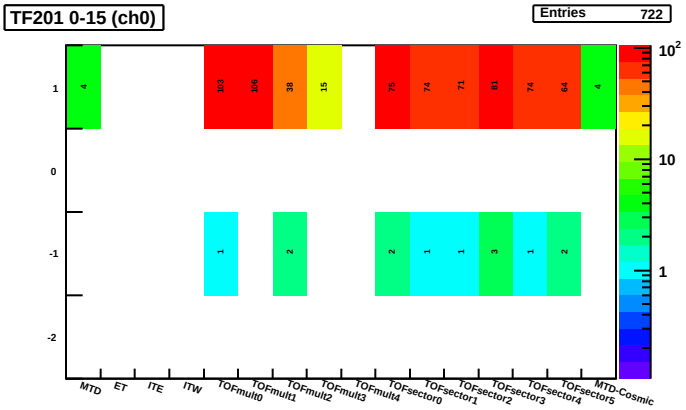

Input to FE003 QT board
Entries 1.360528e+07
ADC
channel

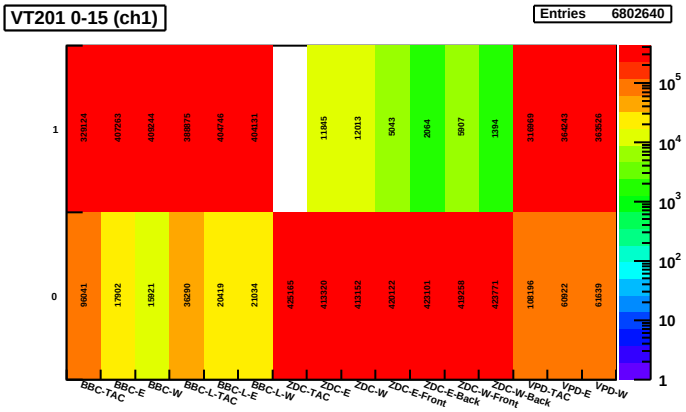

Input to FE004 QT board
Entries 1.360528e+07
ADC
channel

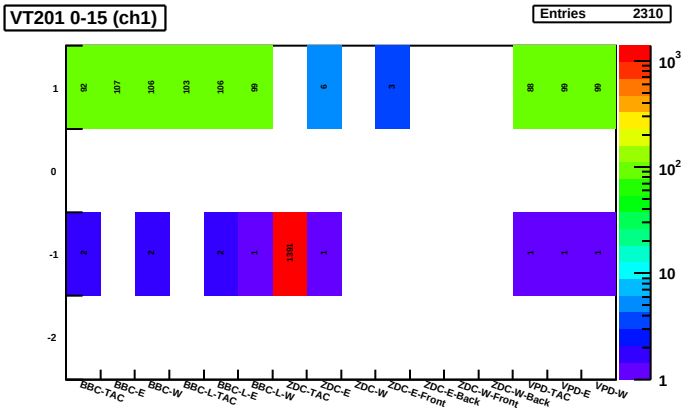

Input to FPE L1 DSM
Entries1700660
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

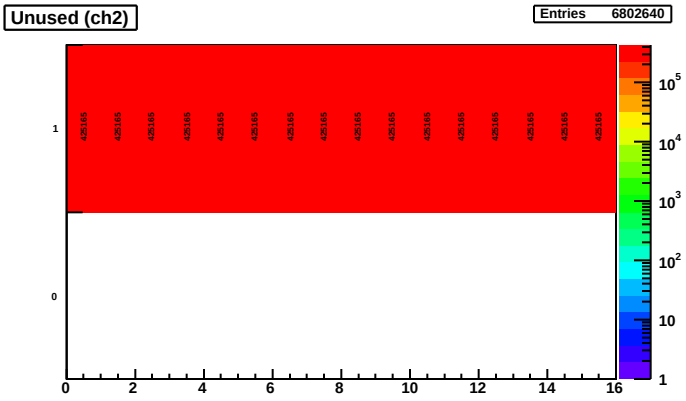

Input to FPE L1 DSM
Entries1698368
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1

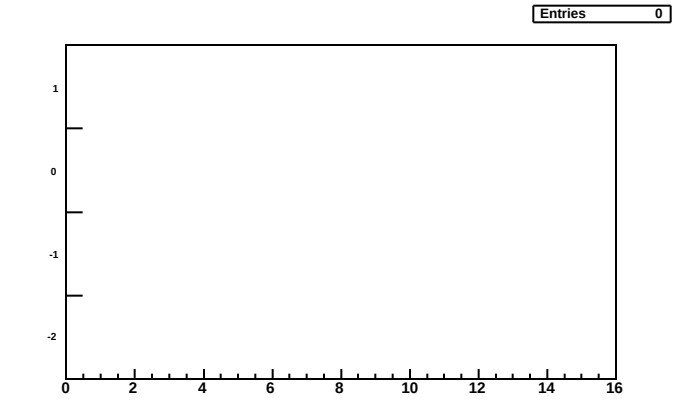

TF201 0-15 (ch0)
Entries 6802640
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

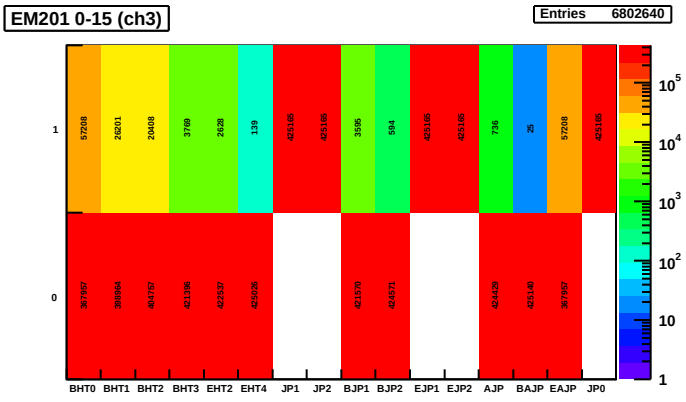

TF201 0-15 (ch0)
Entries 722
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

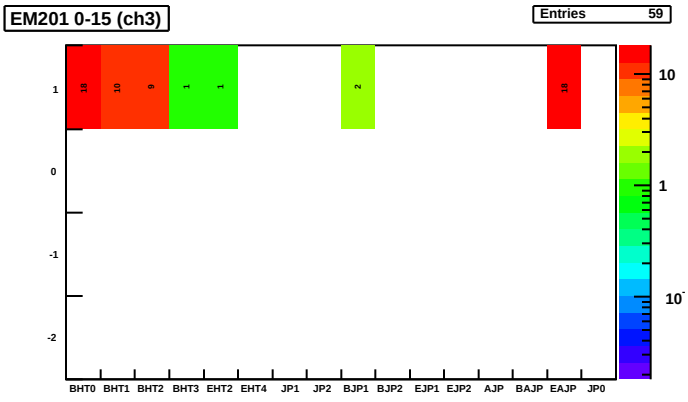

VT201 0-15 (ch1)
Entries 6802640
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2310
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

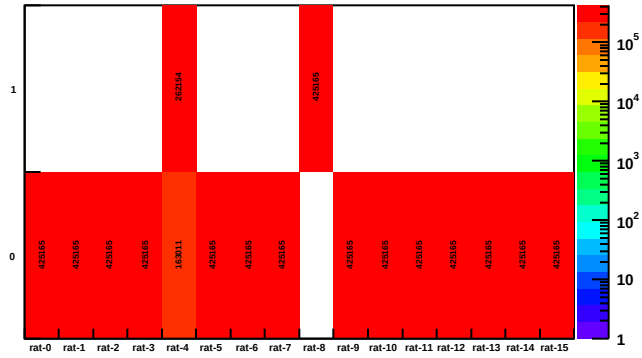

Unused (ch2)
Entries 6802640

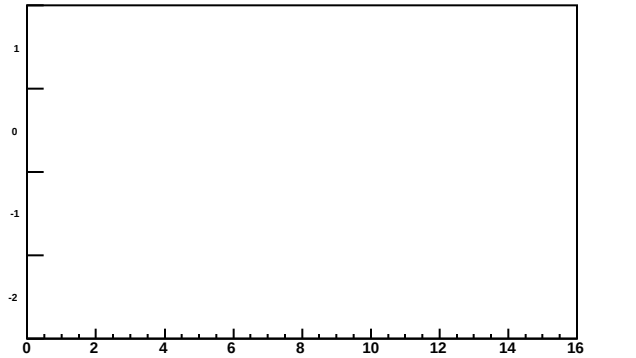

Entries 0


EM201 0-15 (ch3)
Entries 6802640
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

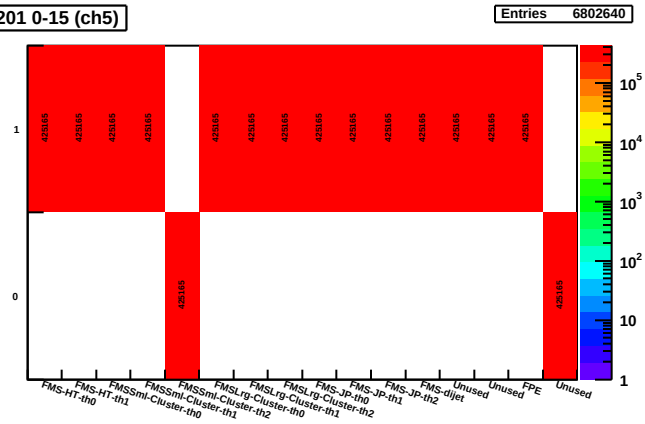

EM201 0-15 (ch3)
Entries 59
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

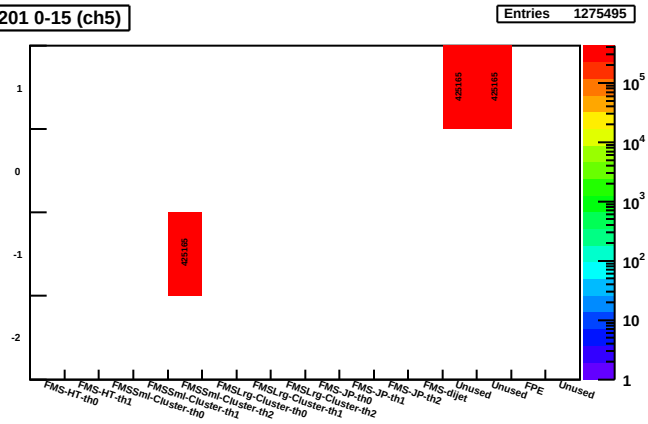




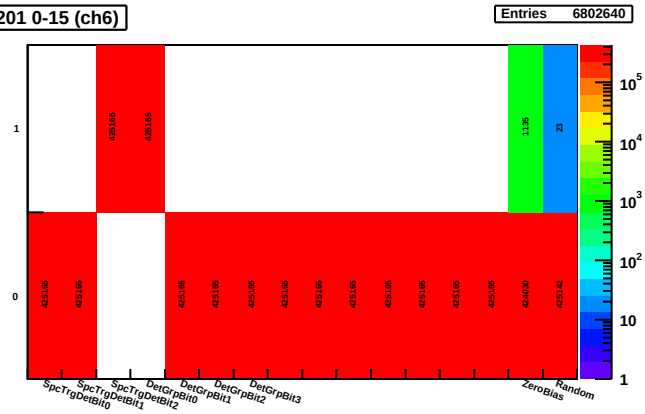
FP201 0-15 (ch5)

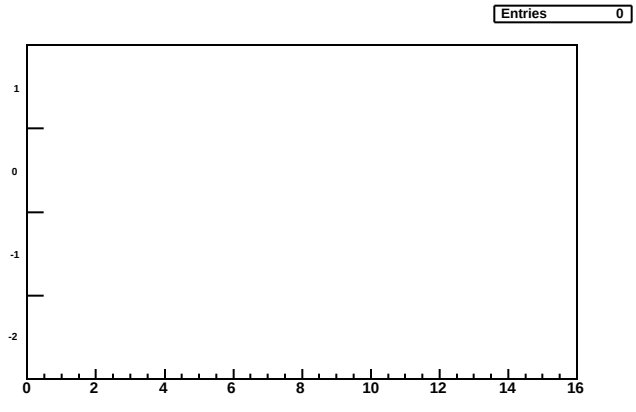
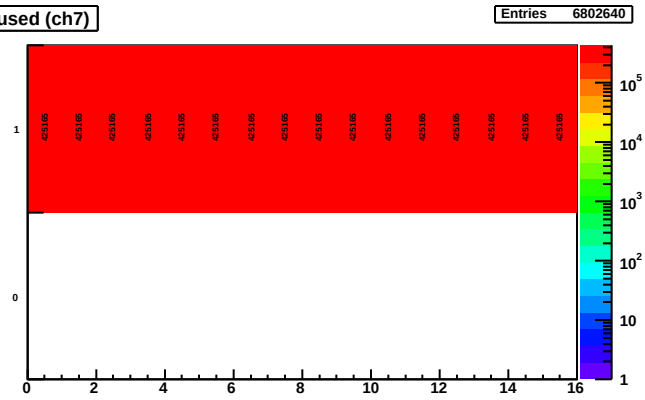
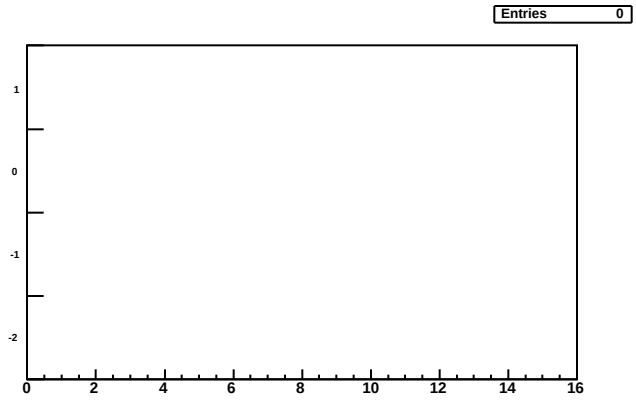


FP201 0-15 (ch5)



ST201 0-15 (ch6)

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