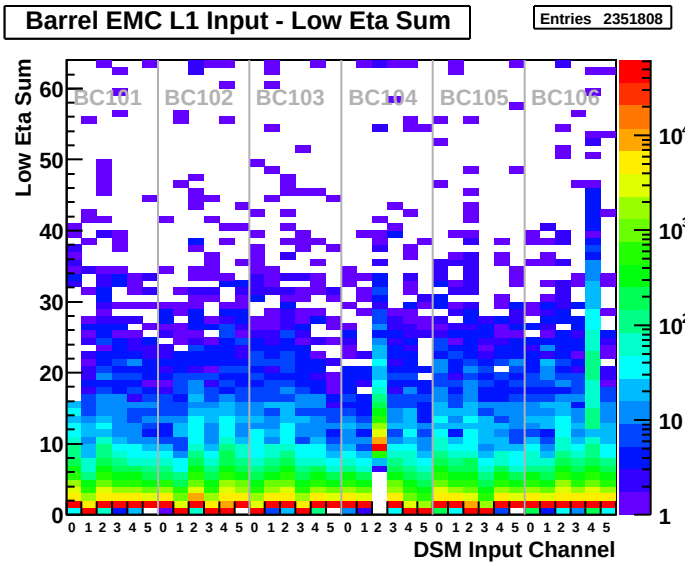
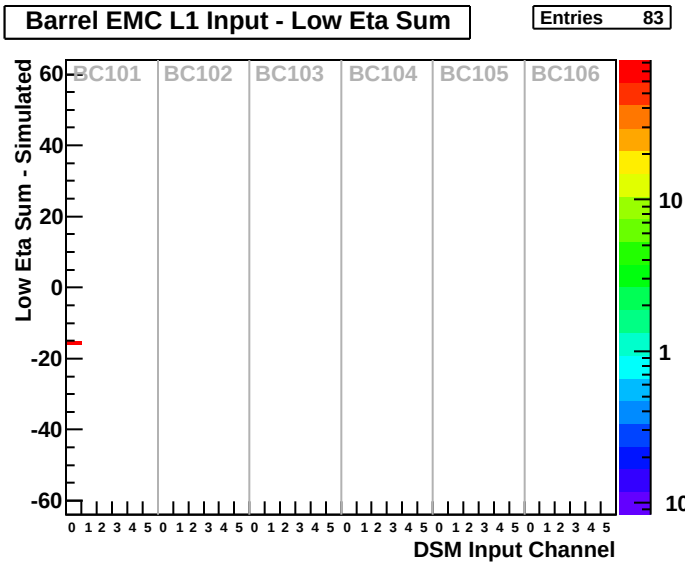
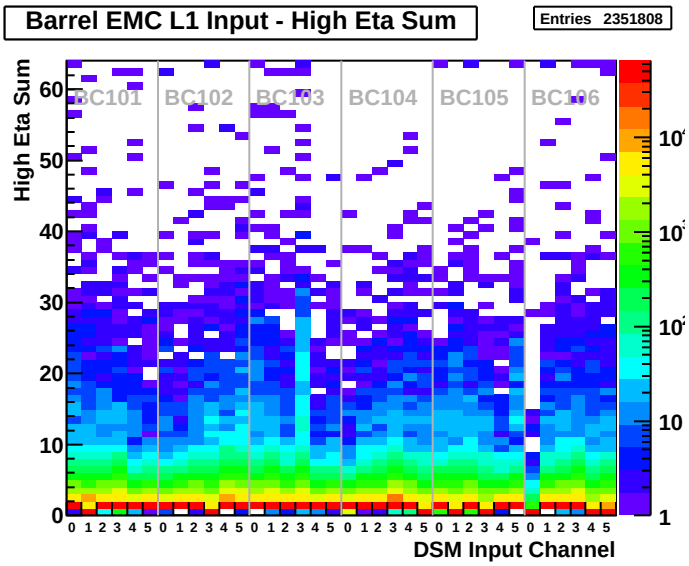
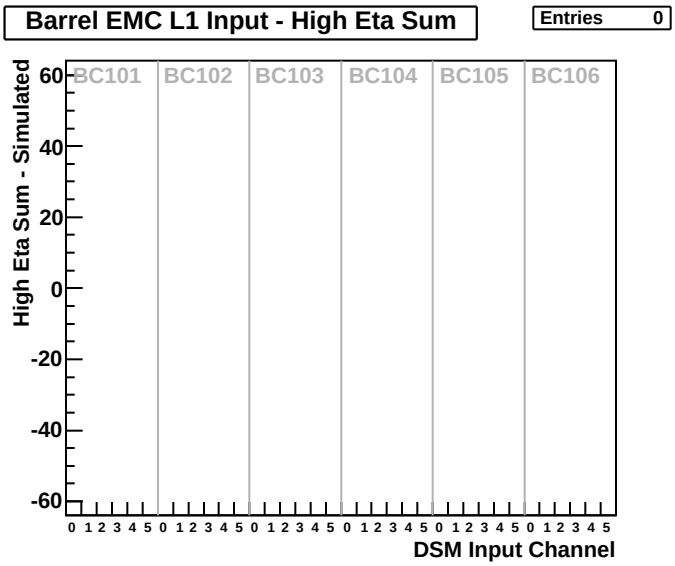
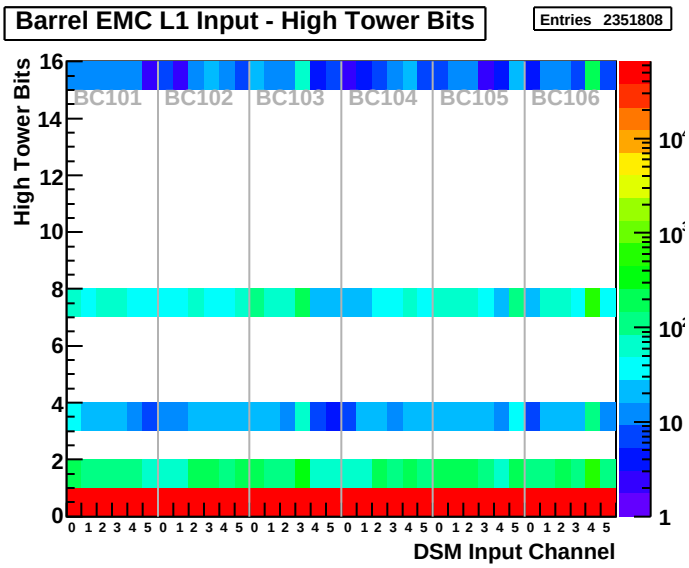
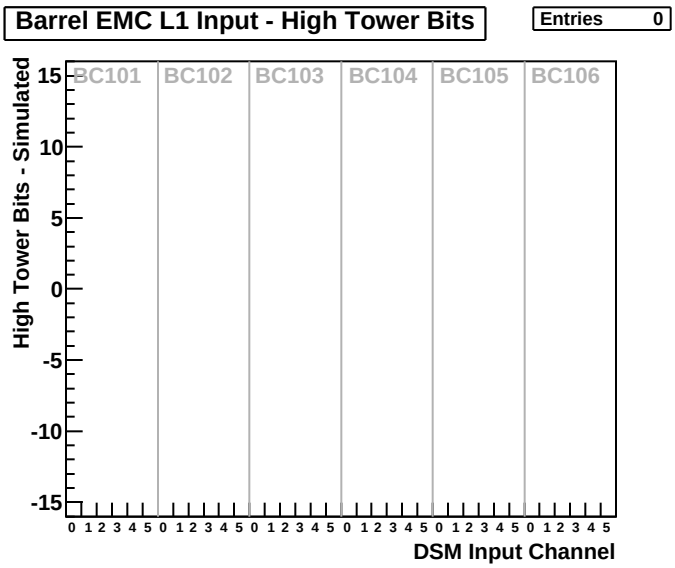

Barrel EMC L1 Input - Low Eta Sum
Entries 2351808
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

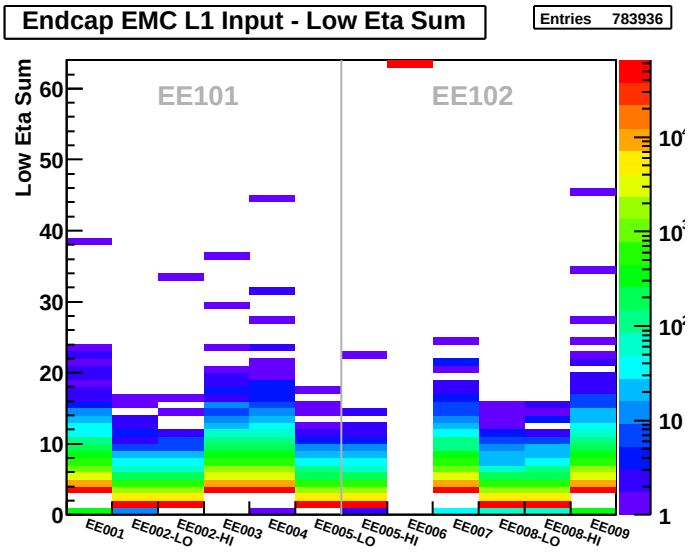

Barrel EMC L1 Input - Low Eta Sum
Entries 83
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

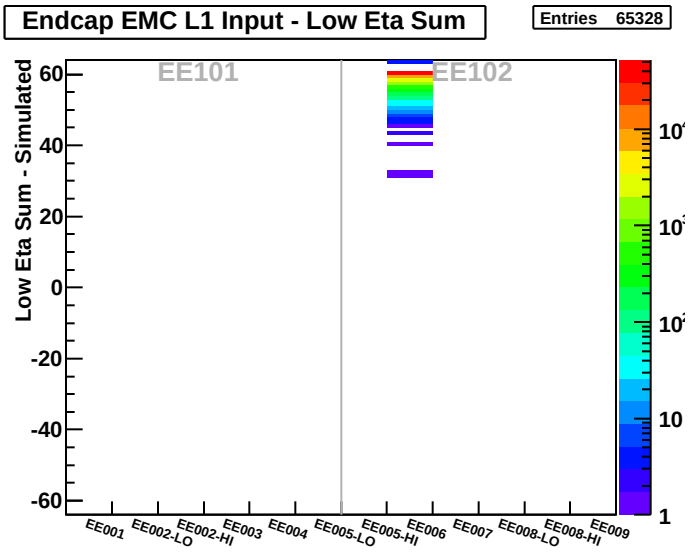

Barrel EMC L1 Input - High Eta Sum
Entries 2351808
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

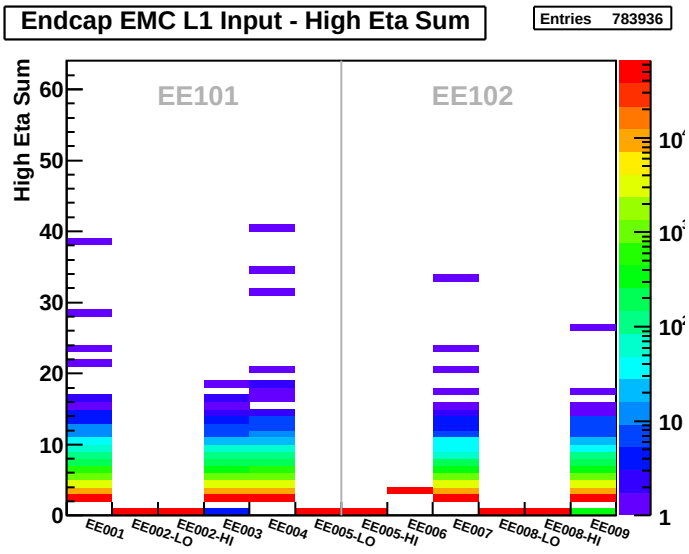

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

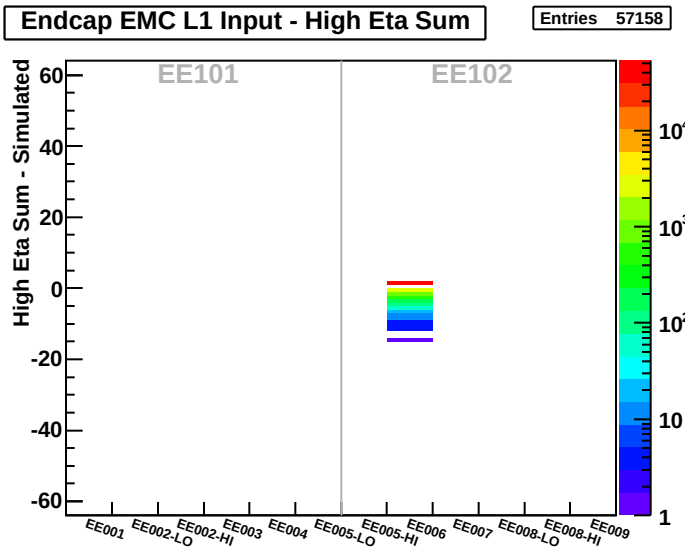

Barrel EMC L1 Input - High Tower Bits
Entries 2351808
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

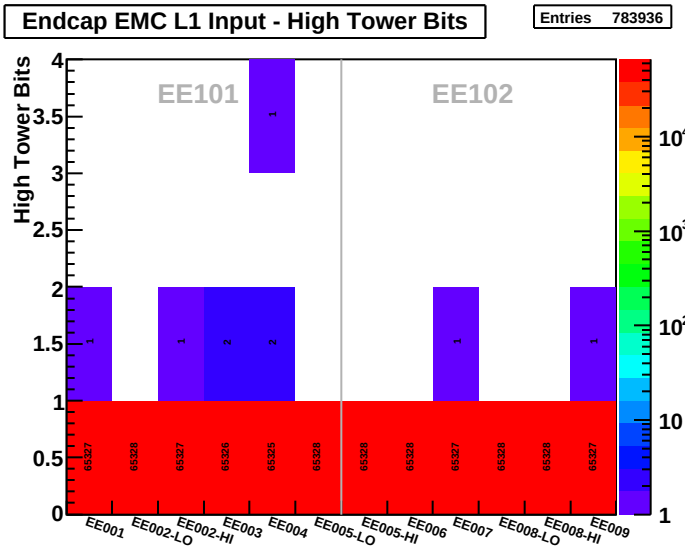

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

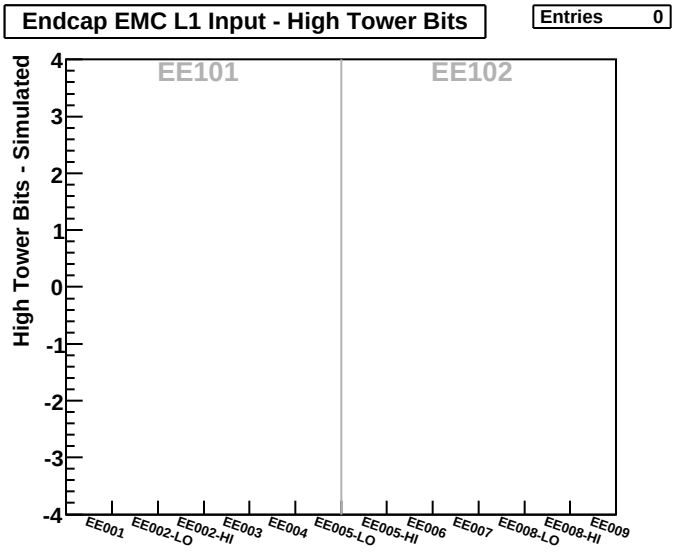

Endcap EMC L1 Input - Low Eta Sum
Entries 783936
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

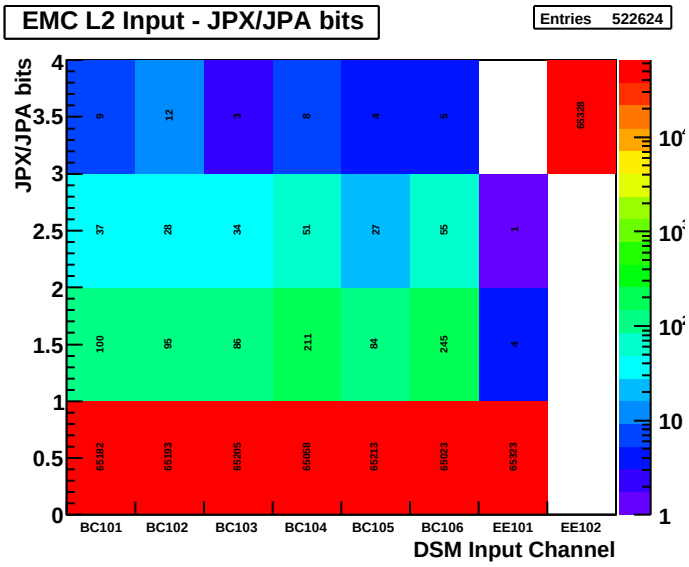

Endcap EMC L1 Input - Low Eta Sum
Entries 65328
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

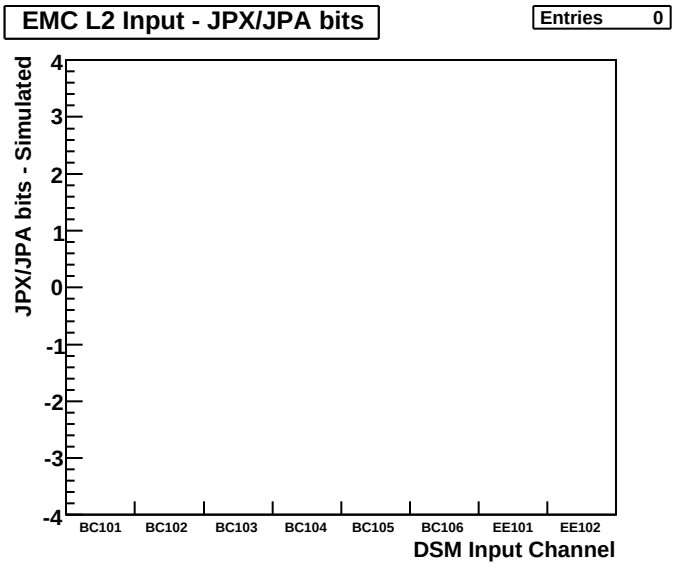

Endcap EMC L1 Input - High Eta Sum
Entries 783936
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

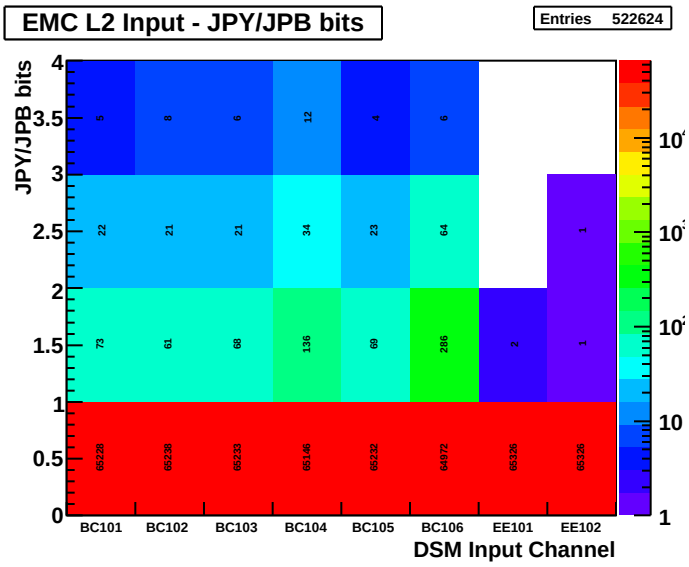

Endcap EMC L1 Input - High Eta Sum
Entries 57158
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

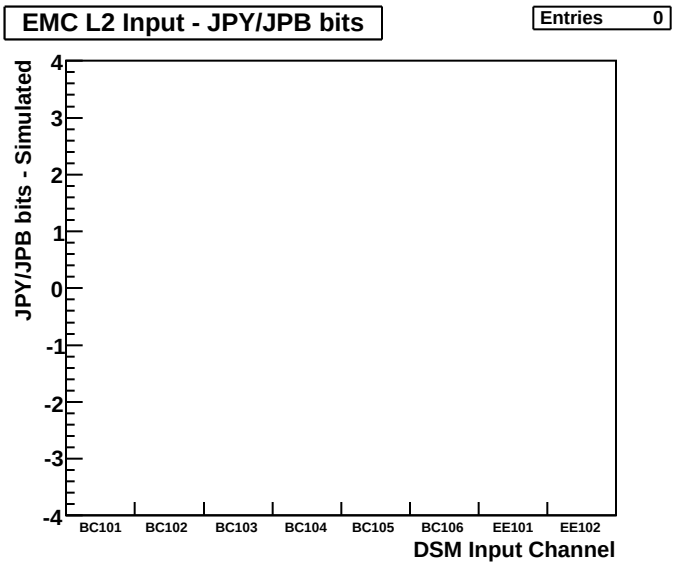

Endcap EMC L1 Input - High Tower Bits
Entries 783936
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

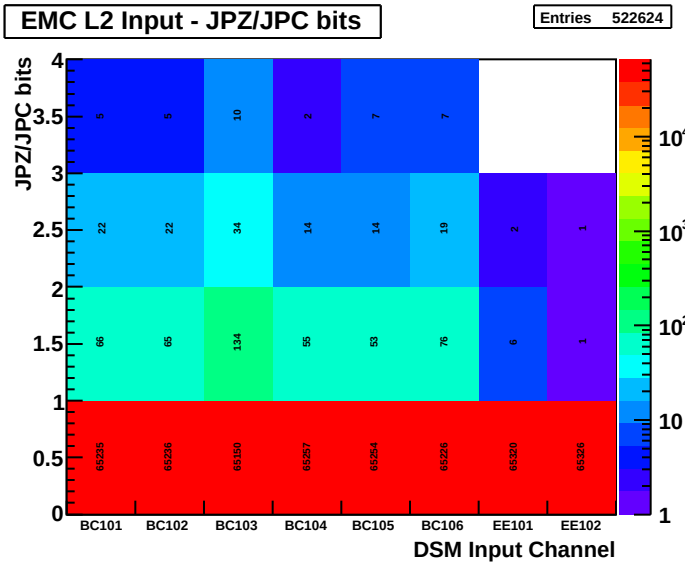

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

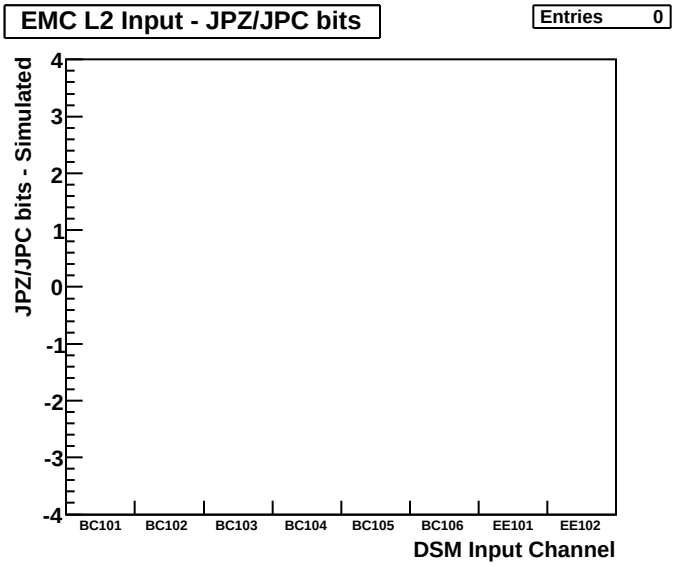

EMC L2 Input - JPX/JPA bits
Entries 522624
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

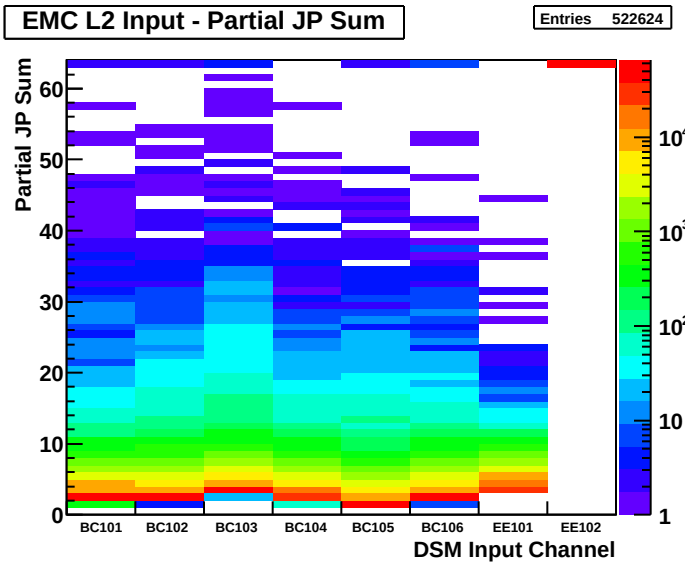

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

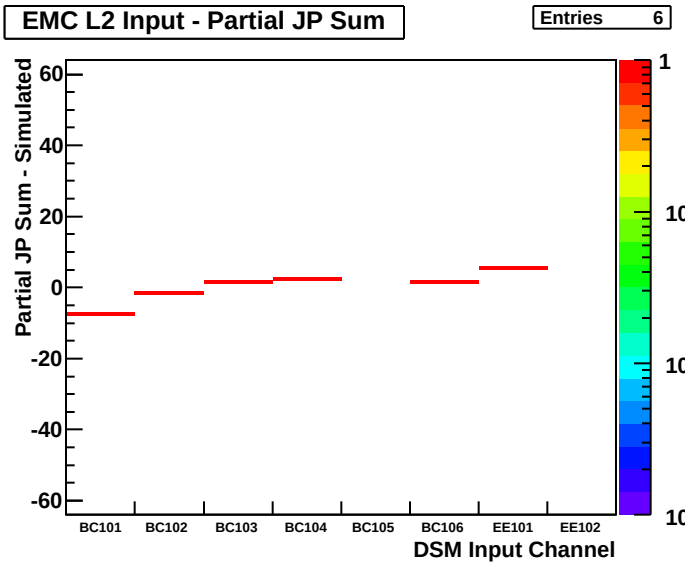

EMC L2 Input - JPY/JPB bits
Entries 522624
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

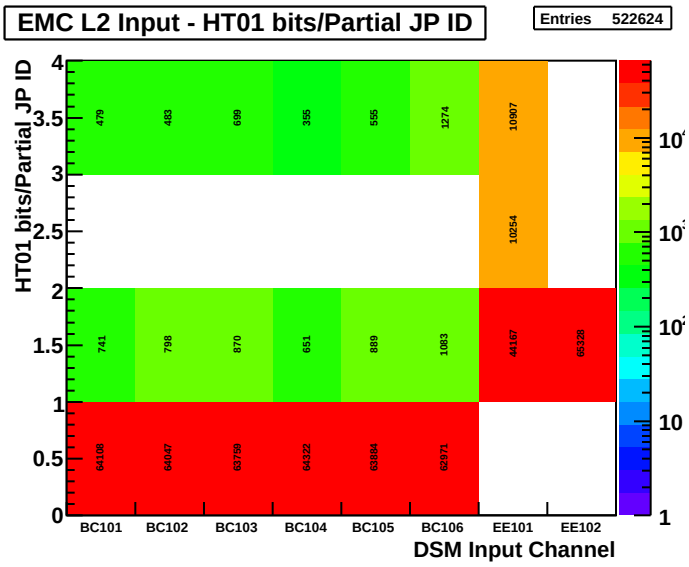

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

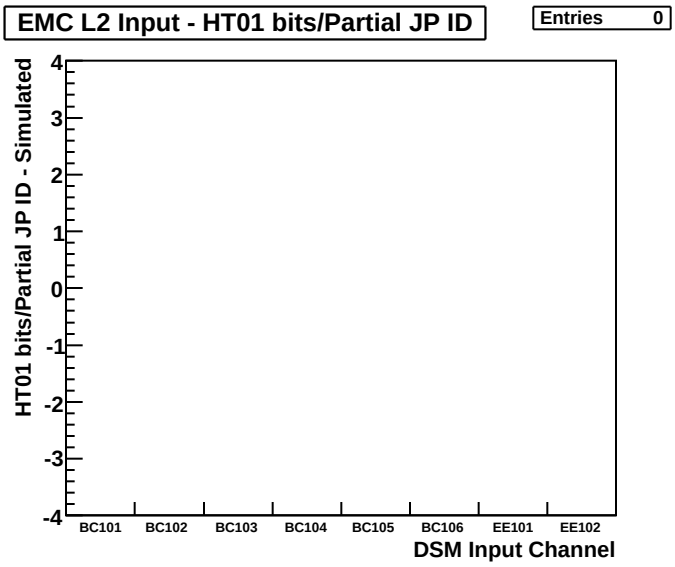

EMC L2 Input - JPZ/JPC bits
Entries 522624
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

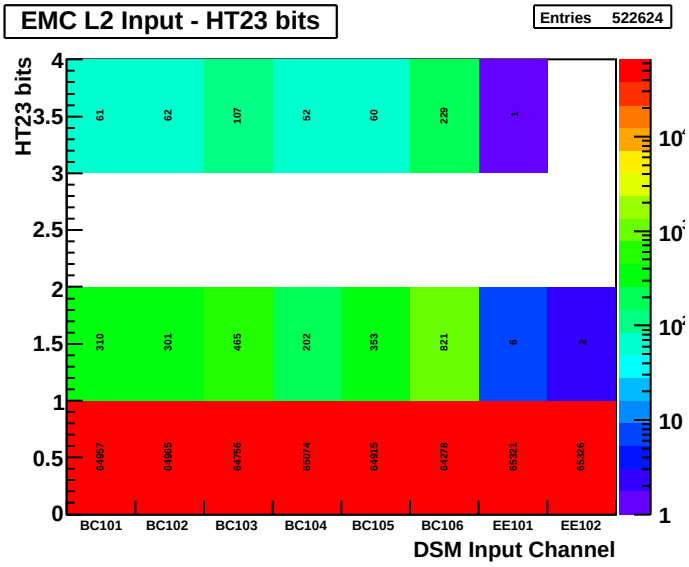

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

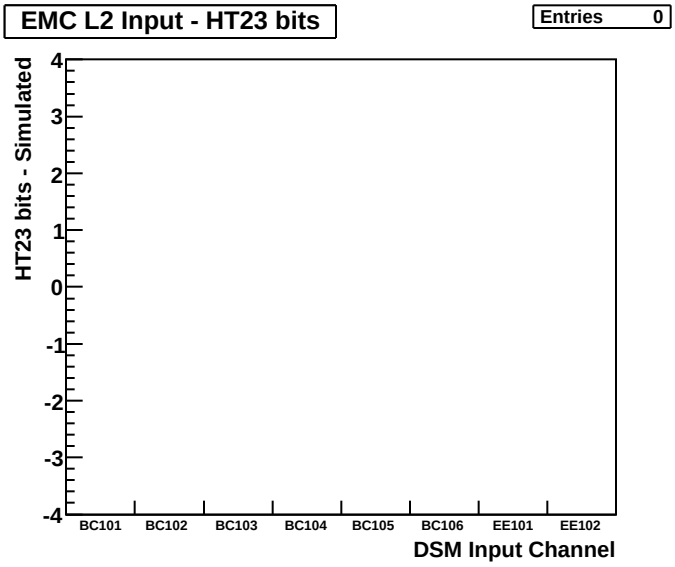

EMC L2 Input - Partial JP Sum
Entries 522624
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

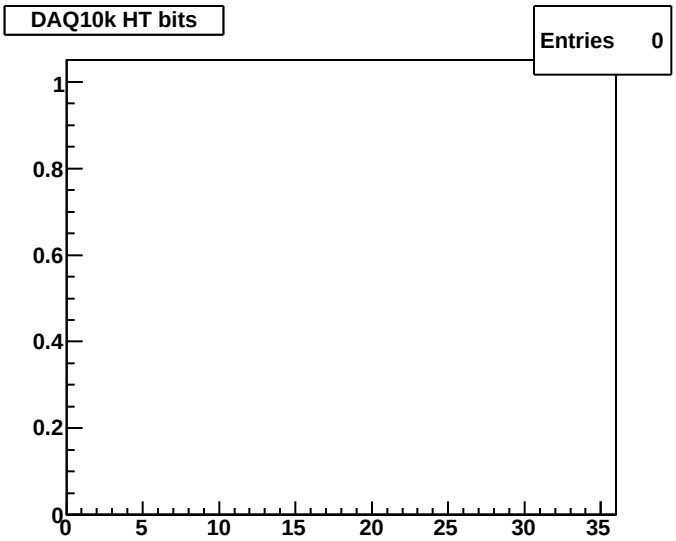

EMC L2 Input - Partial JP Sum
Entries 6
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

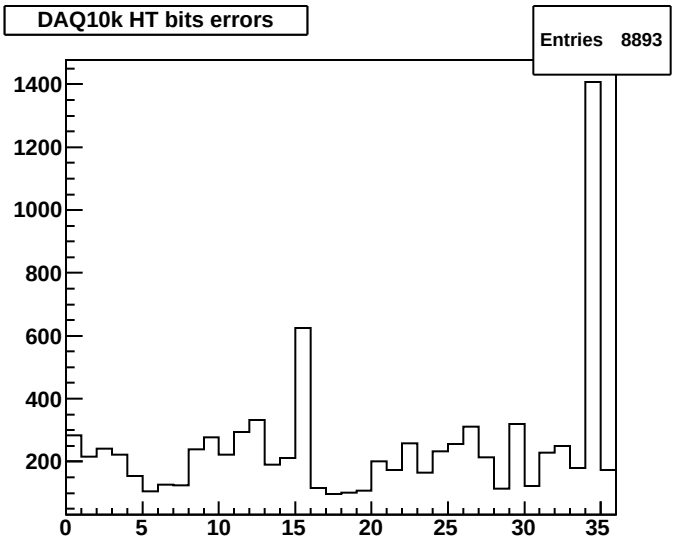

EMC L2 Input - HT01 bits/Partial JP ID
Entries 522624
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

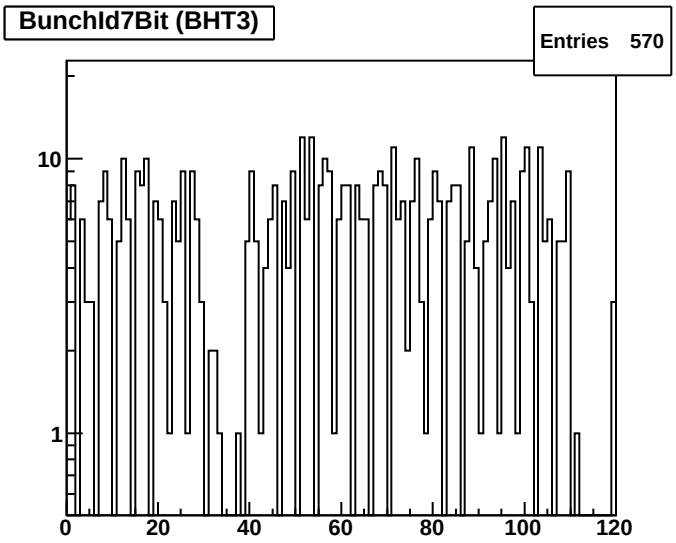

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

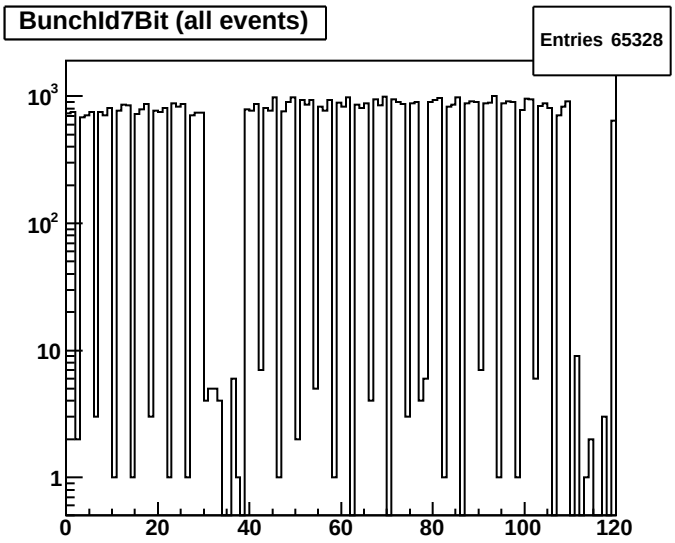

EMC L2 Input - HT23 bits
Entries 522624
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

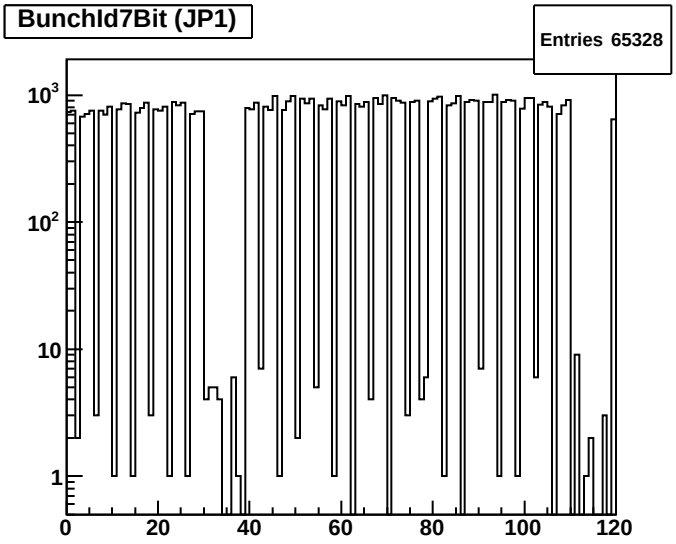

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

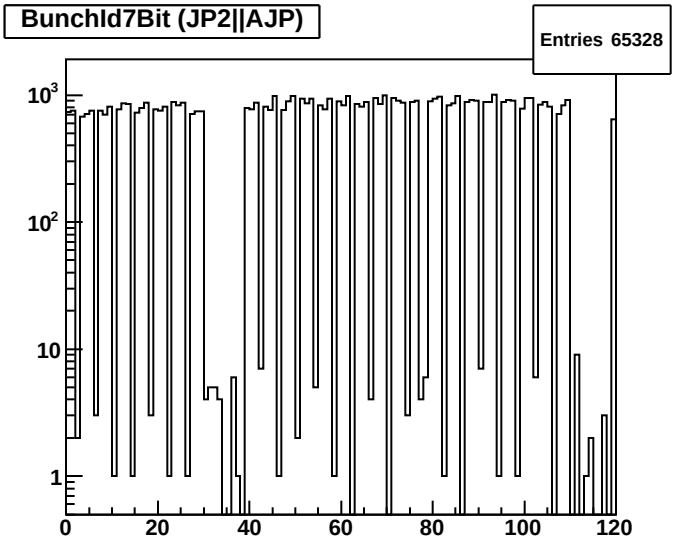

DAQ10k HT bits
Entries 0

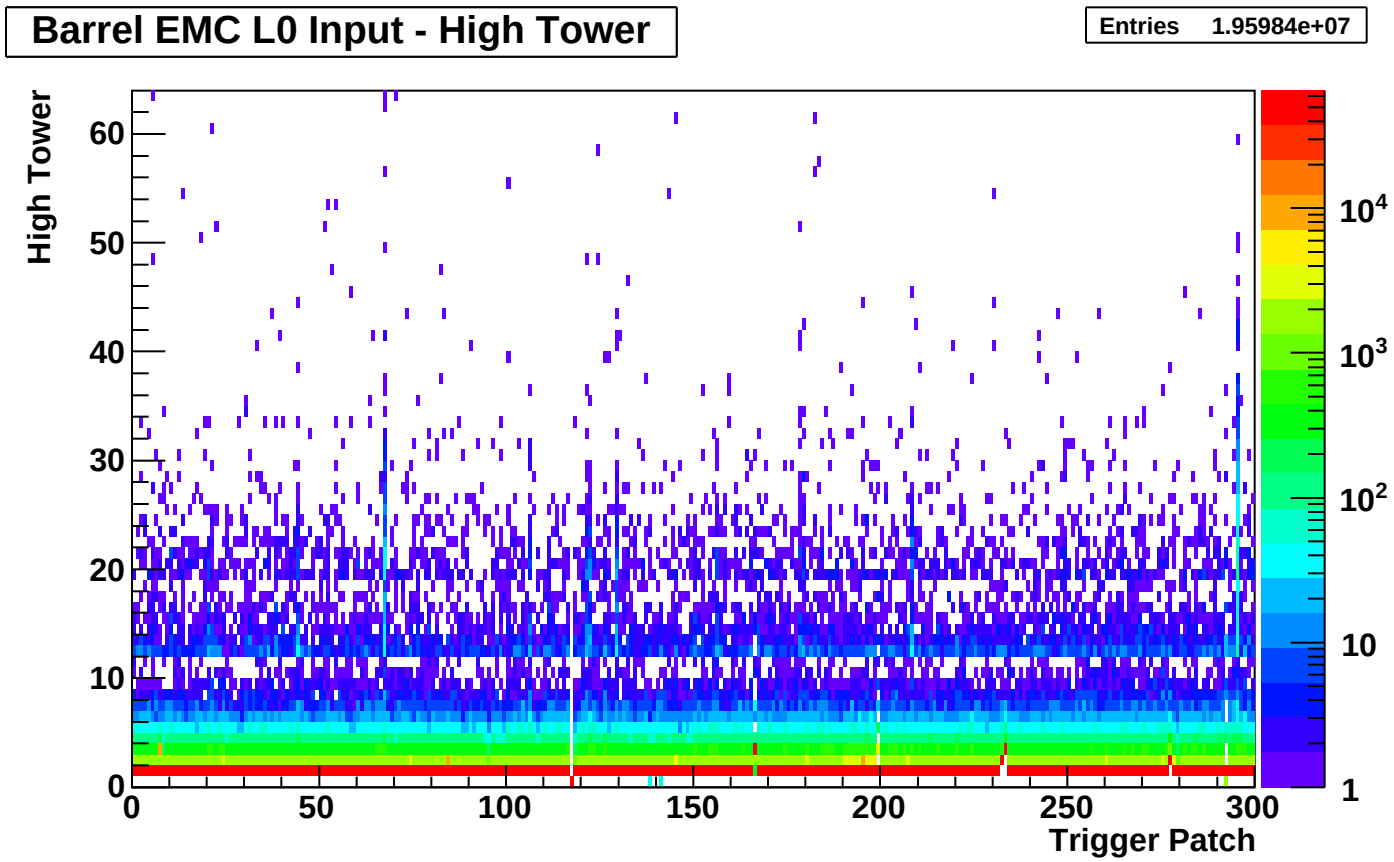

DAQ10k HT bits errors
Entries 8893

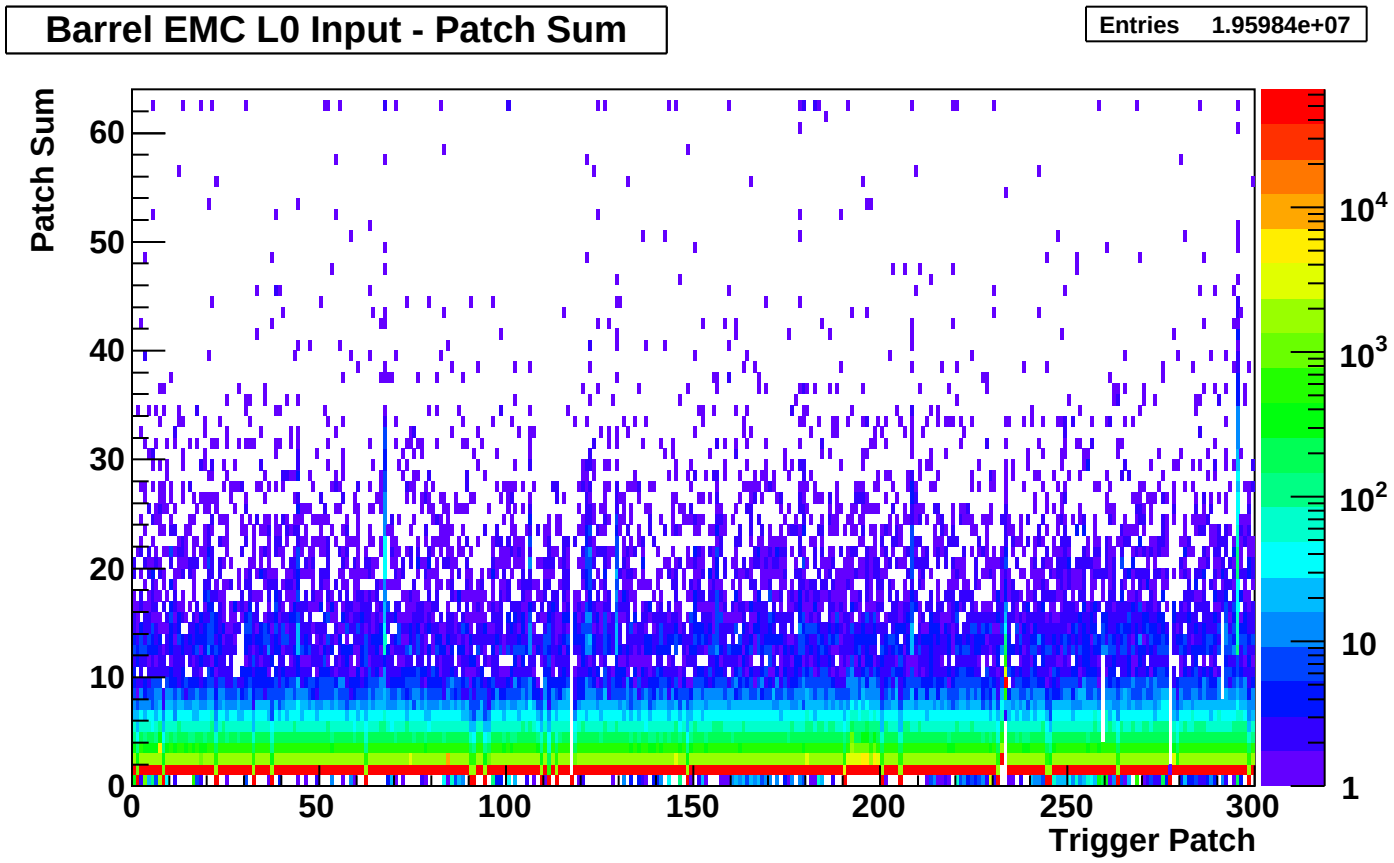

BunchId7Bit (BHT3)
Entries 570

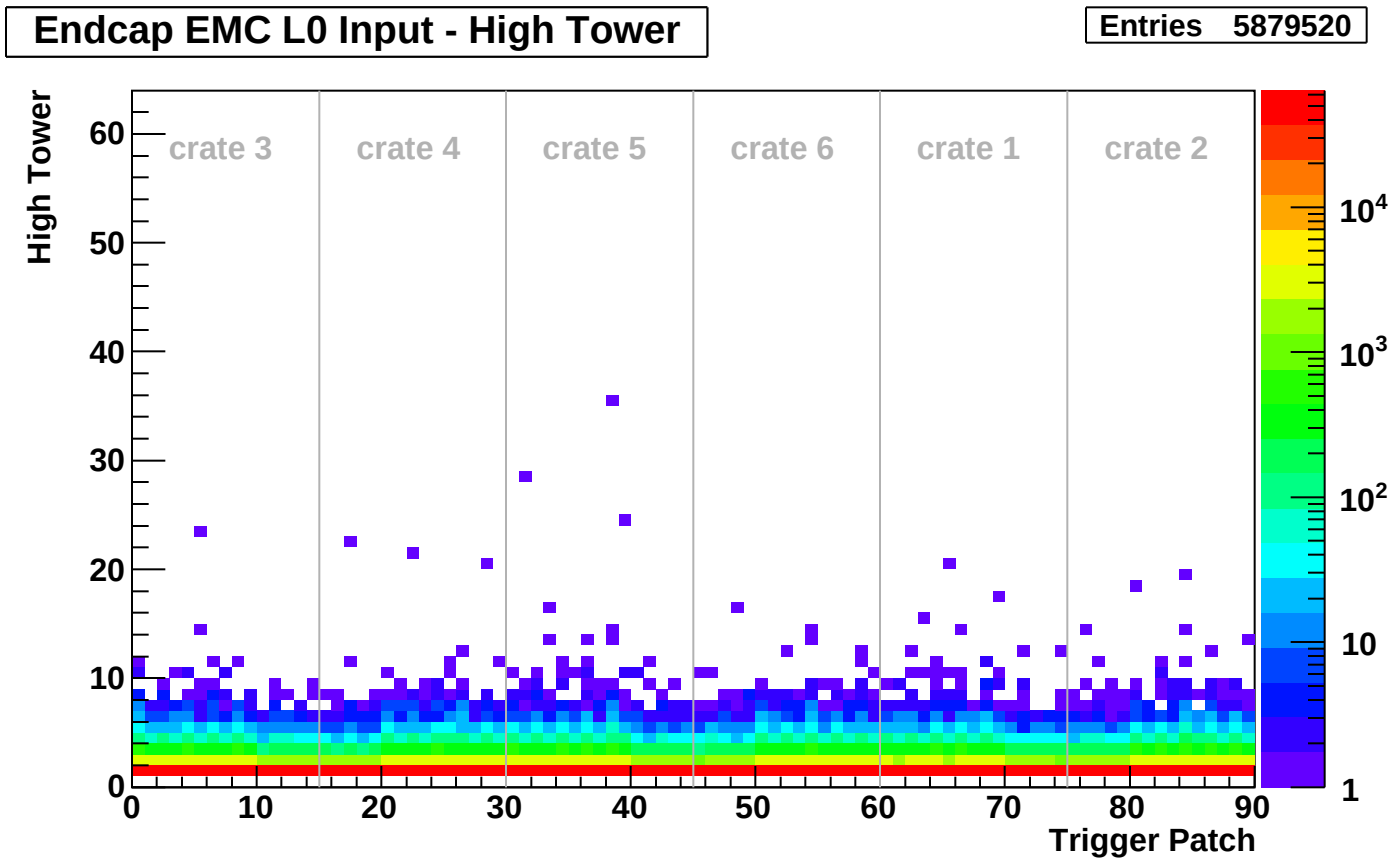

BunchId7Bit (all events)
Entries 65328

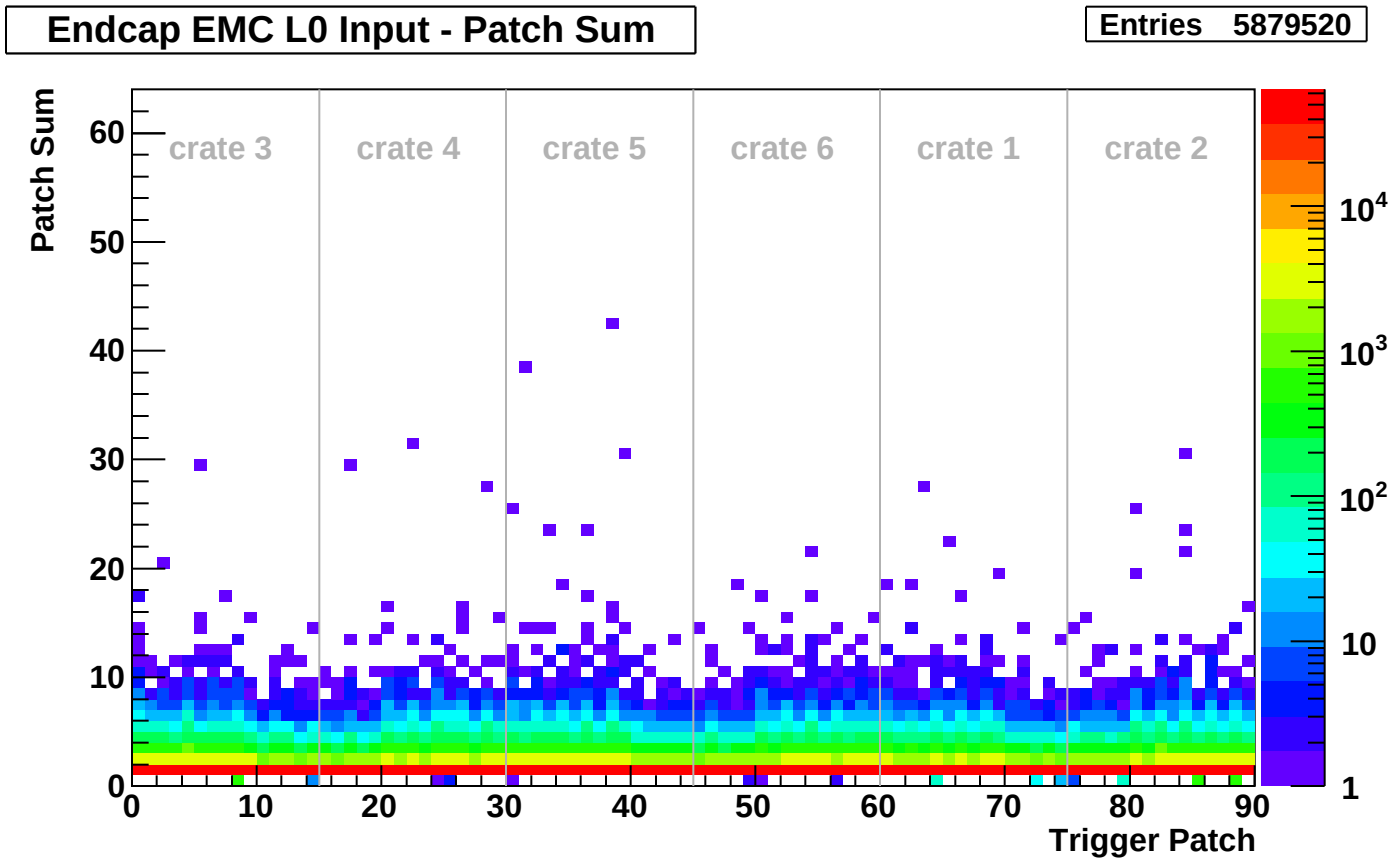

BunchId7Bit (JP1)
Entries 65328

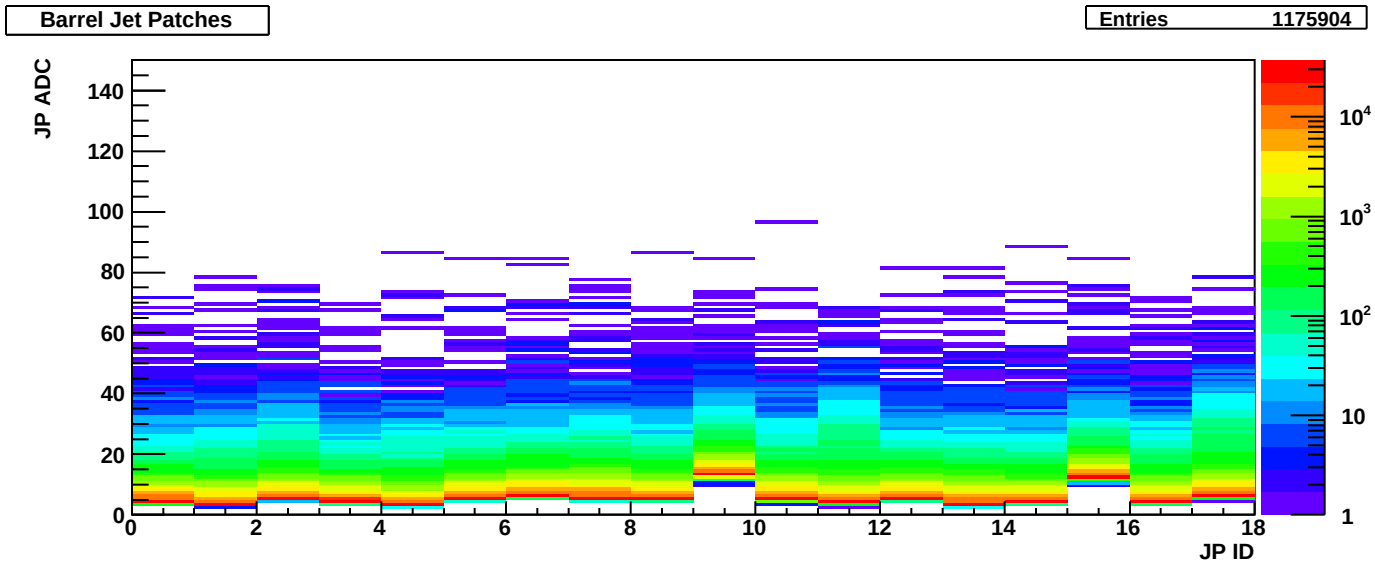

BunchId7Bit (JP2||AJP)
Entries 65328

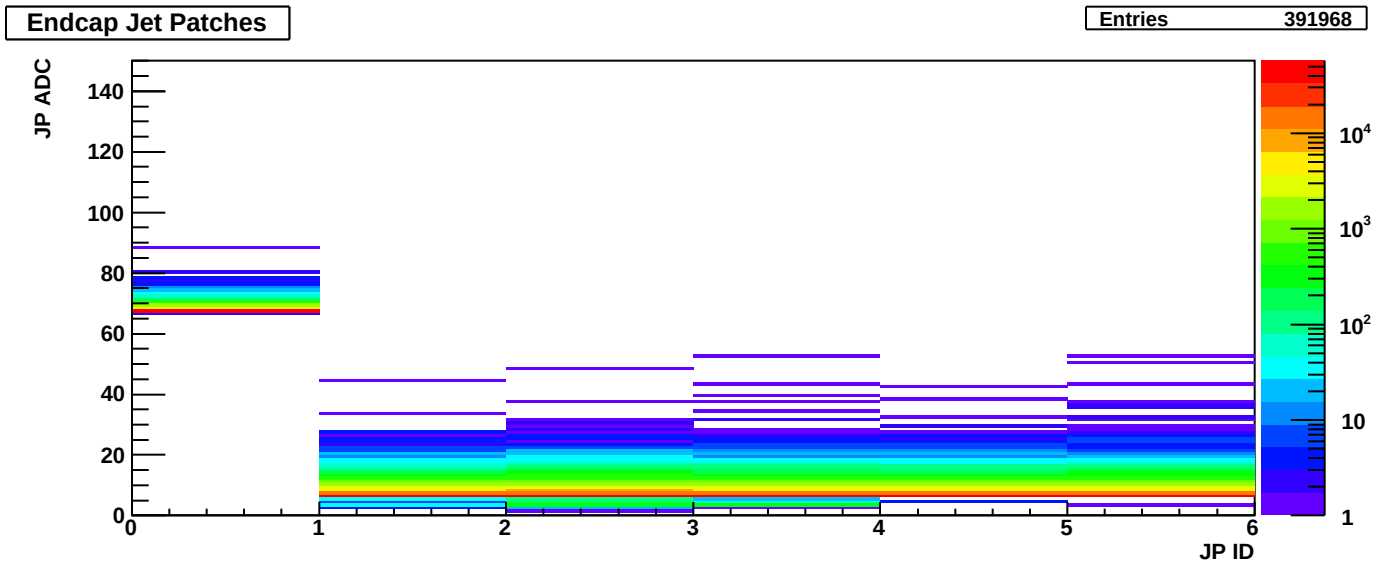

Barrel EMC L0 Input - High Tower
Entries 1.95984e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

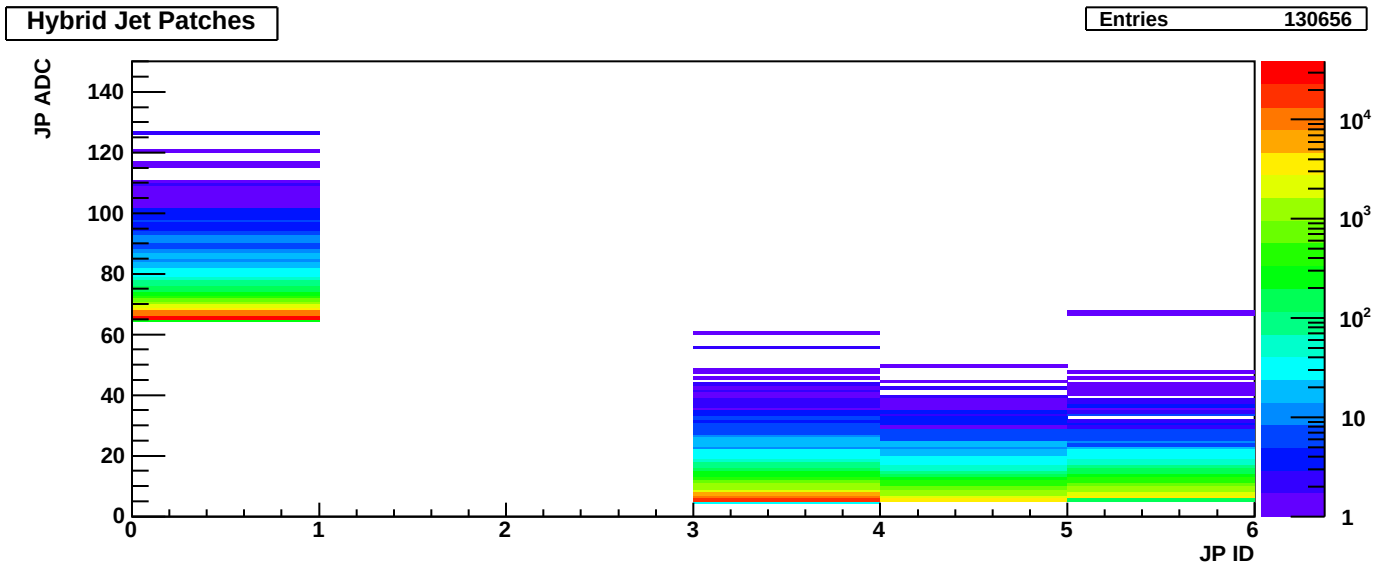

Barrel EMC L0 Input - Patch Sum
Entries 1.95984e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

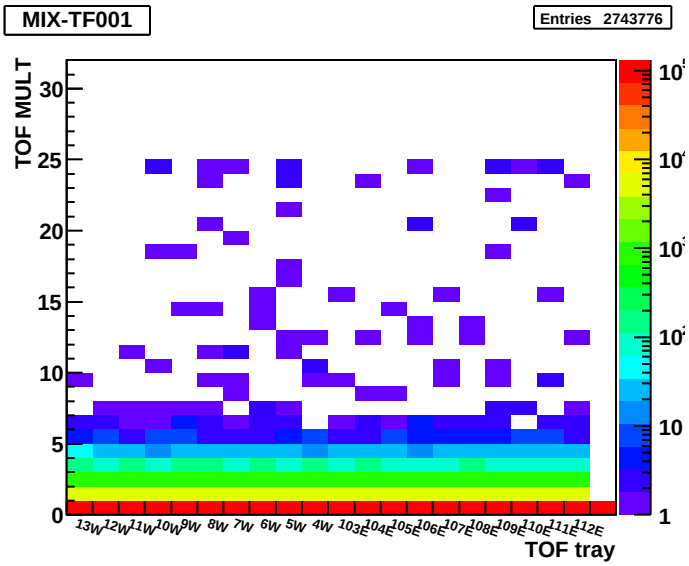

Endcap EMC L0 Input - High Tower
Entries 5879520
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

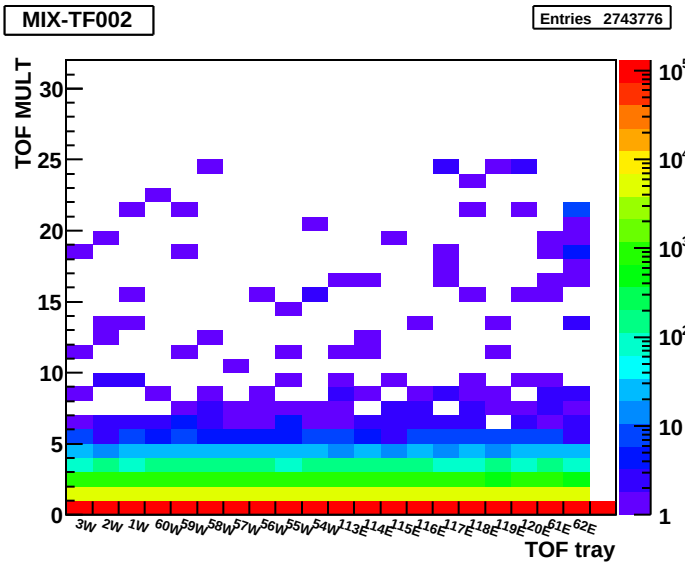

Endcap EMC L0 Input - Patch Sum
Entries 5879520
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

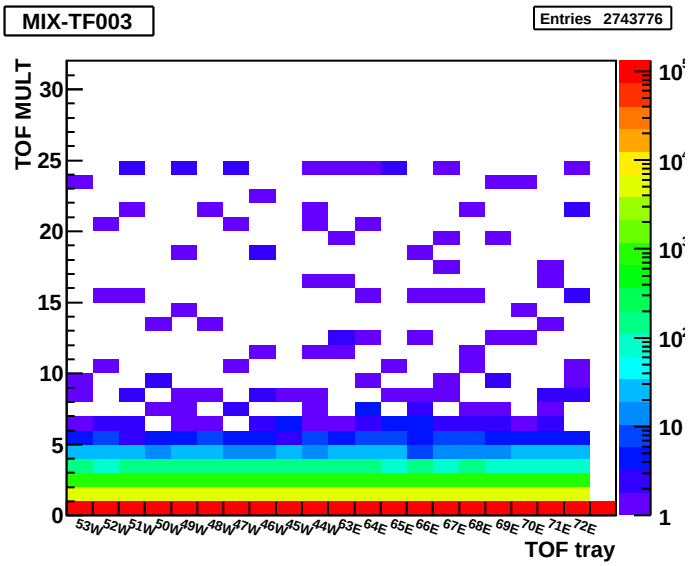

Barrel Jet Patches
Entries 1175904
JP ADC
JP ID

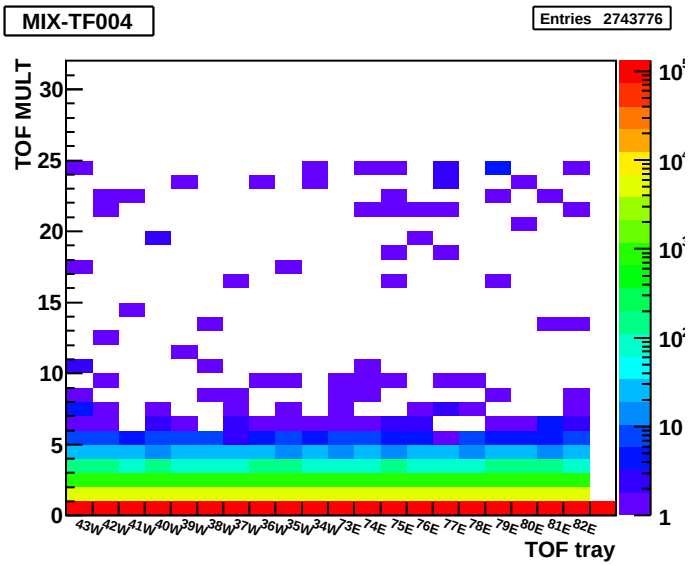

Endcap Jet Patches
Entries 391968
JP ADC
JP ID

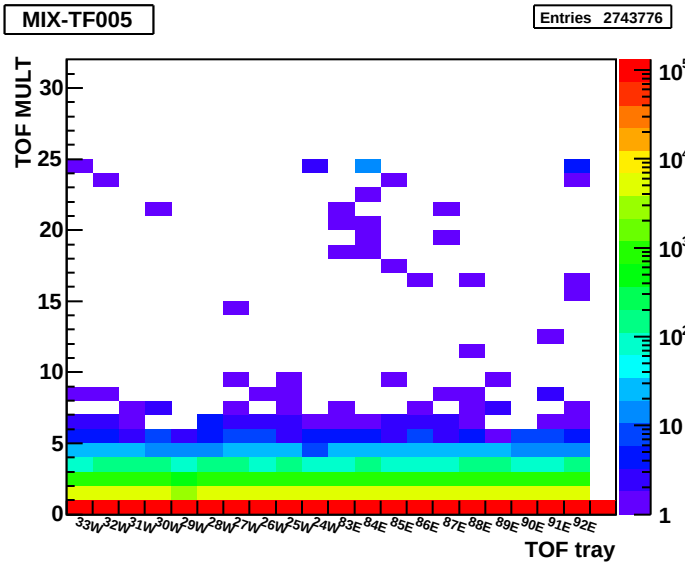

Hybrid Jet Patches
Entries 130656
JP ADC
JP ID

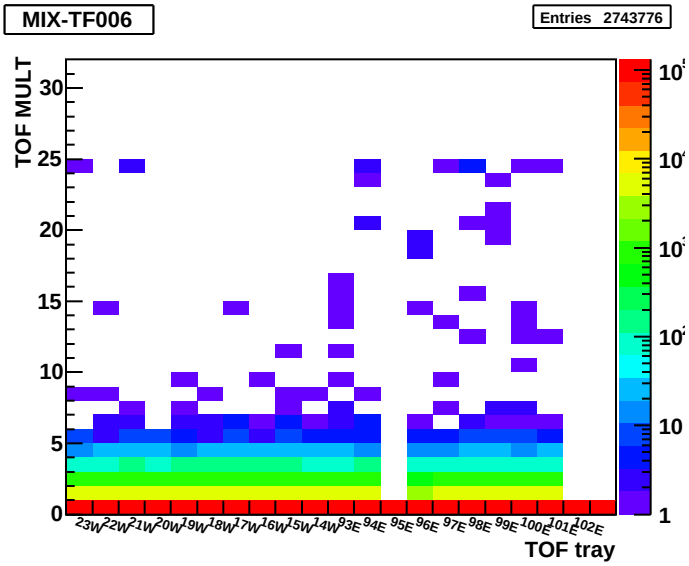

MIX-TF001
Entries 2743776
TOF MULT
TOF tray

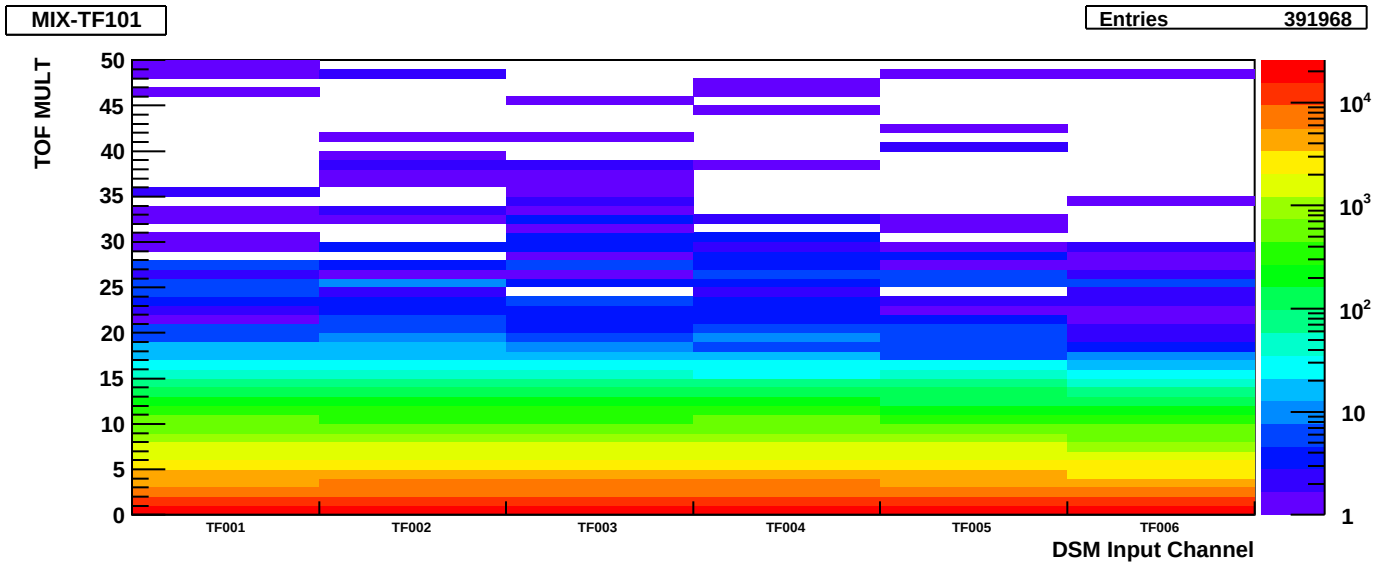

MIX-TF002
Entries 2743776
TOF MULT
TOF tray

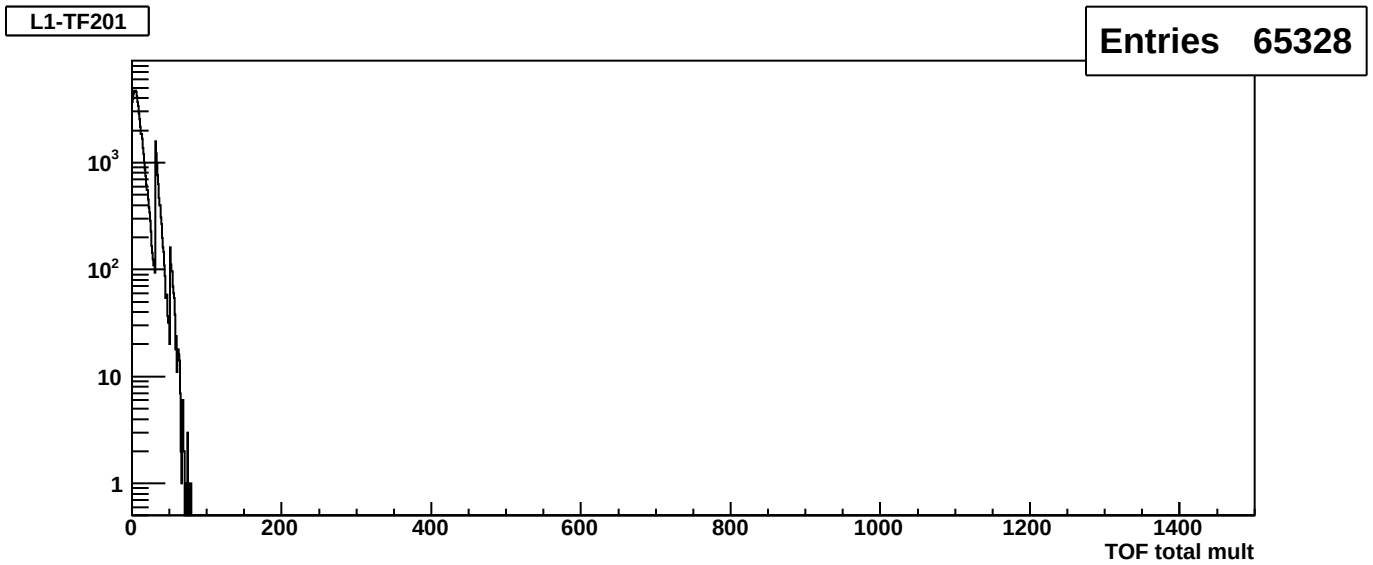

MIX-TF003
Entries 2743776
TOF MULT
TOF tray

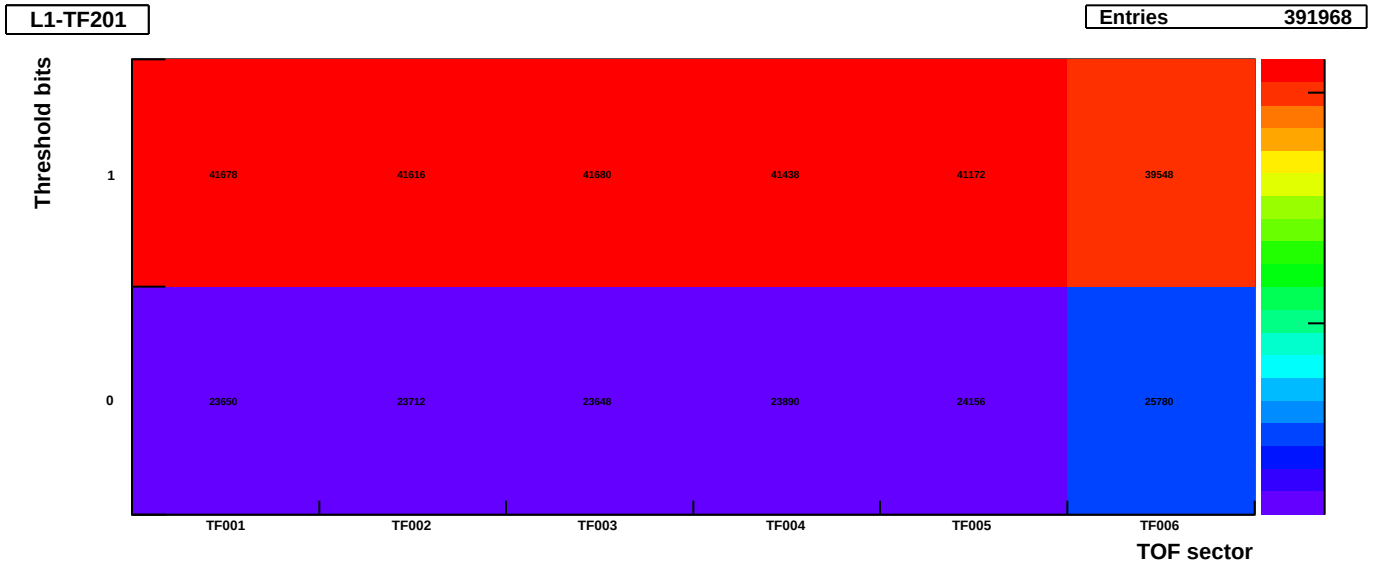

MIX-TF004
Entries 2743776
TOF MULT
TOF tray

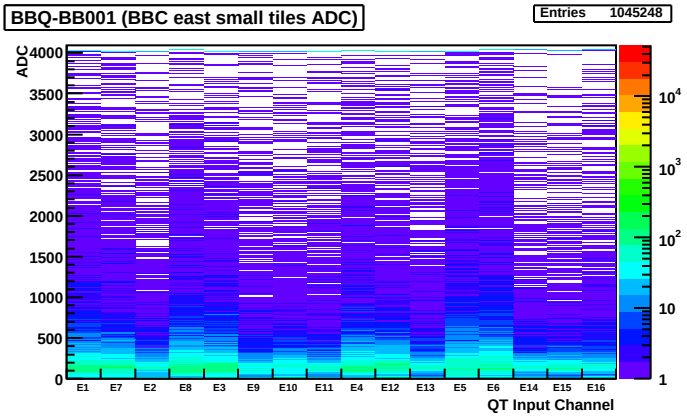

MIX-TF005
Entries 2743776
TOF MULT
TOF tray

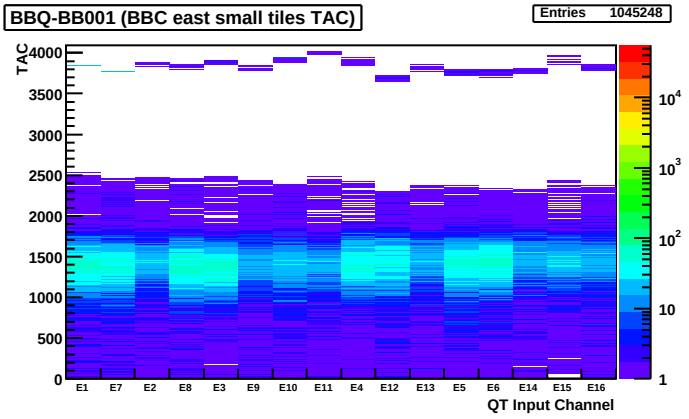

MIX-TF006
Entries 2743776
TOF MULT
TOF tray

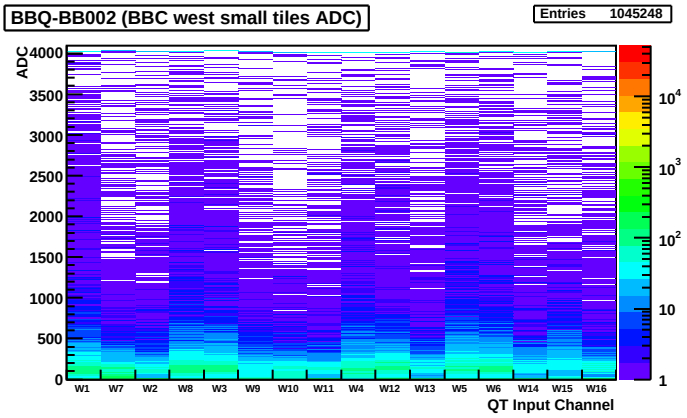

MIX-TF101
Entries 391968
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

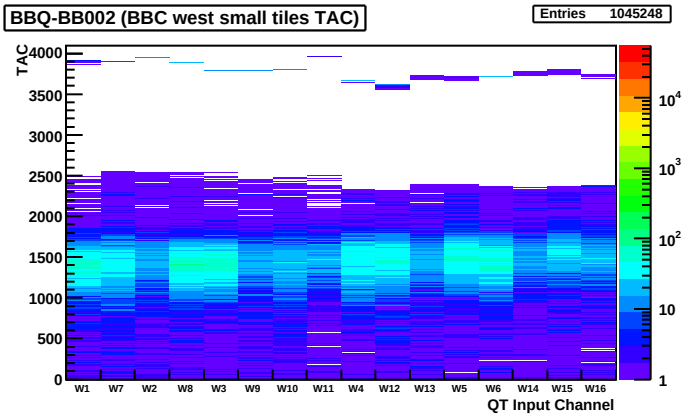

L1-TF201
Entries 65328
TOF total mult

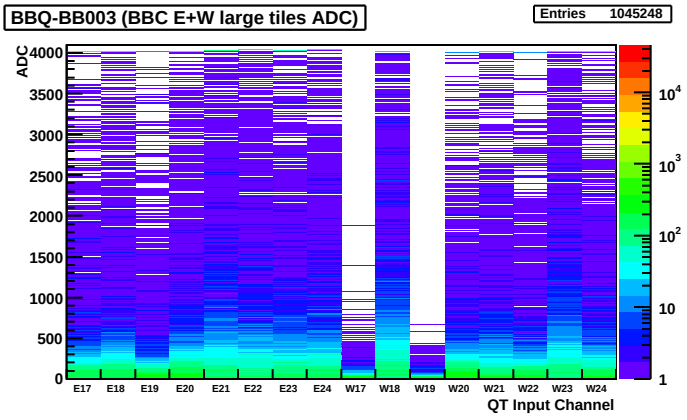

L1-TF201
Entries 391968
Threshold bits
41678
41616
41680
41438
41172
39548
23650
23712
23648
23890
24156
25780
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

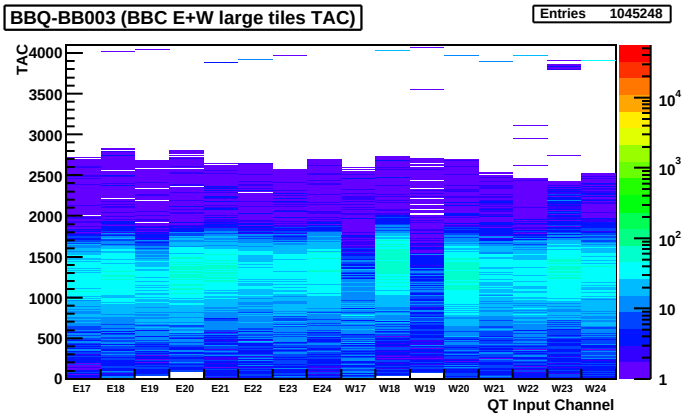

BBQ-BB001 (BBC east small tiles ADC)
Entries 1045248
ADC
QT Input Channel

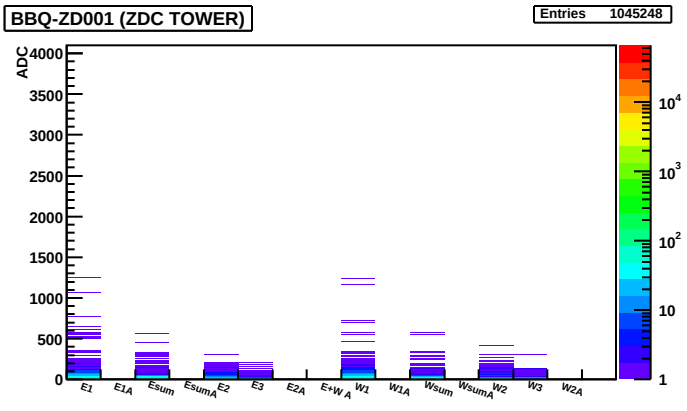

BBQ-BB001 (BBC east small tiles TAC)
Entries 1045248
TAC
QT Input Channel

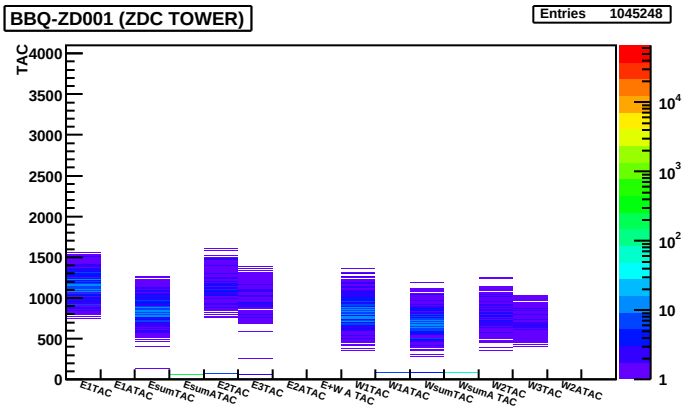

BBQ-BB002 (BBC west small tiles ADC)
Entries 1045248
ADC
QT Input Channel

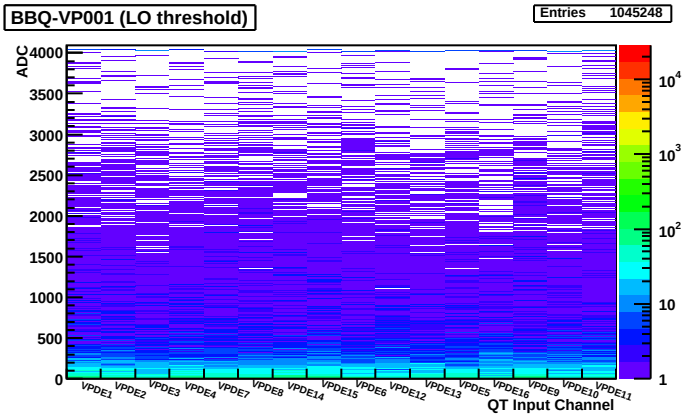

BBQ-BB002 (BBC west small tiles TAC)
Entries 1045248
TAC
QT Input Channel

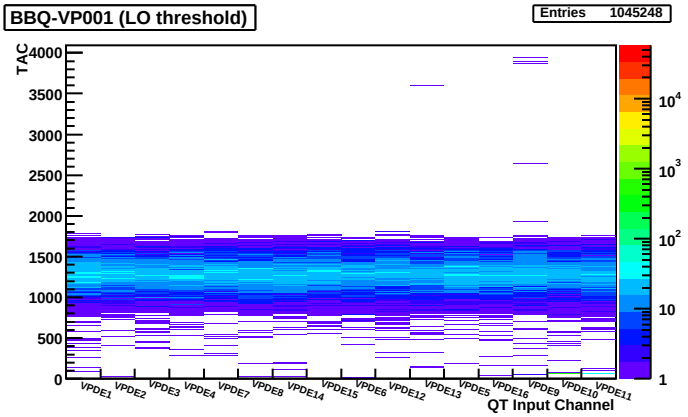

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1045248
ADC
QT Input Channel

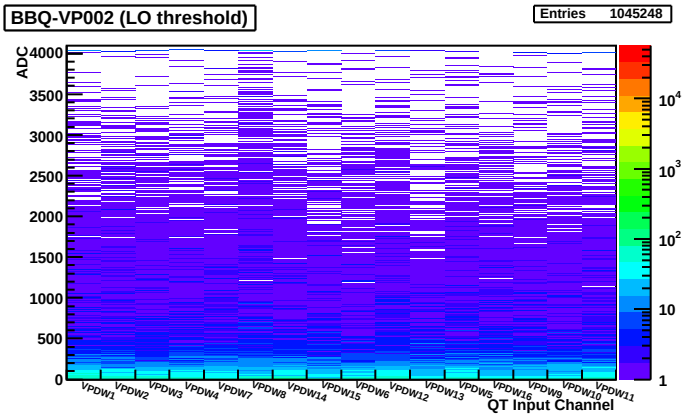

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1045248
TAC
QT Input Channel

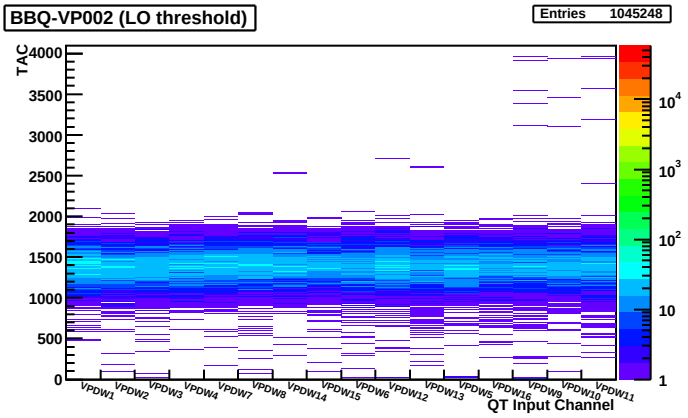

BBQ-ZD001 (ZDC TOWER)
Entries 1045248
ADC

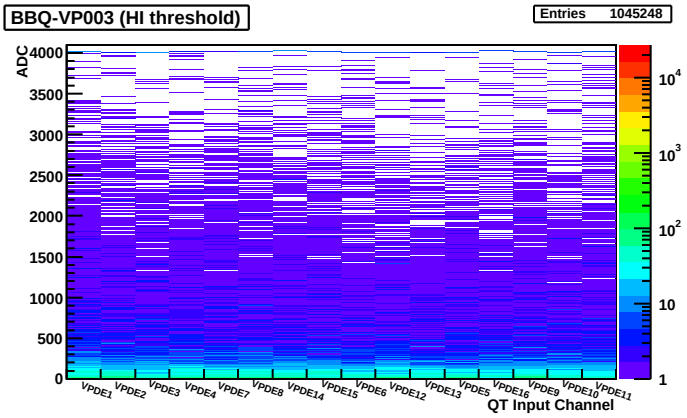

BBQ-ZD001 (ZDC TOWER)
Entries 1045248
TAC

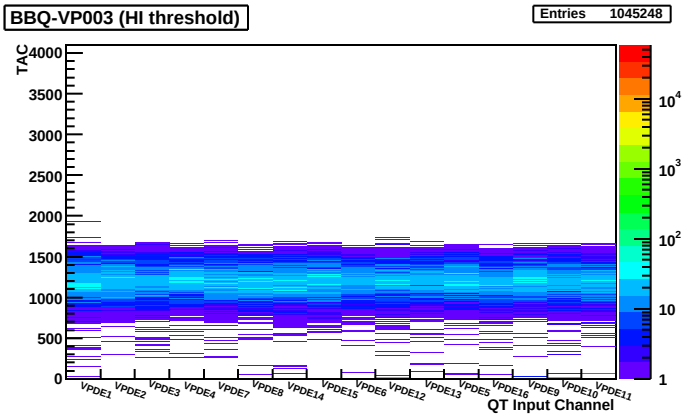

BBQ-VP001 (LO threshold)
Entries 1045248
ADC
QT Input Channel

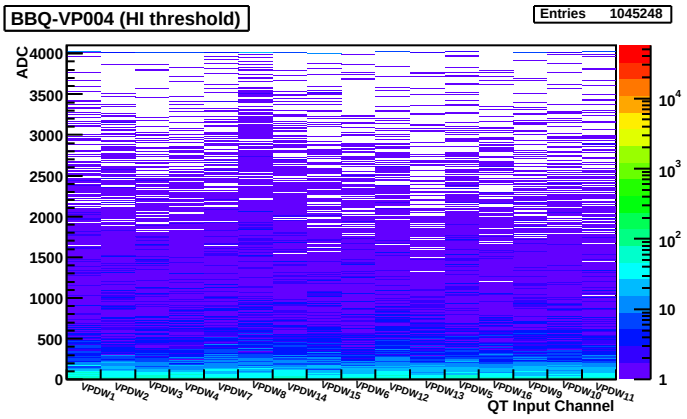

BBQ-VP001 (LO threshold)
Entries 1045248
TAC
QT Input Channel

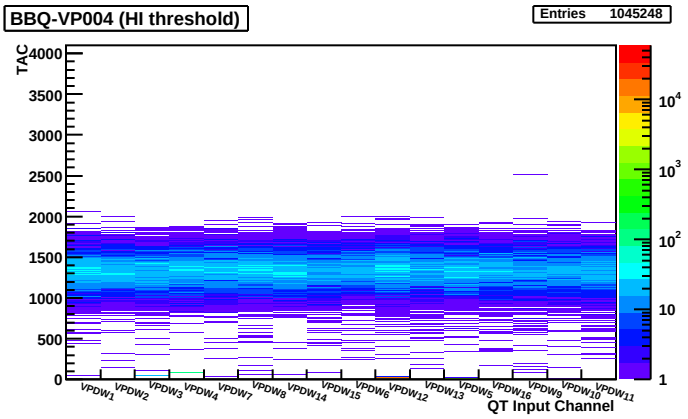

BBQ-VP002 (LO threshold)
Entries 1045248
ADC
QT Input Channel

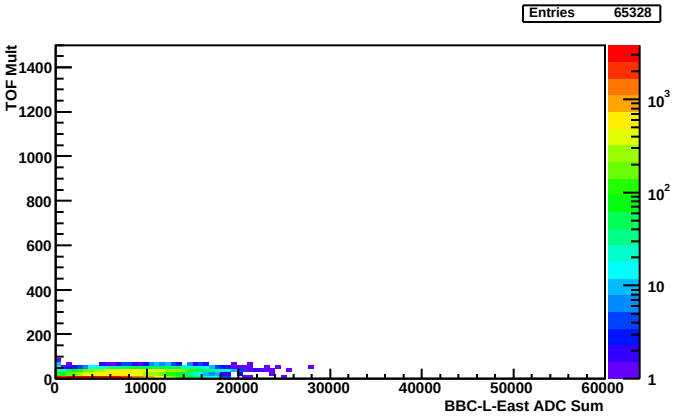

BBQ-VP002 (LO threshold)
Entries 1045248
TAC
QT Input Channel

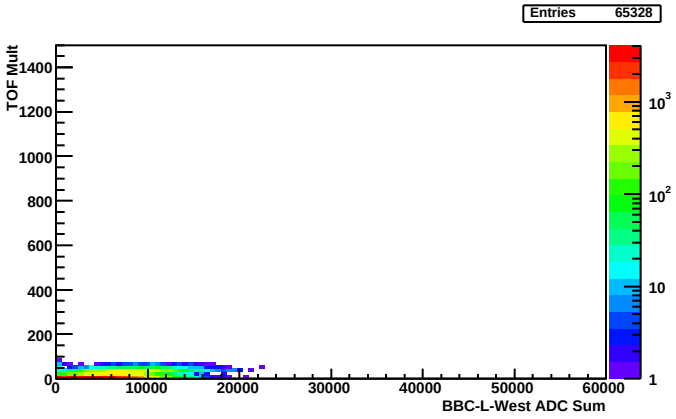

BBQ-VP003 (HI threshold)
Entries 1045248
ADC
QT Input Channel

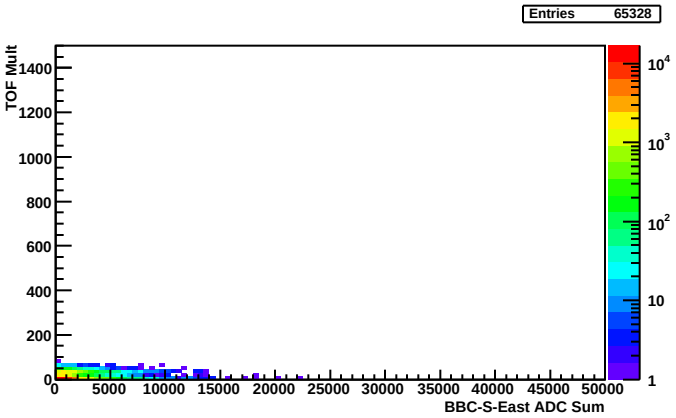

BBQ-VP003 (HI threshold)
Entries 1045248
TAC
QT Input Channel

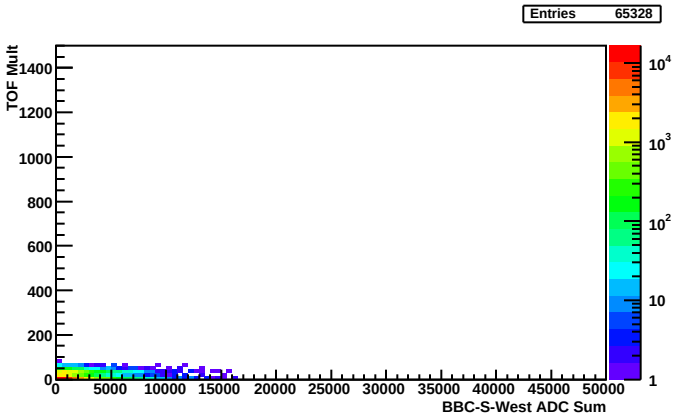

BBQ-VP004 (HI threshold)
Entries 1045248
ADC
QT Input Channel

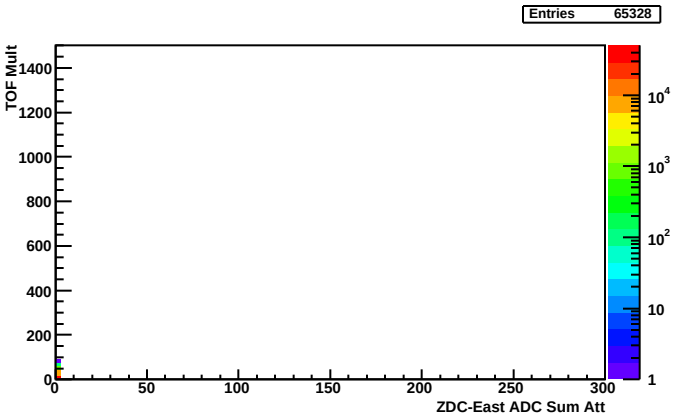

BBQ-VP004 (HI threshold)
Entries 1045248
TAC
QT Input Channel

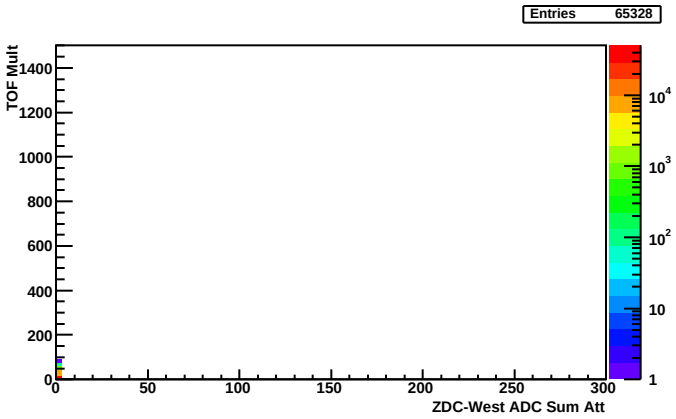

Entries 65328
TOF Mult
BBC-L-East ADC Sum

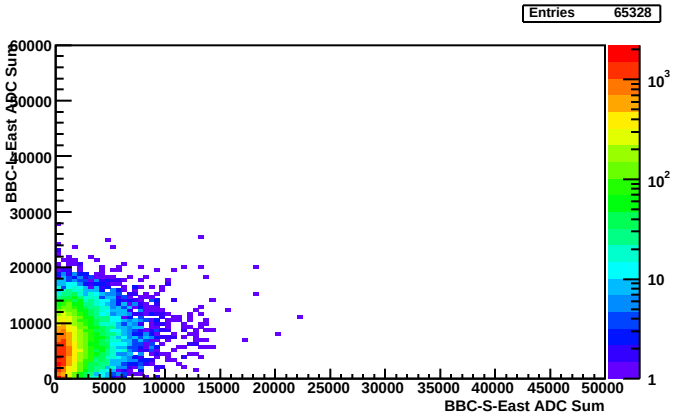

Entries 65328
TOF Mult
BBC-L-West ADC Sum

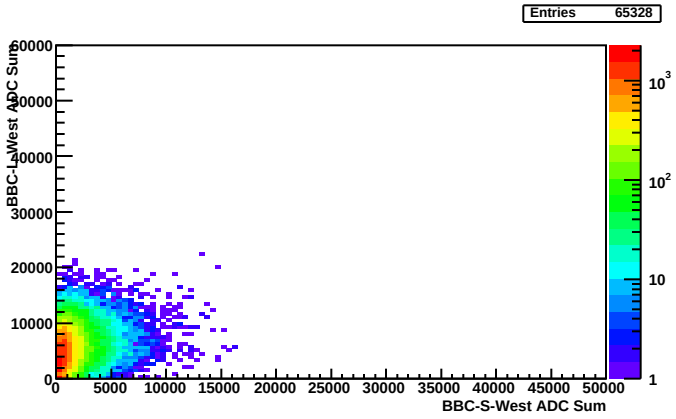

Entries 65328
TOF Mult
BBC-S-East ADC Sum

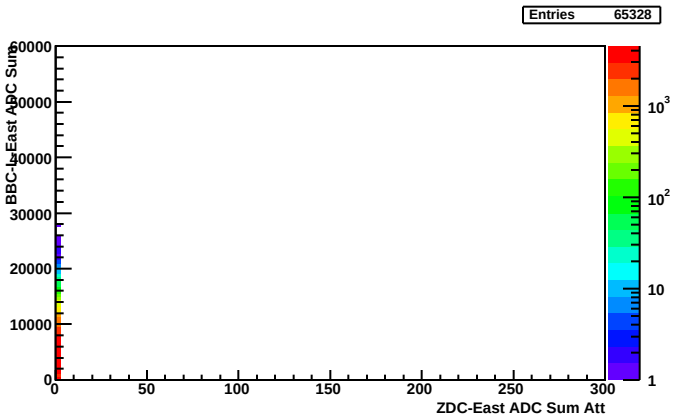

Entries 65328
TOF Mult
BBC-S-West ADC Sum

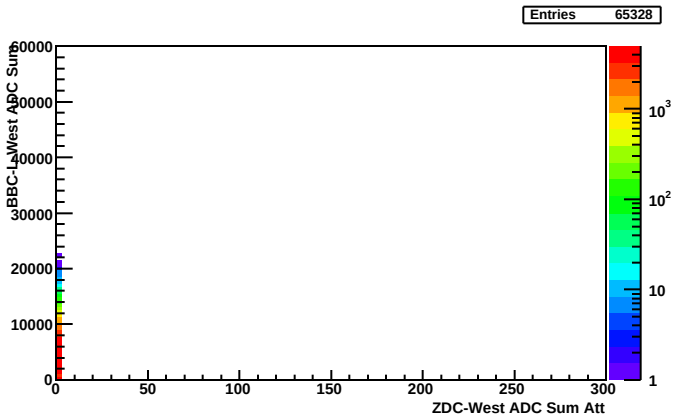

Entries 65328
TOF Mult
ZDC-East ADC Sum Att

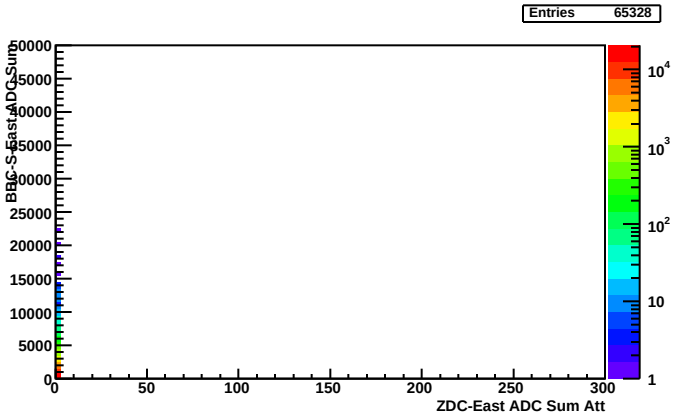

Entries 65328
TOF Mult
ZDC-West ADC Sum Att

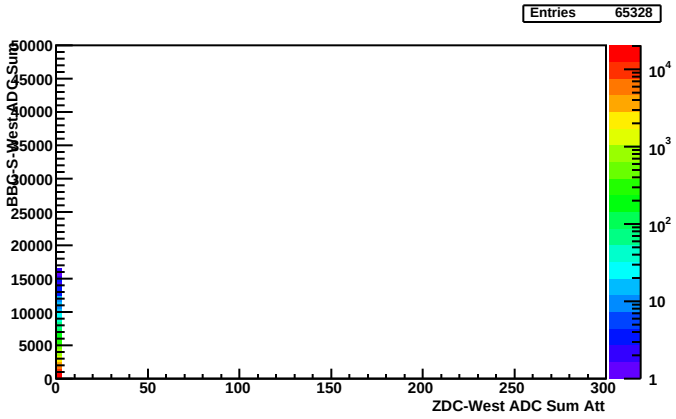

Entries 65328
BBC-L-East ADC Sum
BBC-S-East ADC Sum

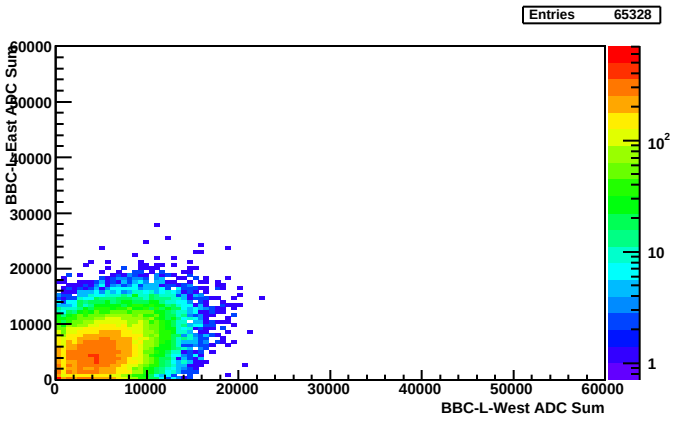

Entries 65328
BBC-L-West ADC Sum
BBC-S-West ADC Sum

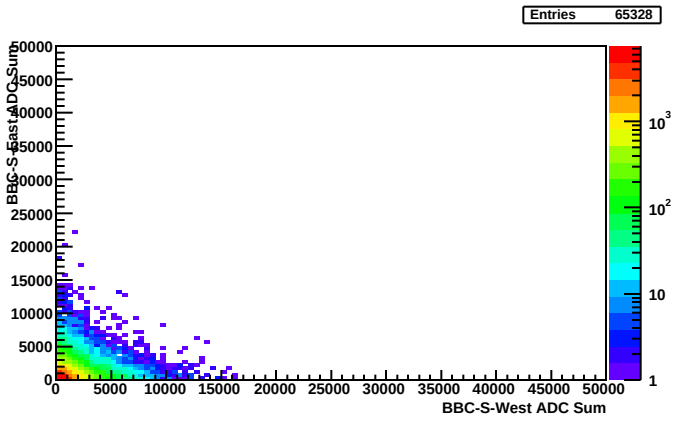

Entries 65328
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

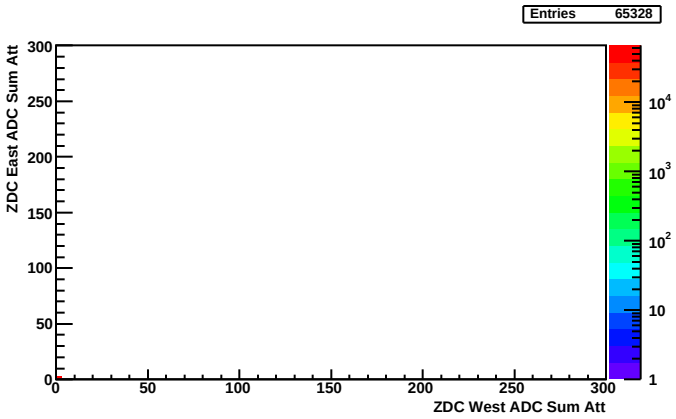

Entries 65328
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

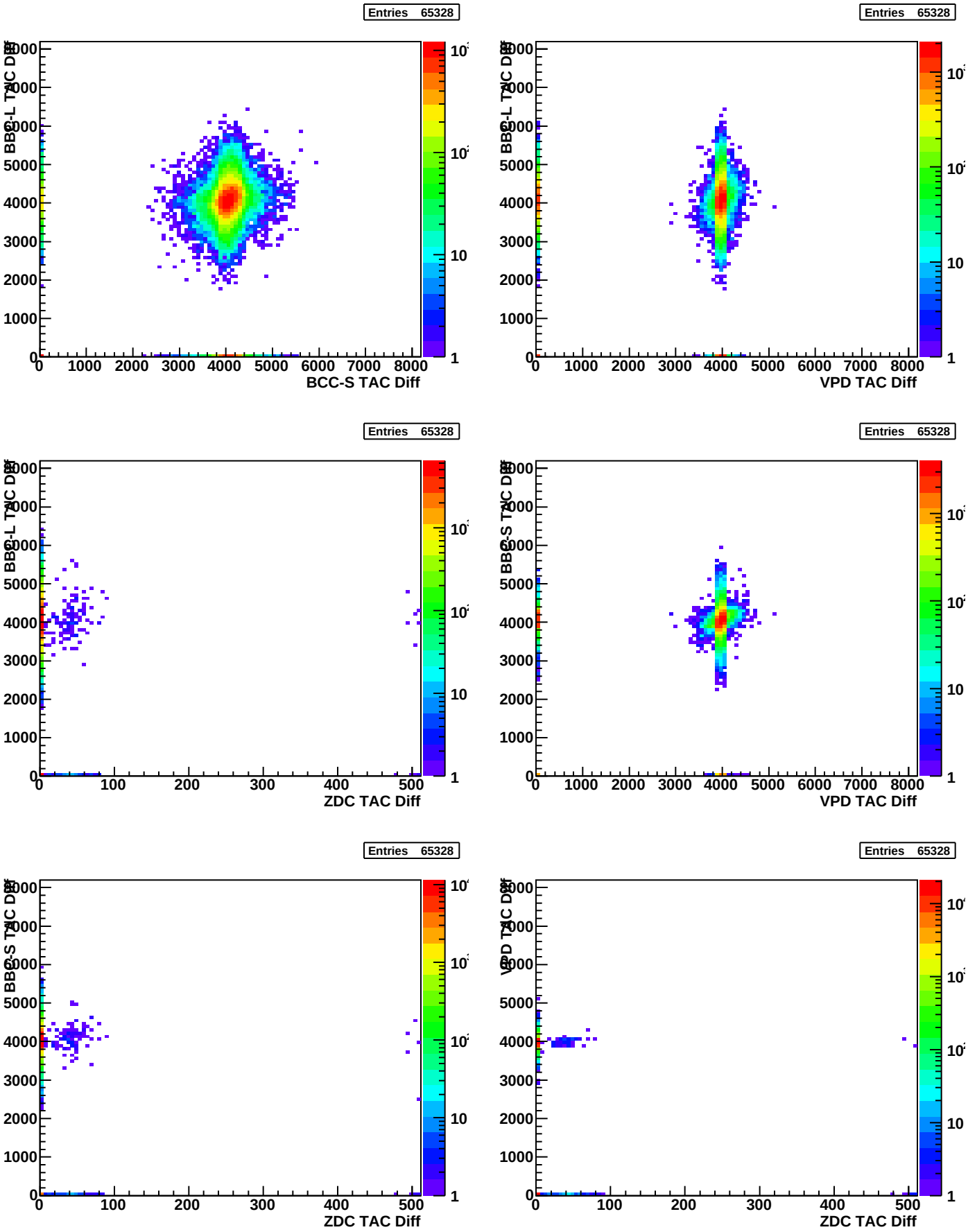

Entries 65328
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

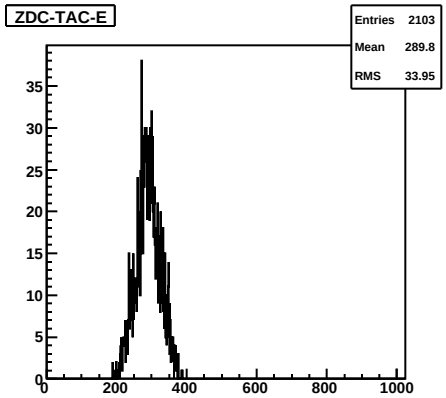

Entries 65328
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

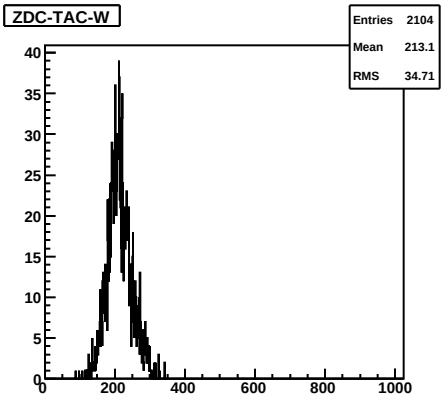

Entries 65328
BBC-L-East ADC Sum
BBC-L-West ADC Sum

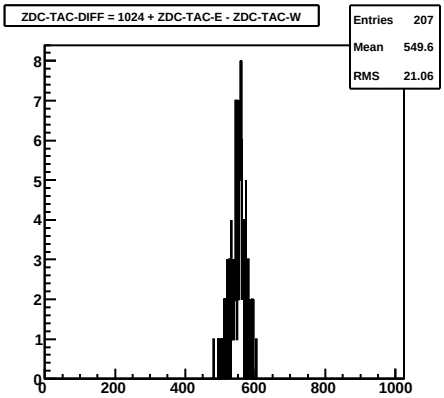

Entries 65328
BBC-S-East ADC Sum
BBC-S-West ADC Sum

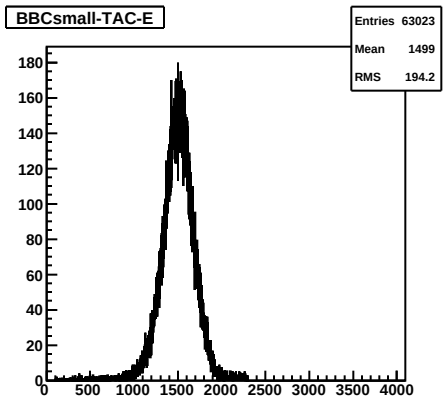

Entries 65328
ZDC East ADC Sum Att
ZDC West ADC Sum Att

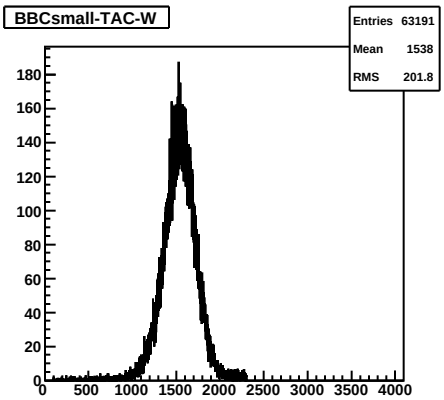

Entries 65328
BBC-L TAC Diff
BCC-S TAC Diff
Entries 65328
BBC-L TAC Diff
VPD TAC Diff
Entries 65328
BBC-L TAC Diff
ZDC TAC Diff
Entries 65328
BBC-S TAC Diff
VPD TAC Diff
Entries 65328
BBC-S TAC Diff
ZDC TAC Diff
Entries 65328
VPD TAC Diff
ZDC TAC Diff

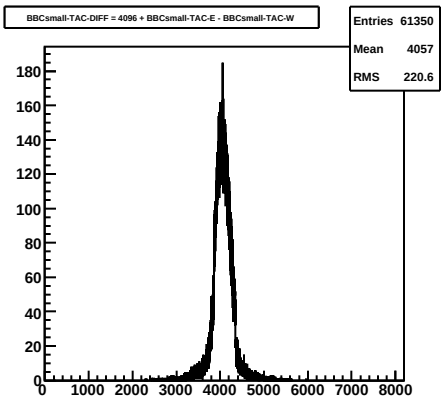

ZDC-TAC-E
Entries 2103
Mean 289.8
RMS 33.95

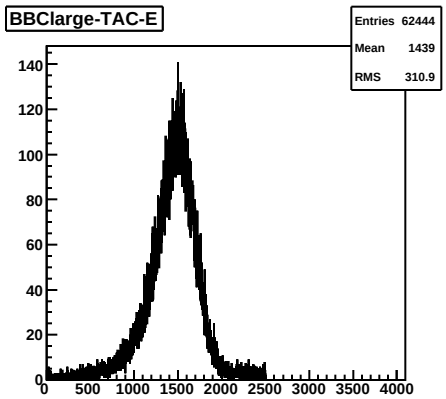

ZDC-TAC-W
Entries 2104
Mean 213.1
RMS 34.71

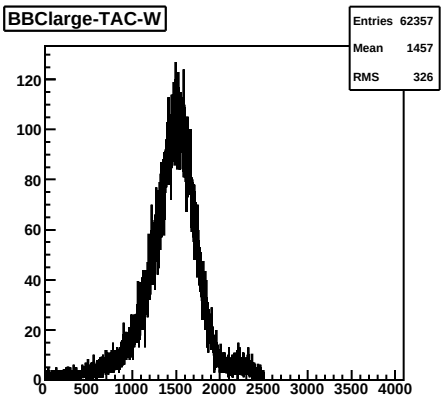

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 207
Mean 549.6
RMS 21.06

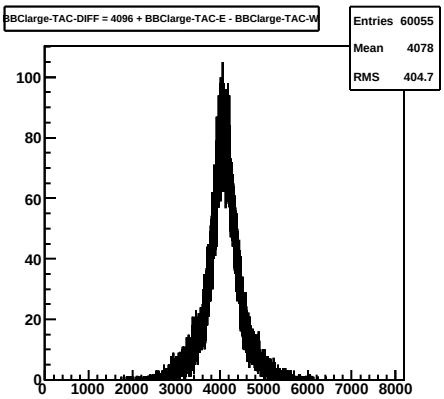

BBCsmall-TAC-E
Entries 63023
Mean 1499
RMS 194.2

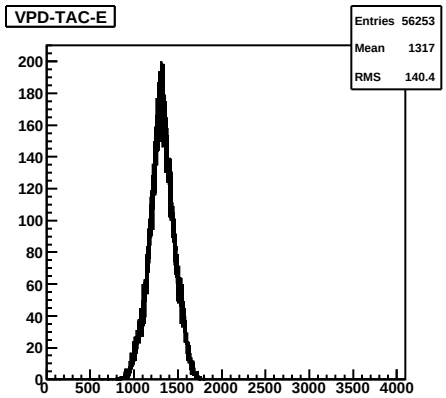

BBCsmall-TAC-W
Entries 63191
Mean 1538
RMS 201.8

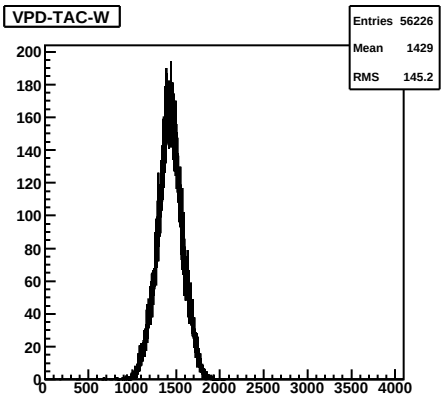

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 61350
Mean 4057
RMS 220.6

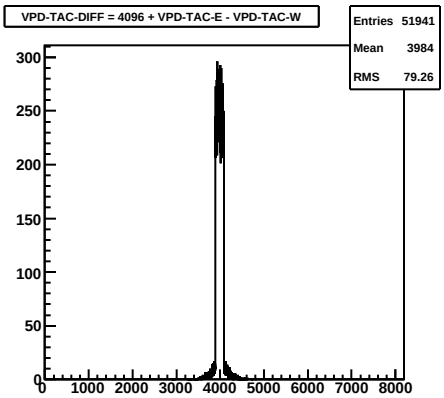

BBClarge-TAC-E
Entries 62444
Mean 1439
RMS 310.9

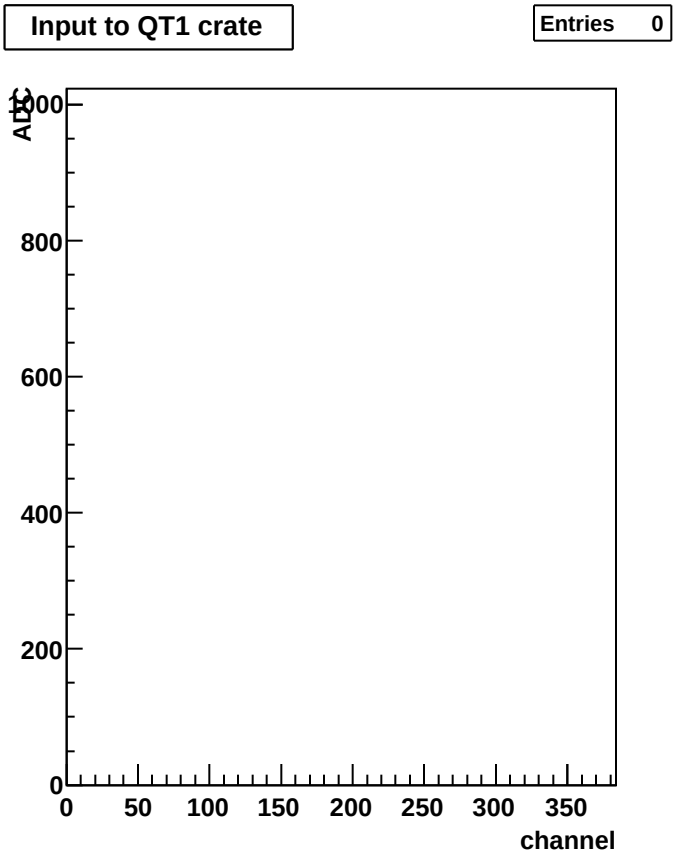

BBClarge-TAC-W
Entries 62357
Mean 1457
RMS 326

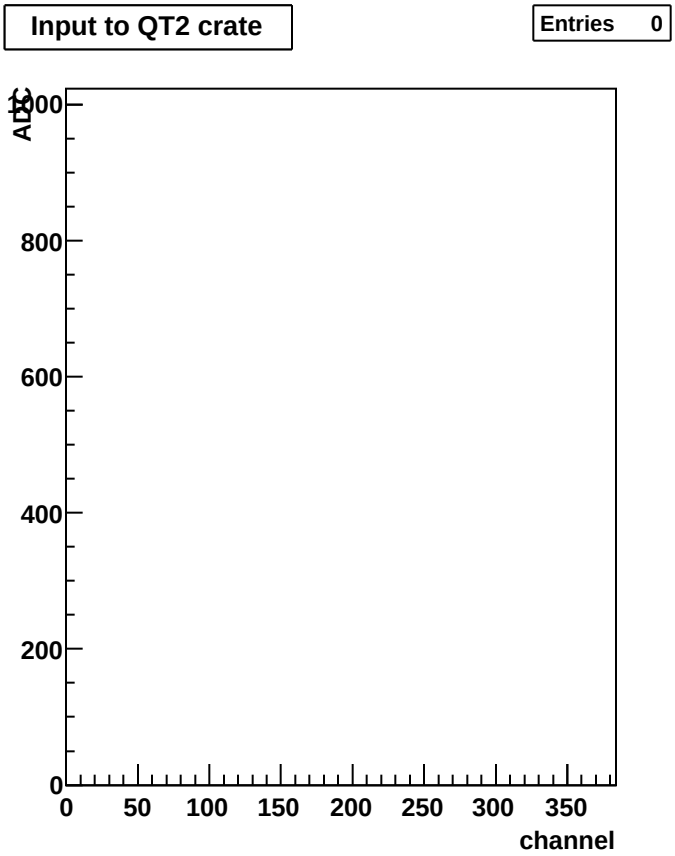

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 60055
Mean 4078
RMS 404.7

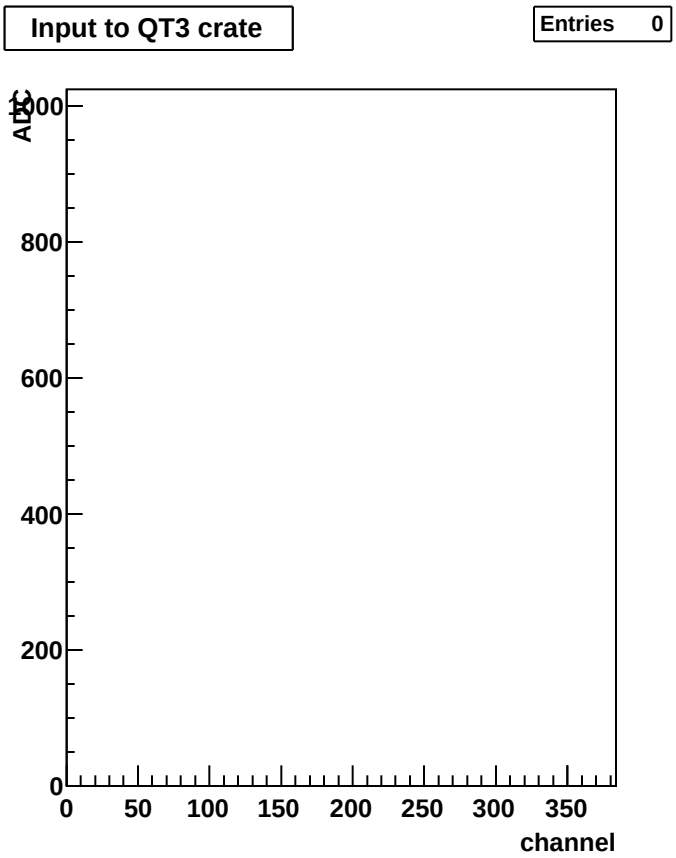

VPD-TAC-E
Entries 56253
Mean 1317
RMS 140.4

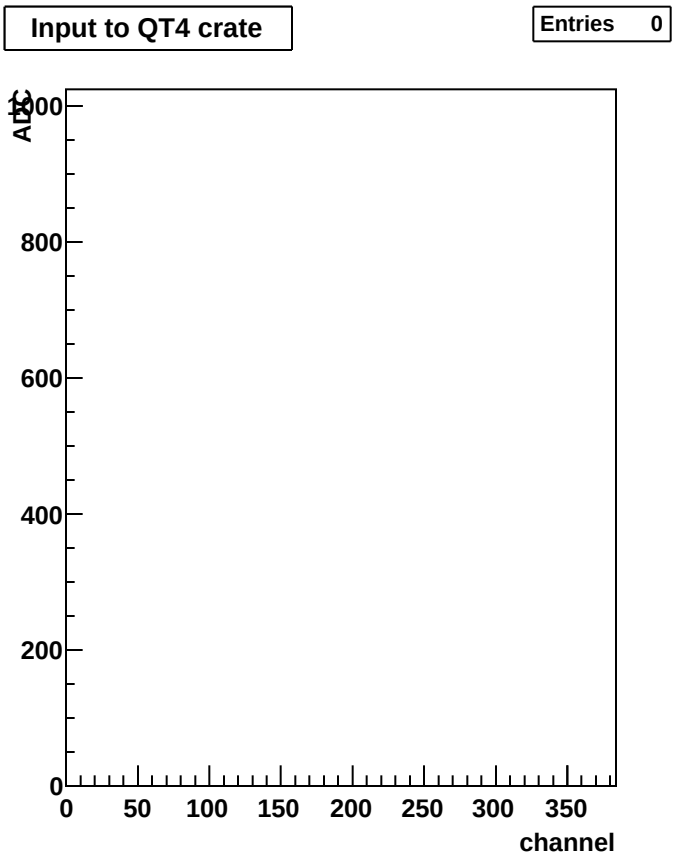

VPD-TAC-W
Entries 56226
Mean 1429
RMS 145.2

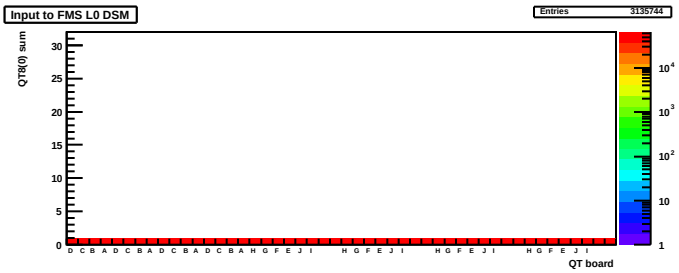

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 51941
Mean 3984
RMS 79.26

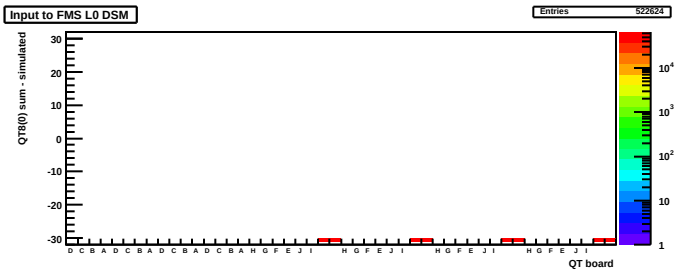

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

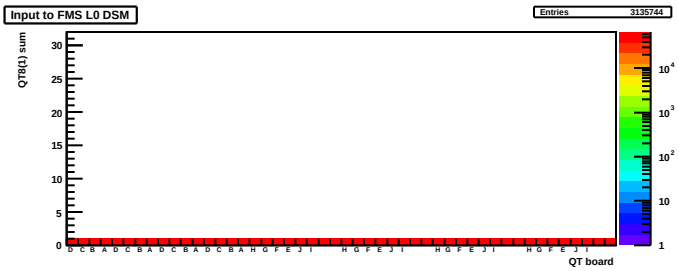

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

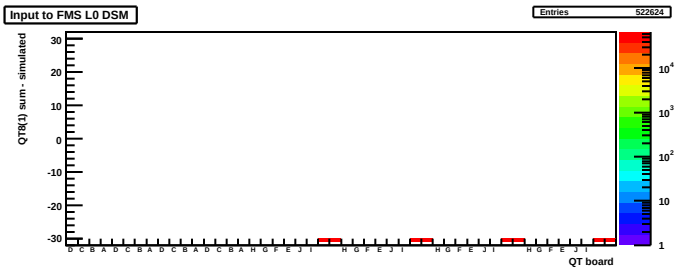

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

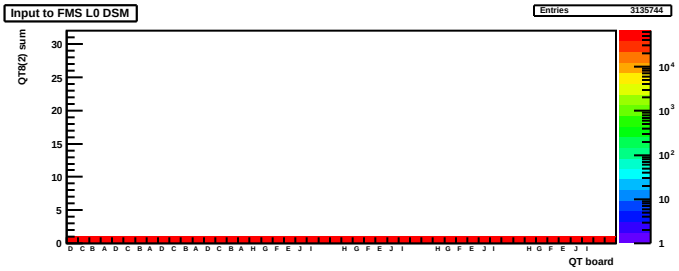

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

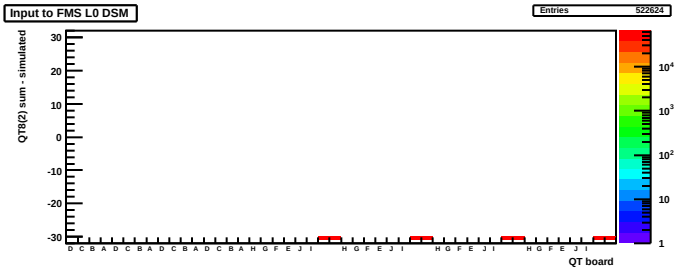

Input to FMS L0 DSM
Entries 3135744
QT8(0) sum
QT board

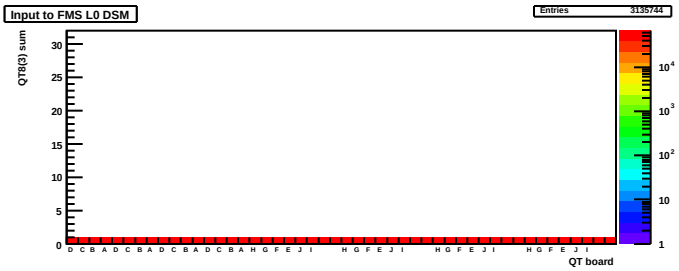

Input to FMS L0 DSM
Entries 522624
QT8(0) sum - simulated
QT board

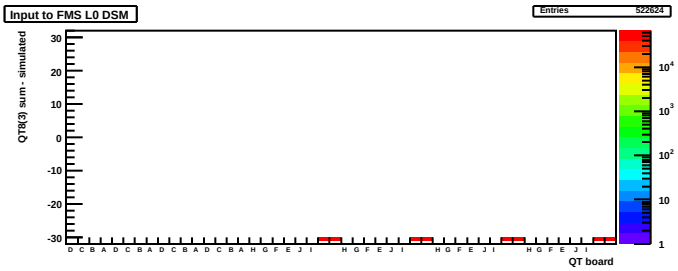

Input to FMS L0 DSM
Entries 3135744
QT8(1) sum
QT board

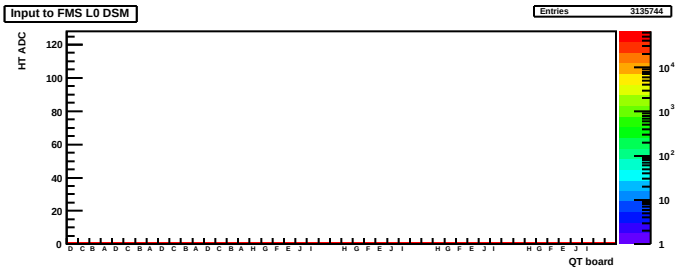

Input to FMS L0 DSM
Entries 522624
QT8(1) sum - simulated
QT board

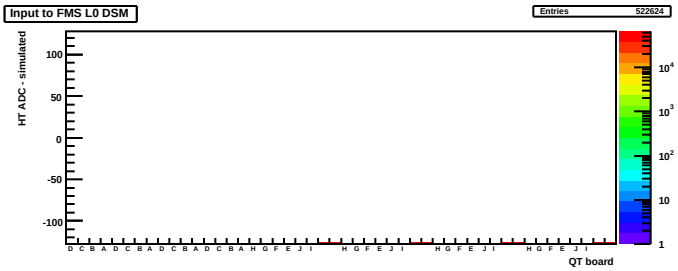

Input to FMS L0 DSM
Entries 3135744
QT8(2) sum
QT board

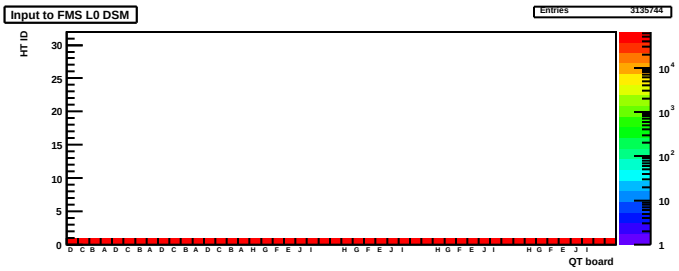

Input to FMS L0 DSM
Entries 522624
QT8(2) sum - simulated
QT board

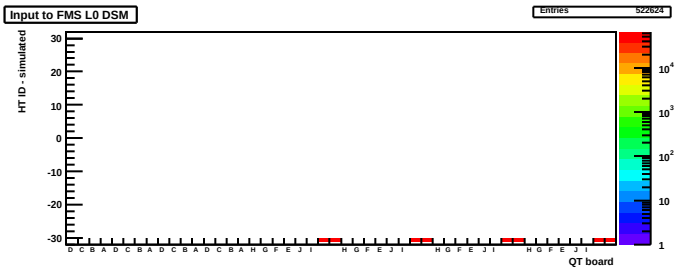

Input to FMS L0 DSM
Entries 3135744
QT8(3) sum
QT board

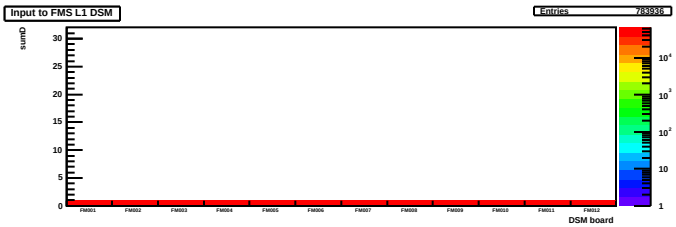

Input to FMS L0 DSM
Entries 522624
QT8(3) sum - simulated
QT board

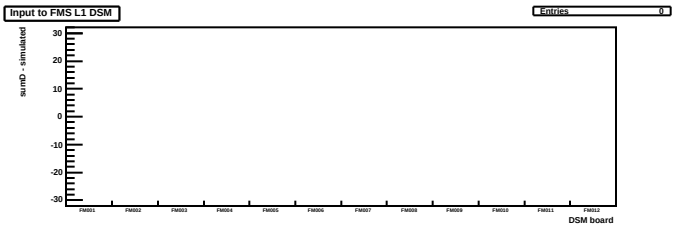

Input to FMS L0 DSM
Entries 3135744
HT ADC
QT board

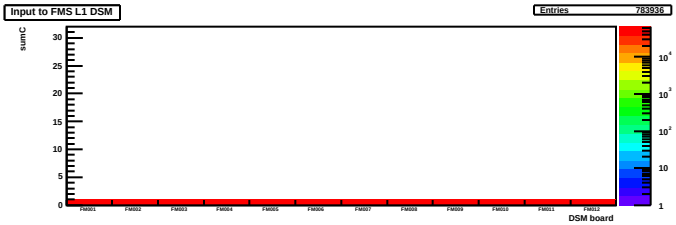

Input to FMS L0 DSM
Entries 522624
HT ADC - simulated
QT board

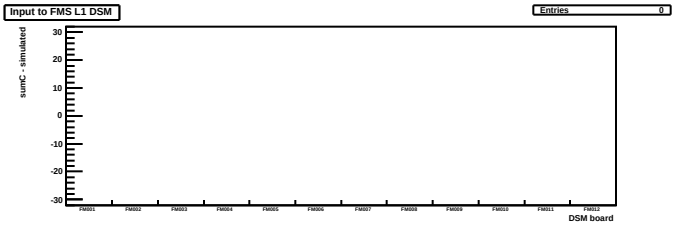

Input to FMS L0 DSM
Entries 3135744
HT ID
QT board

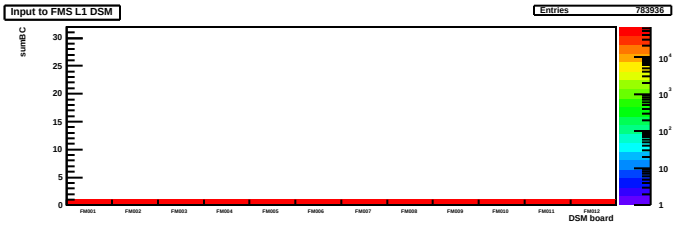

Input to FMS L0 DSM
Entries 522624
HT ID - simulated
QT board

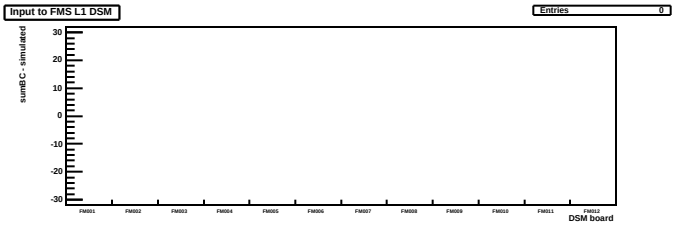

Input to FMS L1 DSM
Entries 783936
sumD
DSM board

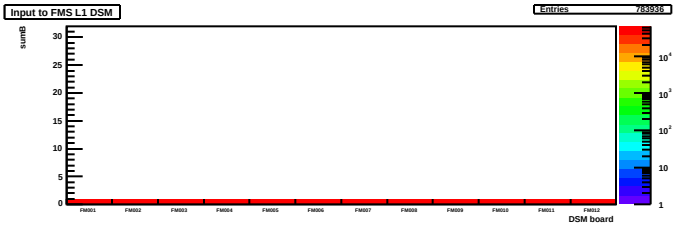

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

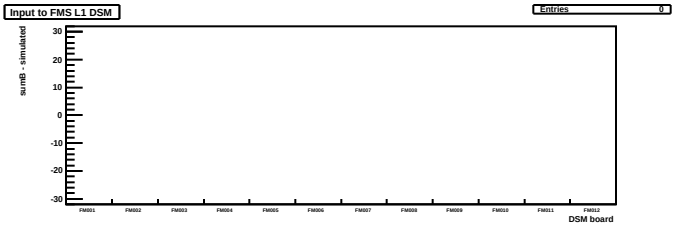

Input to FMS L1 DSM
Entries 783936
sumC
DSM board

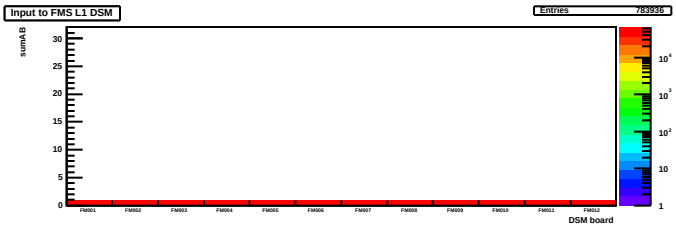

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

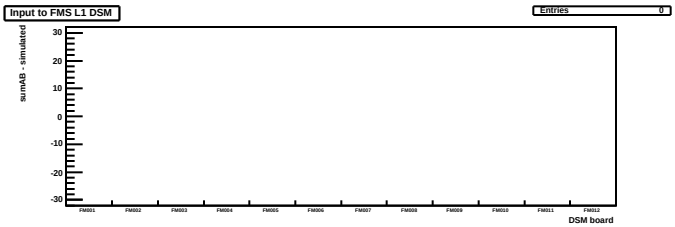

Input to FMS L1 DSM
Entries 783936
sumBC
DSM board

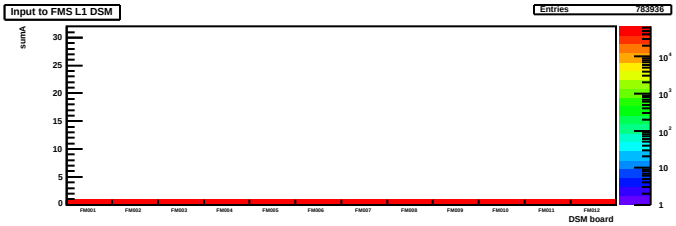

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

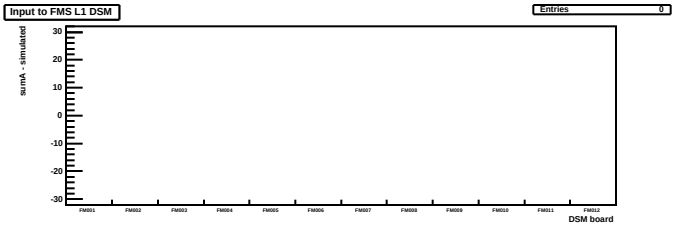

Input to FMS L1 DSM
Entries 783936
sumB
DSM board

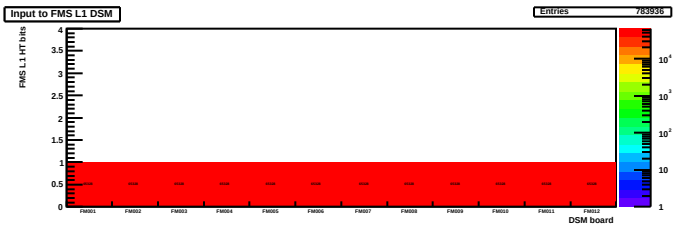

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

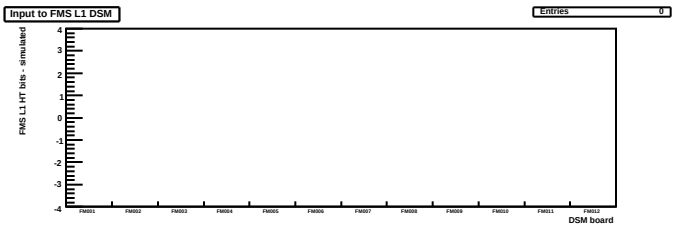

Input to FMS L1 DSM
Entries 783936
sumAB
DSM board

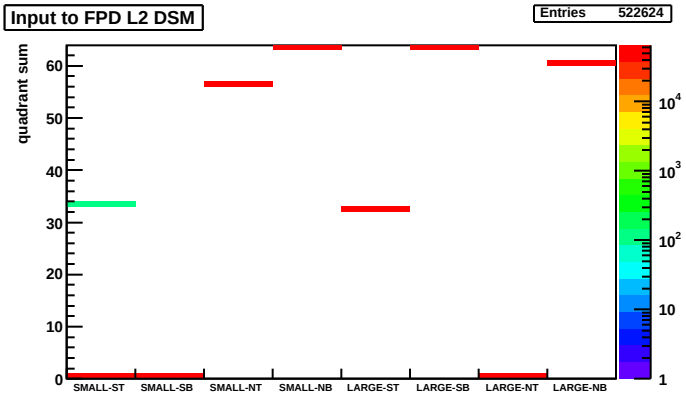

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

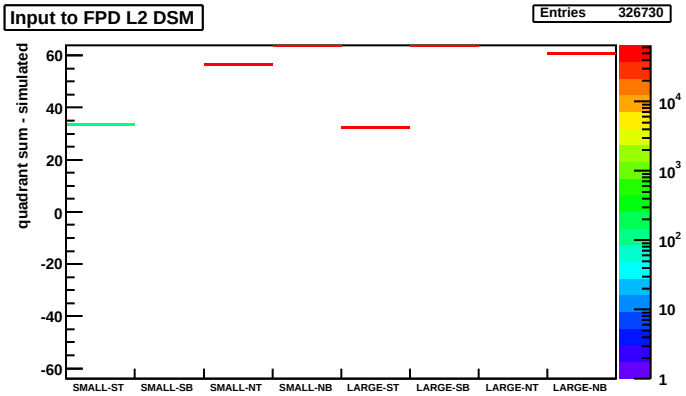

Input to FMS L1 DSM
Entries 783936
sumA
DSM board

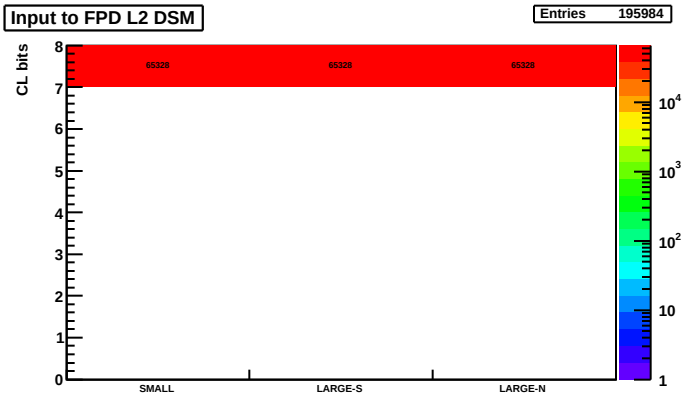

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

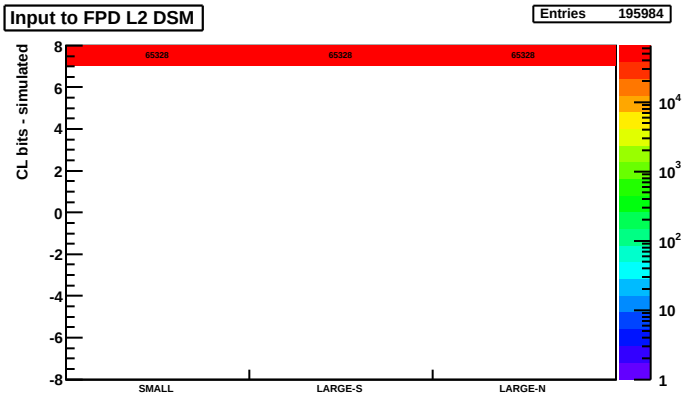

Input to FMS L1 DSM
Entries 783936
FMS L1 HT bits
DSM board

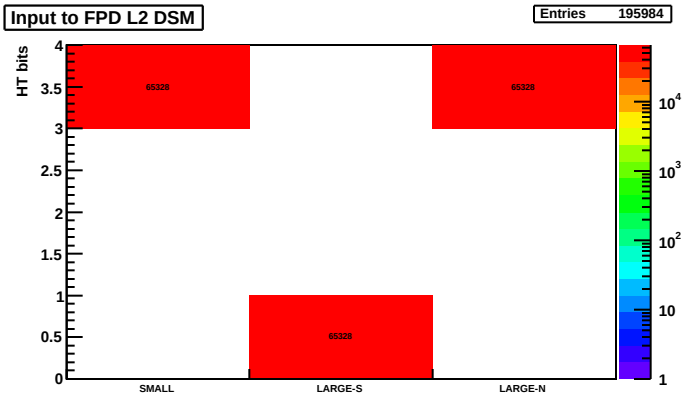

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

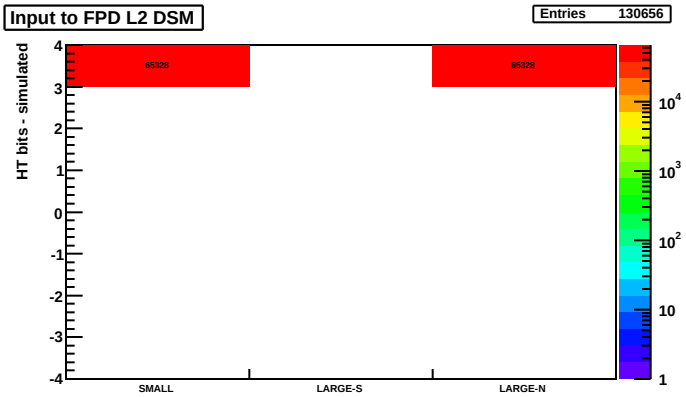

Input to FPD L2 DSM
Entries 522624
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

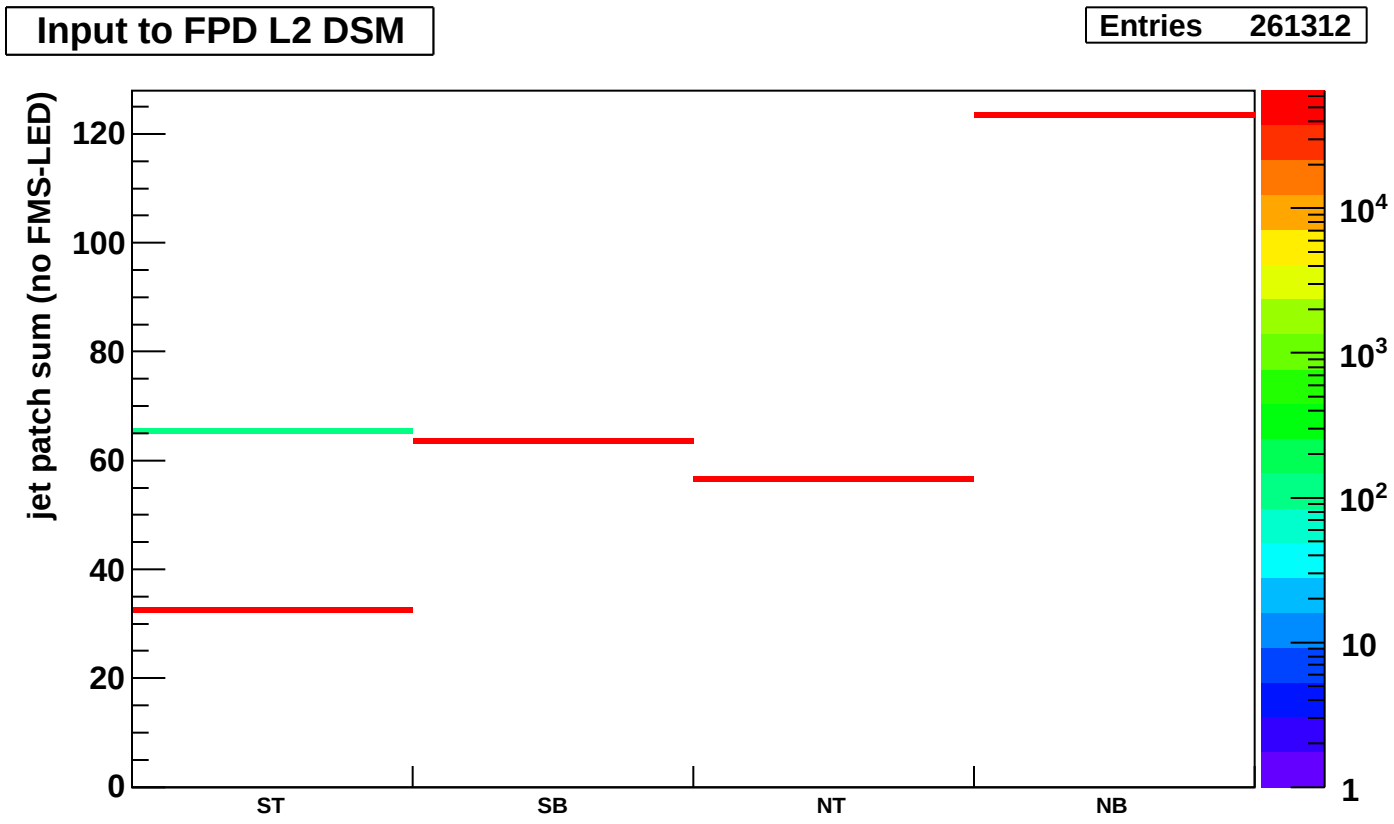

Input to FPD L2 DSM
Entries 326730
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

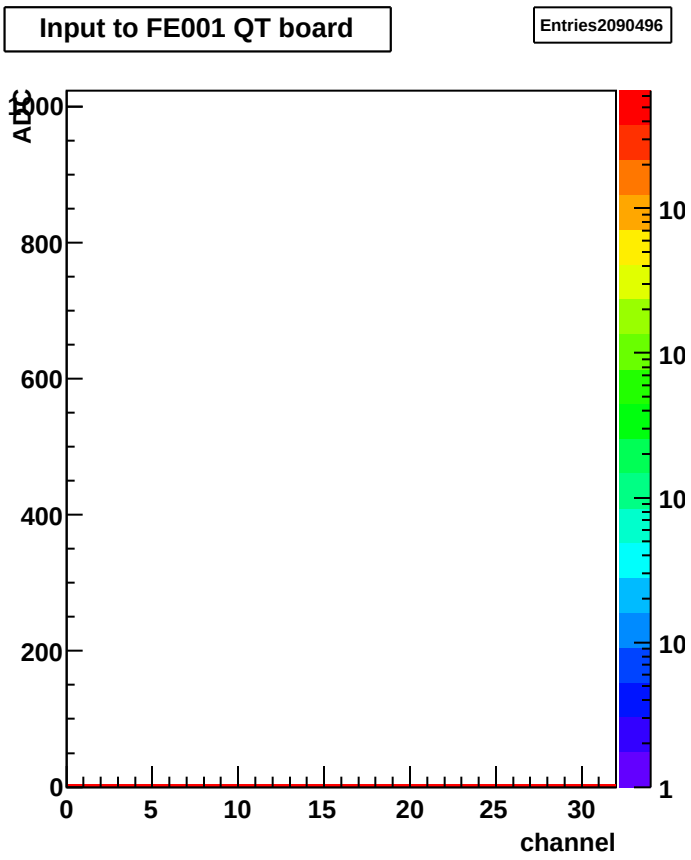

Input to FPD L2 DSM
Entries 195984
CL bits
65328
SMALL
LARGE-S
LARGE-N

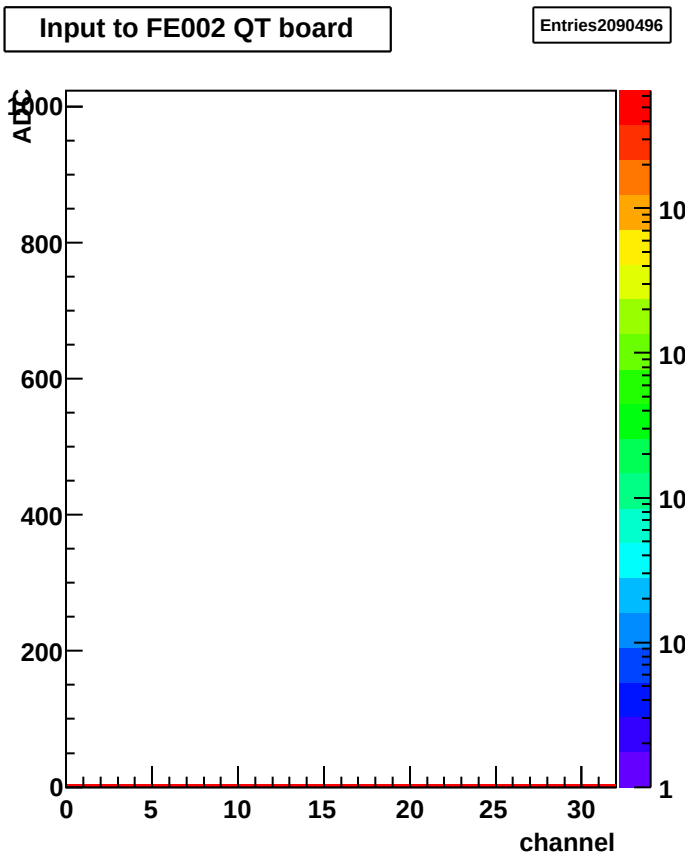

Input to FPD L2 DSM
Entries 195984
CL bits - simulated
65328
SMALL
LARGE-S
LARGE-N

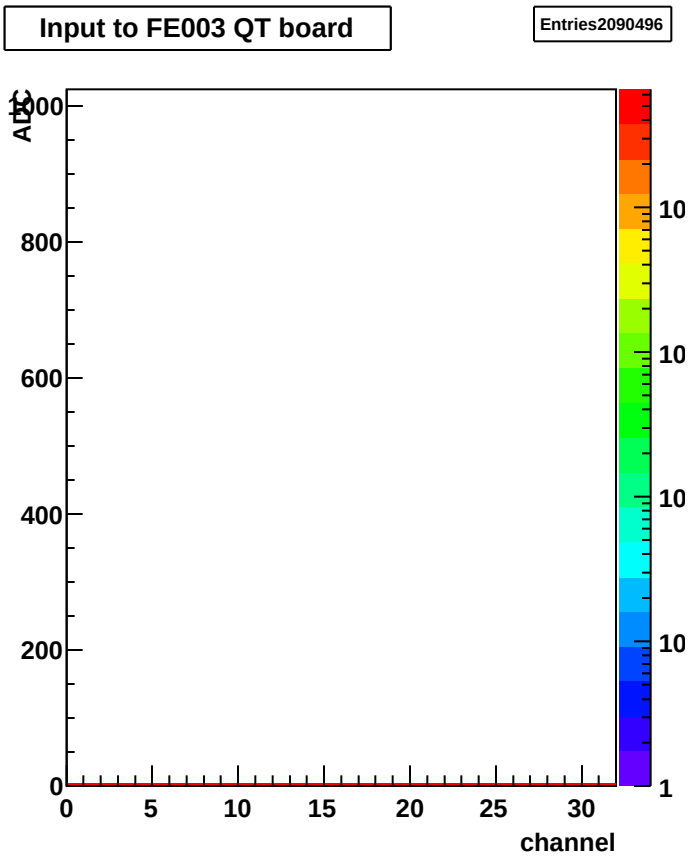

Input to FPD L2 DSM
Entries 195984
HT bits
65328
SMALL
LARGE-S
LARGE-N

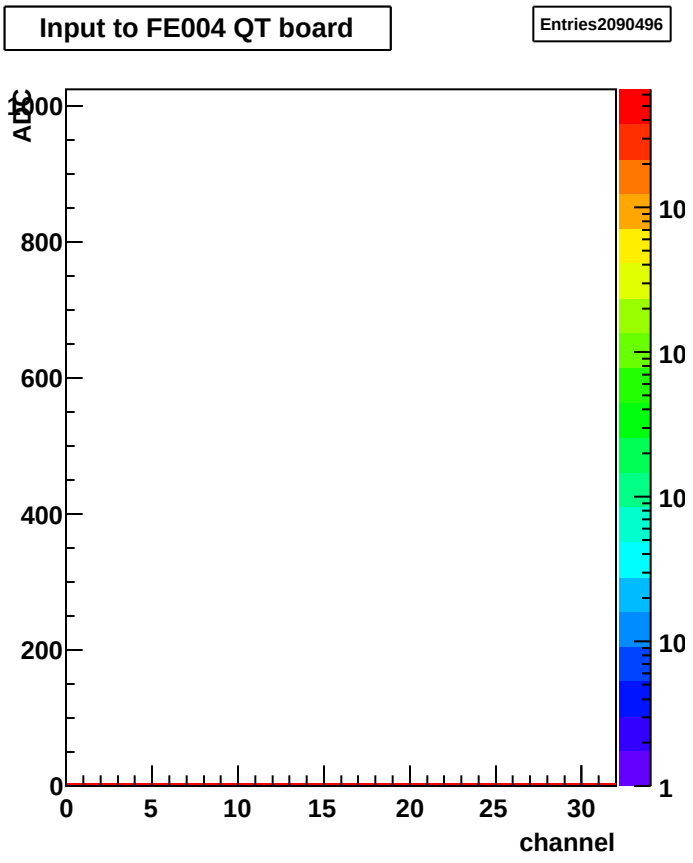

Input to FPD L2 DSM
Entries 130656
HT bits - simulated
65328
SMALL
LARGE-S
LARGE-N

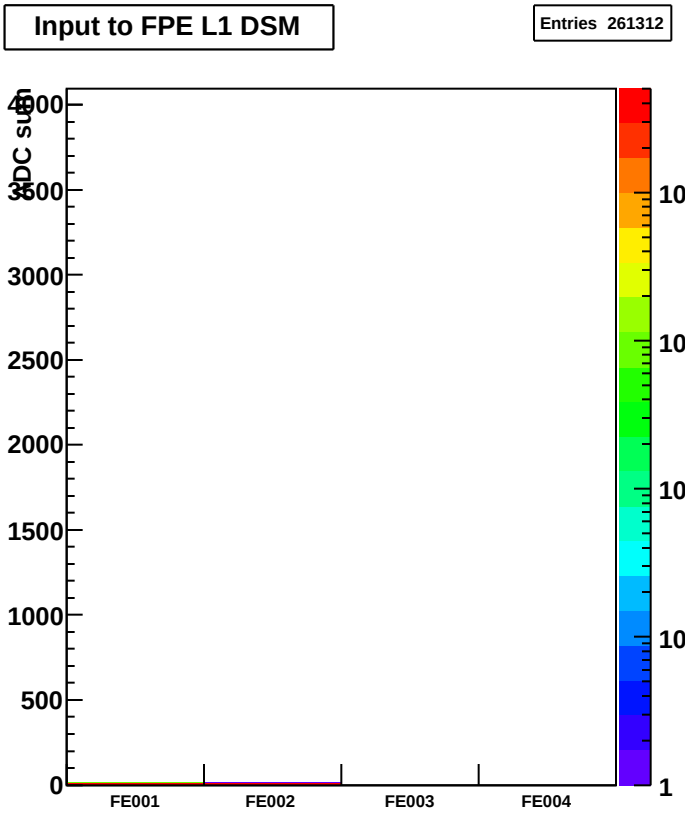

Input to FPD L2 DSM
Entries 261312
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

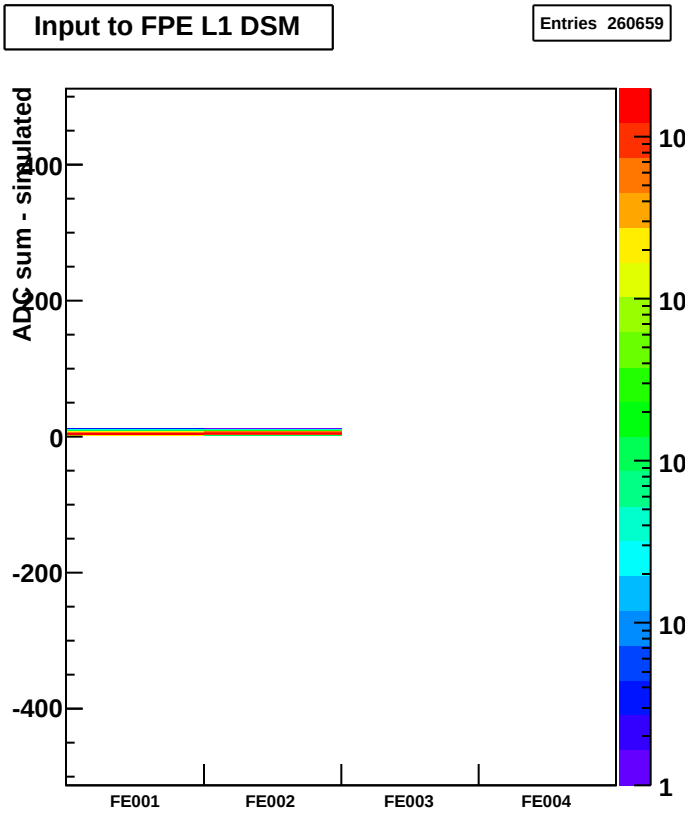

Input to FE001 QT board
Entries2090496
ADC
channel

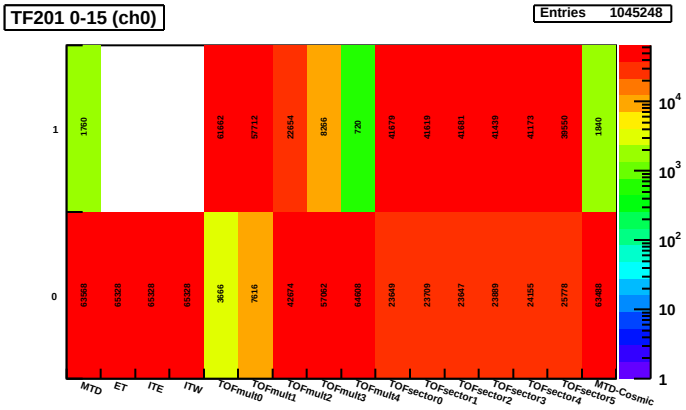

Input to FE002 QT board
Entries2090496
ADC
channel

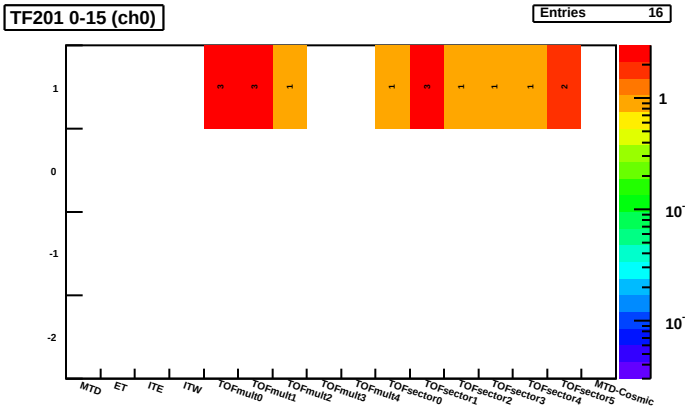

Input to FE003 QT board
Entries2090496
ADC
channel

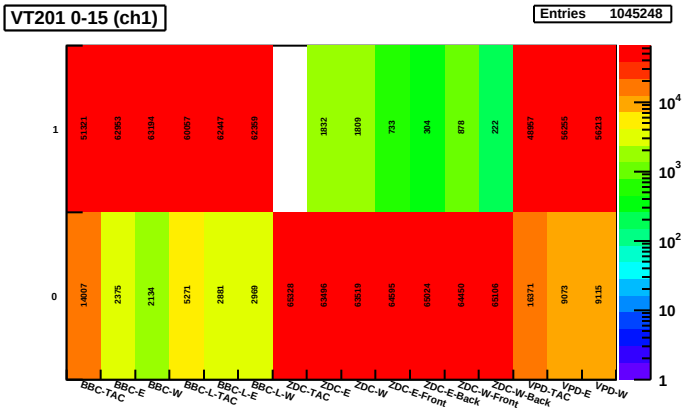

Input to FE004 QT board
Entries2090496
ADC
channel

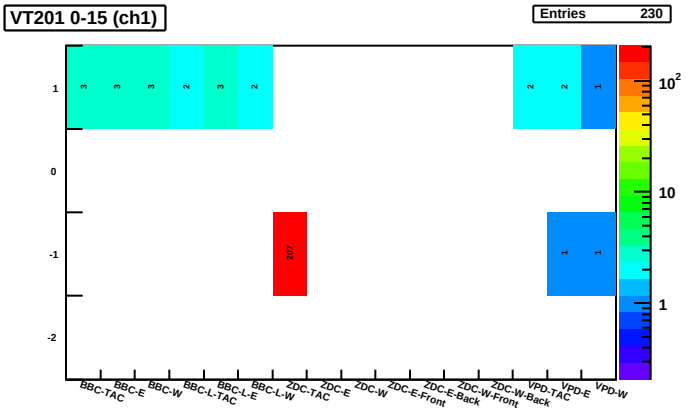

Input to FPE L1 DSM
Entries 261312
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

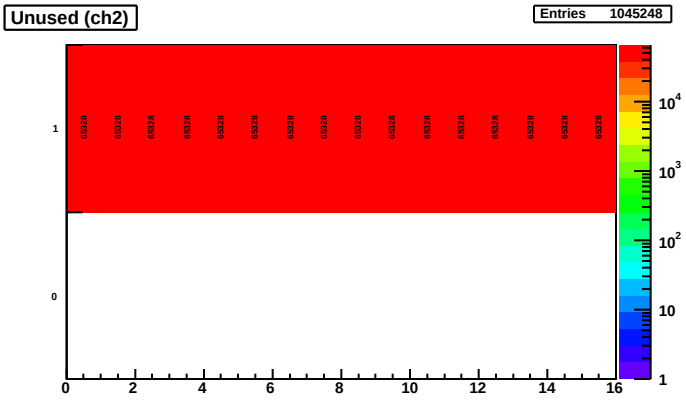

Input to FPE L1 DSM
Entries 260659
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

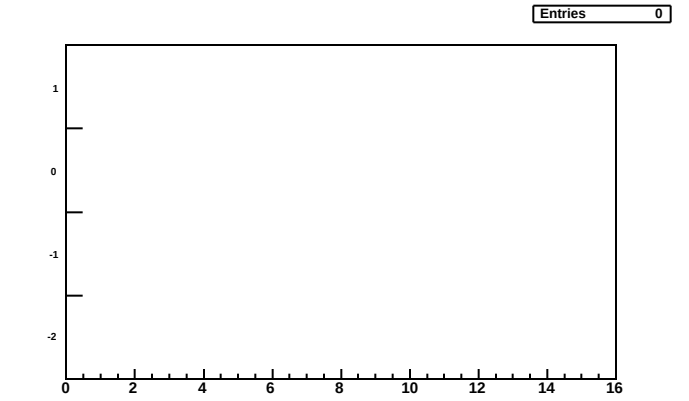

TF201 0-15 (ch0)
Entries 1045248
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

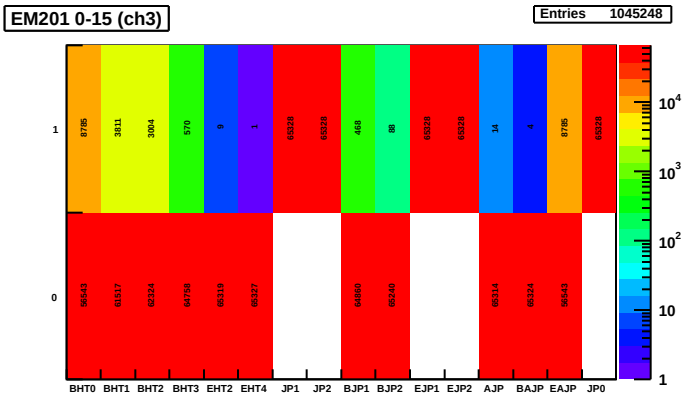

TF201 0-15 (ch0)
Entries 16
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

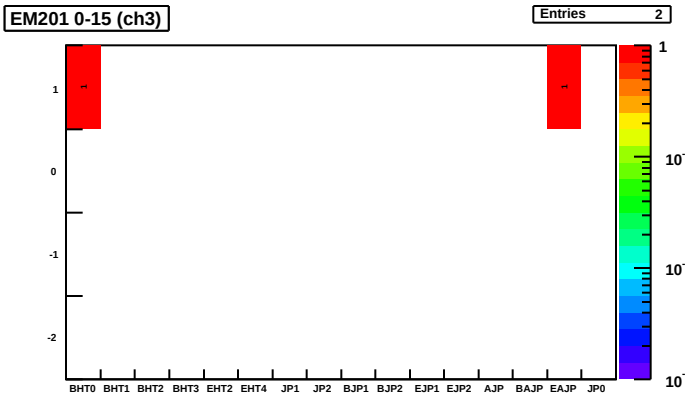

VT201 0-15 (ch1)
Entries 1045248
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 230
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

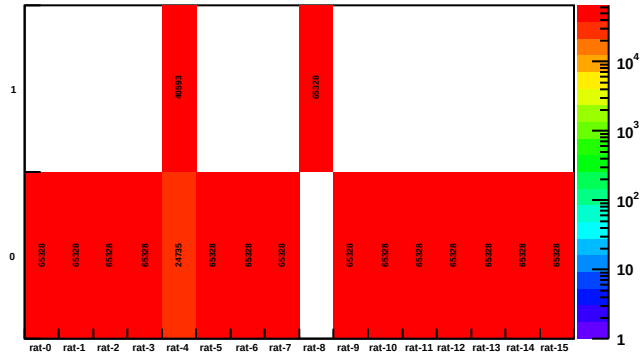

Unused (ch2)
Entries 1045248

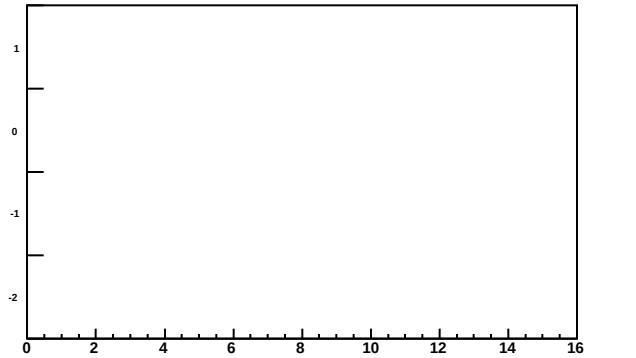

Entries 0


EM201 0-15 (ch3)
Entries 1045248
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

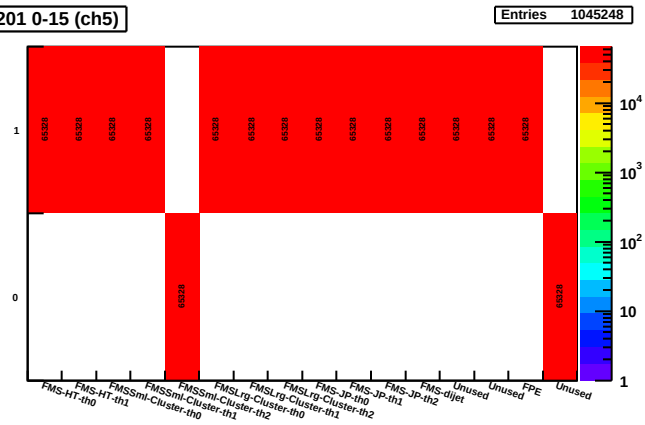

EM201 0-15 (ch3)
Entries 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

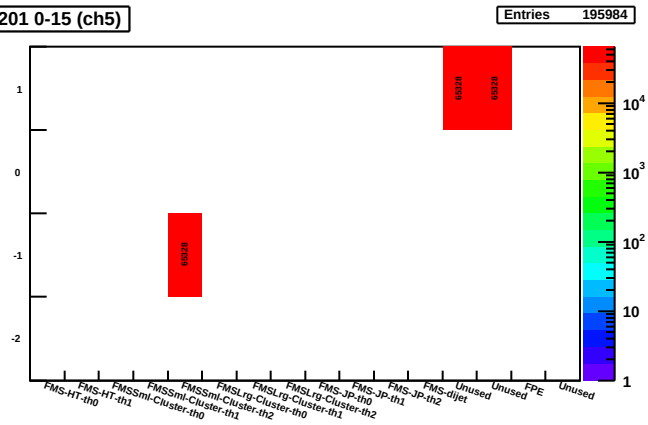




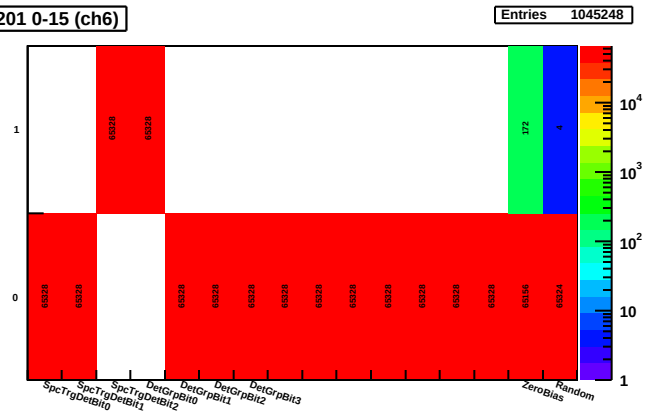
FP201 0-15 (ch5)

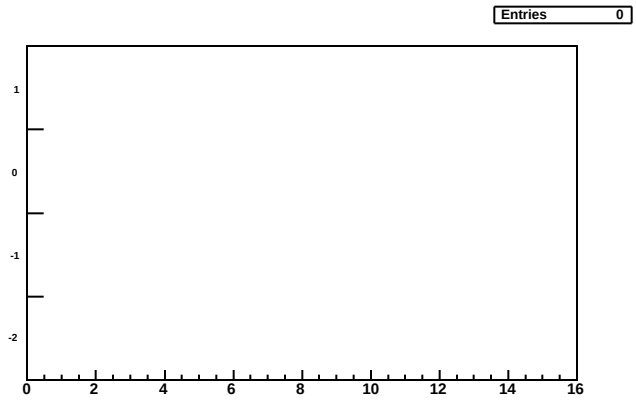
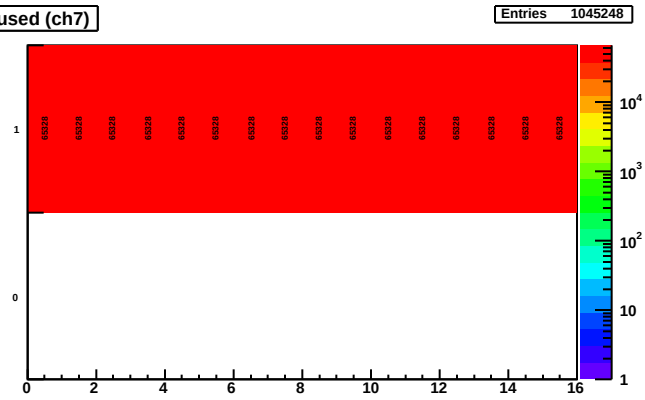
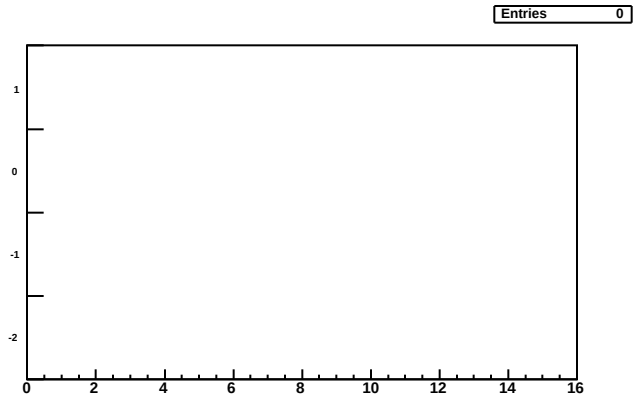


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)