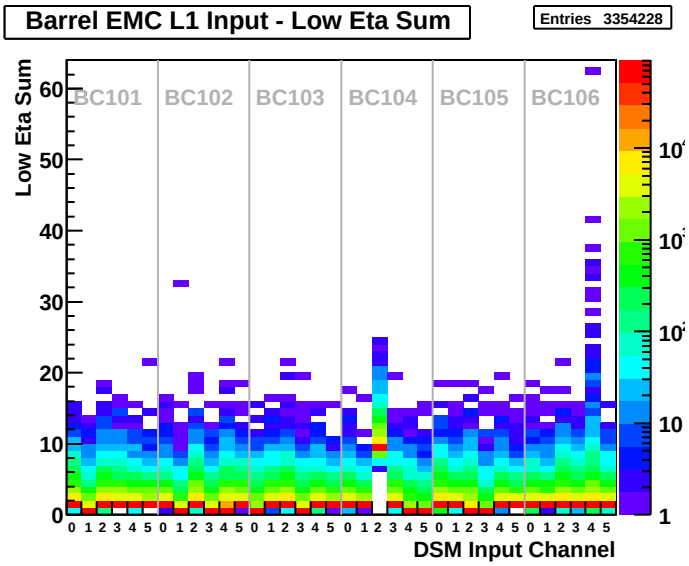
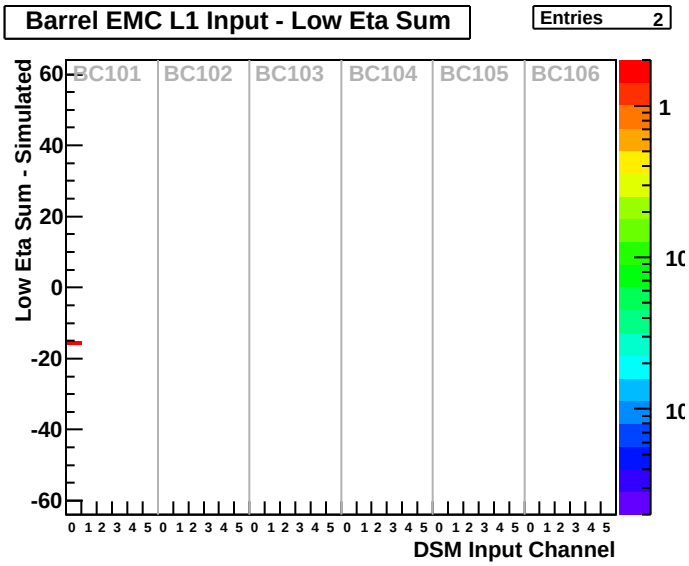
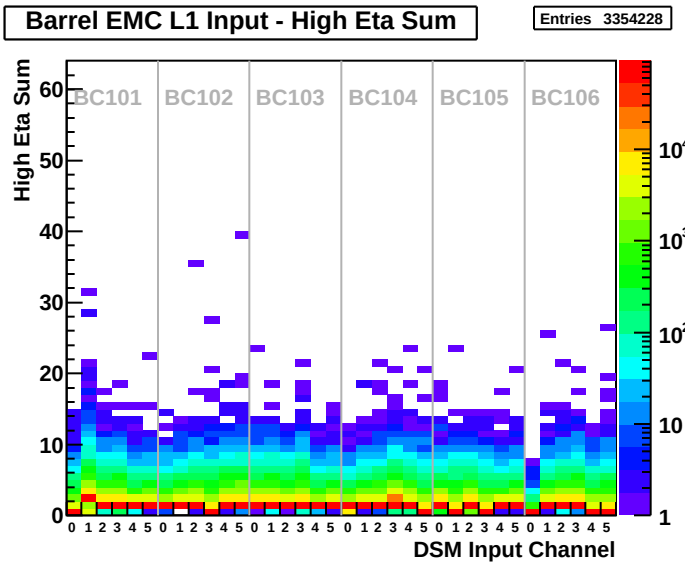
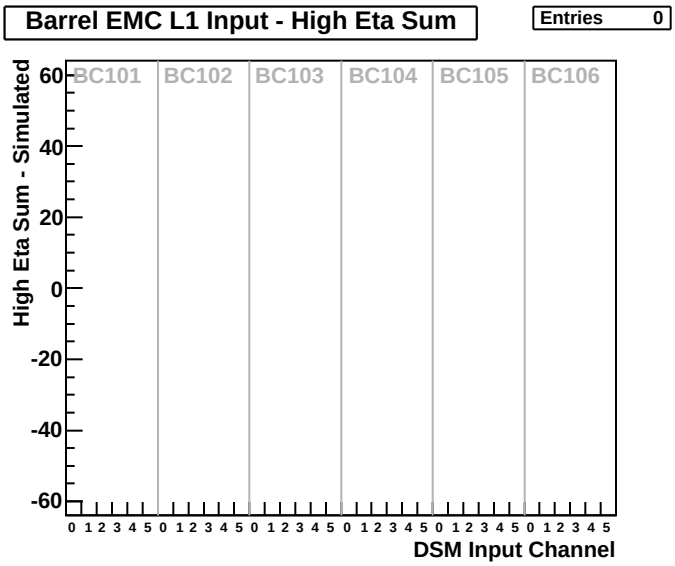
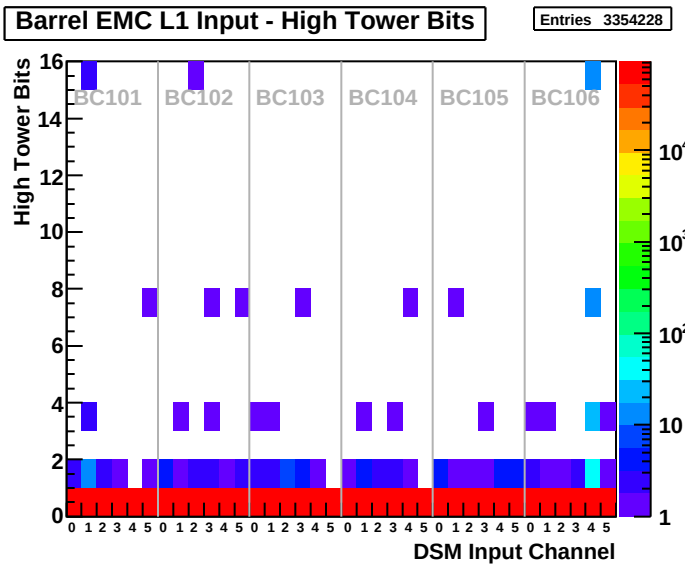
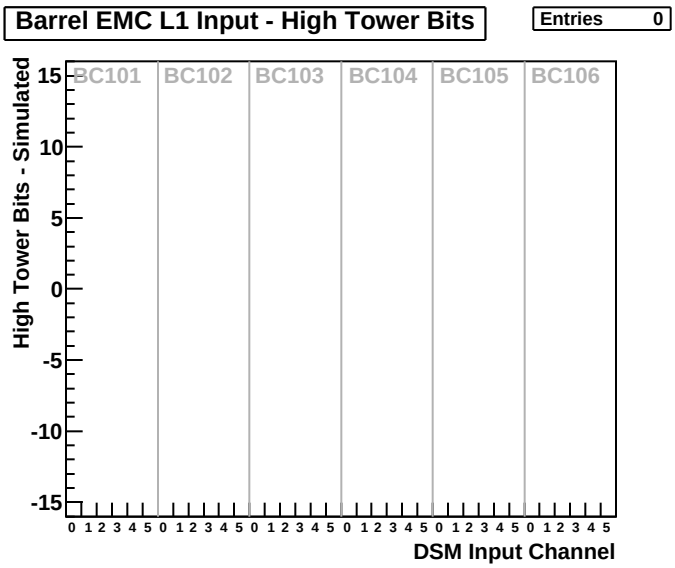

Barrel EMC L1 Input - Low Eta Sum
Entries 3354228
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

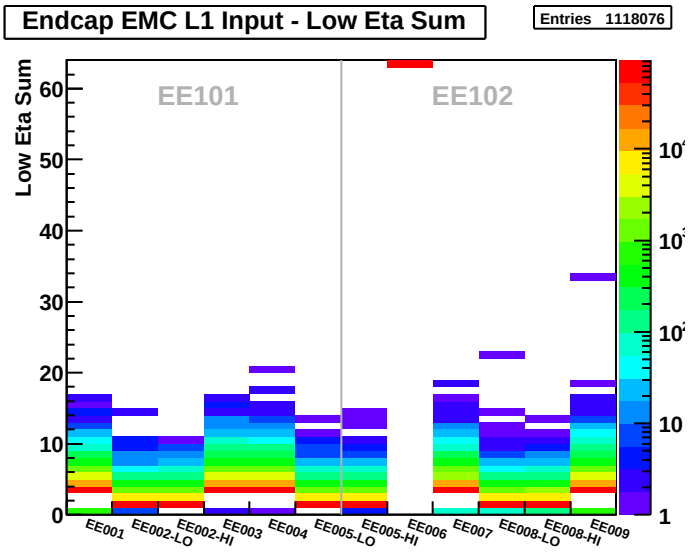

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

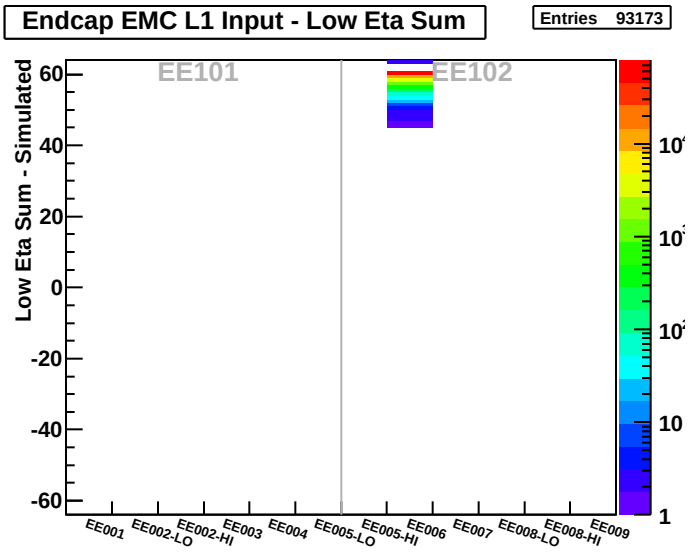

Barrel EMC L1 Input - High Eta Sum
Entries 3354228
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

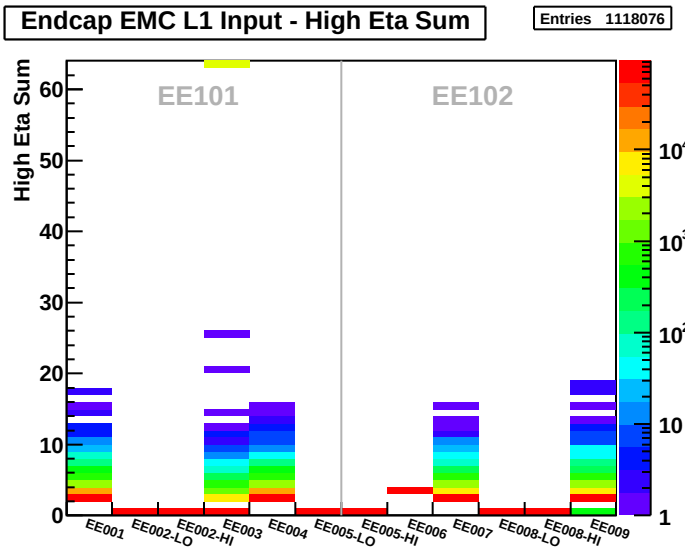

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

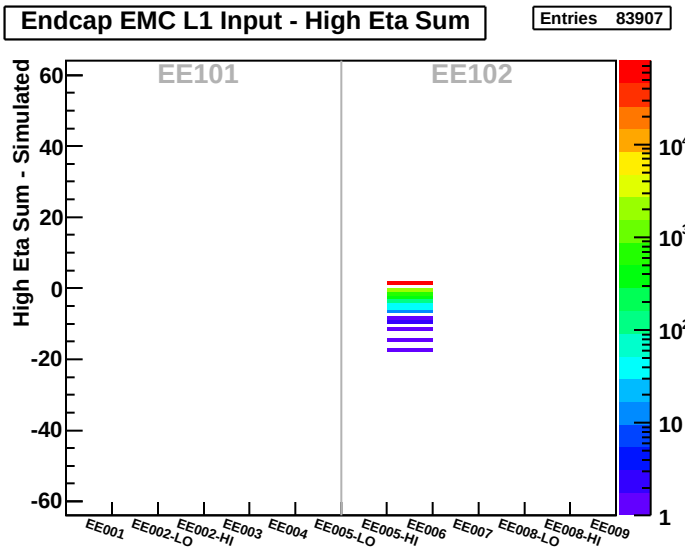

Barrel EMC L1 Input - High Tower Bits
Entries 3354228
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

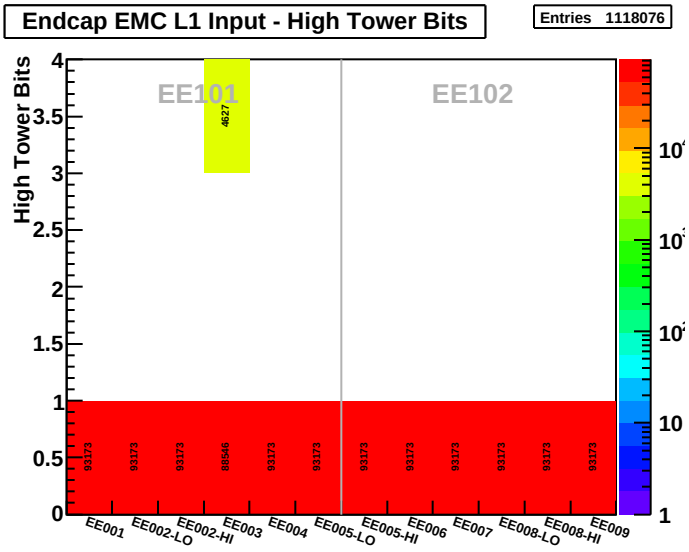

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

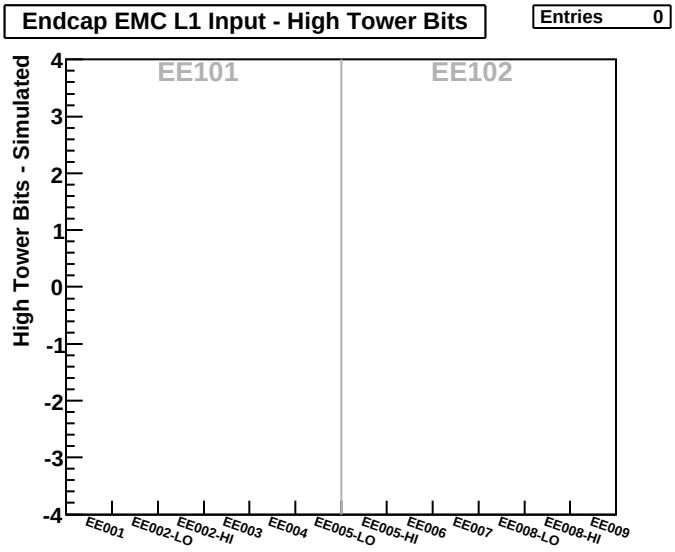

Endcap EMC L1 Input - Low Eta Sum
Entries 1118076
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

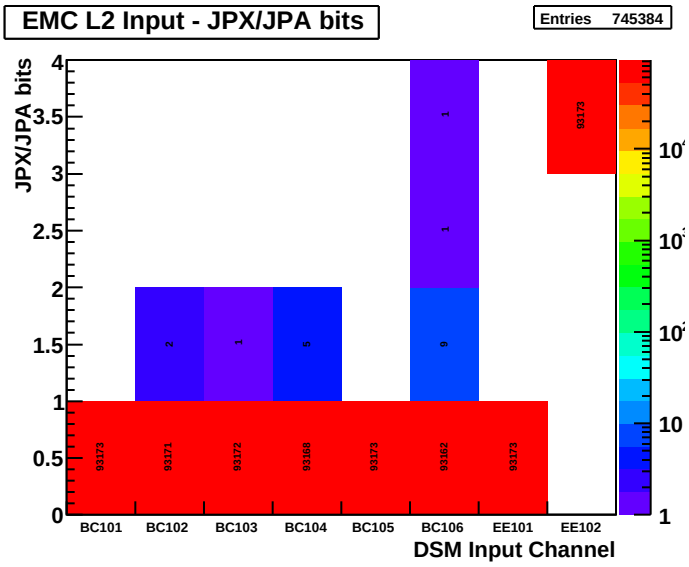

Endcap EMC L1 Input - Low Eta Sum
Entries 93173
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

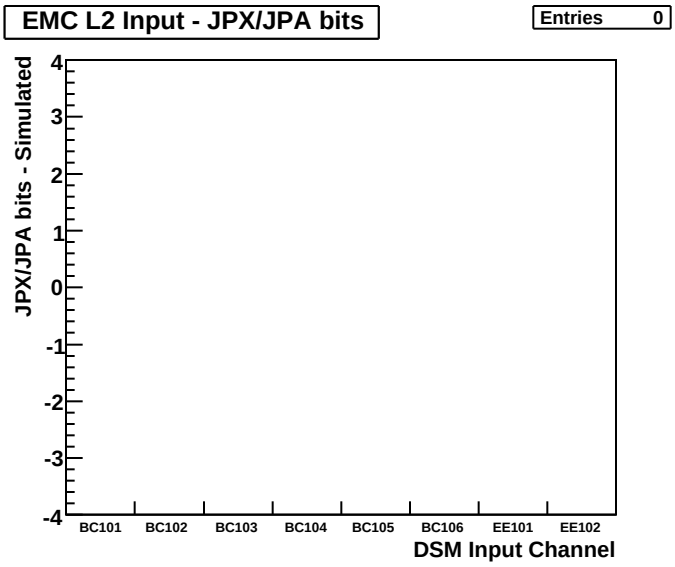

Endcap EMC L1 Input - High Eta Sum
Entries 1118076
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

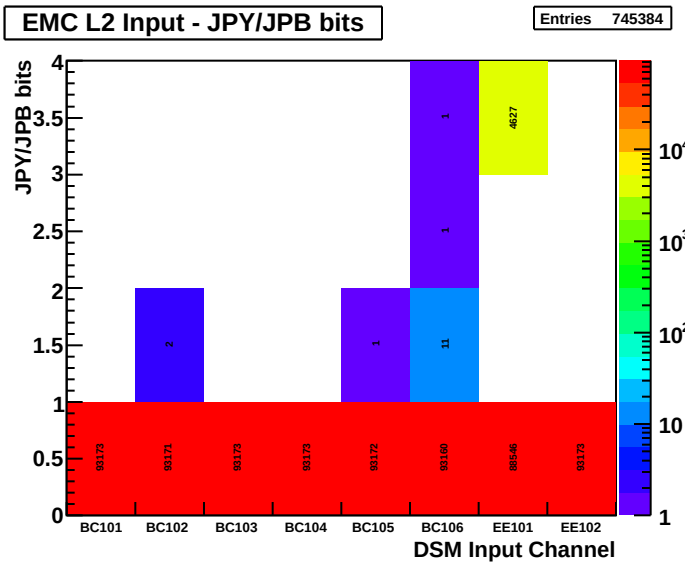

Endcap EMC L1 Input - High Eta Sum
Entries 83907
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

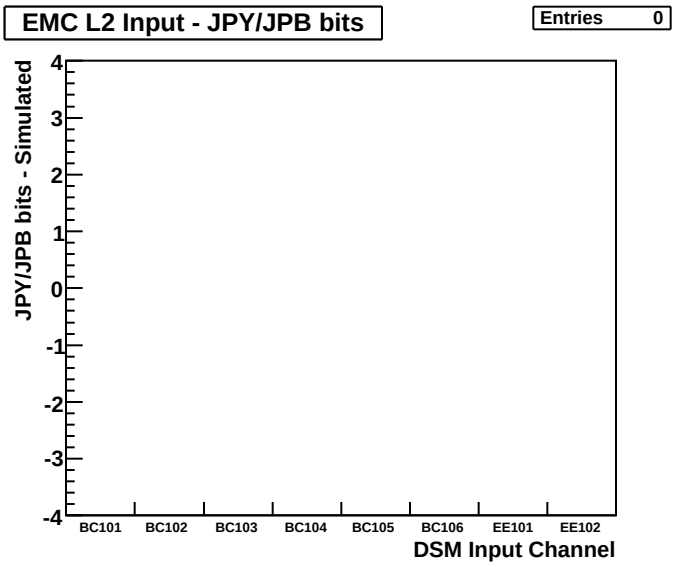

Endcap EMC L1 Input - High Tower Bits
Entries 1118076
High Tower Bits
EE101
EE102
4627
93173
88546
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

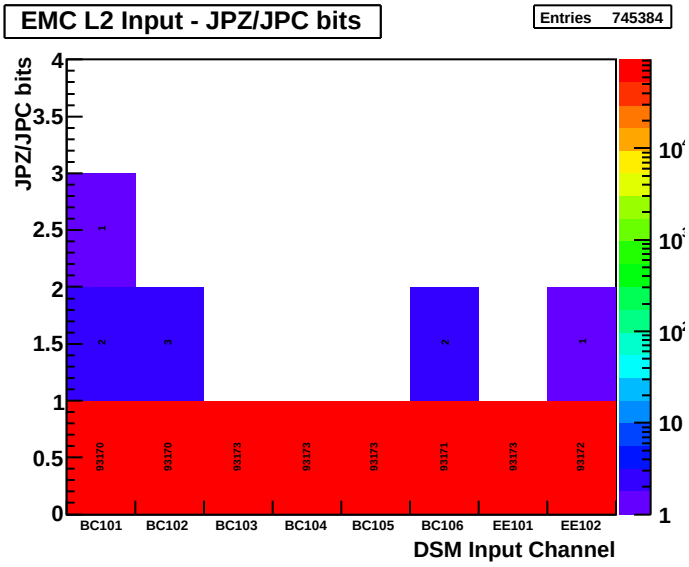

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

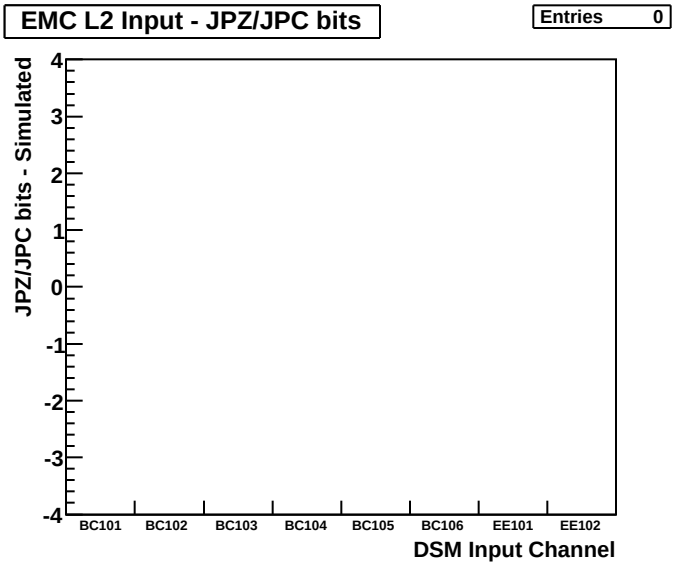

EMC L2 Input - JPX/JPA bits
Entries 745384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

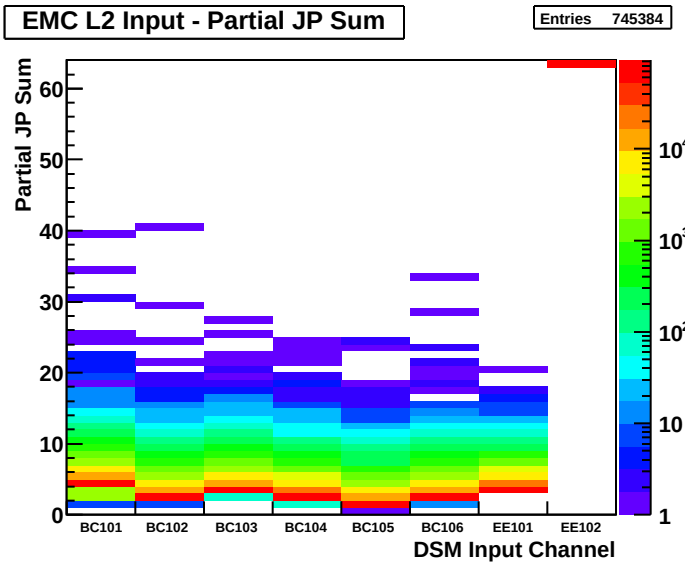

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

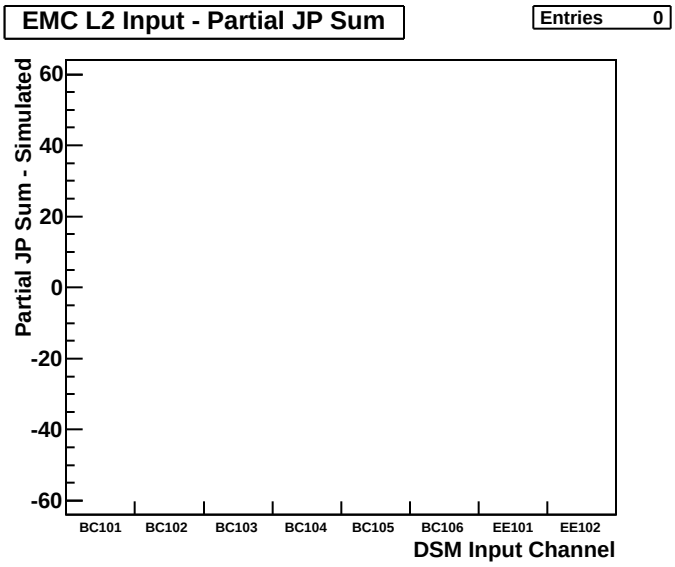

EMC L2 Input - JPY/JPB bits
Entries 745384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

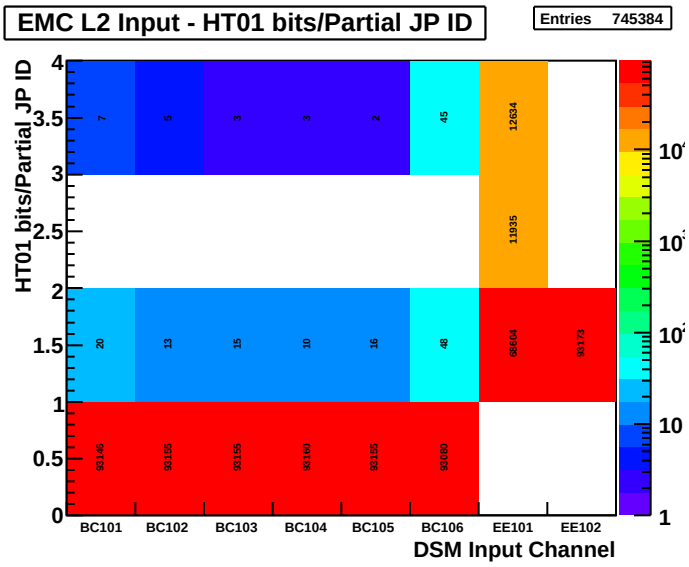

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

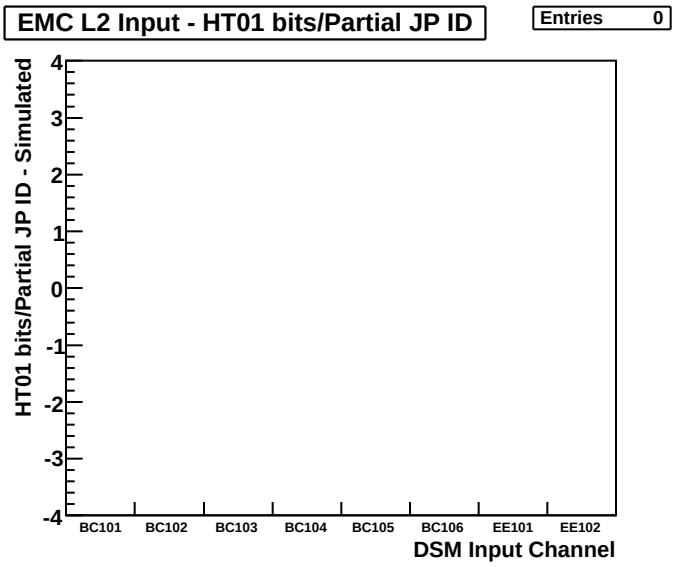

EMC L2 Input - JPZ/JPC bits
Entries 745384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

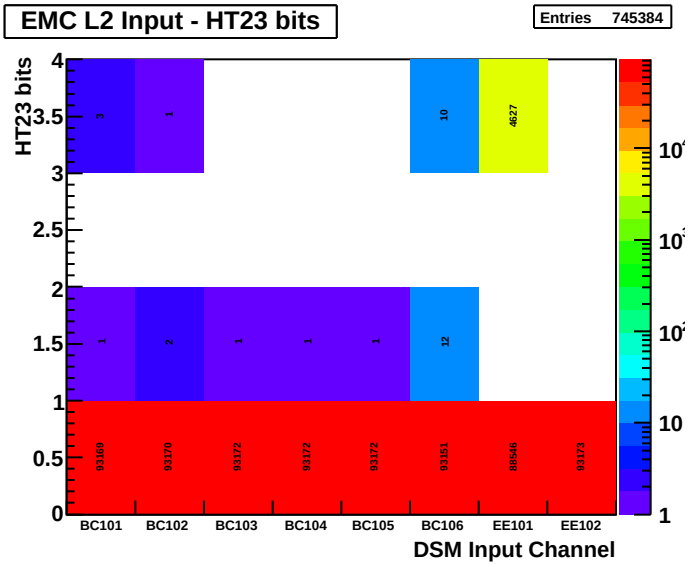

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

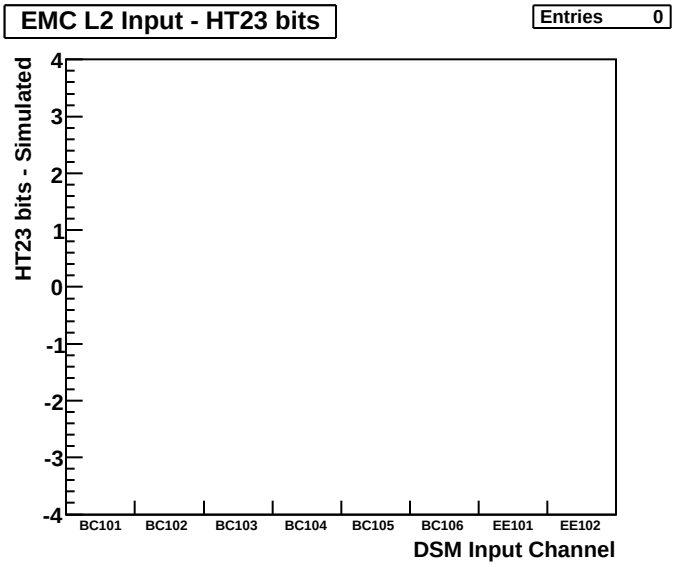

EMC L2 Input - Partial JP Sum
Entries 745384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

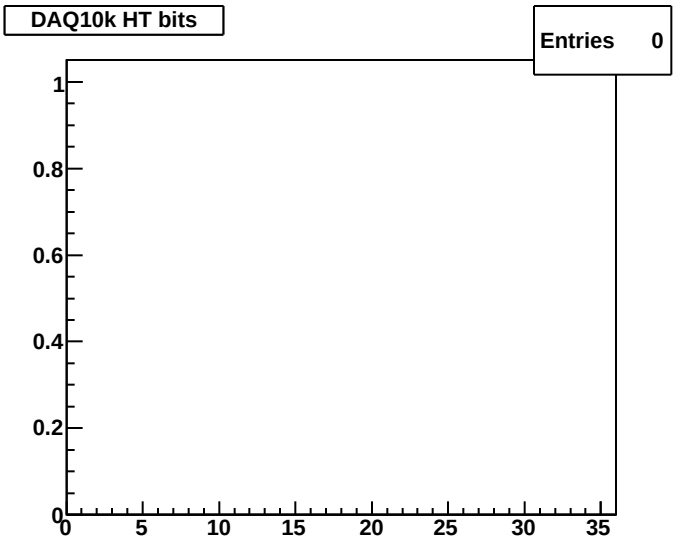

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

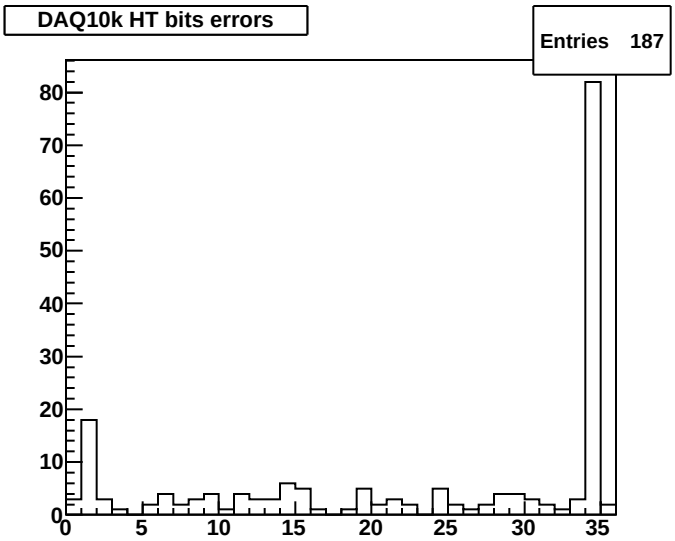

EMC L2 Input - HT01 bits/Partial JP ID
Entries 745384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

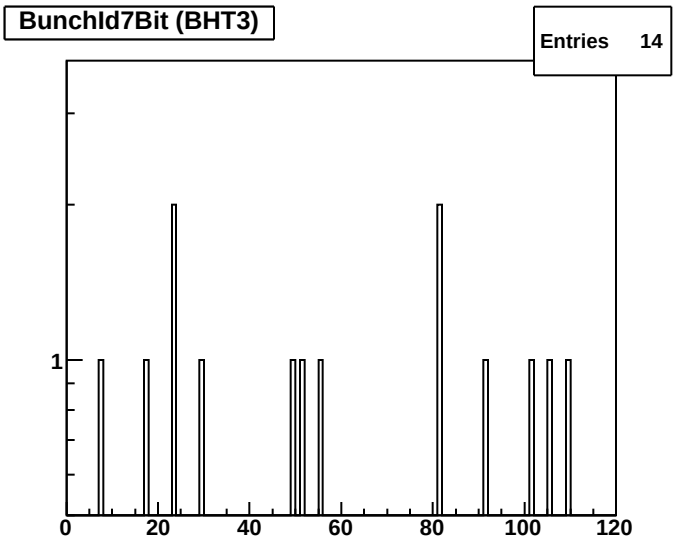

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

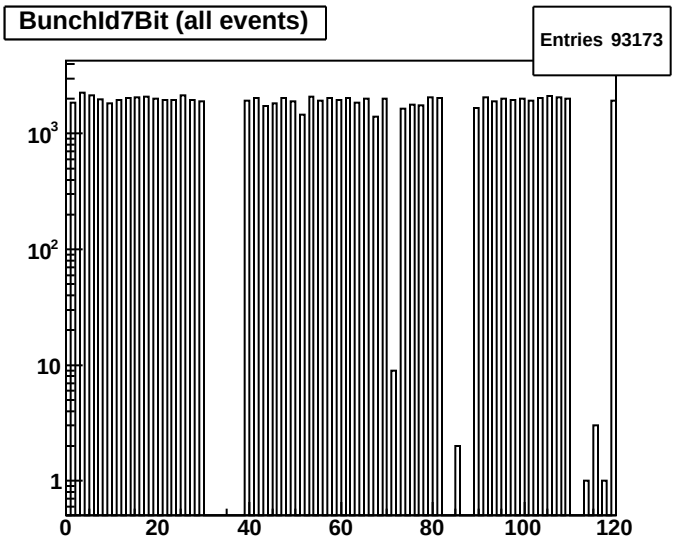

EMC L2 Input - HT23 bits
Entries 745384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

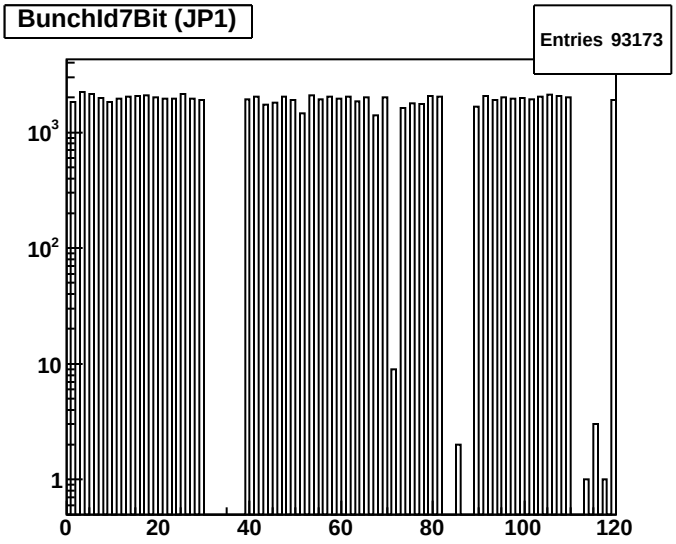

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

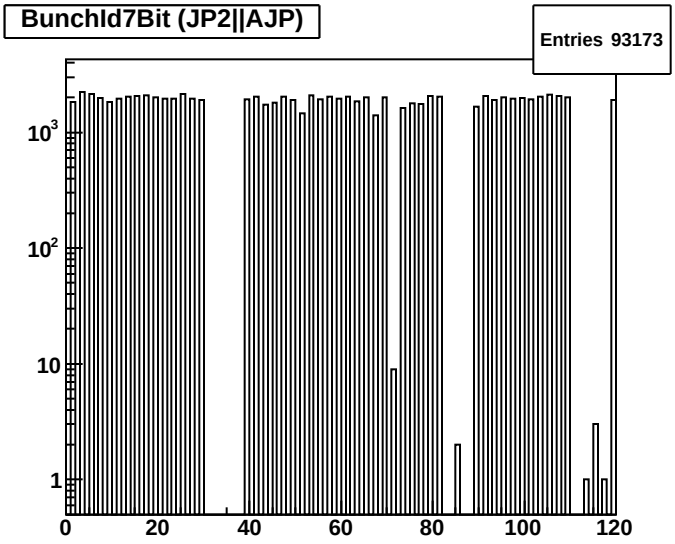

DAQ10k HT bits
Entries 0

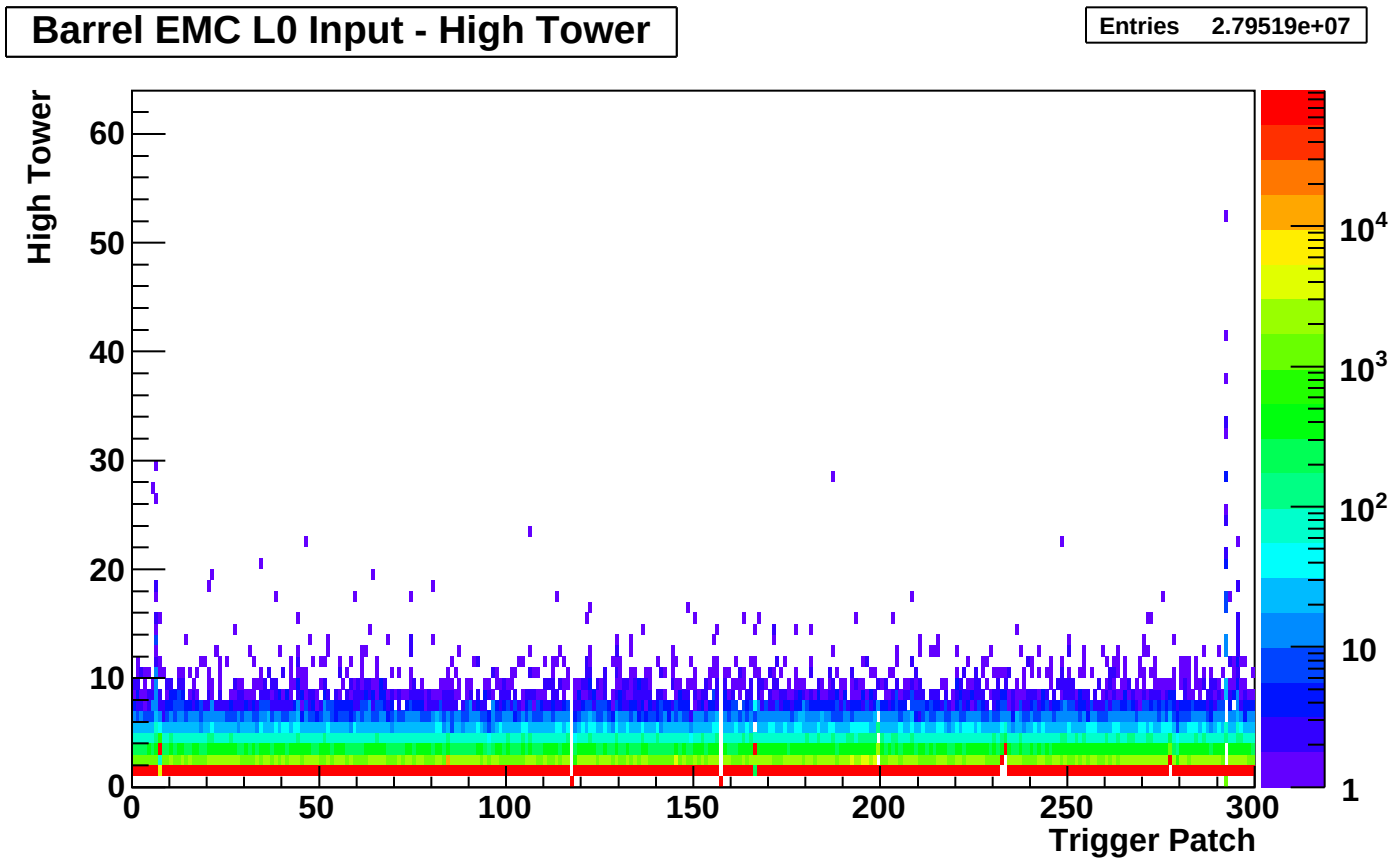

DAQ10k HT bits errors
Entries 187

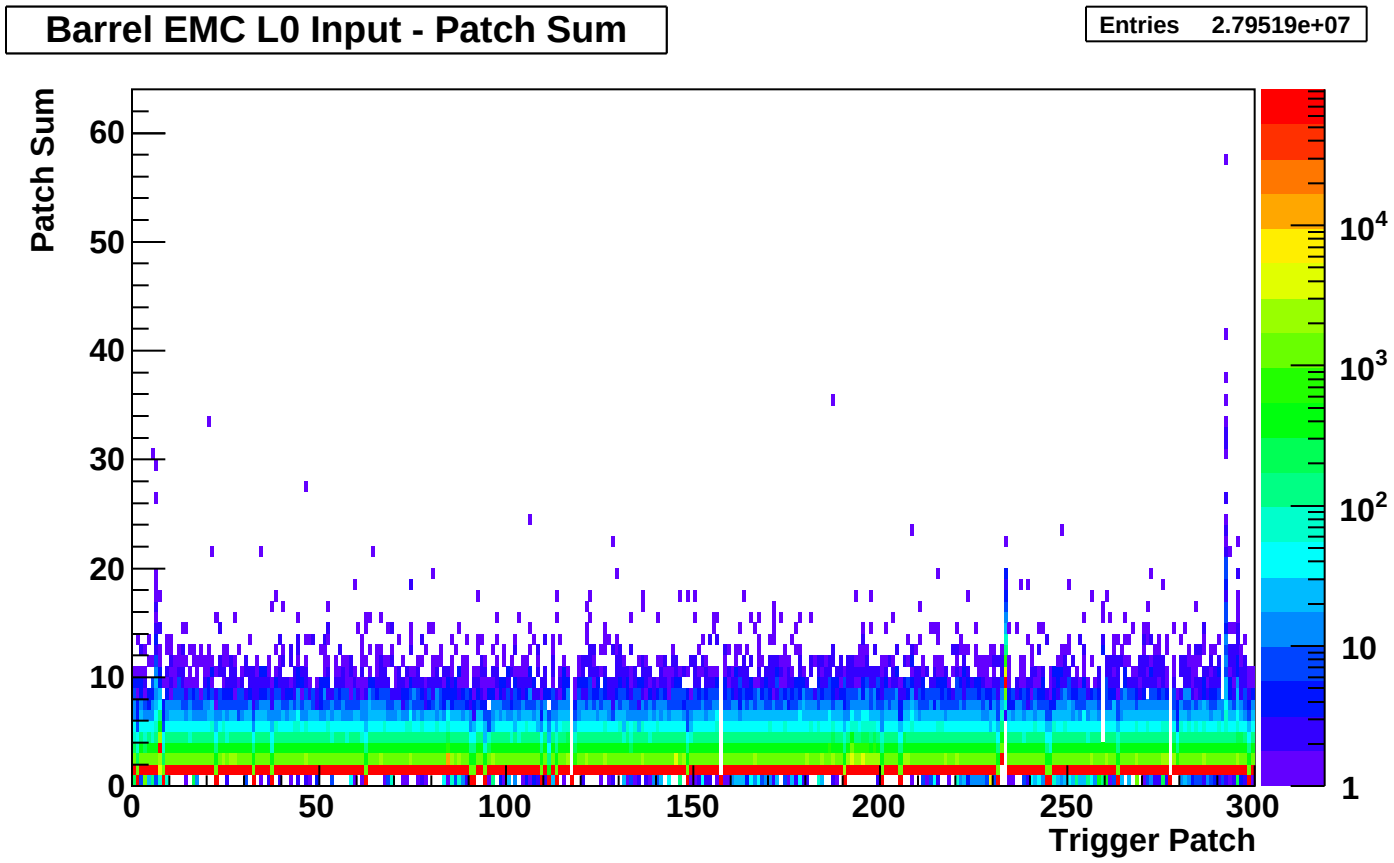

BunchId7Bit (BHT3)
Entries 14

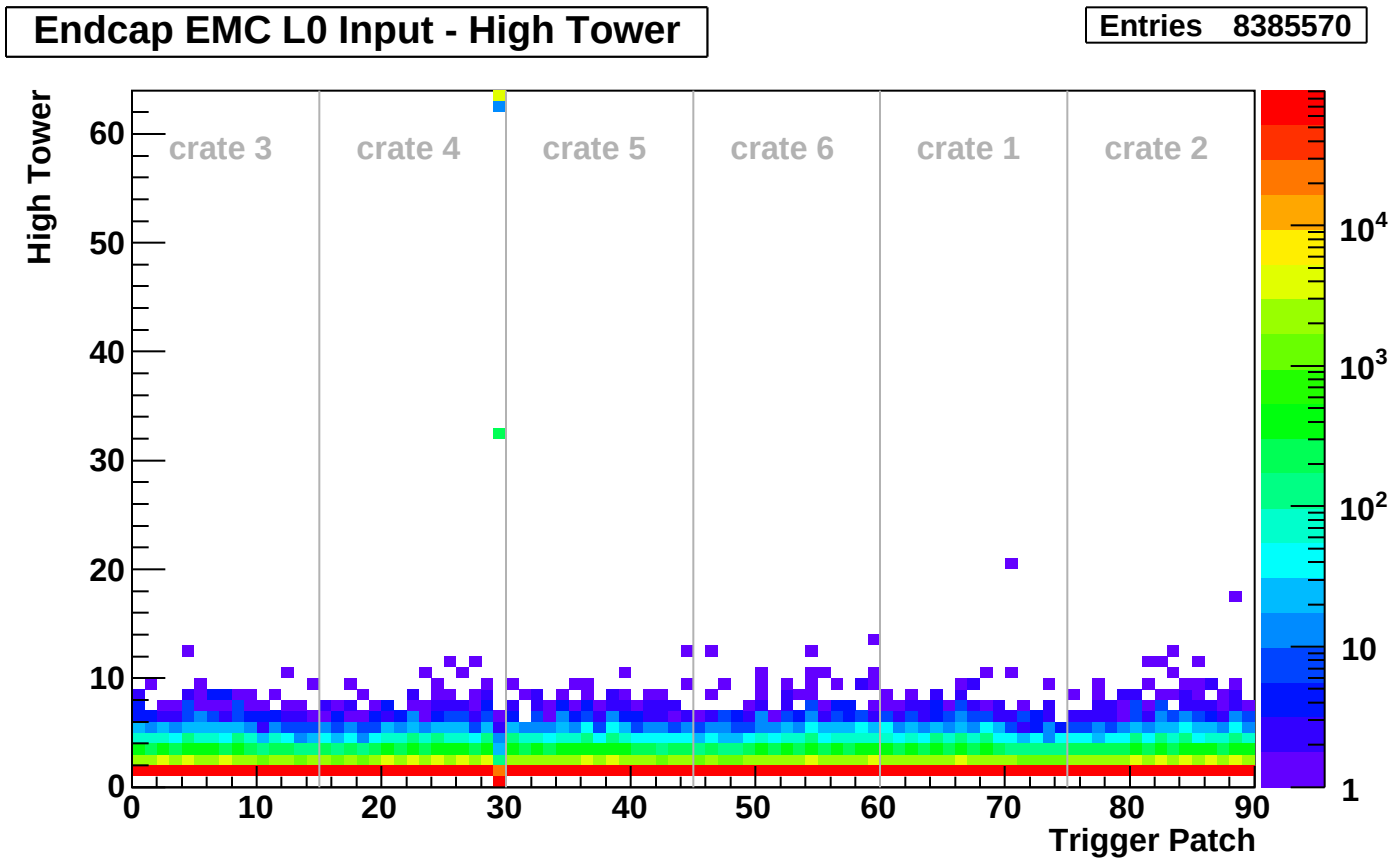

BunchId7Bit (all events)
Entries 93173

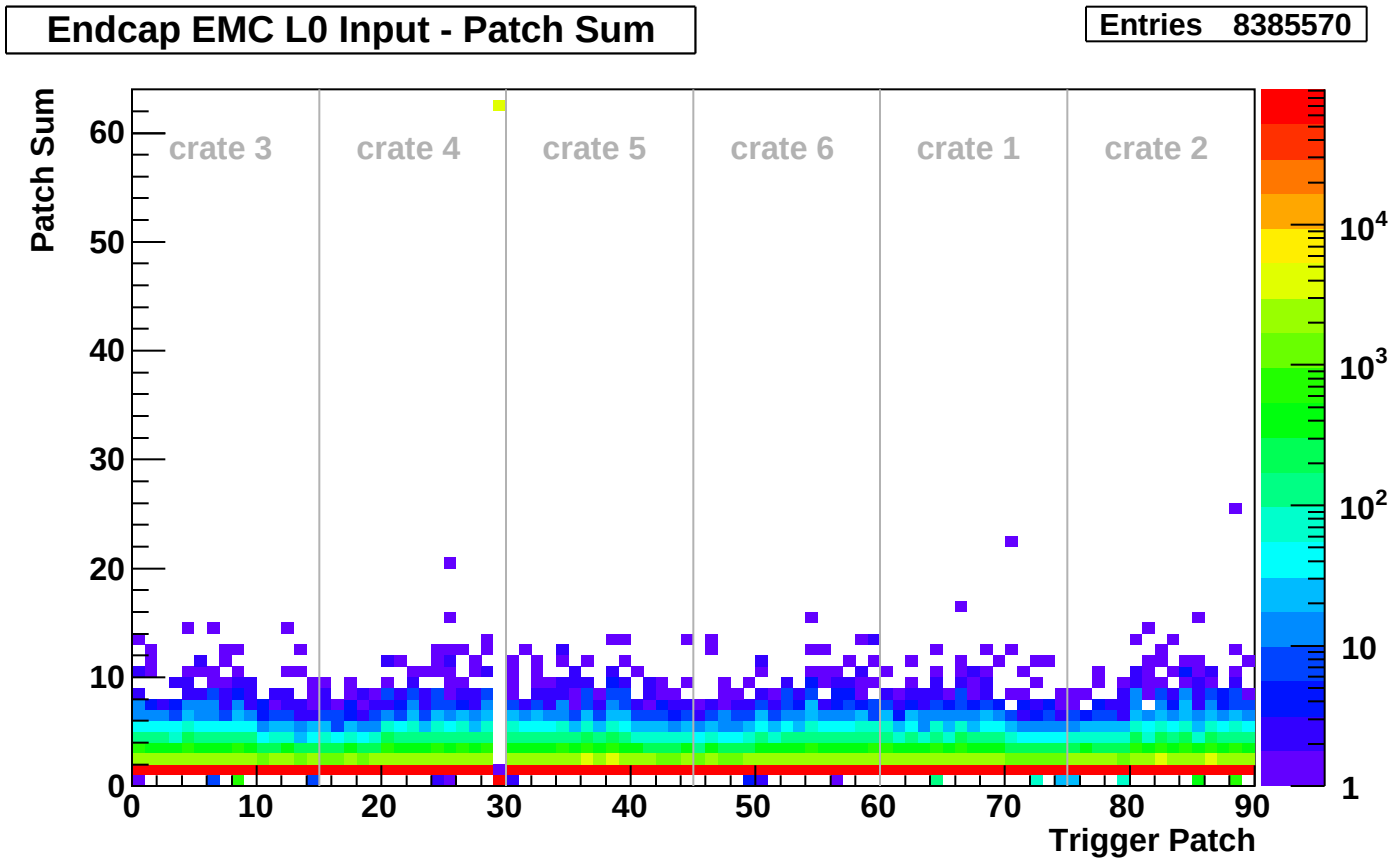

BunchId7Bit (JP1)
Entries 93173

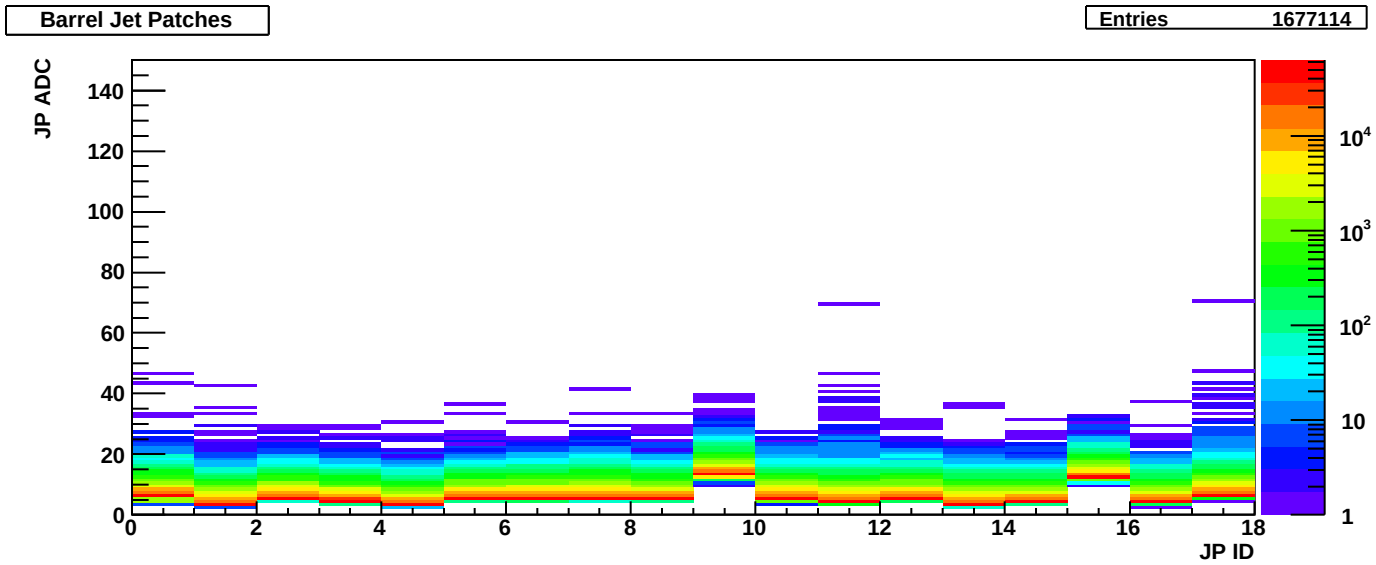

BunchId7Bit (JP2||AJP)
Entries 93173

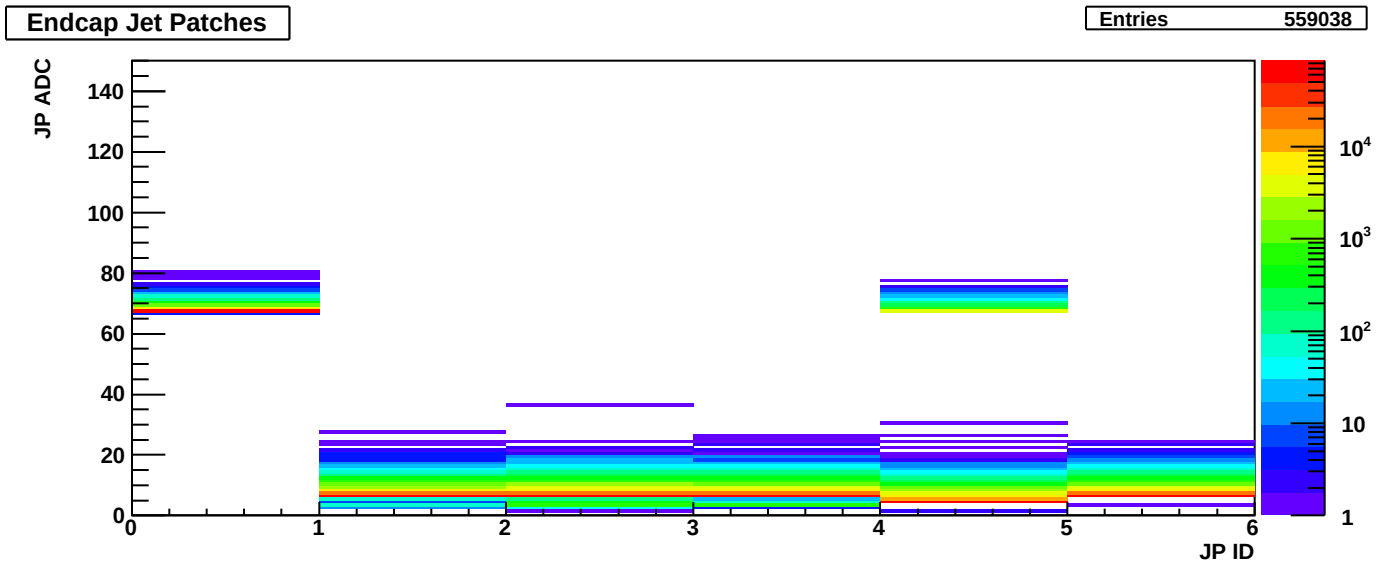

Barrel EMC L0 Input - High Tower
Entries 2.79519e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

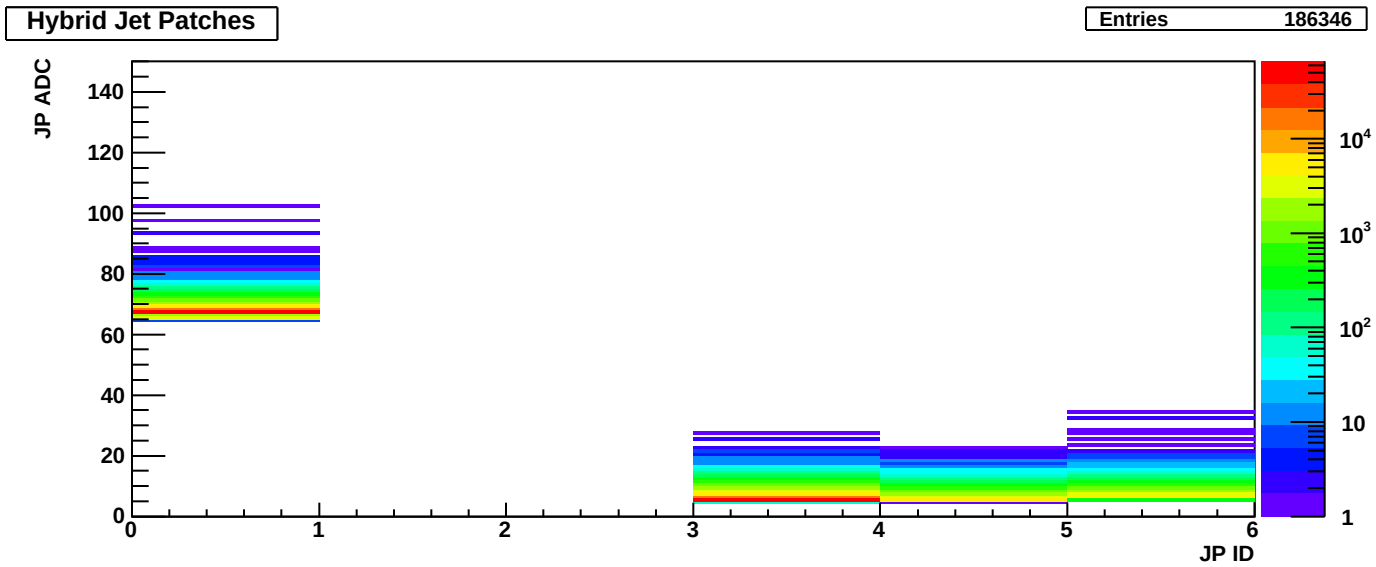

Barrel EMC L0 Input - Patch Sum
Entries 2.79519e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

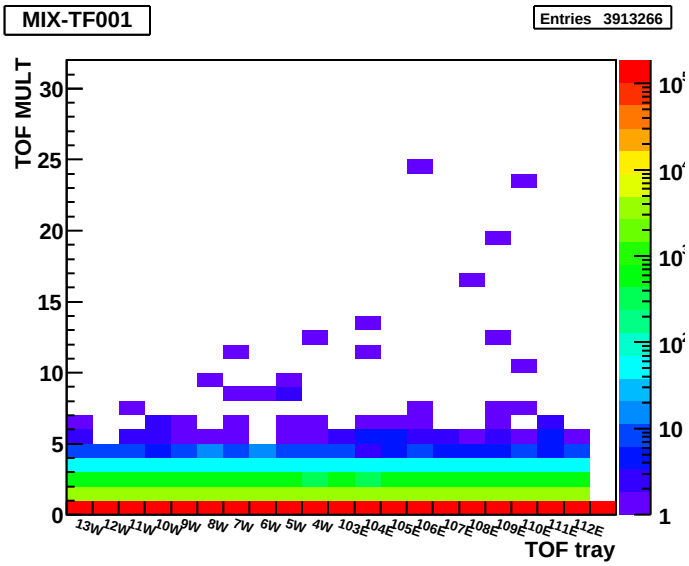

Endcap EMC L0 Input - High Tower
Entries 8385570
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

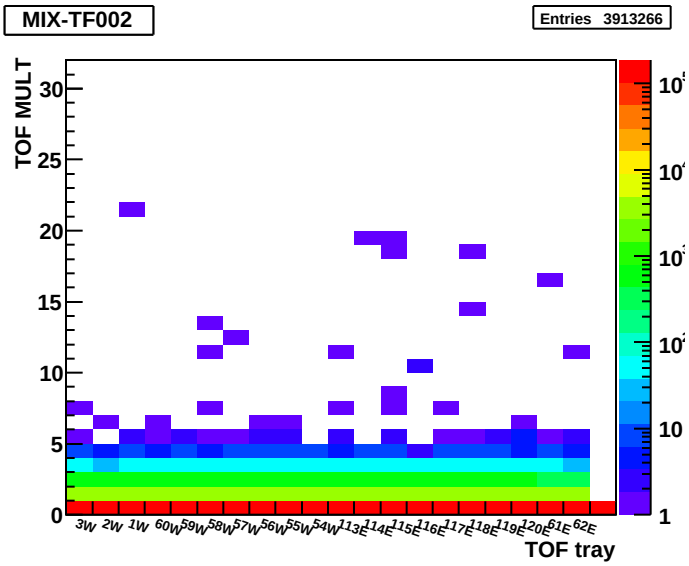

Endcap EMC L0 Input - Patch Sum
Entries 8385570
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

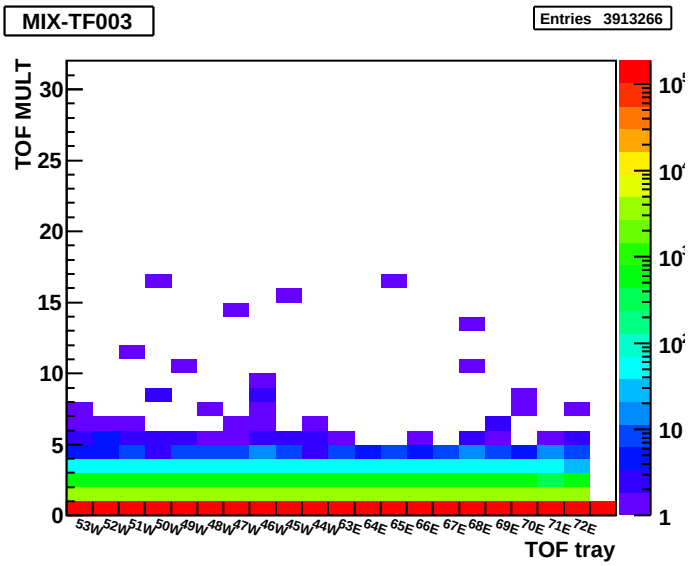

Barrel Jet Patches
Entries 1677114
JP ADC
JP ID

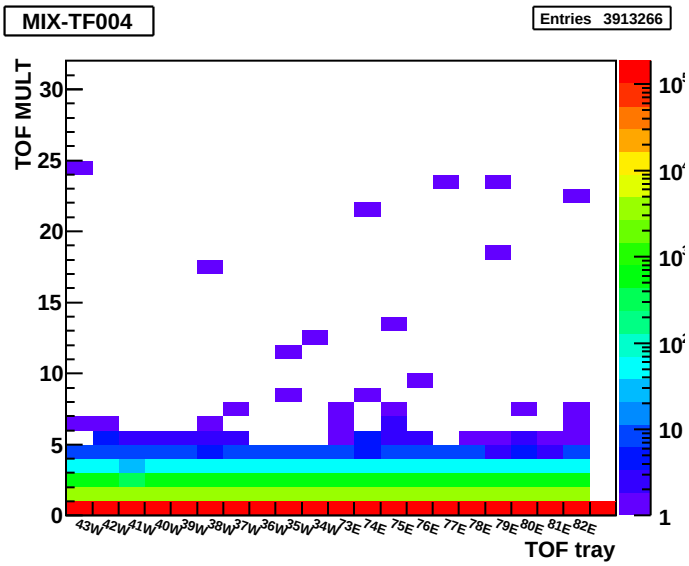

Endcap Jet Patches
Entries 559038
JP ADC
JP ID

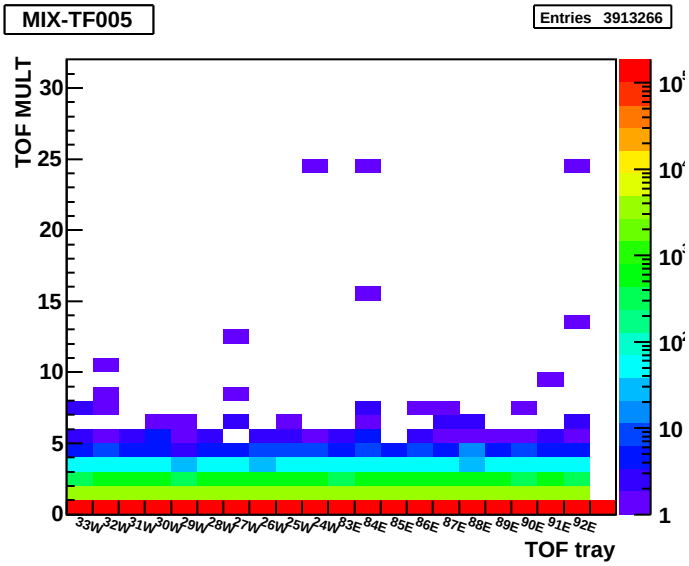

Hybrid Jet Patches
Entries 186346
JP ADC
JP ID

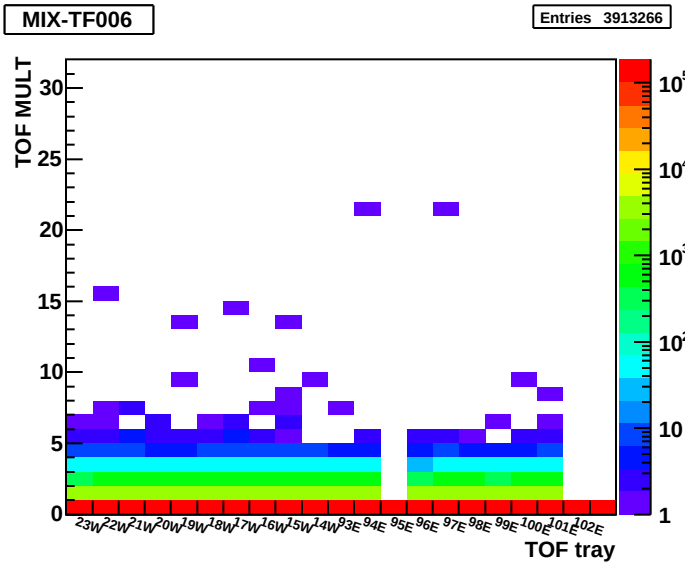

MIX-TF001
Entries 3913266
TOF MULT
TOF tray

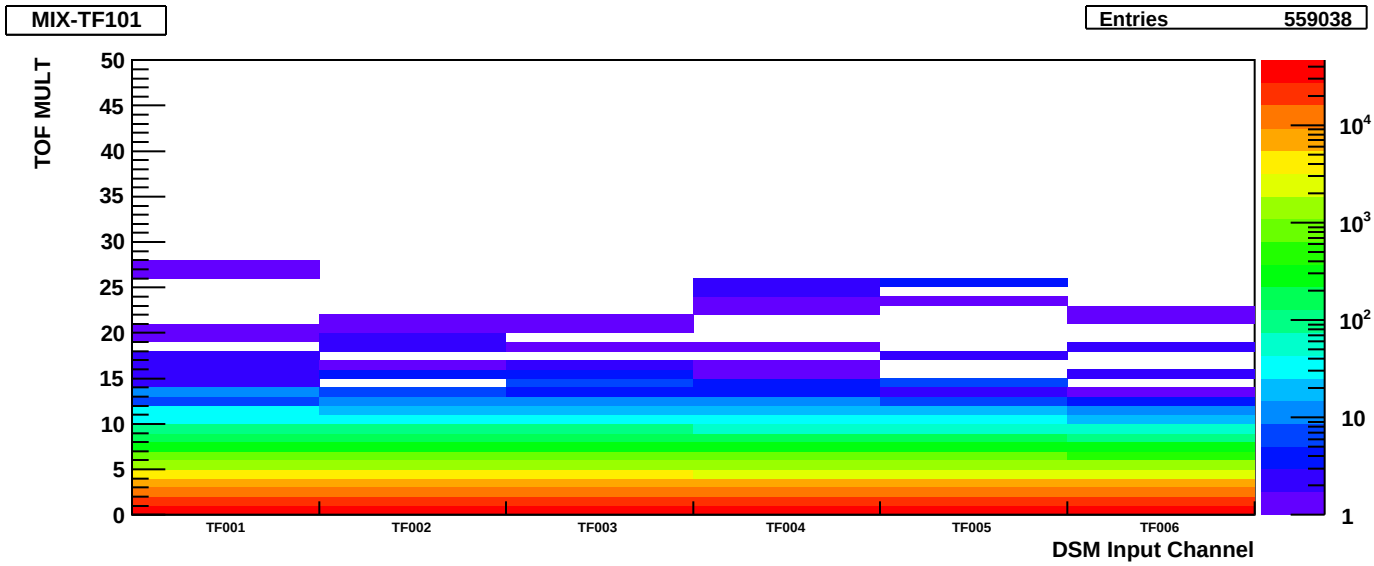

MIX-TF002
Entries 3913266
TOF MULT
TOF tray

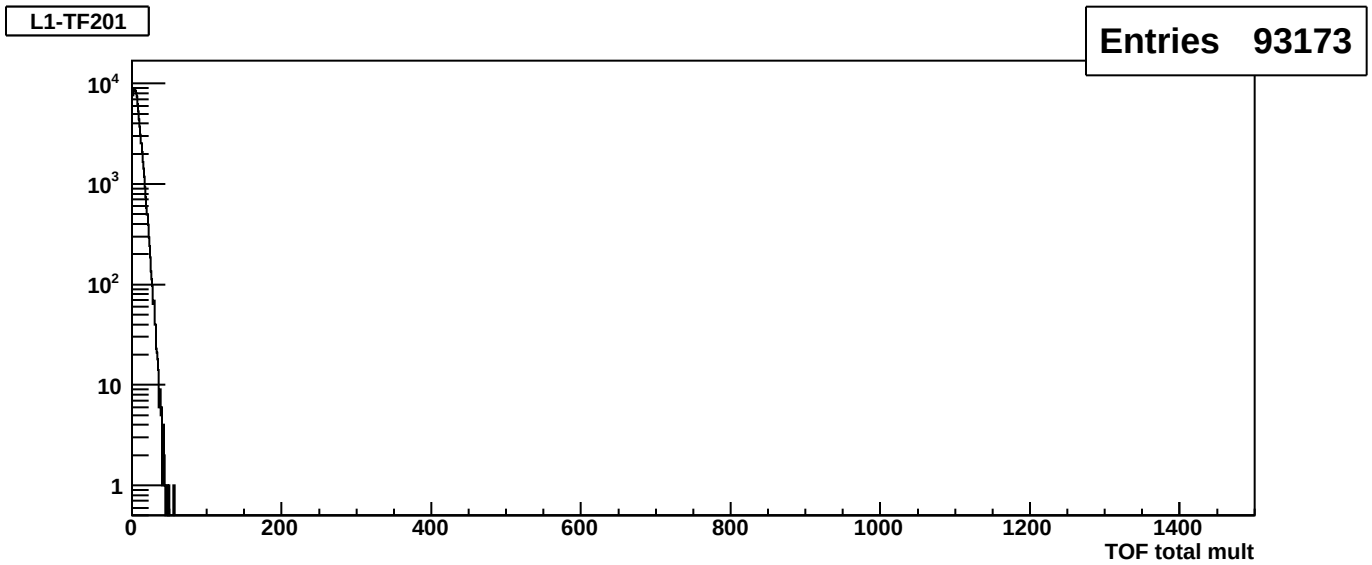

MIX-TF003
Entries 3913266
TOF MULT
TOF tray

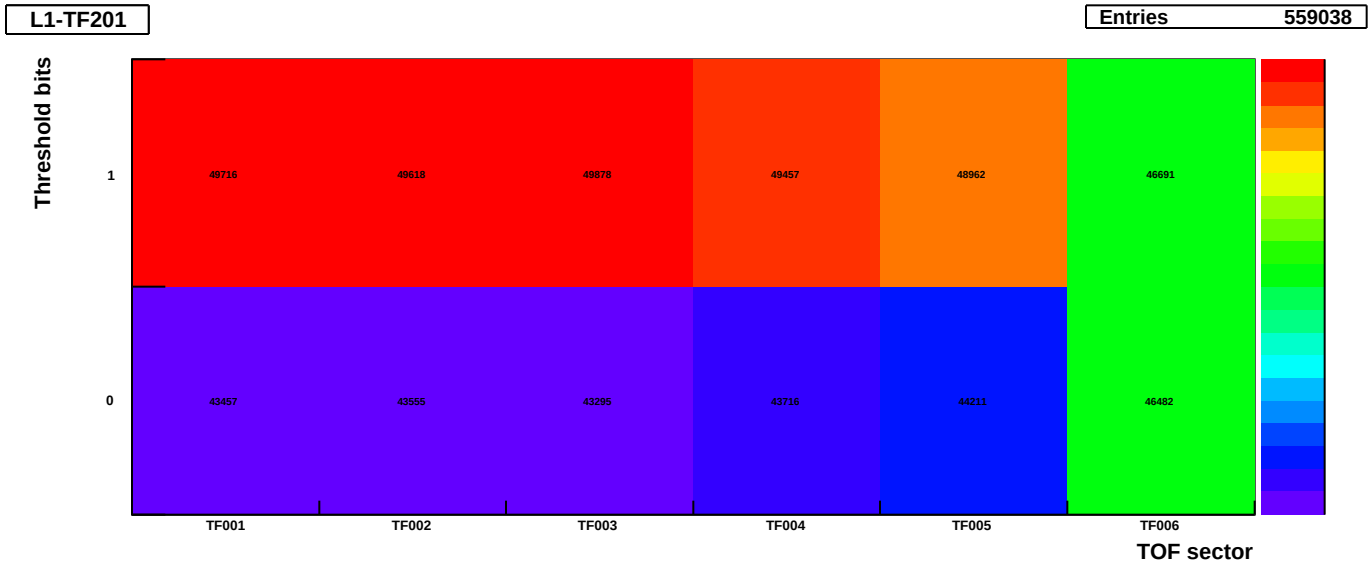

MIX-TF004
Entries 3913266
TOF MULT
TOF tray

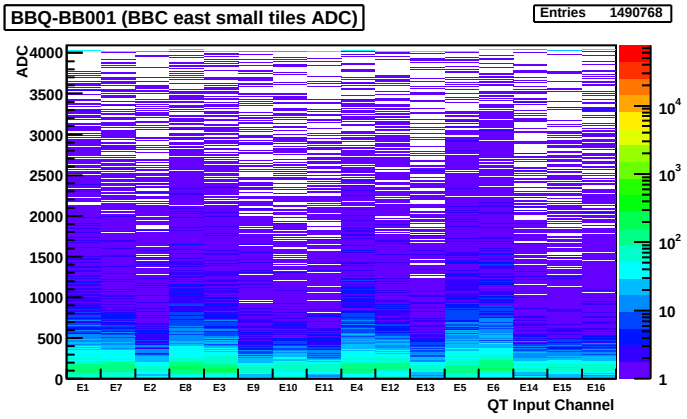

MIX-TF005
Entries 3913266
TOF MULT
TOF tray

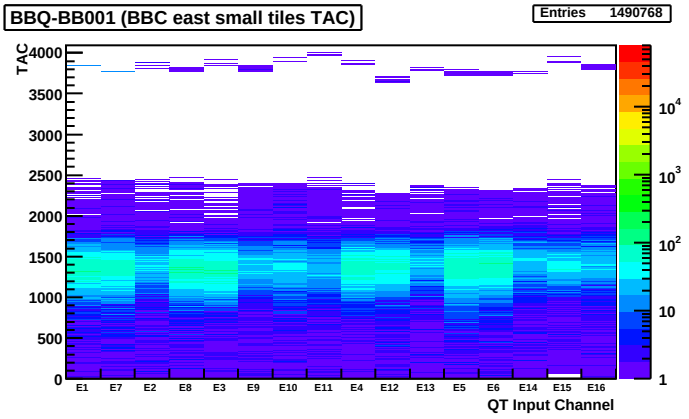

MIX-TF006
Entries 3913266
TOF MULT
TOF tray

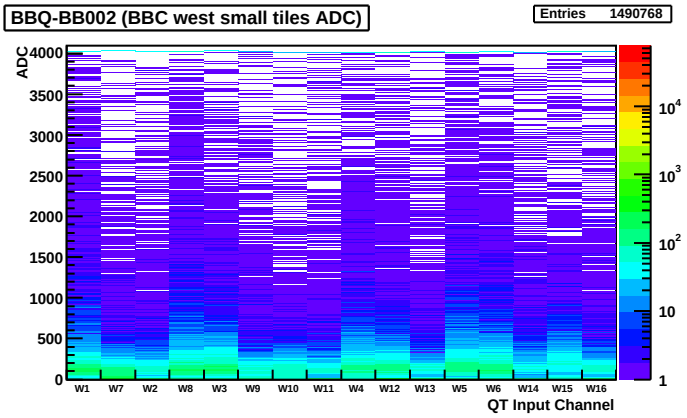

MIX-TF101
Entries 559038
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

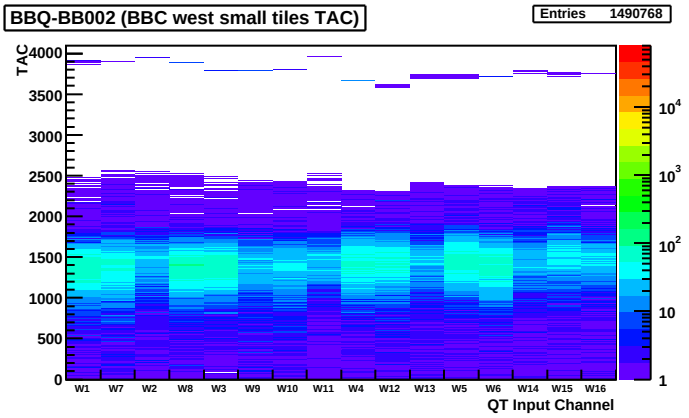

L1-TF201
Entries 93173
TOF total mult

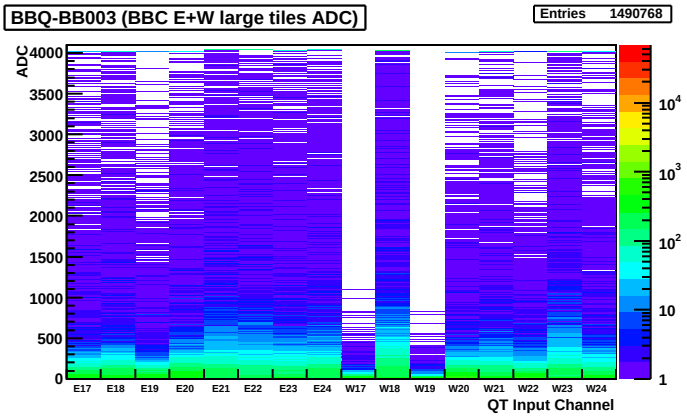

L1-TF201
Entries 559038
Threshold bits
49716
49618
49878
49457
48962
46691
43457
43555
43295
43716
44211
46482
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

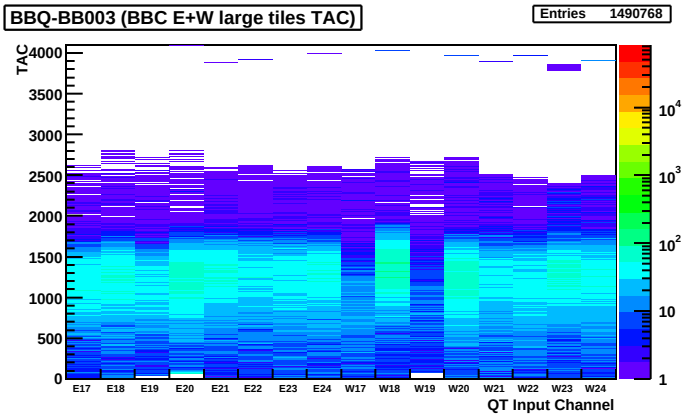

BBQ-BB001 (BBC east small tiles ADC)
Entries 1490768
ADC
QT Input Channel

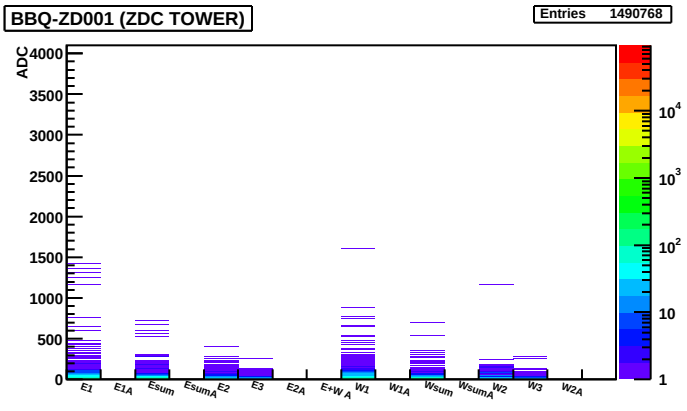

BBQ-BB001 (BBC east small tiles TAC)
Entries 1490768
TAC
QT Input Channel

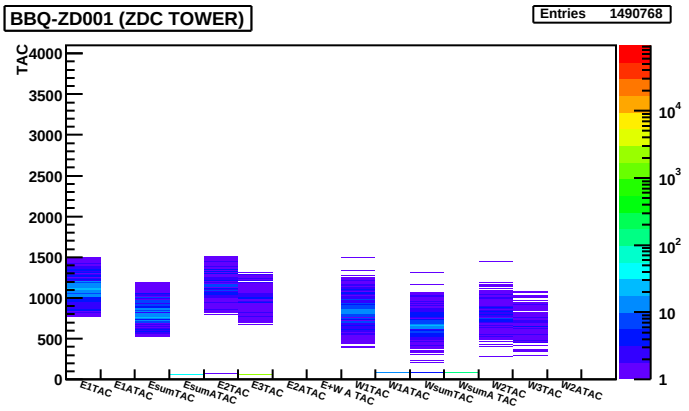

BBQ-BB002 (BBC west small tiles ADC)
Entries 1490768
ADC
QT Input Channel

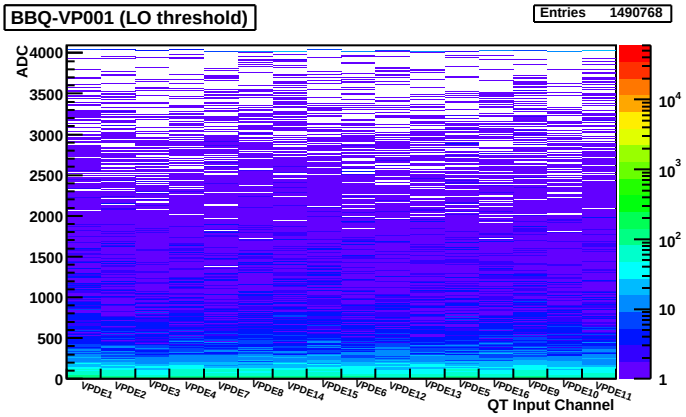

BBQ-BB002 (BBC west small tiles TAC)
Entries 1490768
TAC
QT Input Channel

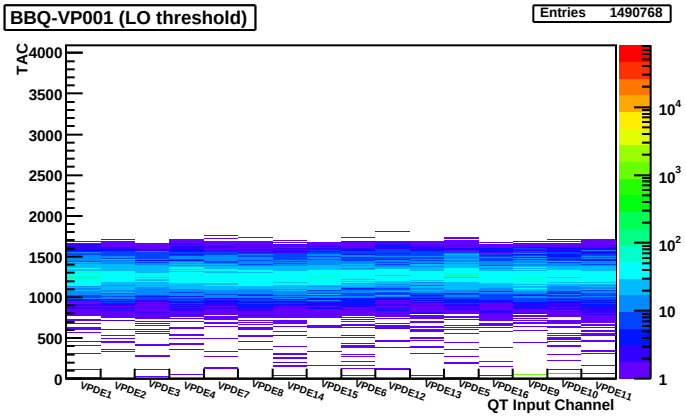

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1490768
ADC
QT Input Channel

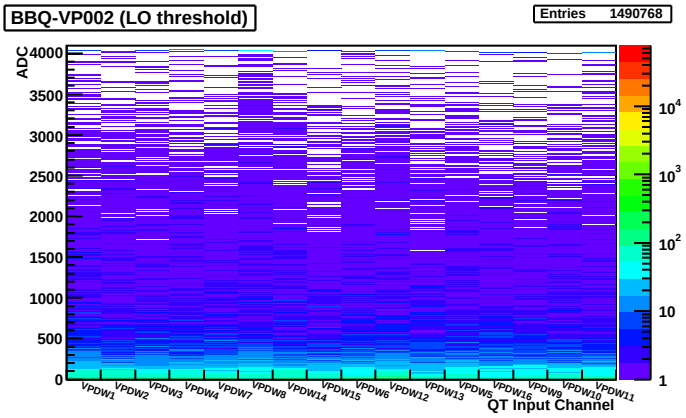

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1490768
TAC
QT Input Channel

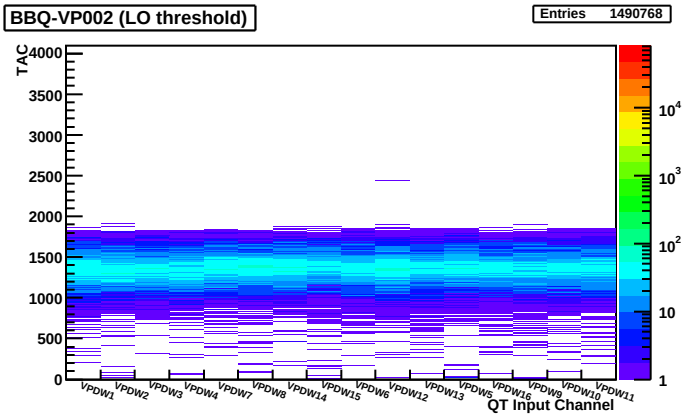

BBQ-ZD001 (ZDC TOWER)
Entries 1490768
ADC

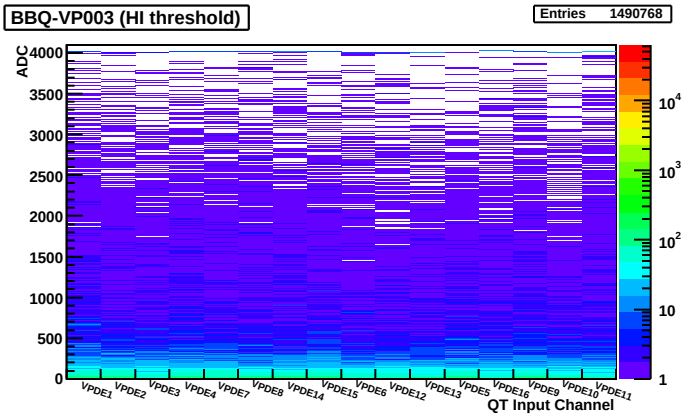

BBQ-ZD001 (ZDC TOWER)
Entries 1490768
TAC

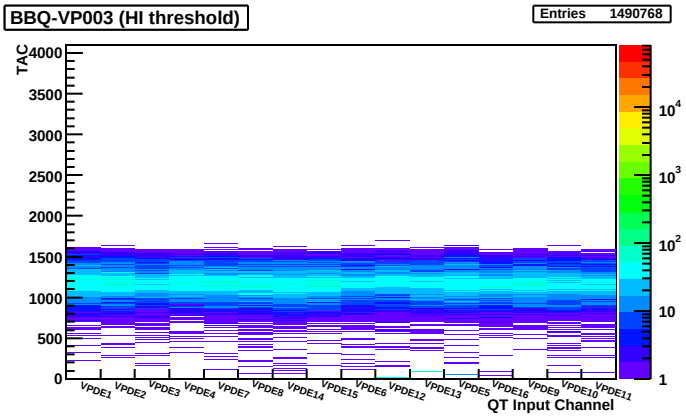

BBQ-VP001 (LO threshold)
Entries 1490768
ADC
QT Input Channel

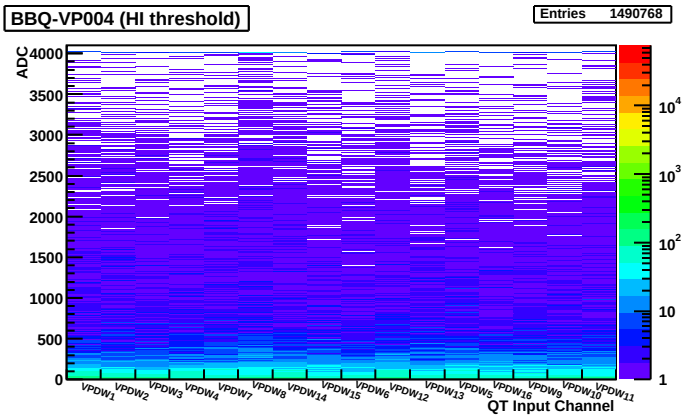

BBQ-VP001 (LO threshold)
Entries 1490768
TAC
QT Input Channel

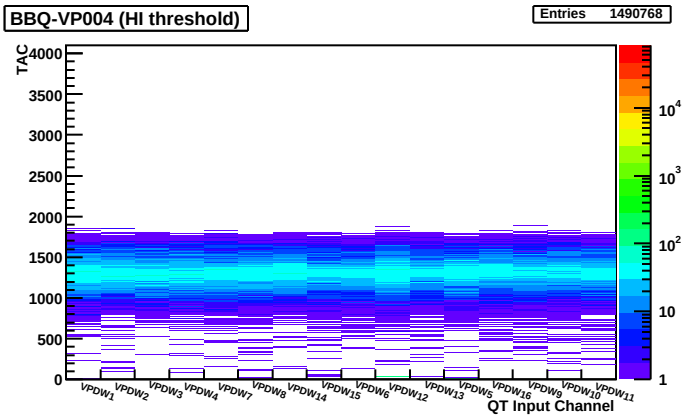

BBQ-VP002 (LO threshold)
Entries 1490768
ADC
QT Input Channel

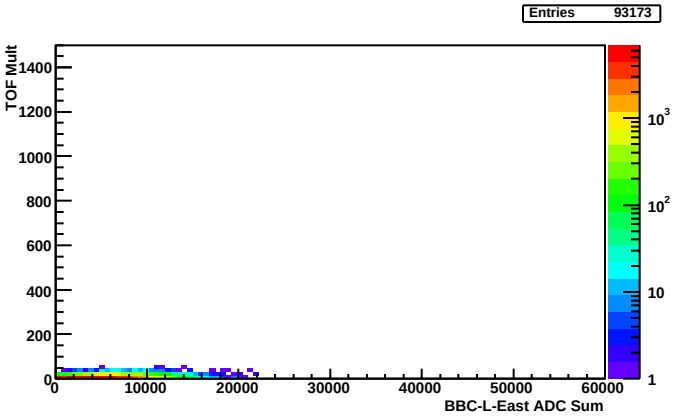

BBQ-VP002 (LO threshold)
Entries 1490768
TAC
QT Input Channel

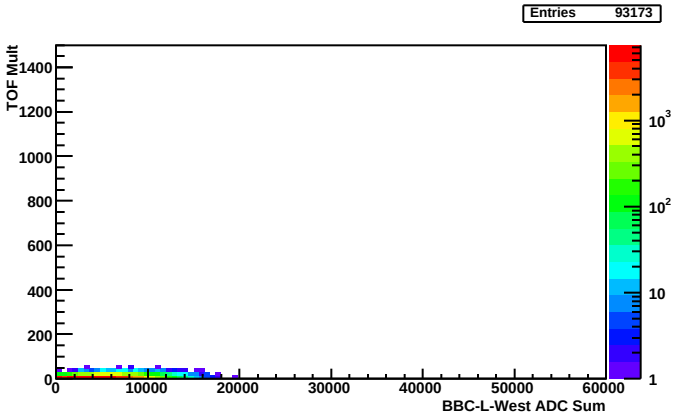

BBQ-VP003 (HI threshold)
Entries 1490768
ADC
QT Input Channel

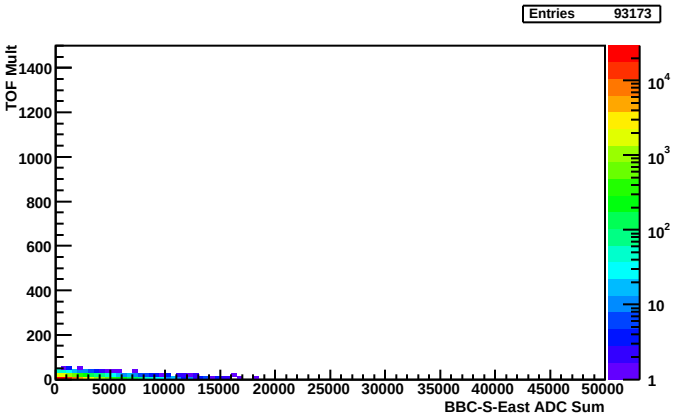

BBQ-VP003 (HI threshold)
Entries 1490768
TAC
QT Input Channel

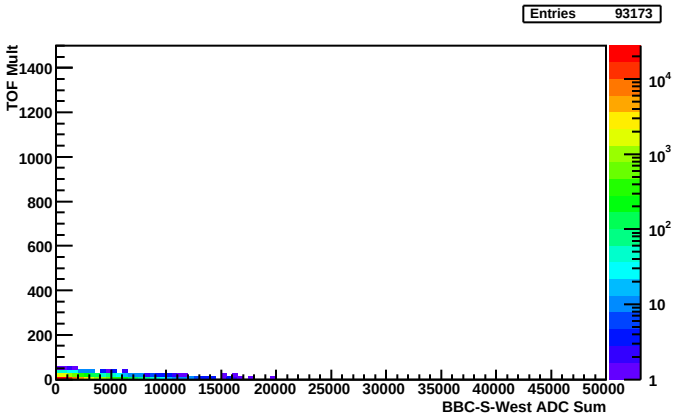

BBQ-VP004 (HI threshold)
Entries 1490768
ADC
QT Input Channel

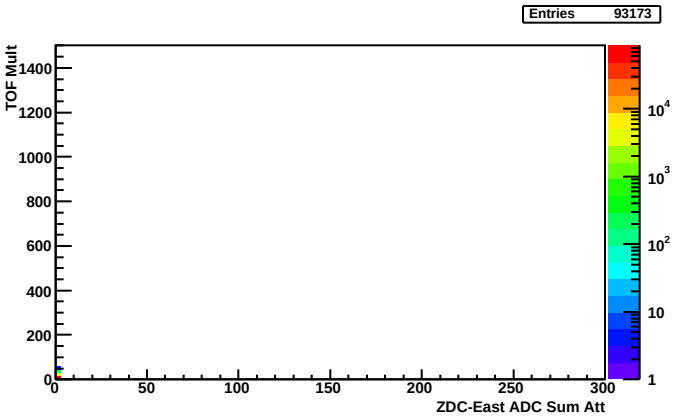

BBQ-VP004 (HI threshold)
Entries 1490768
TAC
QT Input Channel

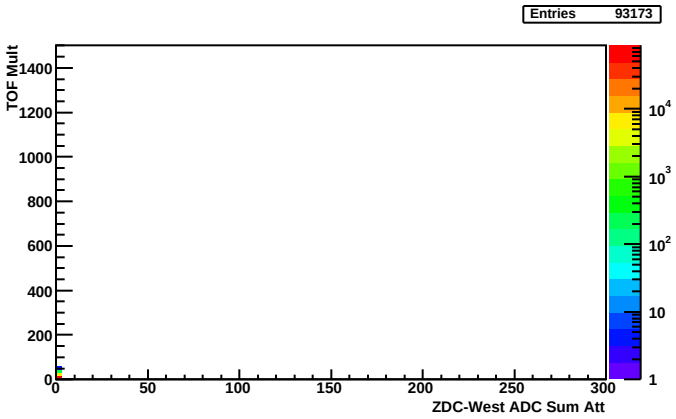

Entries 93173
TOF Mult
BBC-L-East ADC Sum

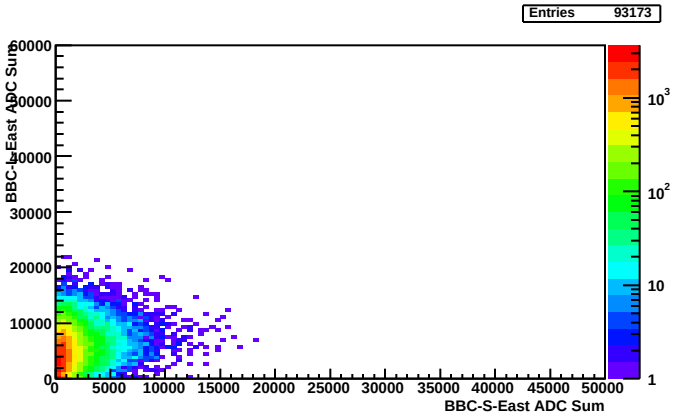

Entries 93173
TOF Mult
BBC-L-West ADC Sum

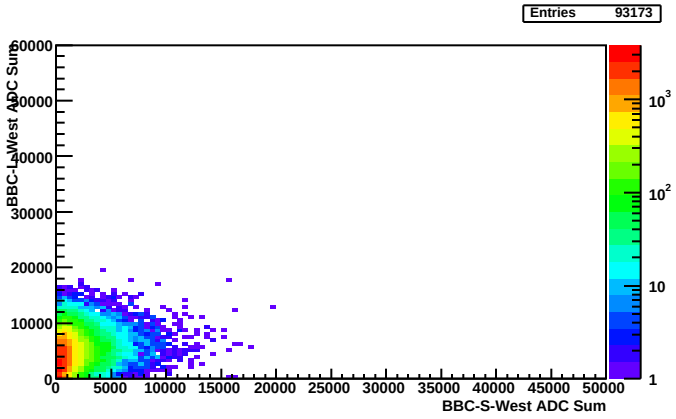

Entries 93173
TOF Mult
BBC-S-East ADC Sum

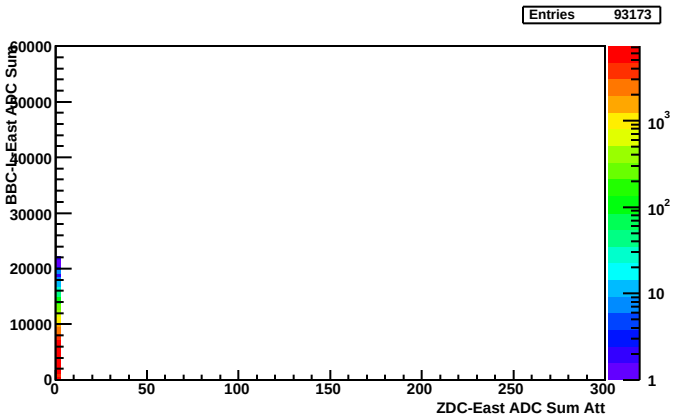

Entries 93173
TOF Mult
BBC-S-West ADC Sum

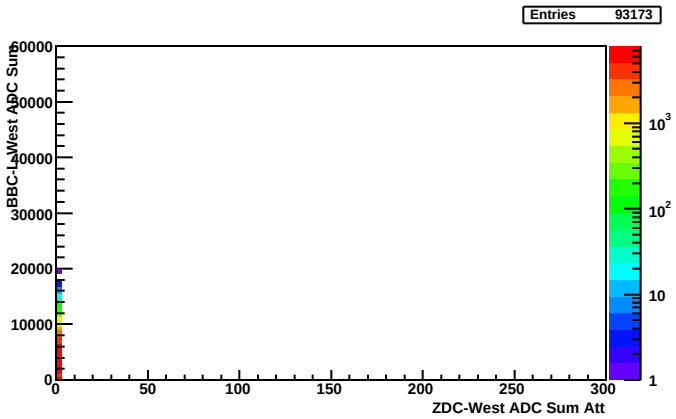

Entries 93173
TOF Mult
ZDC-East ADC Sum Att

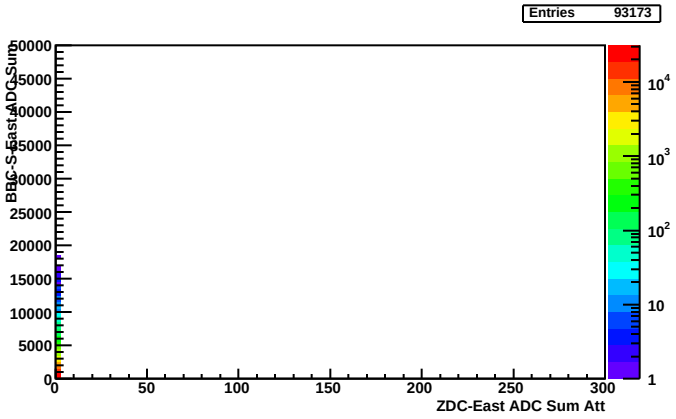

Entries 93173
TOF Mult
ZDC-West ADC Sum Att

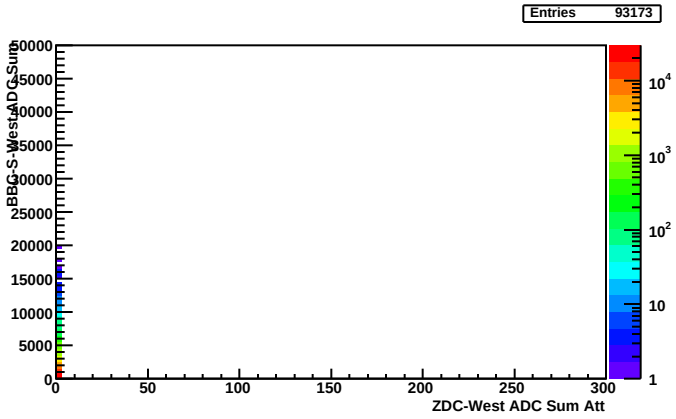

Entries 93173
BBC-L-East ADC Sum
BBC-S-East ADC Sum

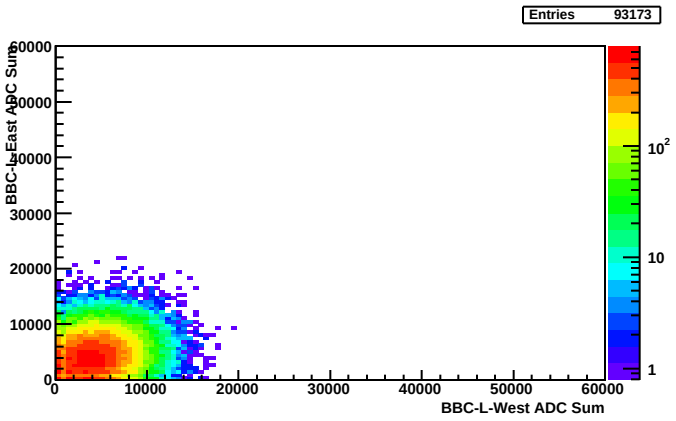

Entries 93173
BBC-L-West ADC Sum
BBC-S-West ADC Sum

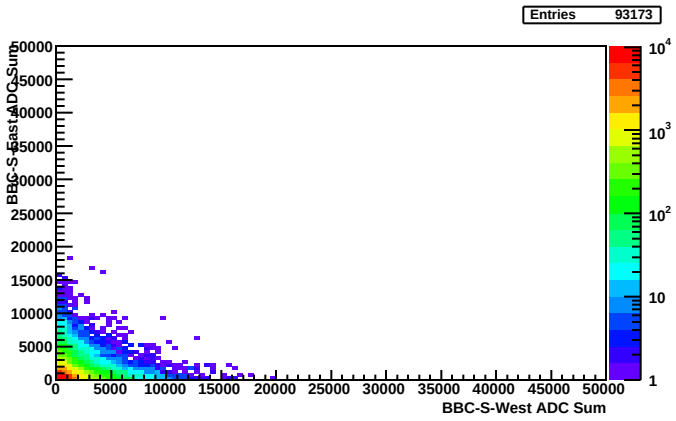

Entries 93173
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

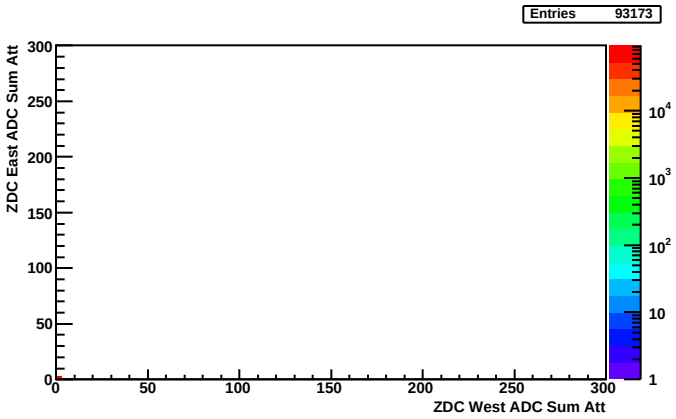

Entries 93173
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

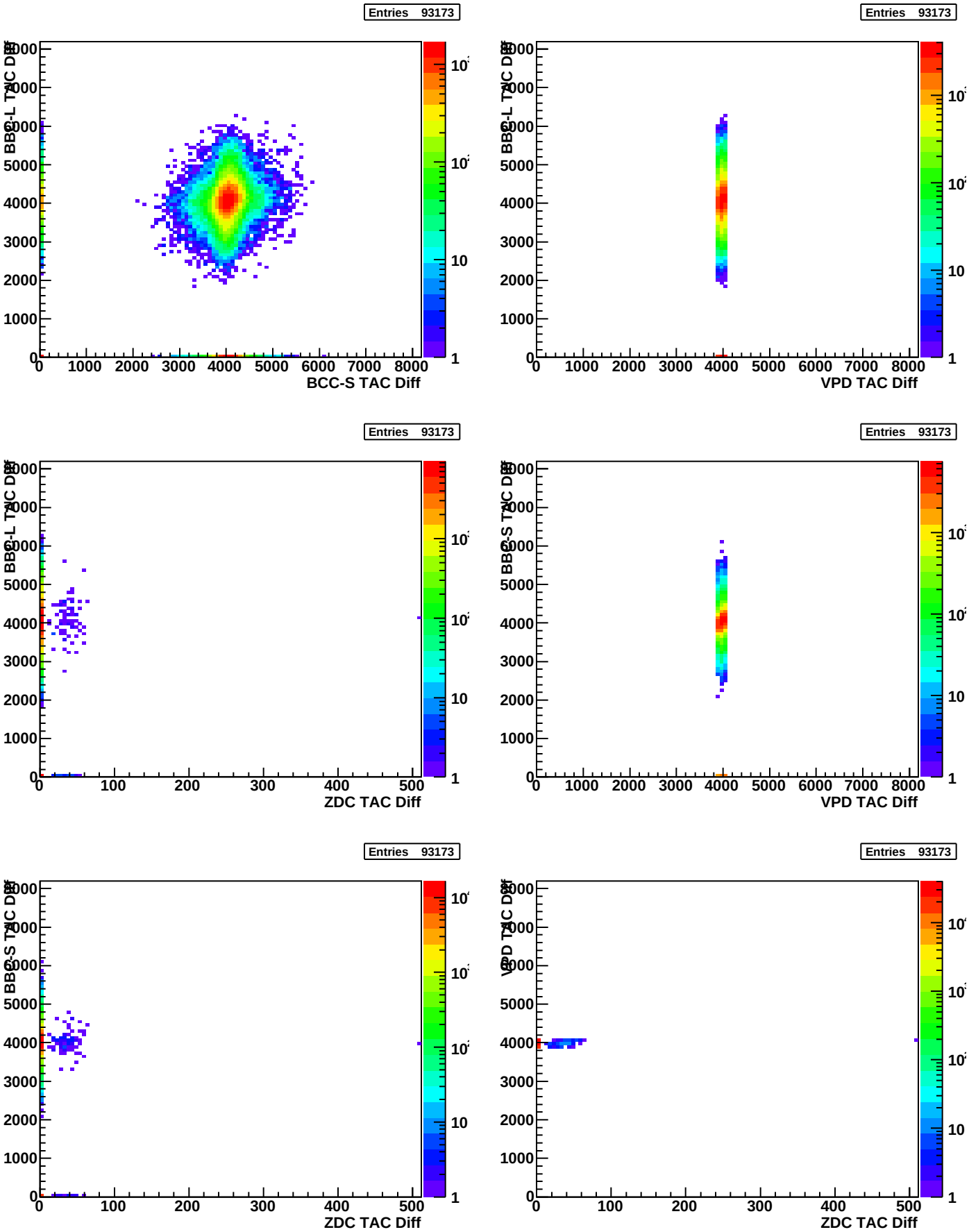

Entries 93173
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

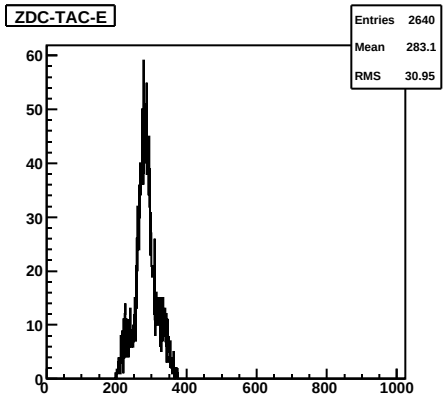

Entries 93173
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

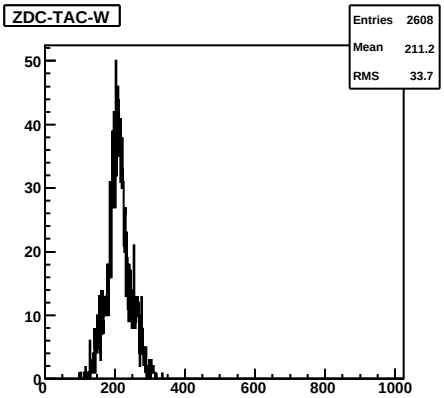

Entries 93173
BBC-L-East ADC Sum
BBC-L-West ADC Sum

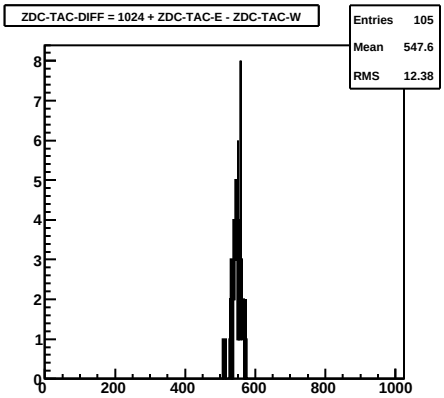

Entries 93173
BBC-S-East ADC Sum
BBC-S-West ADC Sum

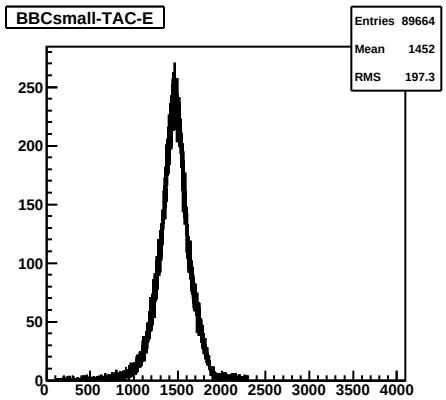

Entries 93173
ZDC East ADC Sum Att
ZDC West ADC Sum Att

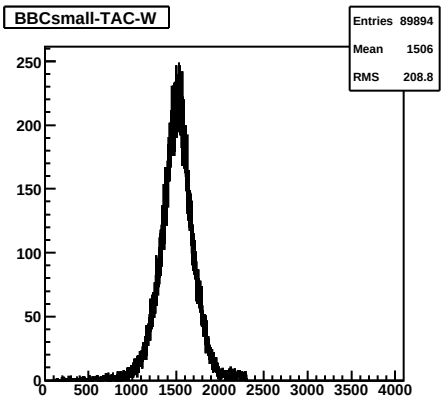

Entries 93173
BBC-L TAC Diff
BCC-S TAC Diff
Entries 93173
BBC-L TAC Diff
VPD TAC Diff
Entries 93173
BBC-L TAC Diff
ZDC TAC Diff
Entries 93173
BBC-S TAC Diff
VPD TAC Diff
Entries 93173
BBC-S TAC Diff
ZDC TAC Diff
Entries 93173
VPD TAC Diff
ZDC TAC Diff

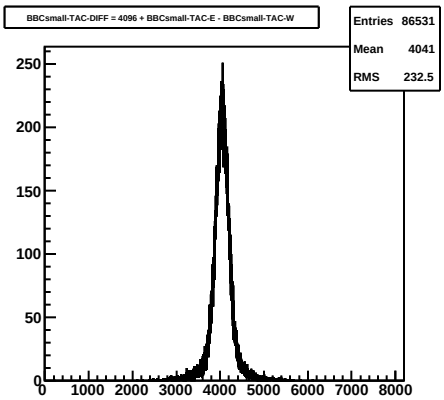

ZDC-TAC-E
Entries 2640
Mean 283.1
RMS 30.95

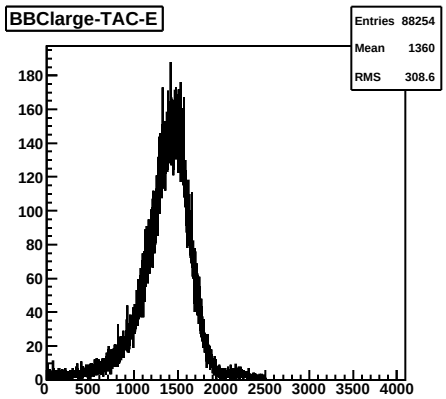

ZDC-TAC-W
Entries 2608
Mean 211.2
RMS 33.7

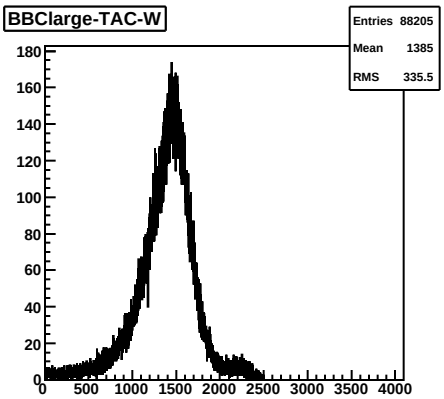

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 105
Mean 547.6
RMS 12.38

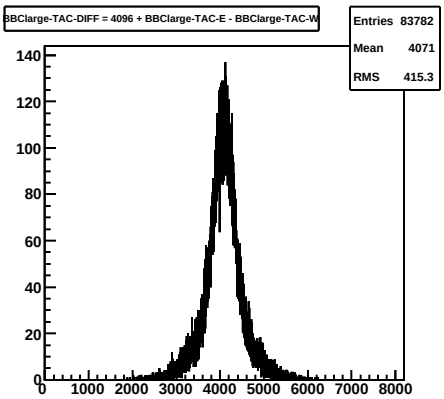

BBCsmall-TAC-E
Entries 89664
Mean 1452
RMS 197.3

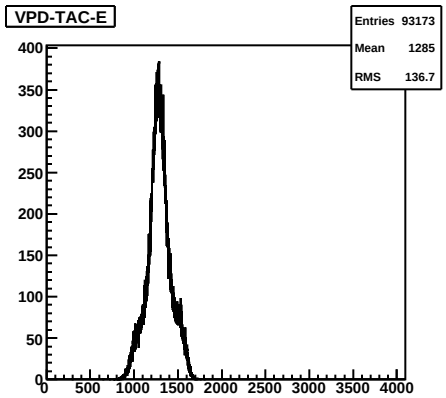

BBCsmall-TAC-W
Entries 89894
Mean 1506
RMS 208.8

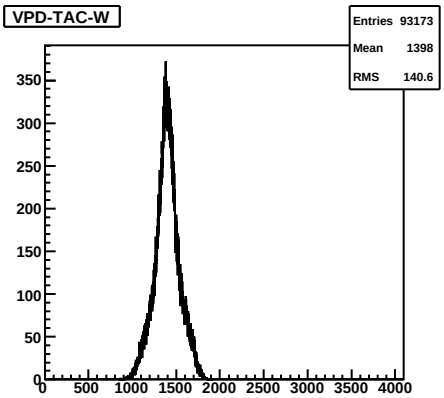

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 86531
Mean 4041
RMS 232.5

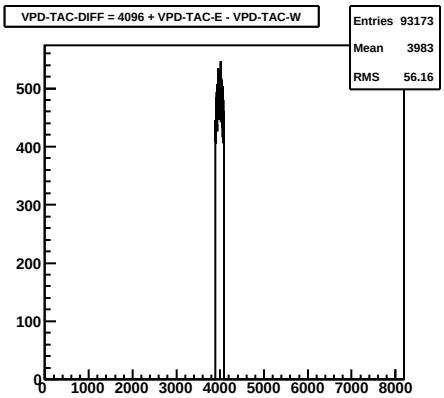

BBClarge-TAC-E
Entries 88254
Mean 1360
RMS 308.6

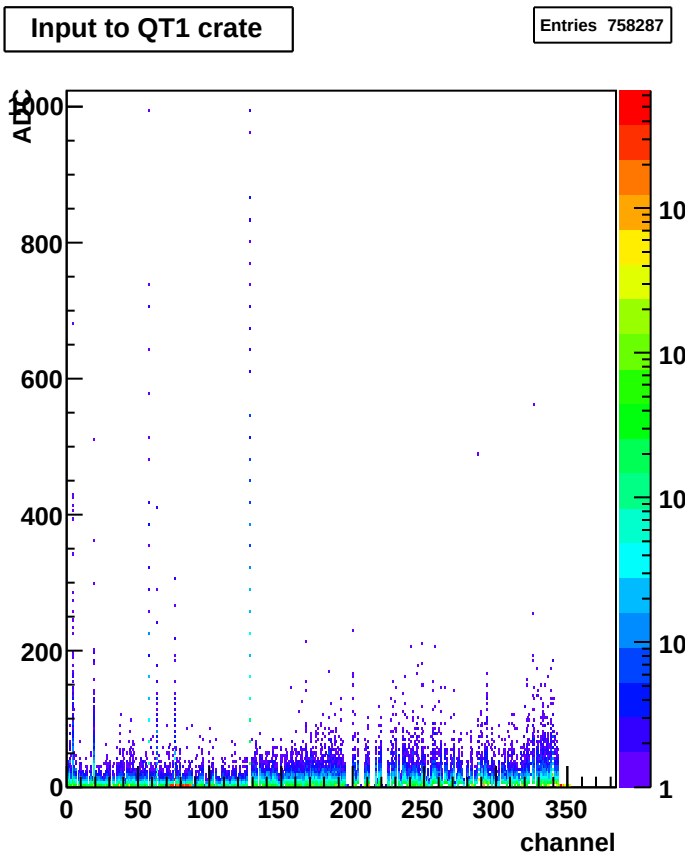

BBClarge-TAC-W
Entries 88205
Mean 1385
RMS 335.5

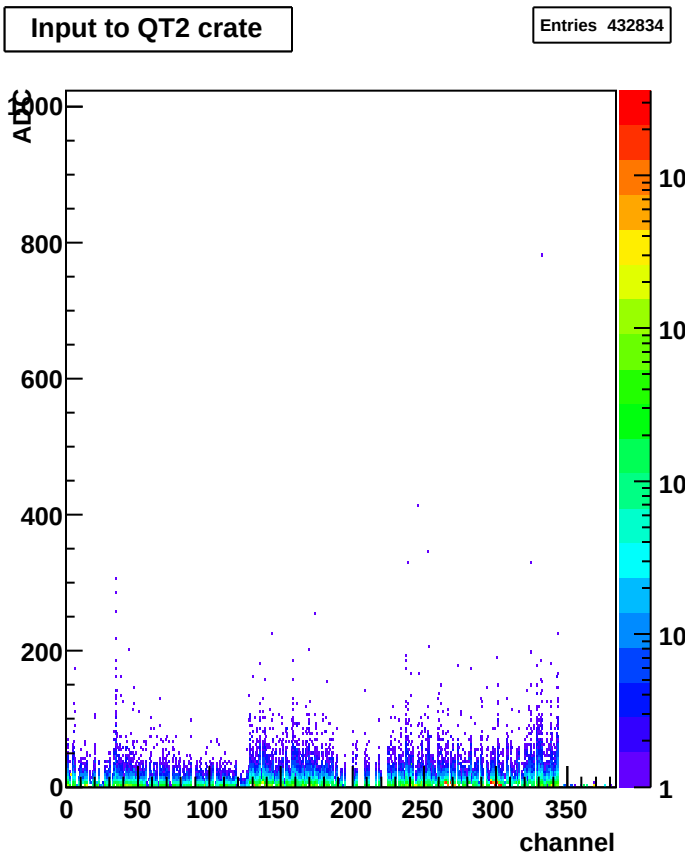

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 83782
Mean 4071
RMS 415.3

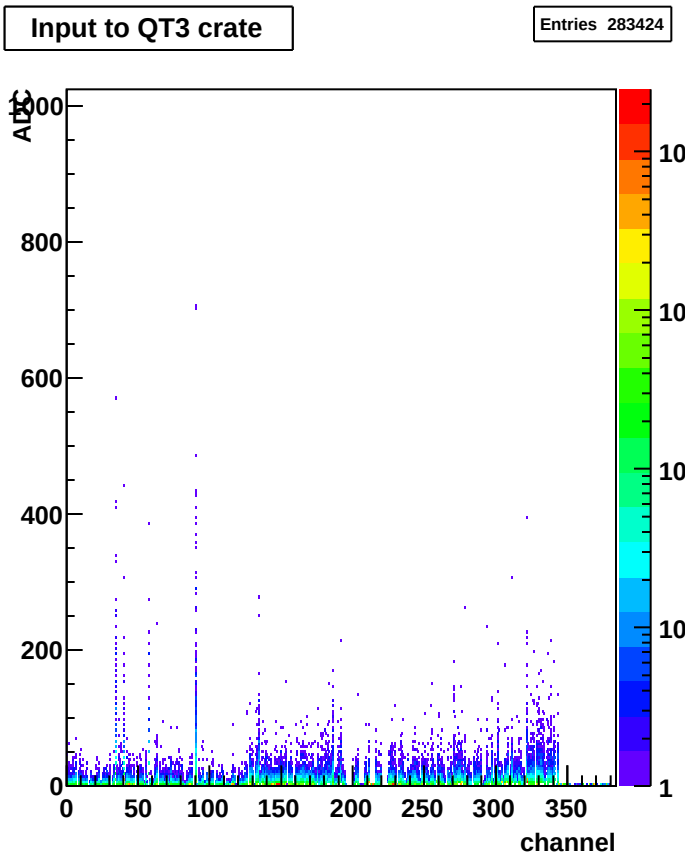

VPD-TAC-E
Entries 93173
Mean 1285
RMS 136.7

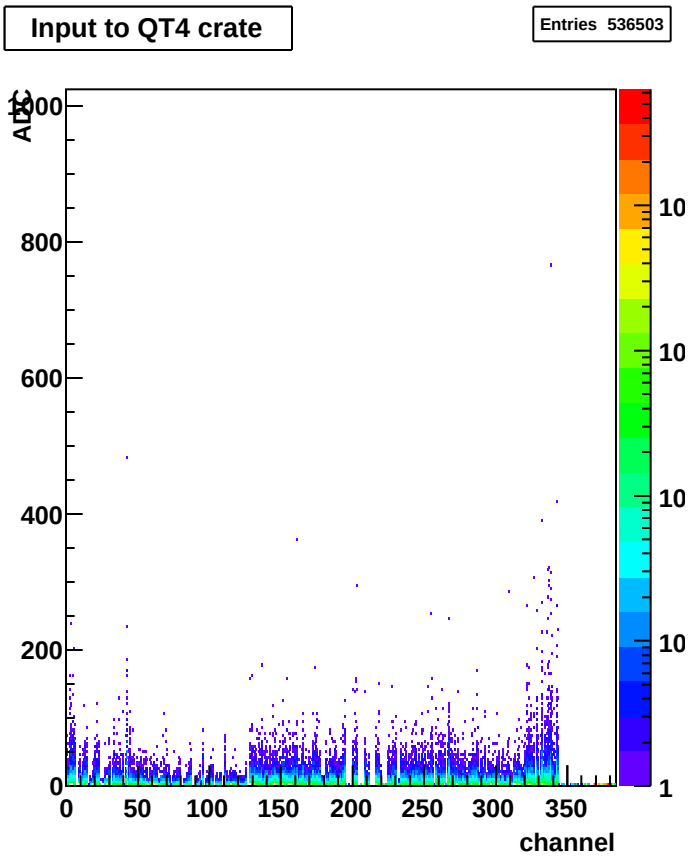

VPD-TAC-W
Entries 93173
Mean 1398
RMS 140.6

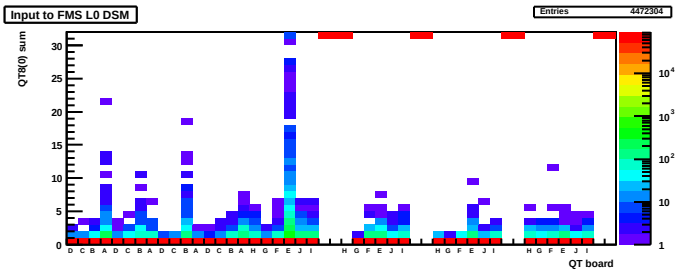

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 93173
Mean 3983
RMS 56.16

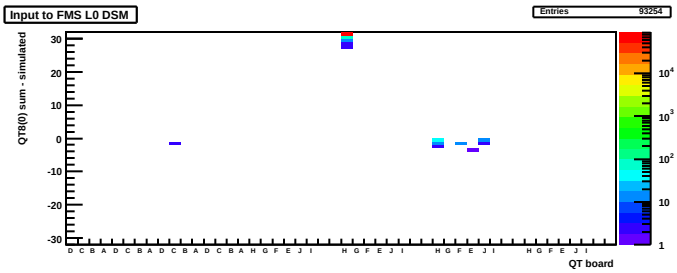

Input to QT1 crate
Entries 758287
ADC
channel

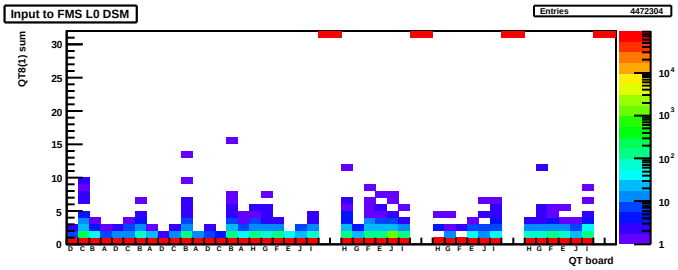

Input to QT2 crate
Entries 432834
ADC
channel

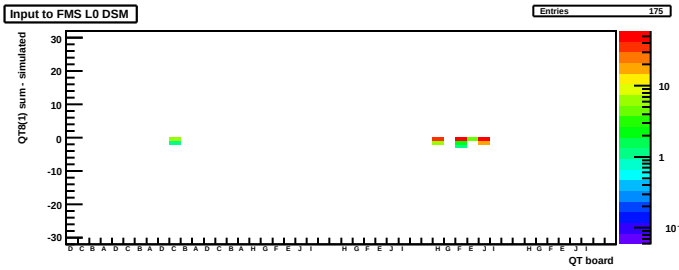

Input to QT3 crate
Entries 283424
ADC
channel

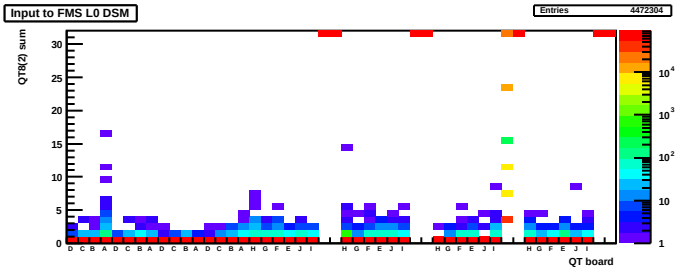

Input to QT4 crate
Entries 536503
ADC
channel

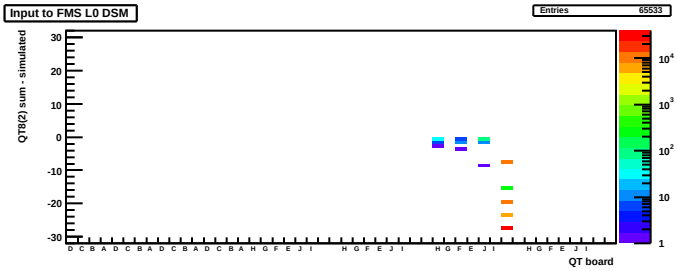

Input to FMS L0 DSM
Entries 4472304
QT8(0) sum
QT board

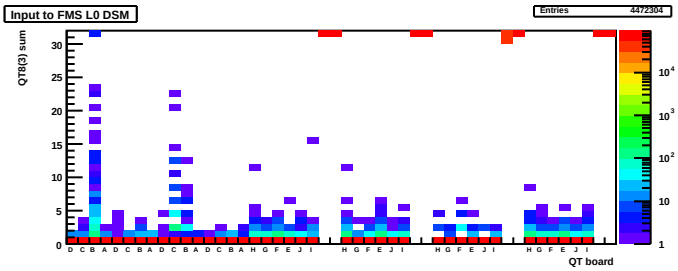

Input to FMS L0 DSM
Entries 93254
QT8(0) sum - simulated
QT board

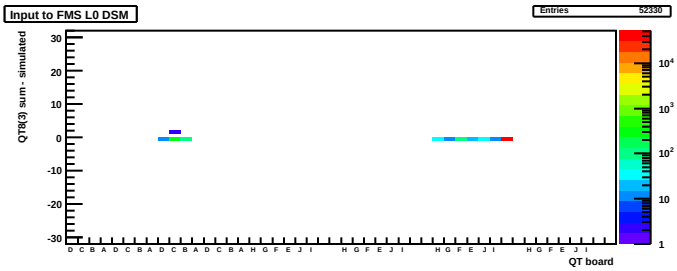

Input to FMS L0 DSM
Entries 4472304
QT8(1) sum
QT board

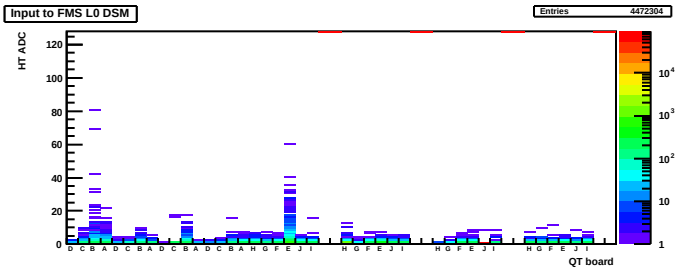

Input to FMS L0 DSM
Entries 175
QT8(1) sum - simulated
QT board

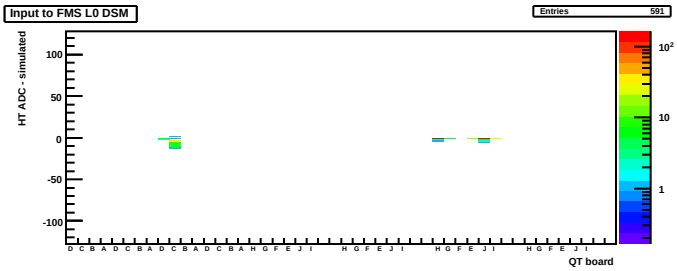

Input to FMS L0 DSM
Entries 4472304
QT8(2) sum
QT board

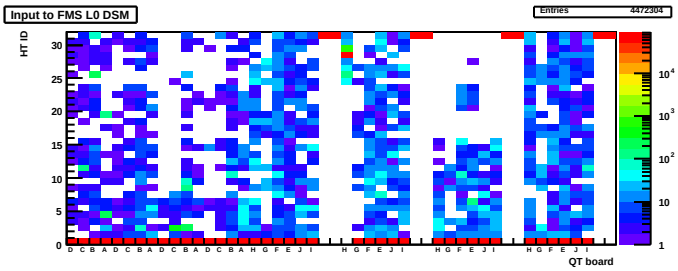

Input to FMS L0 DSM
Entries 65533
QT8(2) sum - simulated
QT board

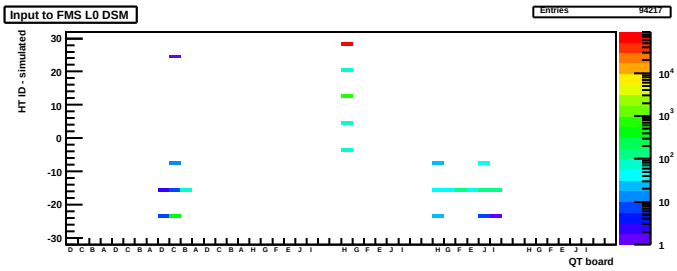

Input to FMS L0 DSM
Entries 4472304
QT8(3) sum
QT board

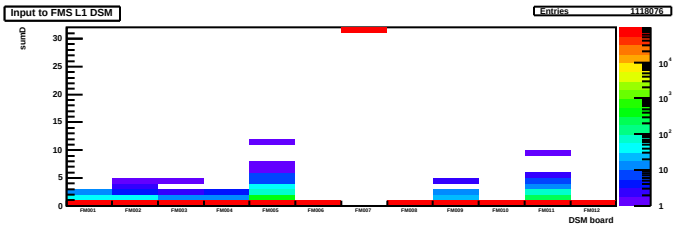

Input to FMS L0 DSM
Entries 52330
QT8(3) sum - simulated
QT board

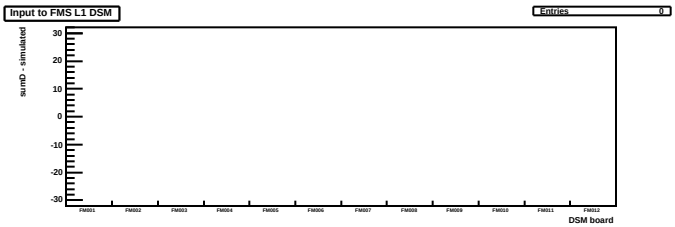

Input to FMS L0 DSM
Entries 4472304
HT ADC
QT board

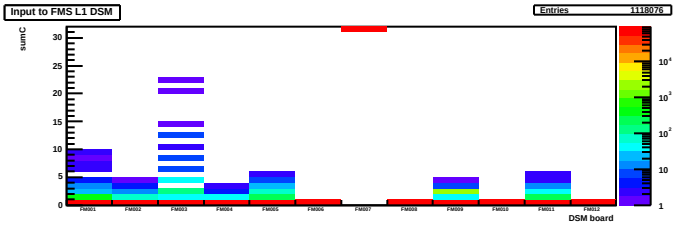

Input to FMS L0 DSM
Entries 591
HT ADC - simulated
QT board

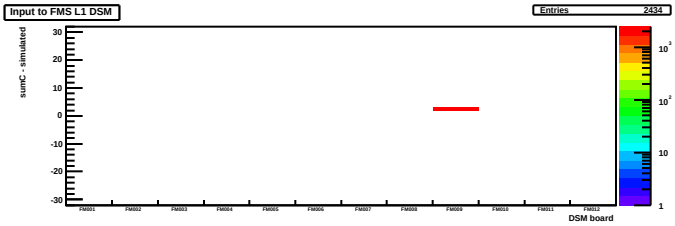

Input to FMS L0 DSM
Entries 4472304
HT ID
QT board

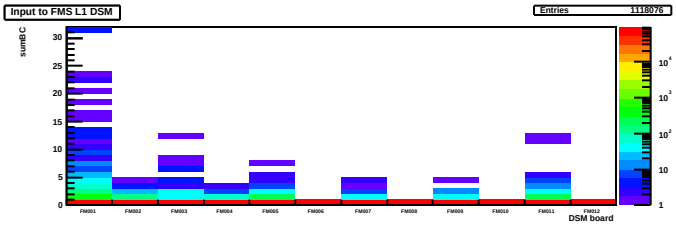

Input to FMS L0 DSM
Entries 94217
HT ID - simulated
QT board

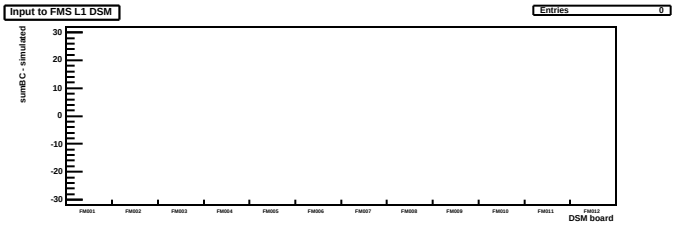

Input to FMS L1 DSM
Entries 1118076
sumD
DSM board

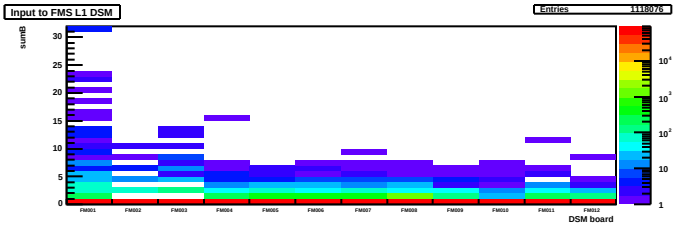

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

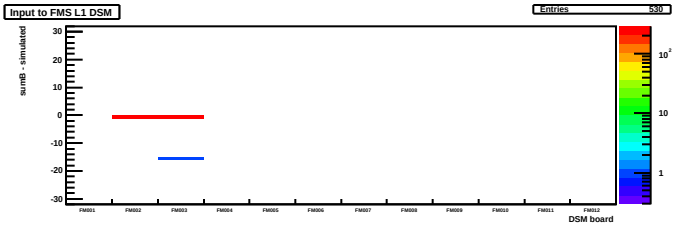

Input to FMS L1 DSM
Entries 1118076
sumC
DSM board

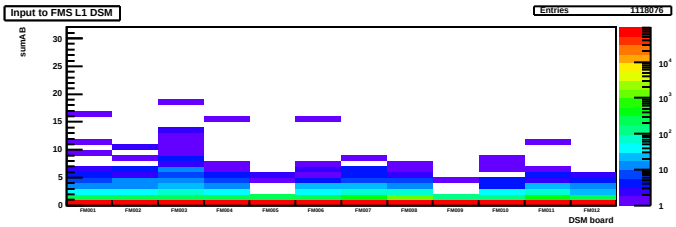

Input to FMS L1 DSM
Entries 2434
sumC - simulated
DSM board

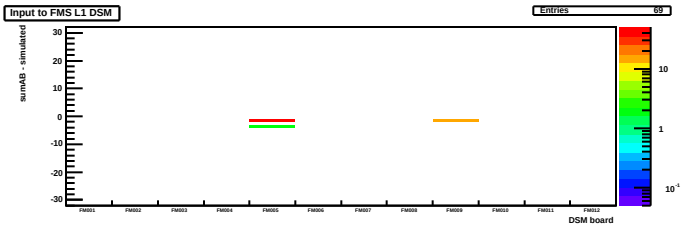

Input to FMS L1 DSM
Entries 1118076
sumBC
DSM board

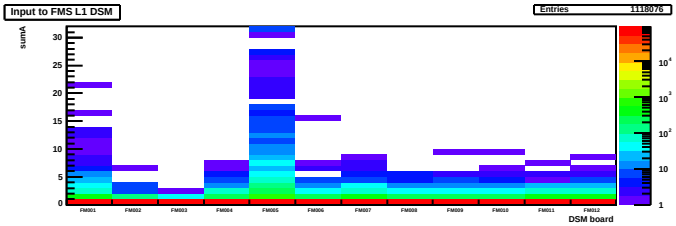

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

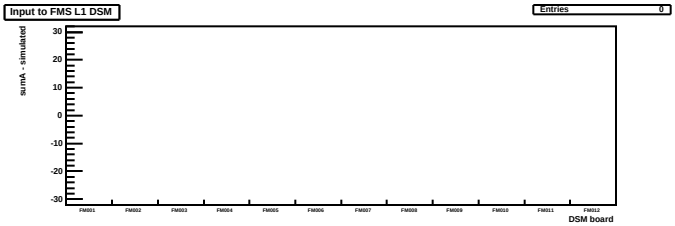

Input to FMS L1 DSM
Entries 1118076
sumB
DSM board

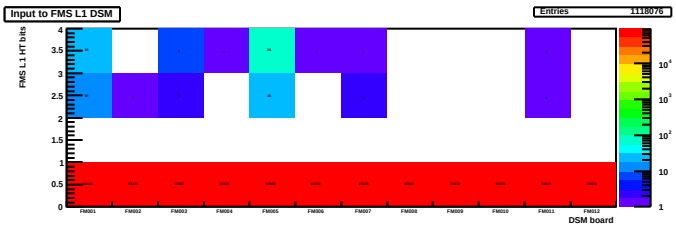

Input to FMS L1 DSM
Entries 530
sumB - simulated
DSM board

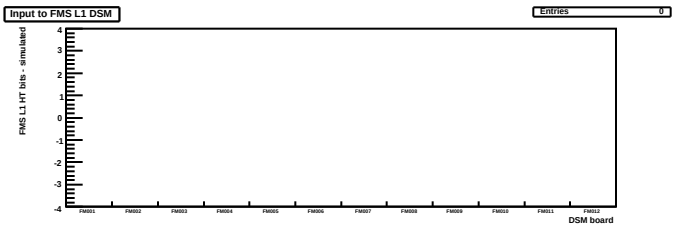

Input to FMS L1 DSM
Entries 1118076
sumAB
DSM board

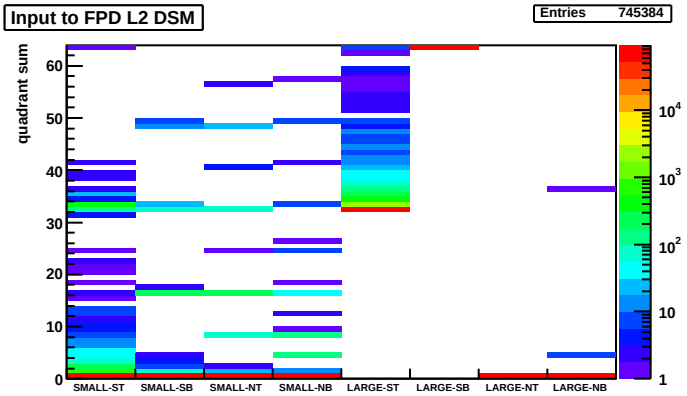

Input to FMS L1 DSM
Entries 69
sumAB - simulated
DSM board

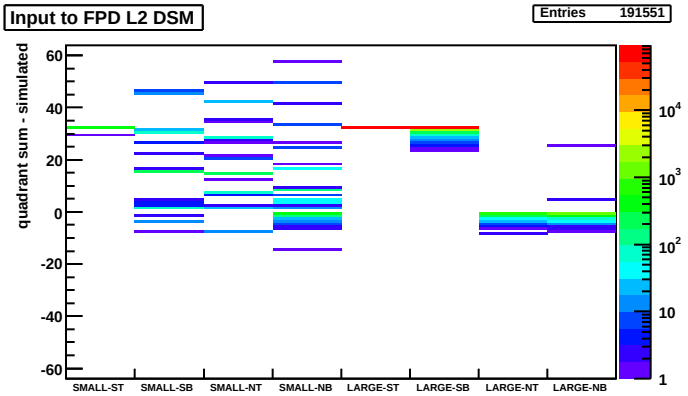

Input to FMS L1 DSM
Entries 1118076
sumA
DSM board

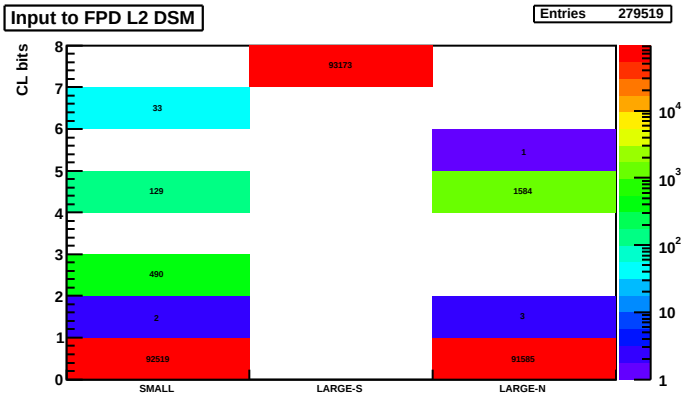

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

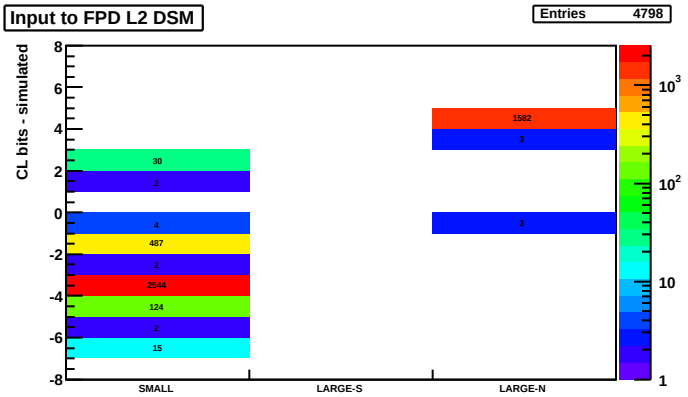

Input to FMS L1 DSM
Entries 1118076
FMS L1 HT bits
DSM board

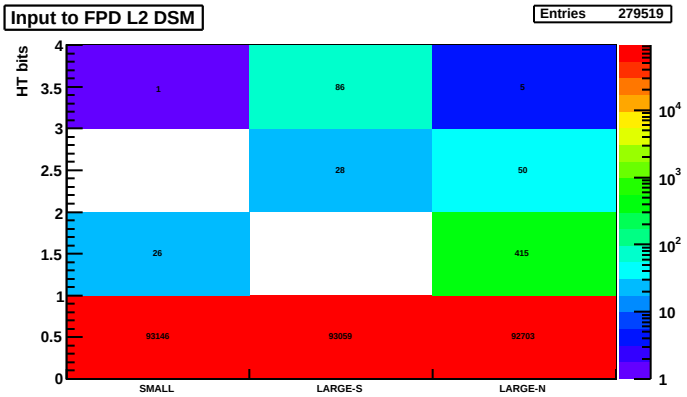

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

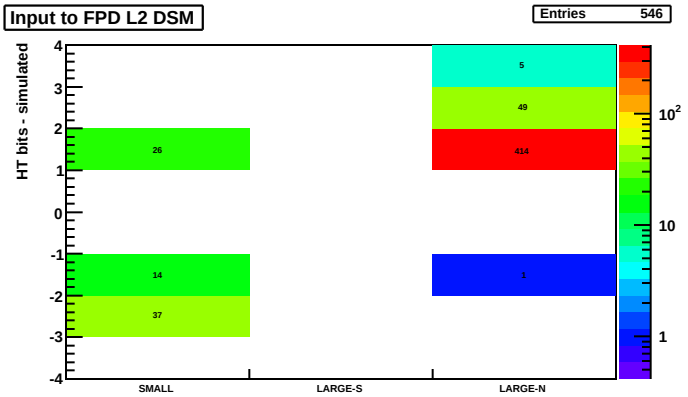

Input to FPD L2 DSM
Entries 745384
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

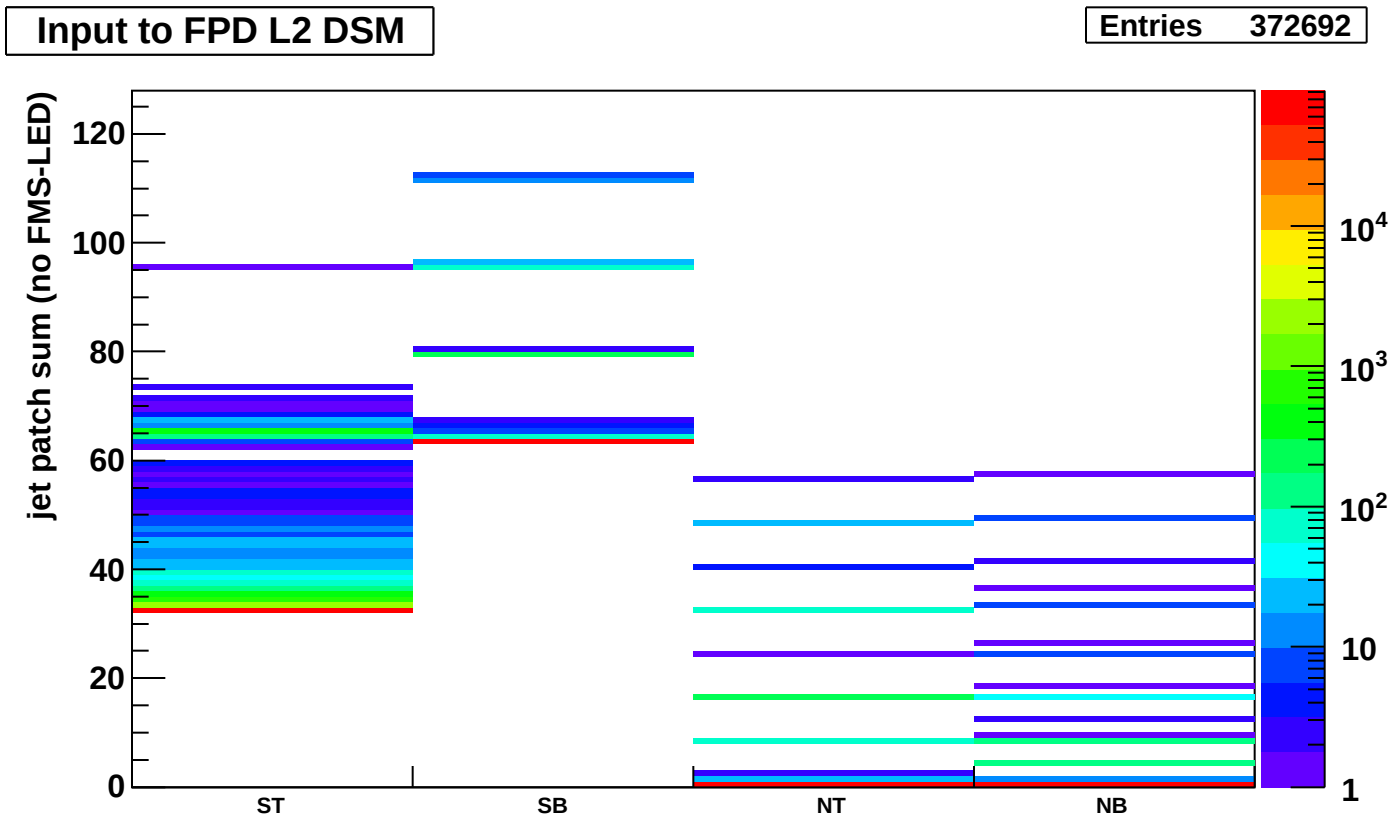

Input to FPD L2 DSM
Entries 191551
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

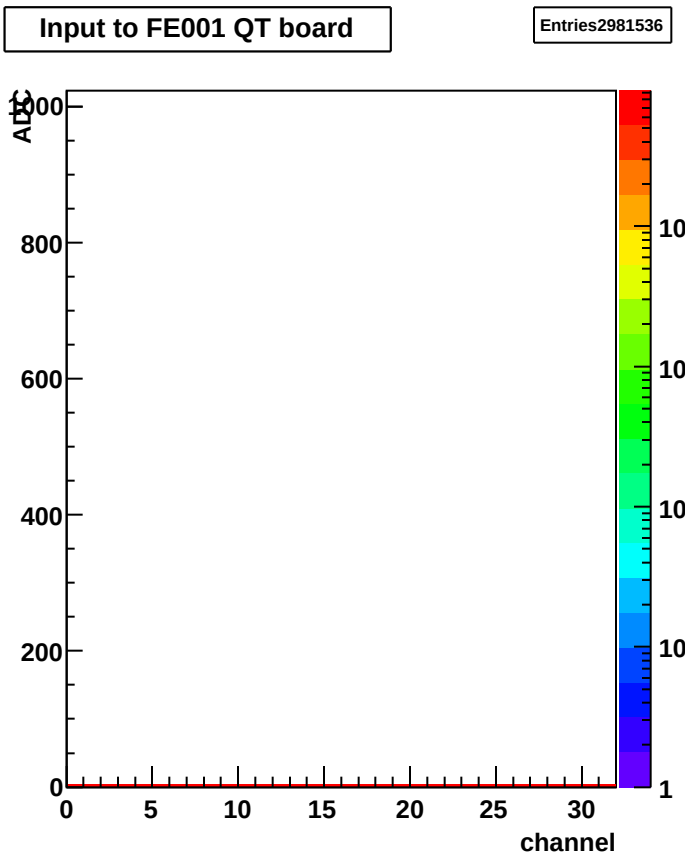

Input to FPD L2 DSM
Entries 279519
CL bits
93173
33
129
490
2
92519
1
1584
3
91585
SMALL
LARGE-S
LARGE-N

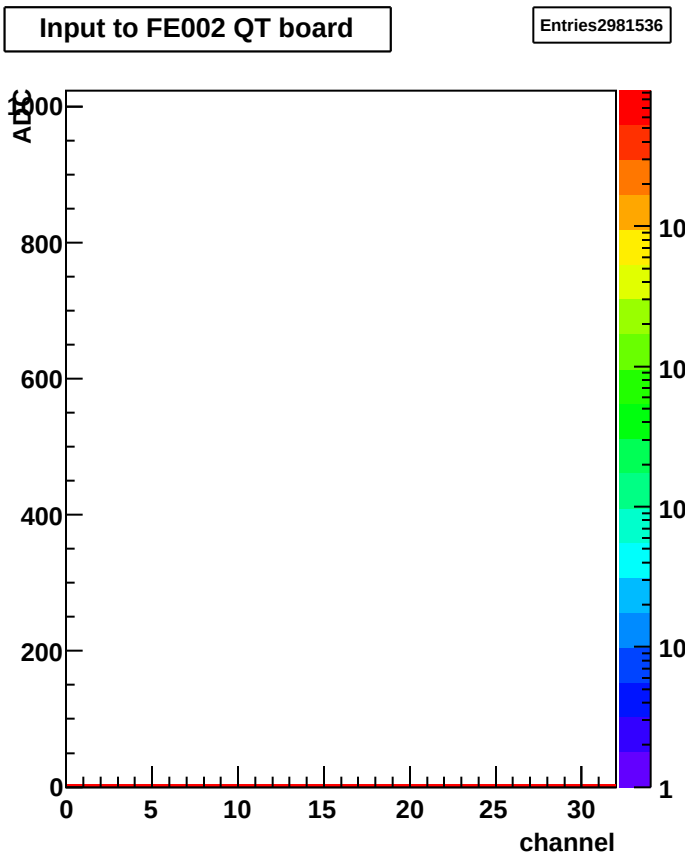

Input to FPD L2 DSM
Entries 4798
CL bits - simulated
30
2
4
487
2
2544
124
2
15
1582
3
3
SMALL
LARGE-S
LARGE-N

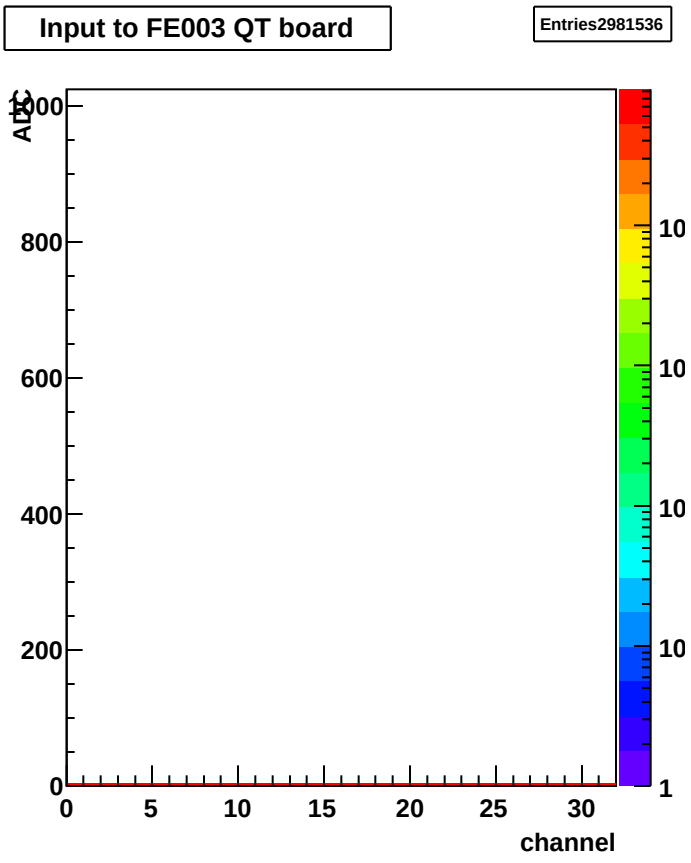

Input to FPD L2 DSM
Entries 279519
HT bits
1
86
5
28
50
26
415
93146
93059
92703
SMALL
LARGE-S
LARGE-N

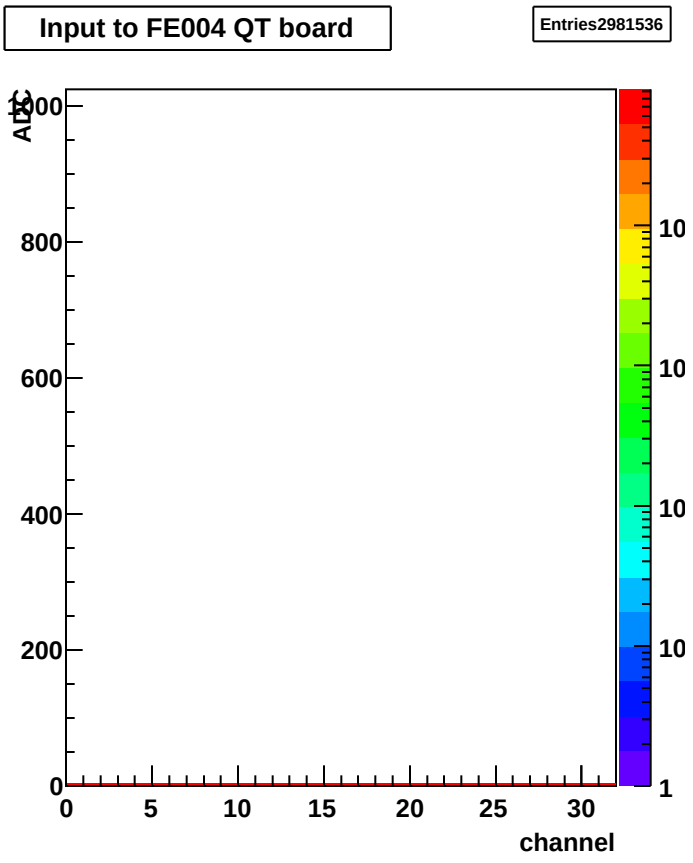

Input to FPD L2 DSM
Entries 546
HT bits - simulated
5
49
26
414
14
1
37
SMALL
LARGE-S
LARGE-N

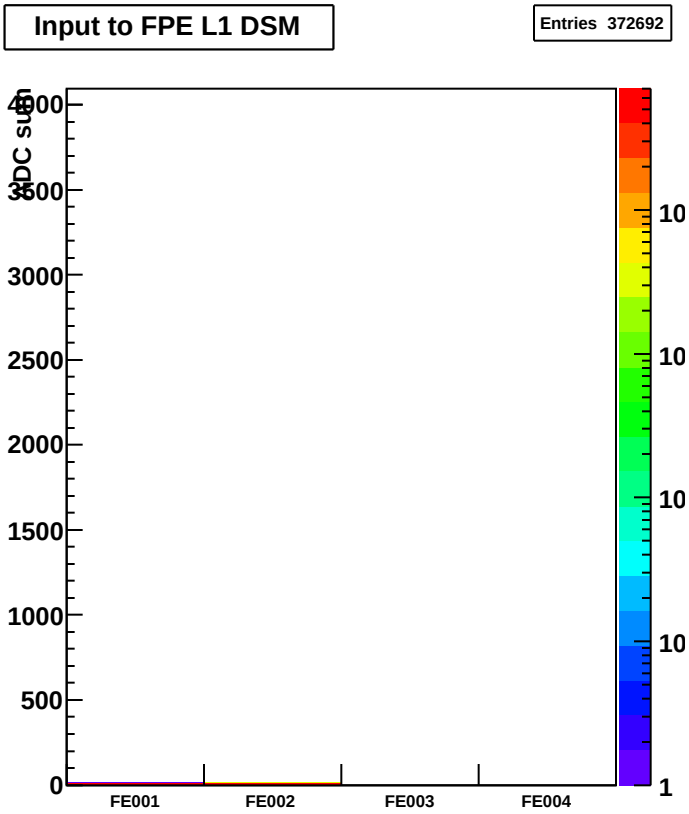

Input to FPD L2 DSM
Entries 372692
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

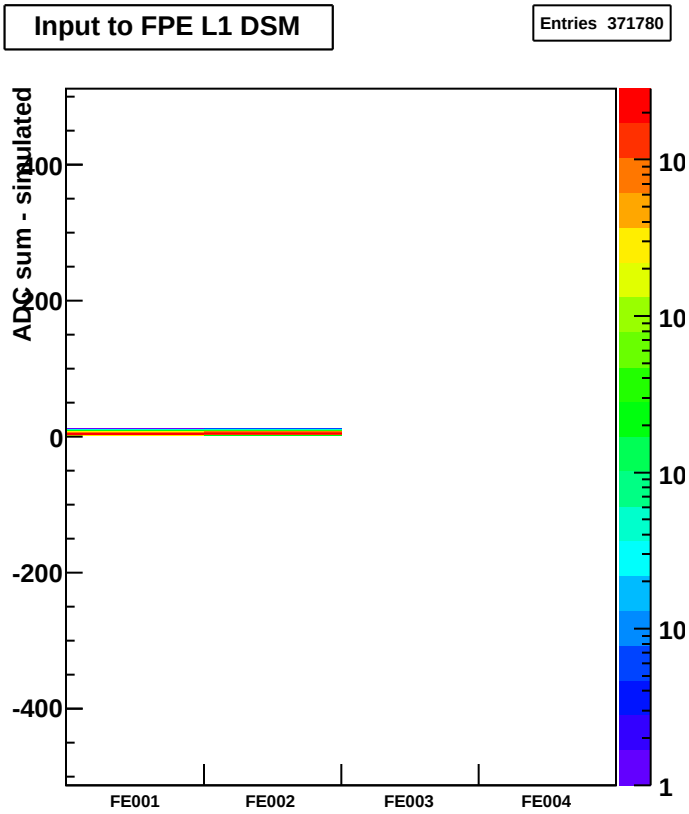

Input to FE001 QT board
Entries2981536
ADC
channel

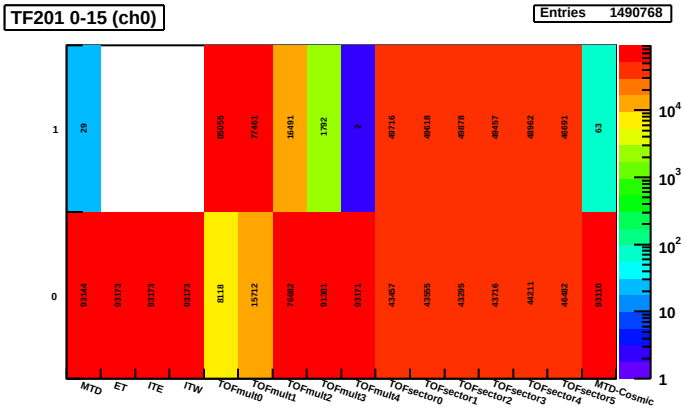

Input to FE002 QT board
Entries2981536
ADC
channel

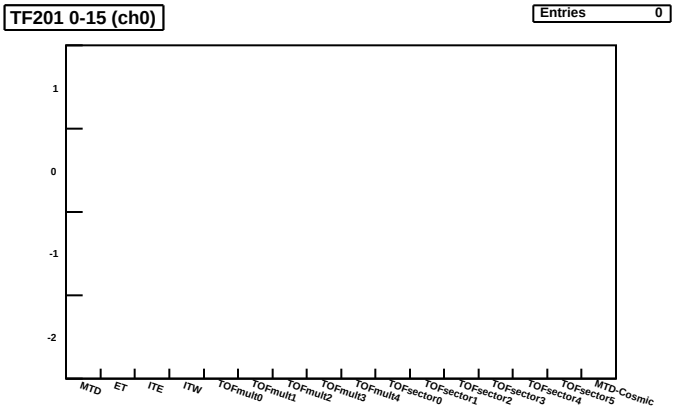

Input to FE003 QT board
Entries2981536
ADC
channel

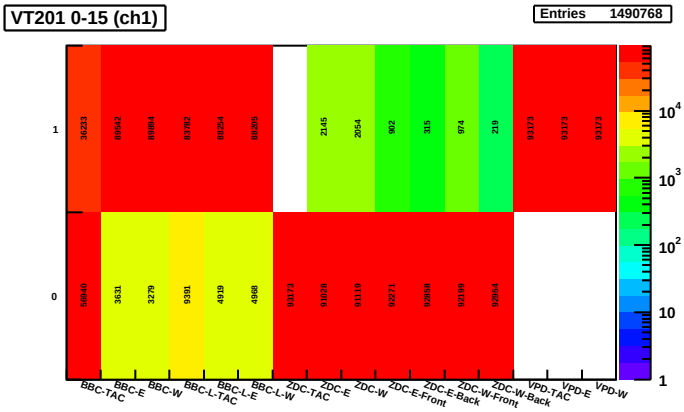

Input to FE004 QT board
Entries2981536
ADC
channel

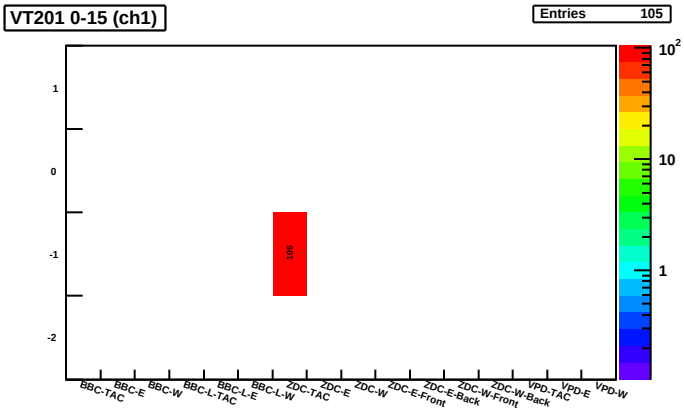

Input to FPE L1 DSM
Entries 372692
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

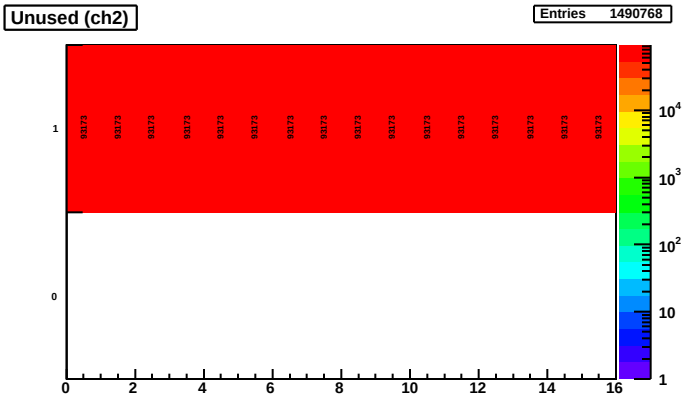

Input to FPE L1 DSM
Entries 371780
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

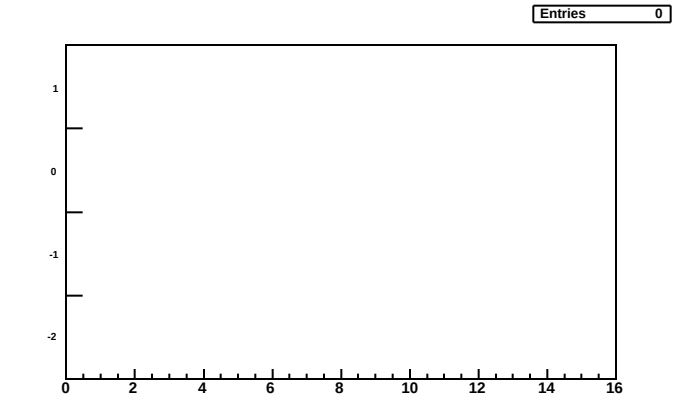

TF201 0-15 (ch0)
Entries 1490768
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

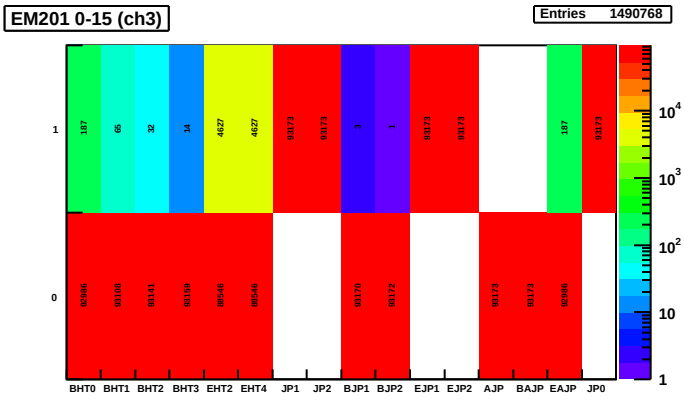

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

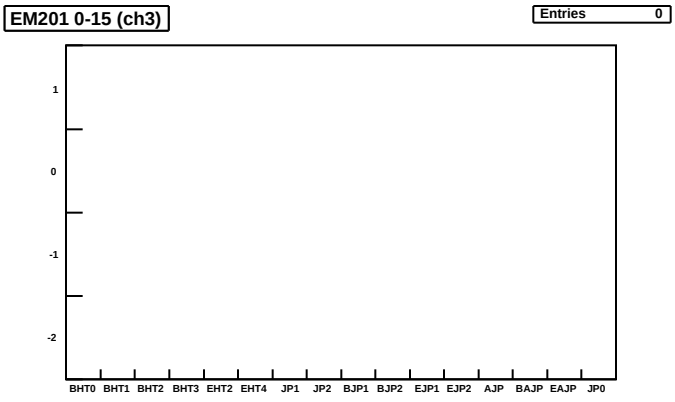

VT201 0-15 (ch1)
Entries 1490768
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 105
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

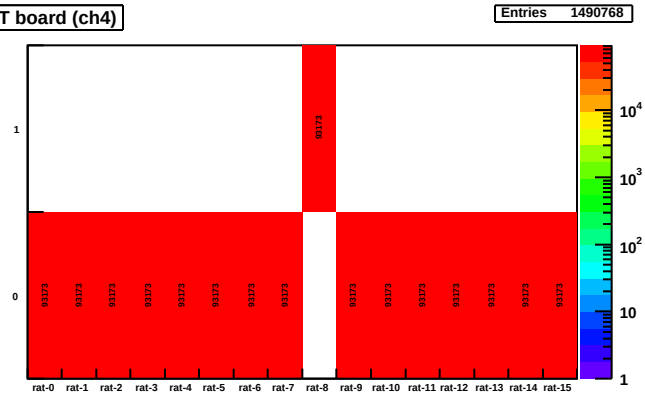

Unused (ch2)
Entries 1490768

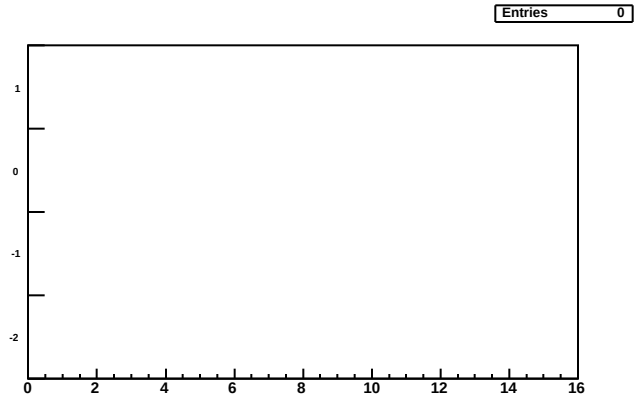

Entries 0


EM201 0-15 (ch3)
Entries 1490768
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

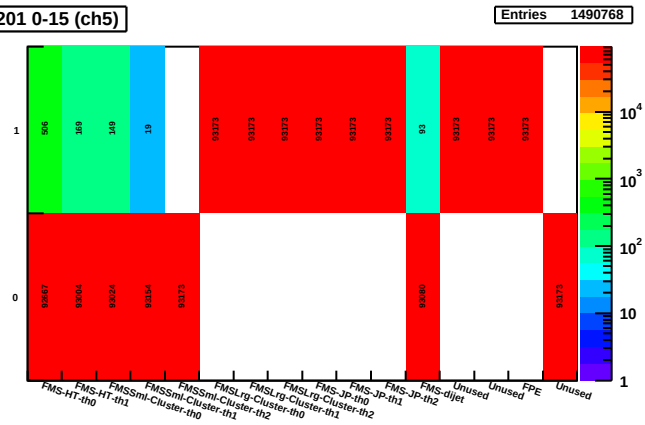

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

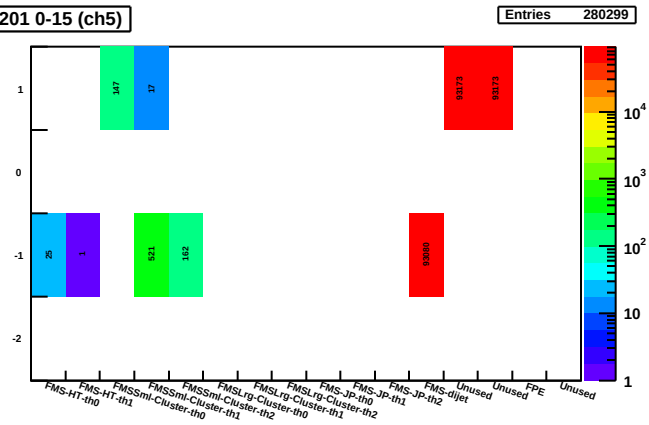




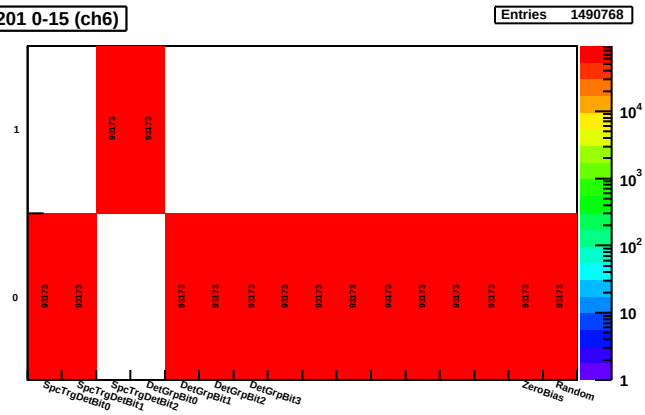
FP201 0-15 (ch5)

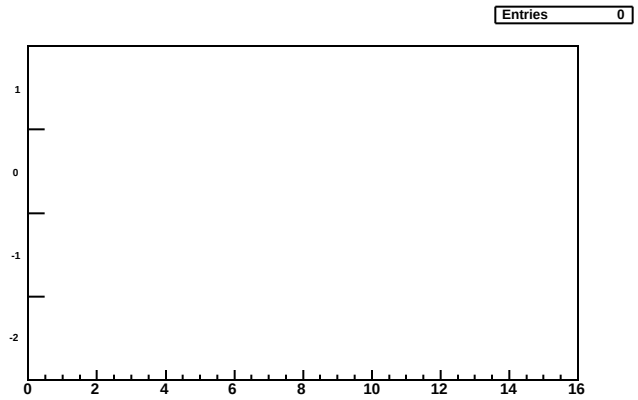
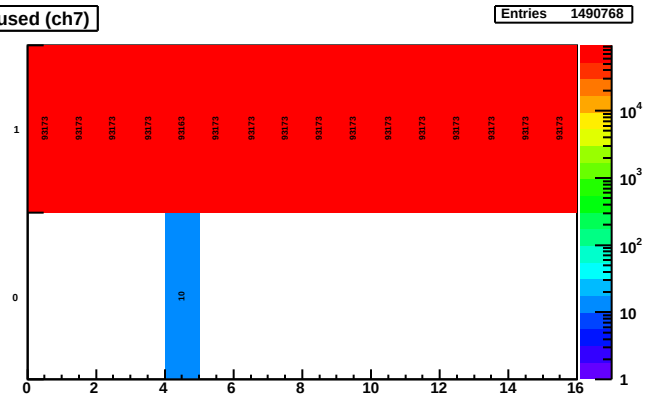
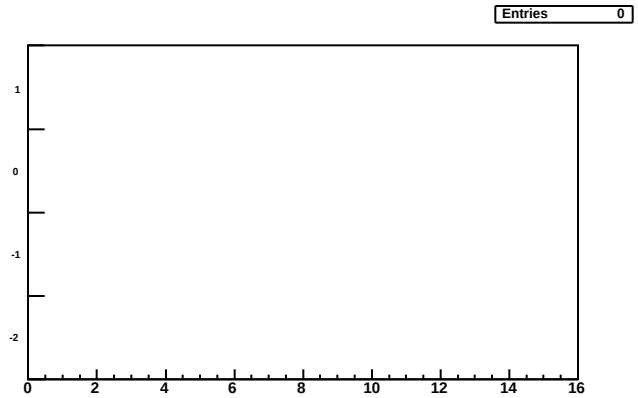


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)