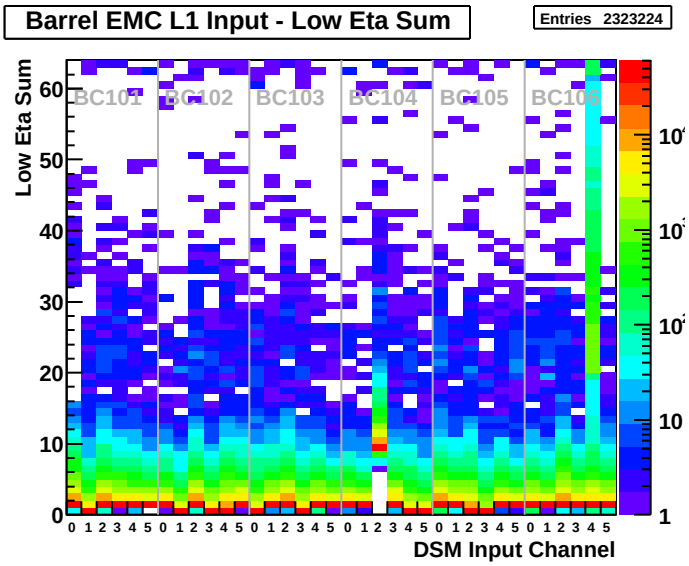
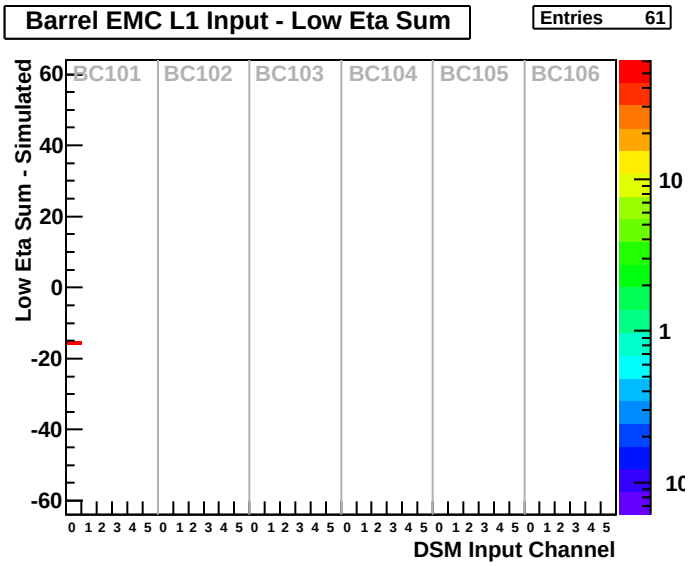
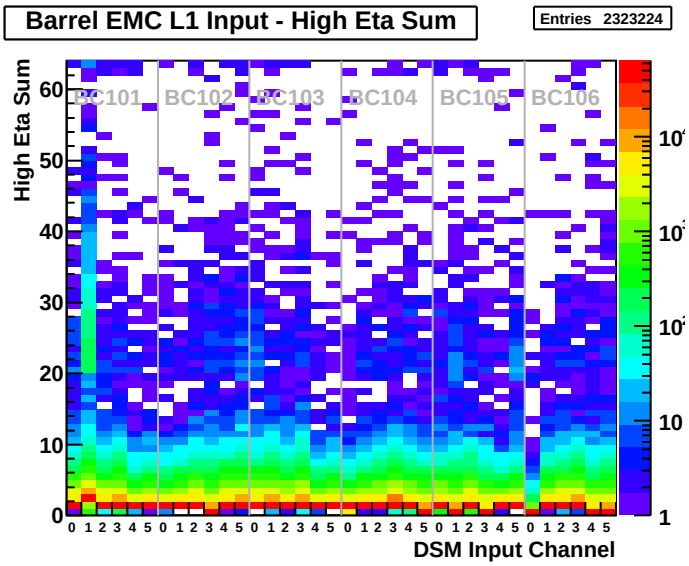
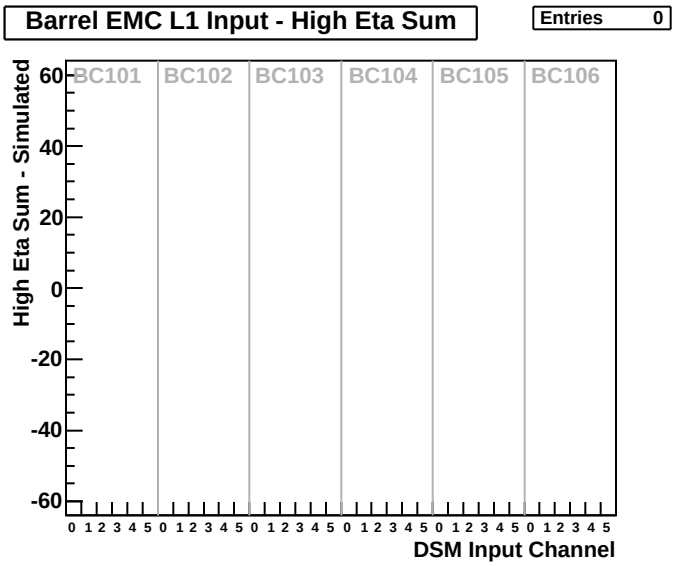
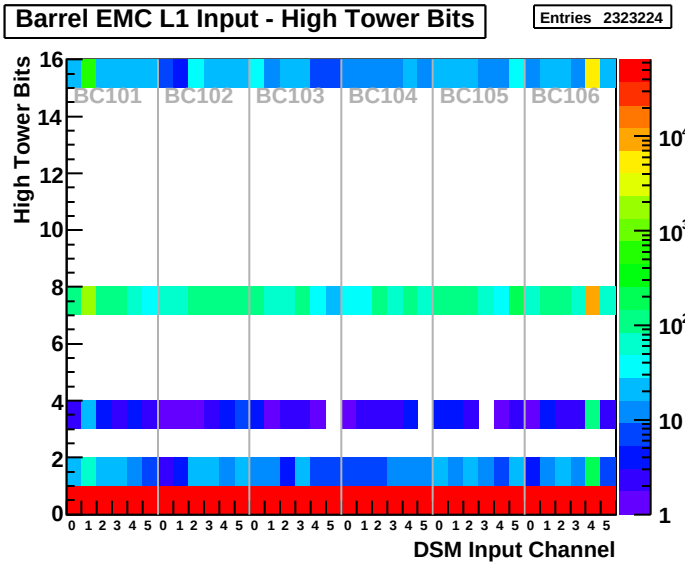
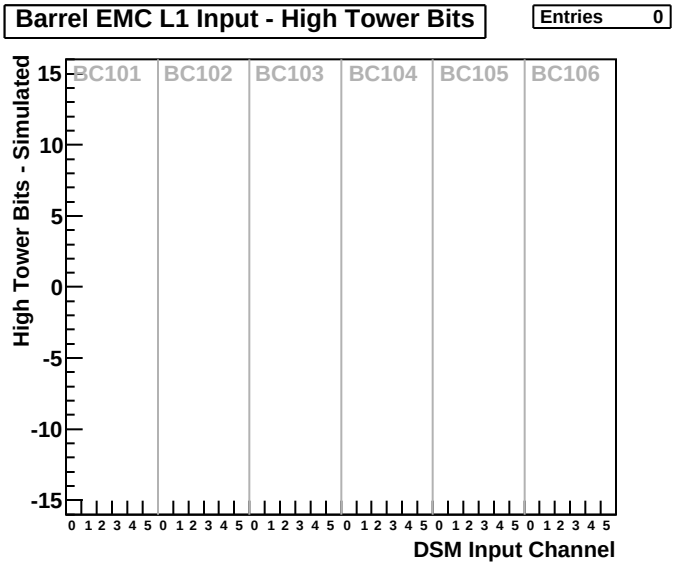

Barrel EMC L1 Input - Low Eta Sum
Entries 2323224
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

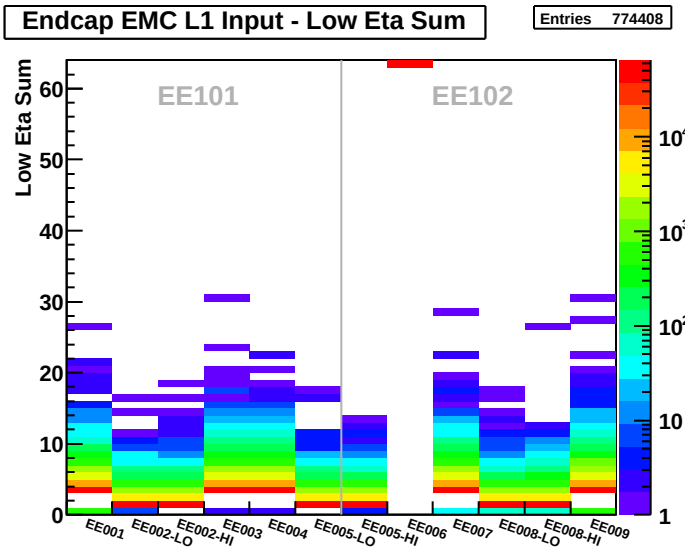

Barrel EMC L1 Input - Low Eta Sum
Entries 61
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

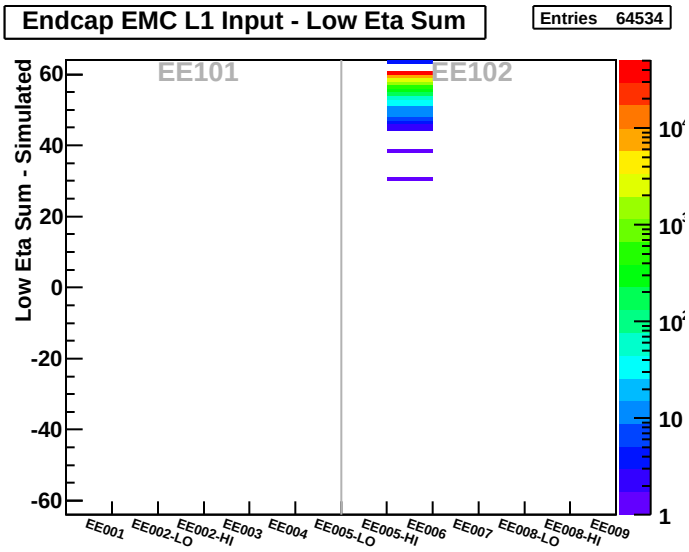

Barrel EMC L1 Input - High Eta Sum
Entries 2323224
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

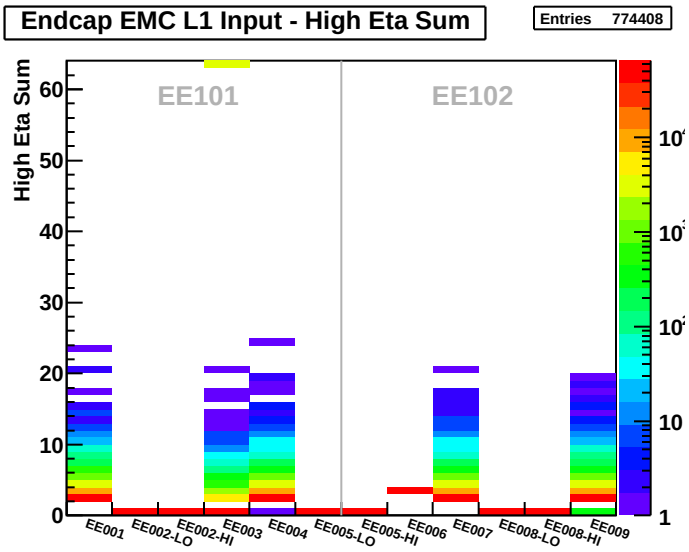

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

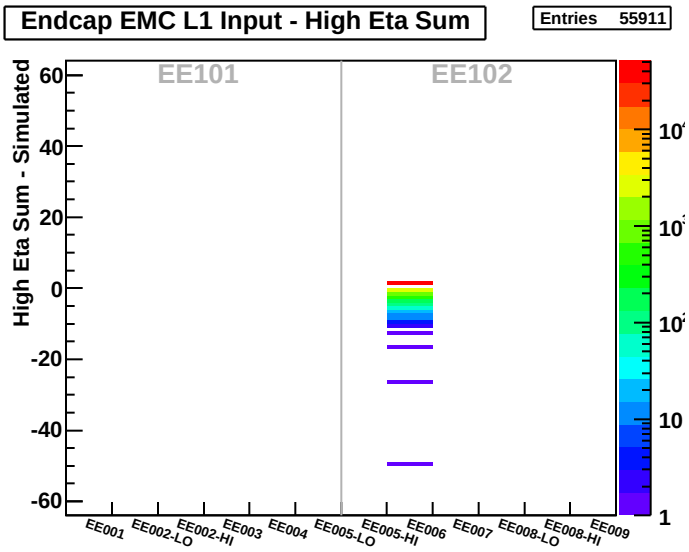

Barrel EMC L1 Input - High Tower Bits
Entries 2323224
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

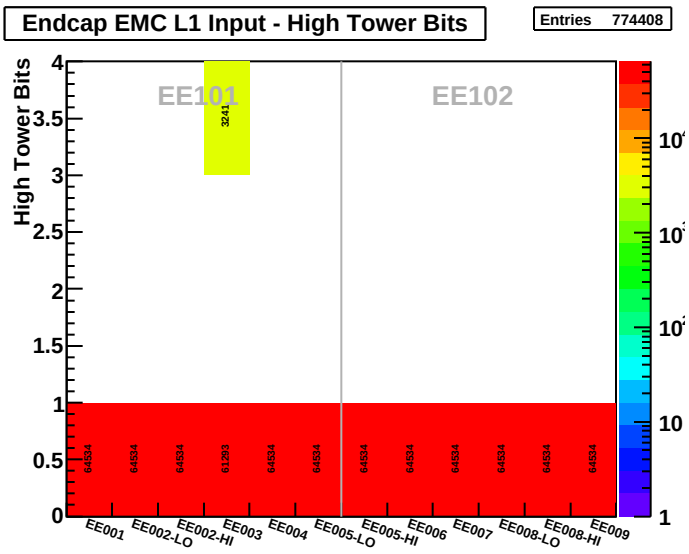

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

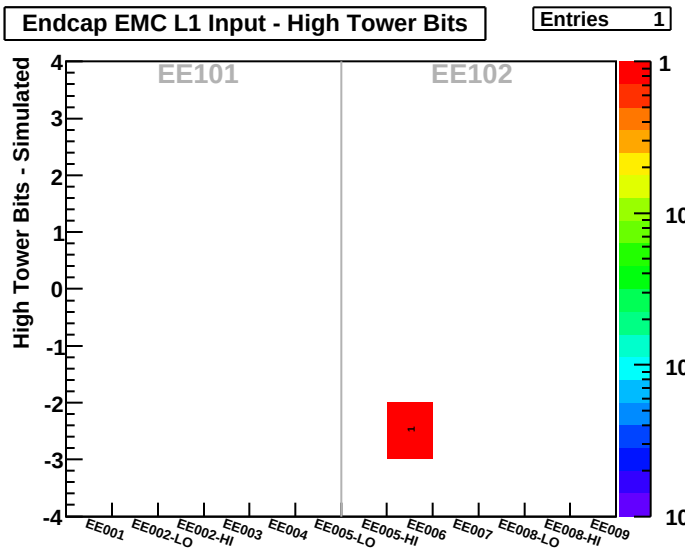

Endcap EMC L1 Input - Low Eta Sum
Entries 774408
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

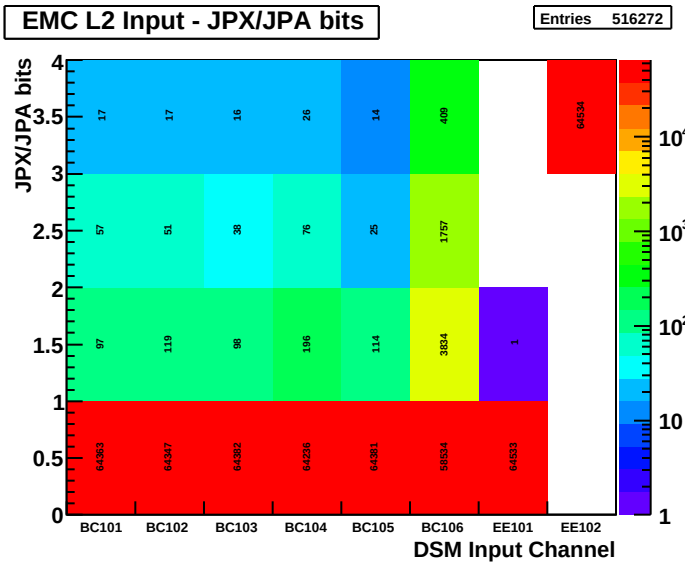

Endcap EMC L1 Input - Low Eta Sum
Entries 64534
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

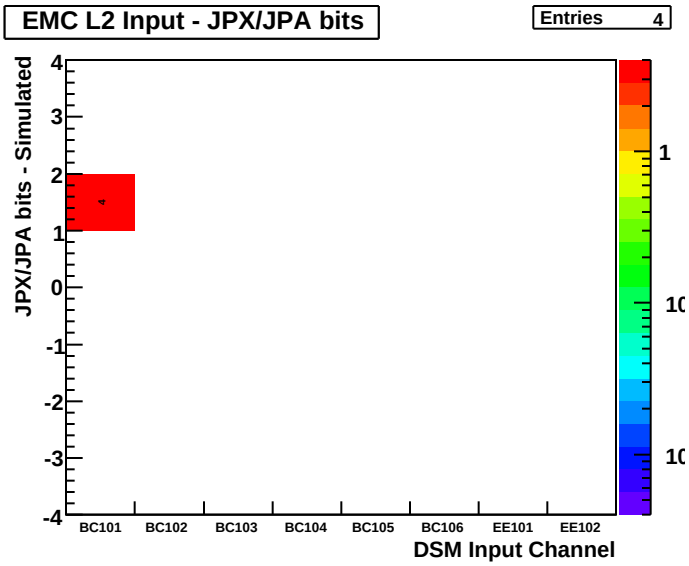

Endcap EMC L1 Input - High Eta Sum
Entries 774408
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

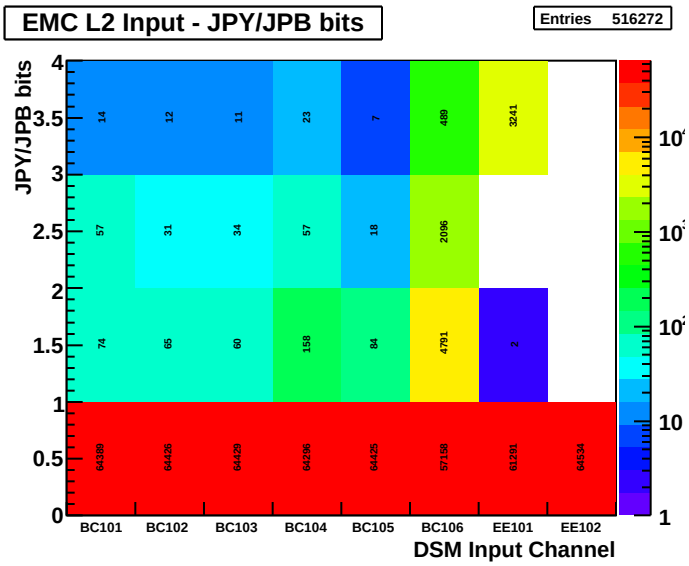

Endcap EMC L1 Input - High Eta Sum
Entries 55911
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

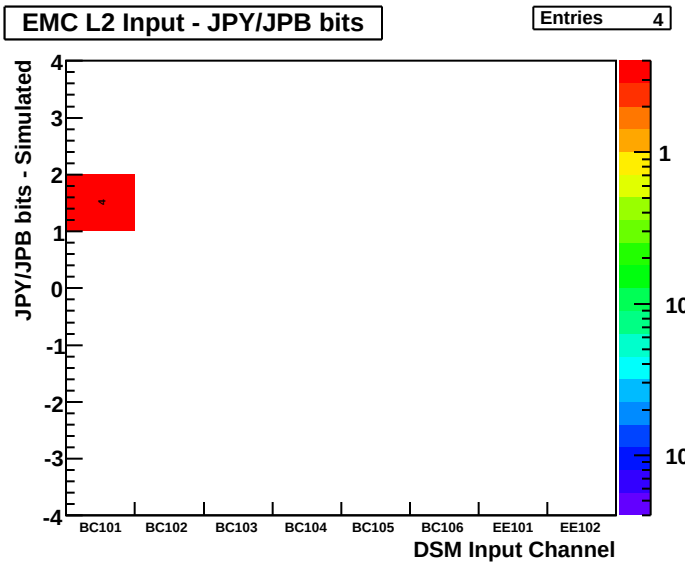

Endcap EMC L1 Input - High Tower Bits
Entries 774408
High Tower Bits
EE101
EE102
3241
64534 64534 64534 61293 64534 64534 64534 64534 64534 64534 64534 64534
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

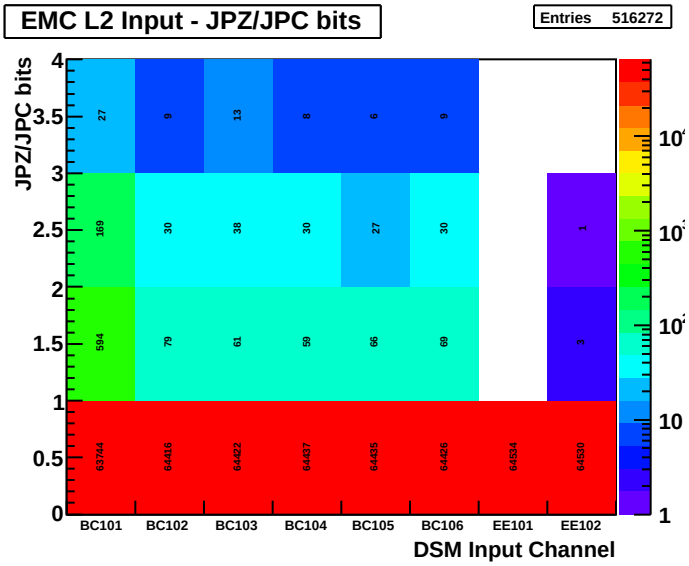

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

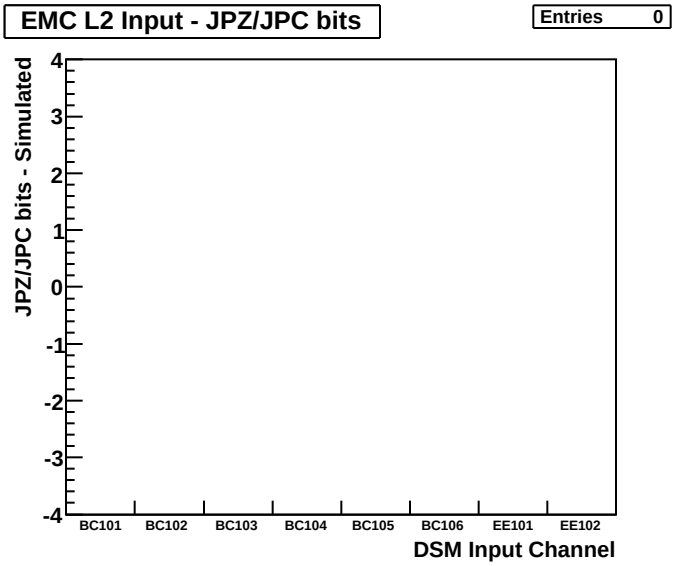

EMC L2 Input - JPX/JPA bits
Entries 516272
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
17 17 16 26 14 409 64534
57 51 38 76 25 1757
97 119 98 196 114 3834 1
64363 64347 64382 64236 64381 58534 64533

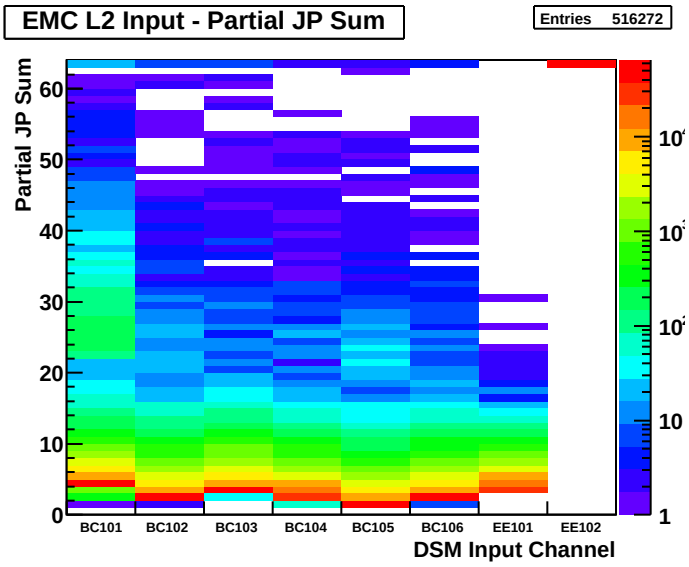

EMC L2 Input - JPX/JPA bits
Entries 4
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4

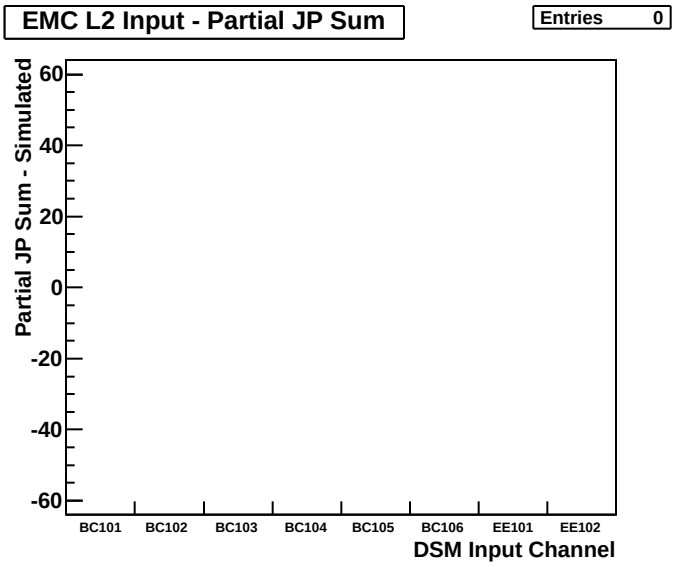

EMC L2 Input - JPY/JPB bits
Entries 516272
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
14 12 11 23 7 488 3241
57 31 34 57 18 2096
74 65 60 158 84 4791 2
64389 64426 64429 64296 64425 57158 61291 64534

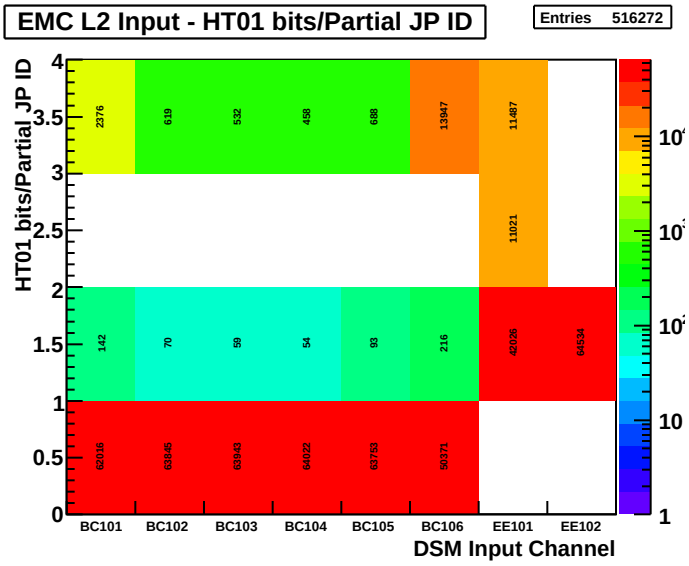

EMC L2 Input - JPY/JPB bits
Entries 4
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4

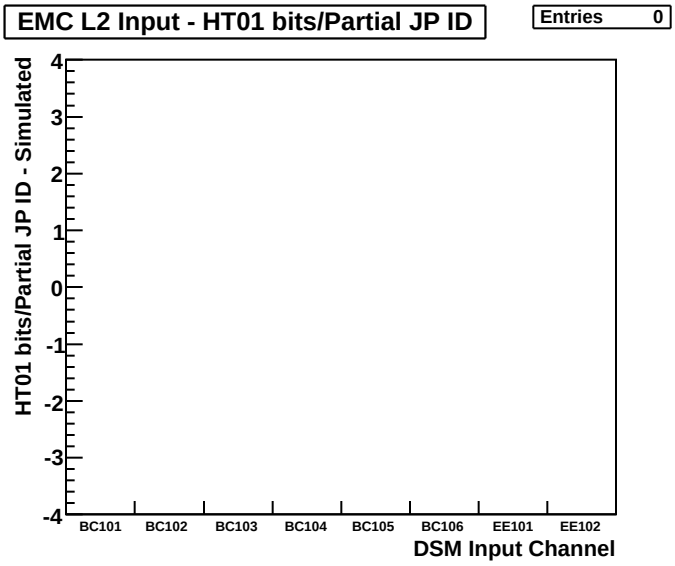

EMC L2 Input - JPZ/JPC bits
Entries 516272
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
27 9 13 8 6 9
169 30 38 30 27 30 1
594 79 61 59 66 69 3
63744 64416 64422 64437 64435 64426 64534 64530

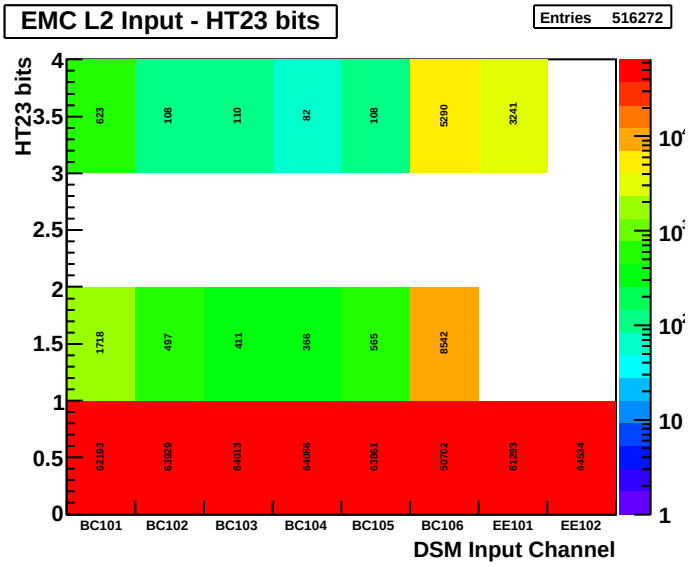

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

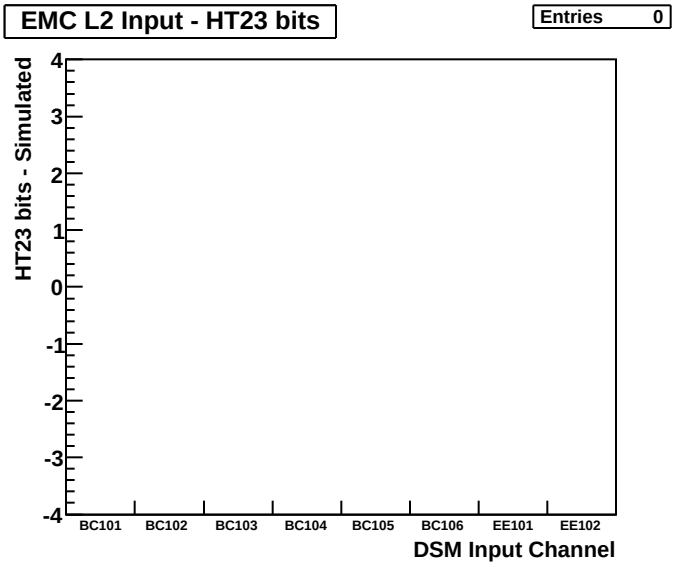

EMC L2 Input - Partial JP Sum
Entries 516272
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

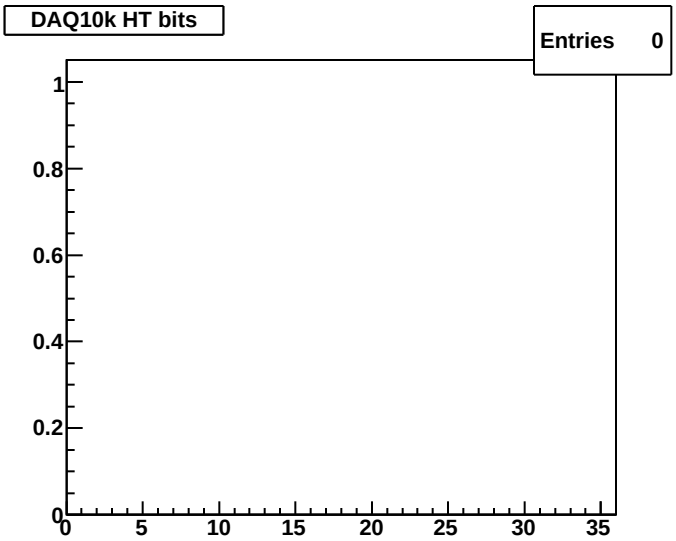

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

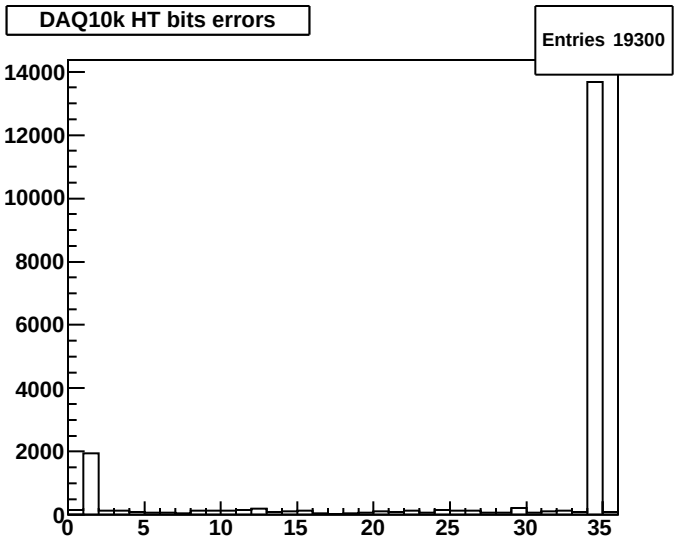

EMC L2 Input - HT01 bits/Partial JP ID
Entries 516272
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

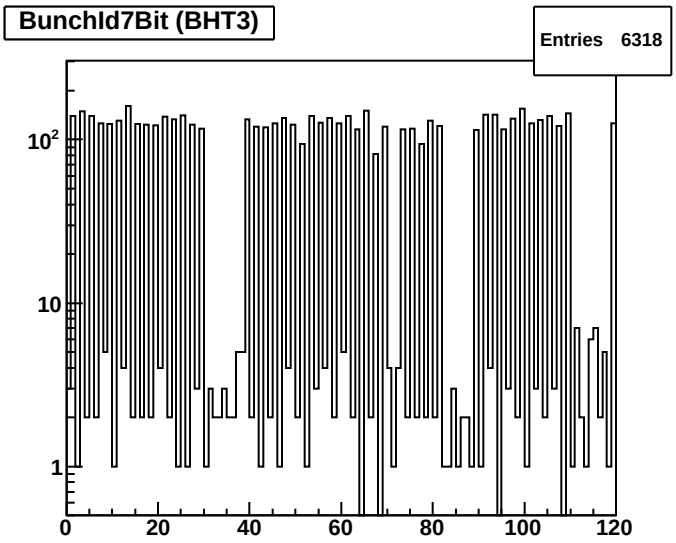

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

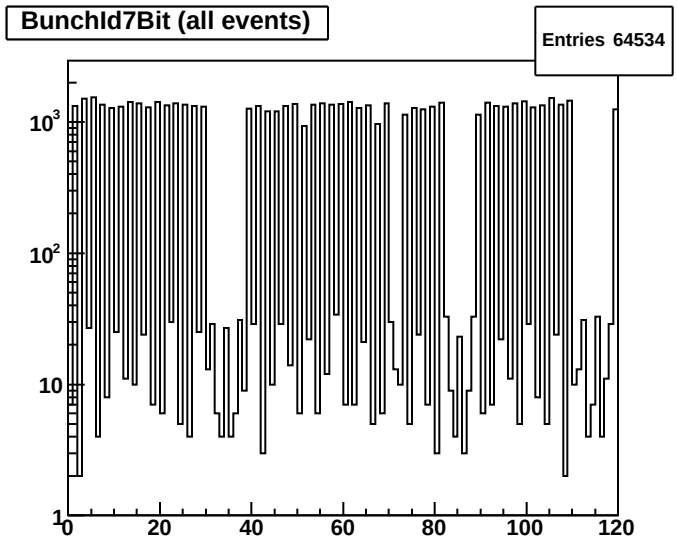

EMC L2 Input - HT23 bits
Entries 516272
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

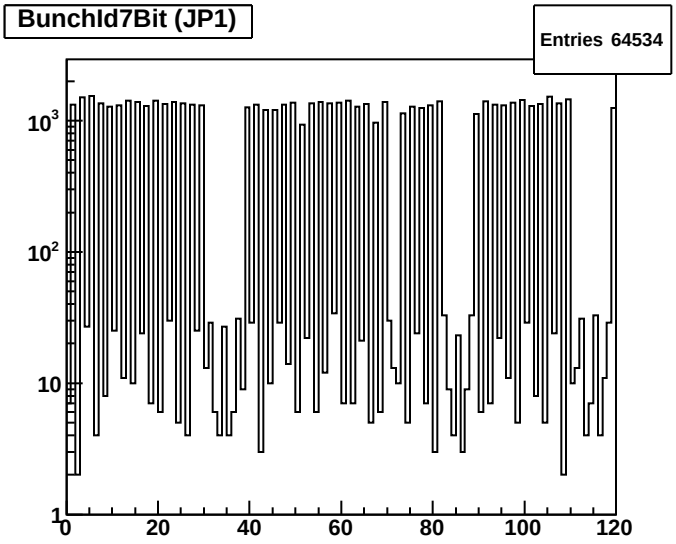

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

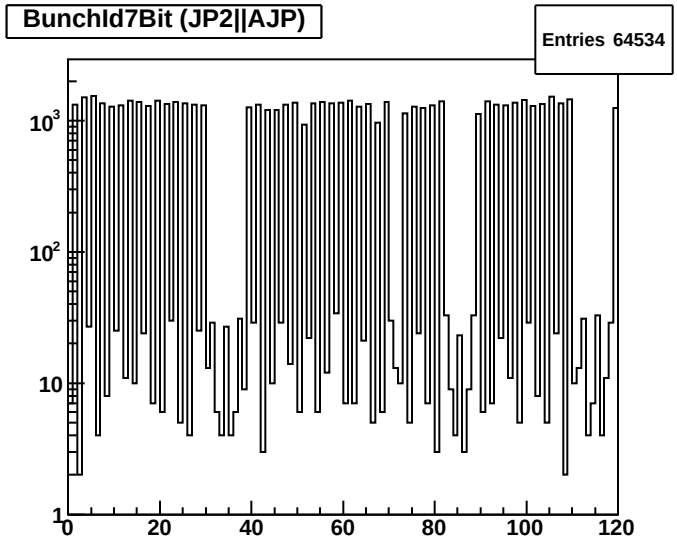

DAQ10k HT bits
Entries 0

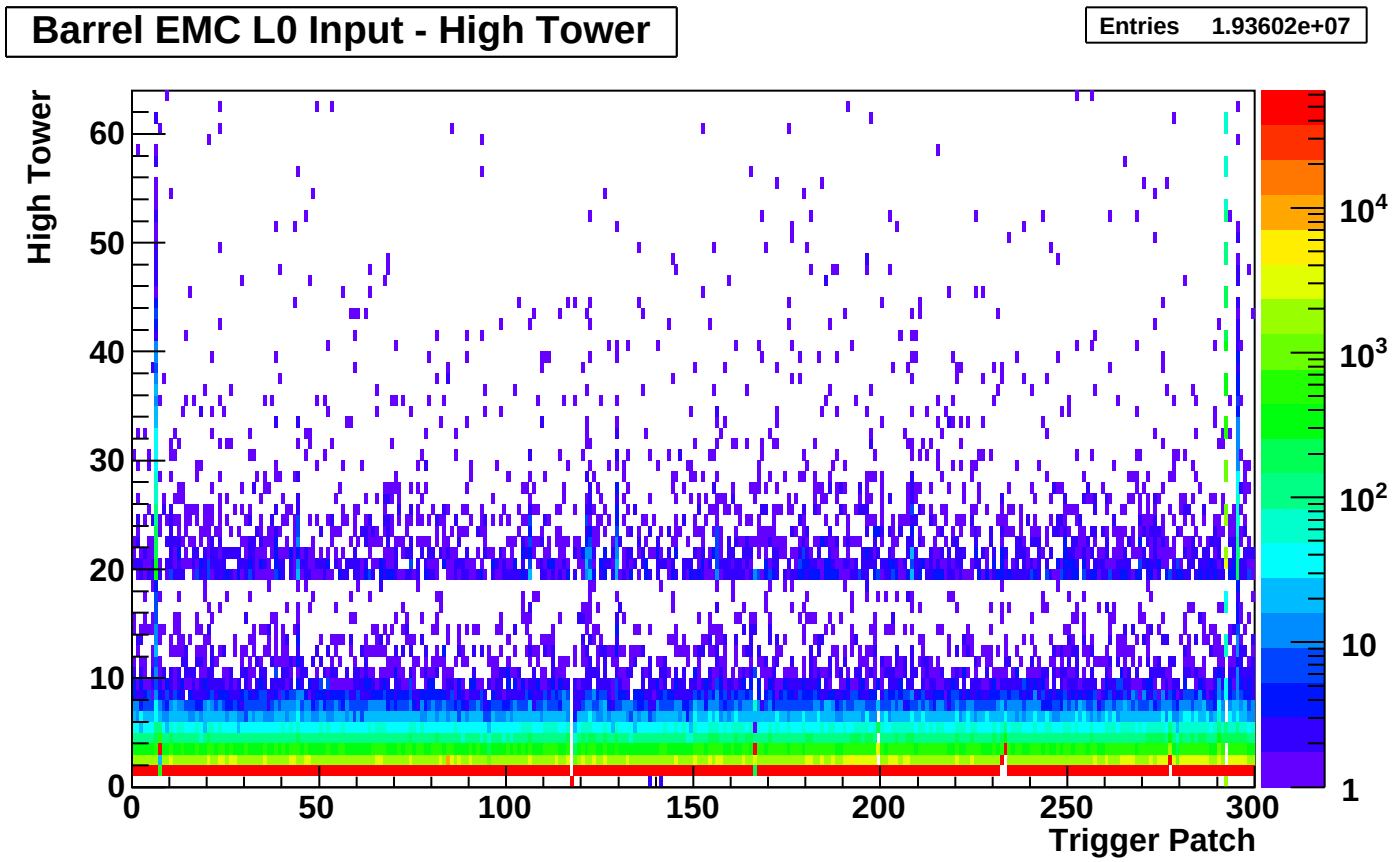

DAQ10k HT bits errors
Entries 19300

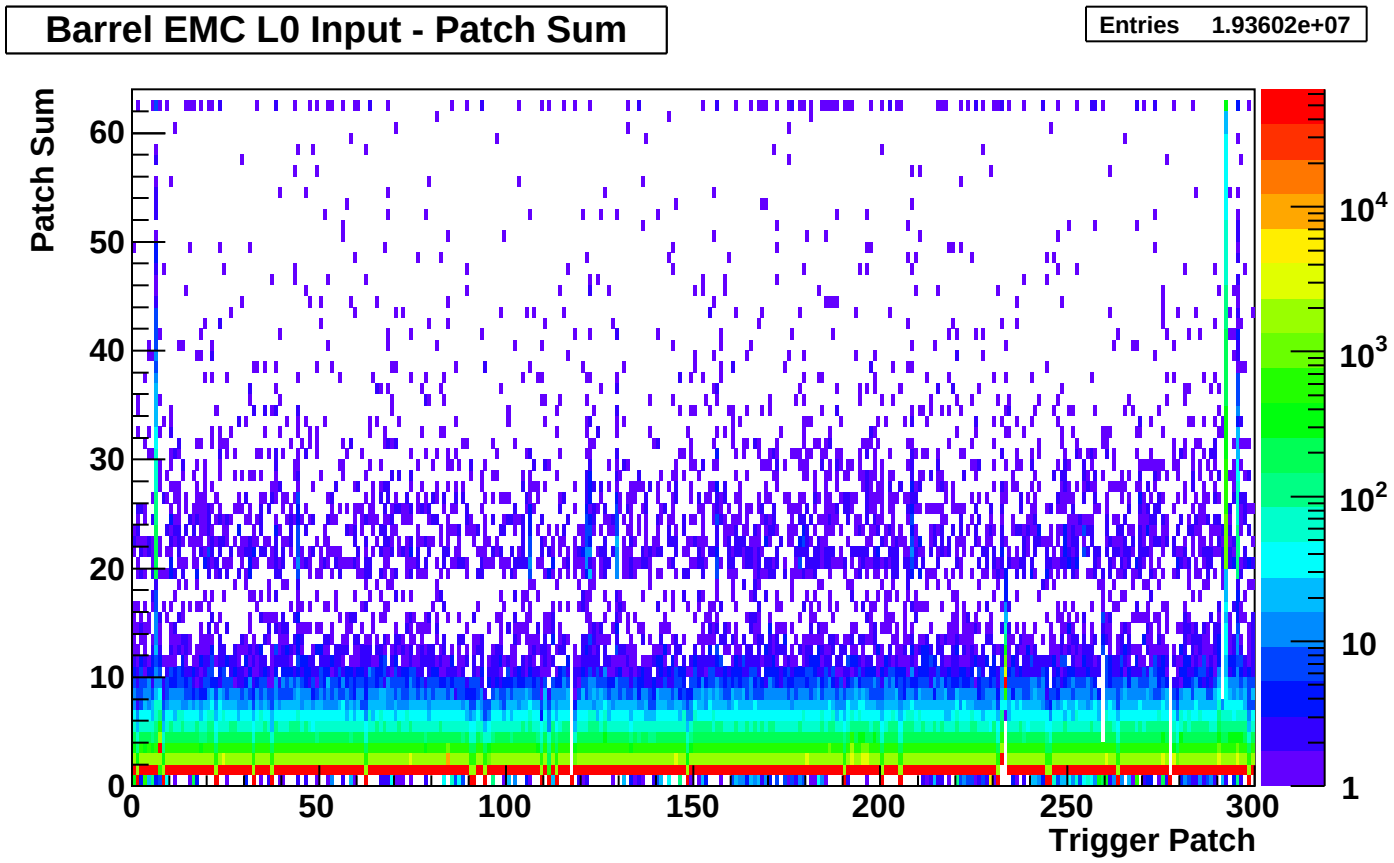

BunchId7Bit (BHT3)
Entries 6318

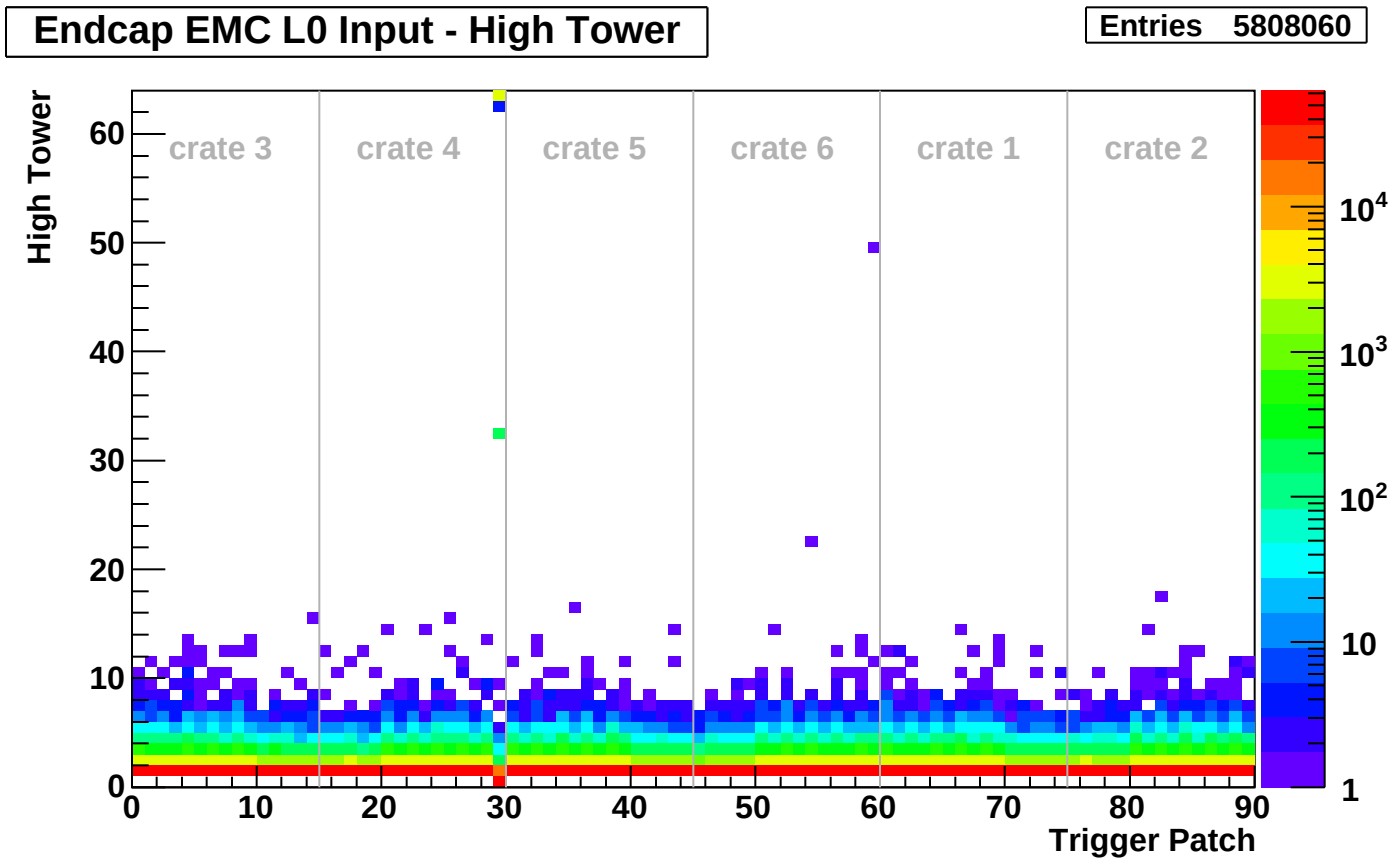

BunchId7Bit (all events)
Entries 64534

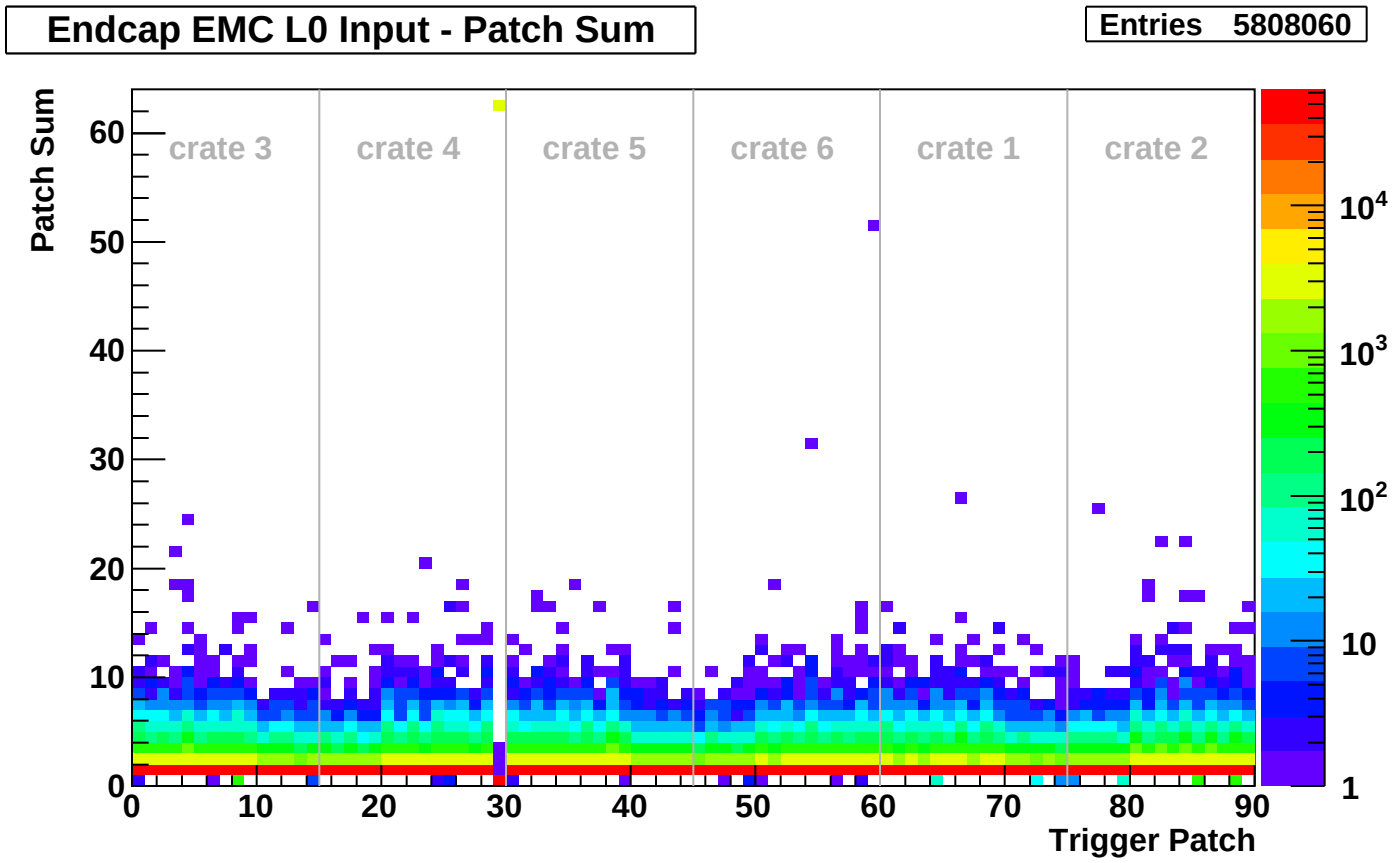

BunchId7Bit (JP1)
Entries 64534

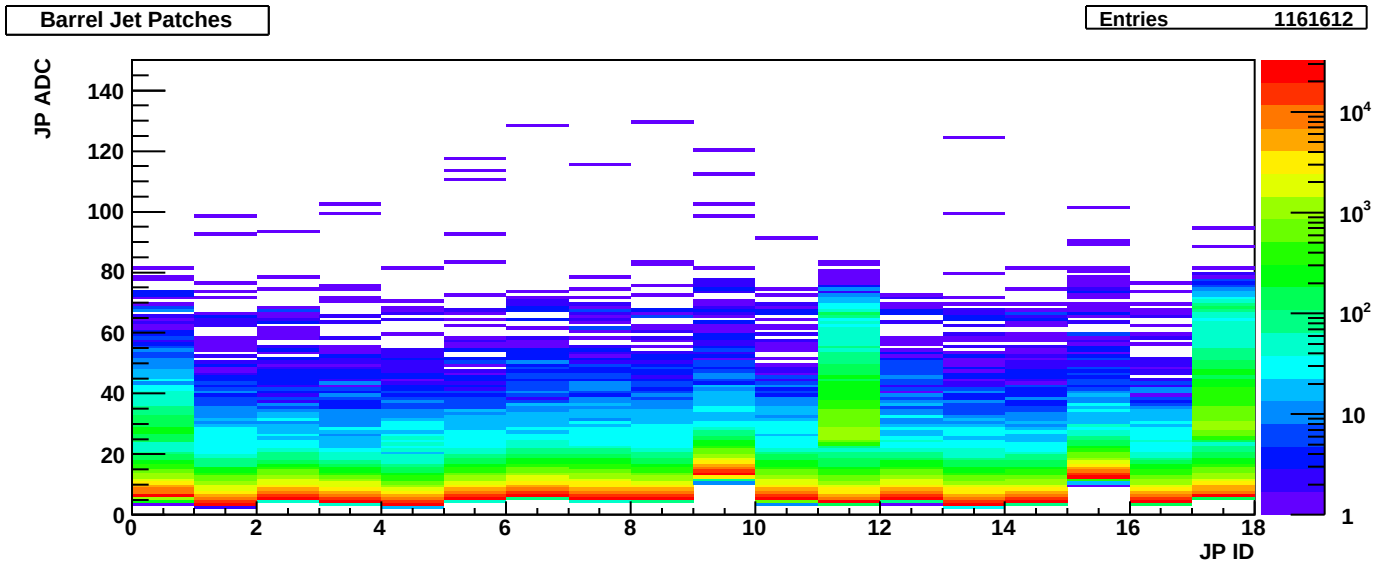

BunchId7Bit (JP2||AJP)
Entries 64534

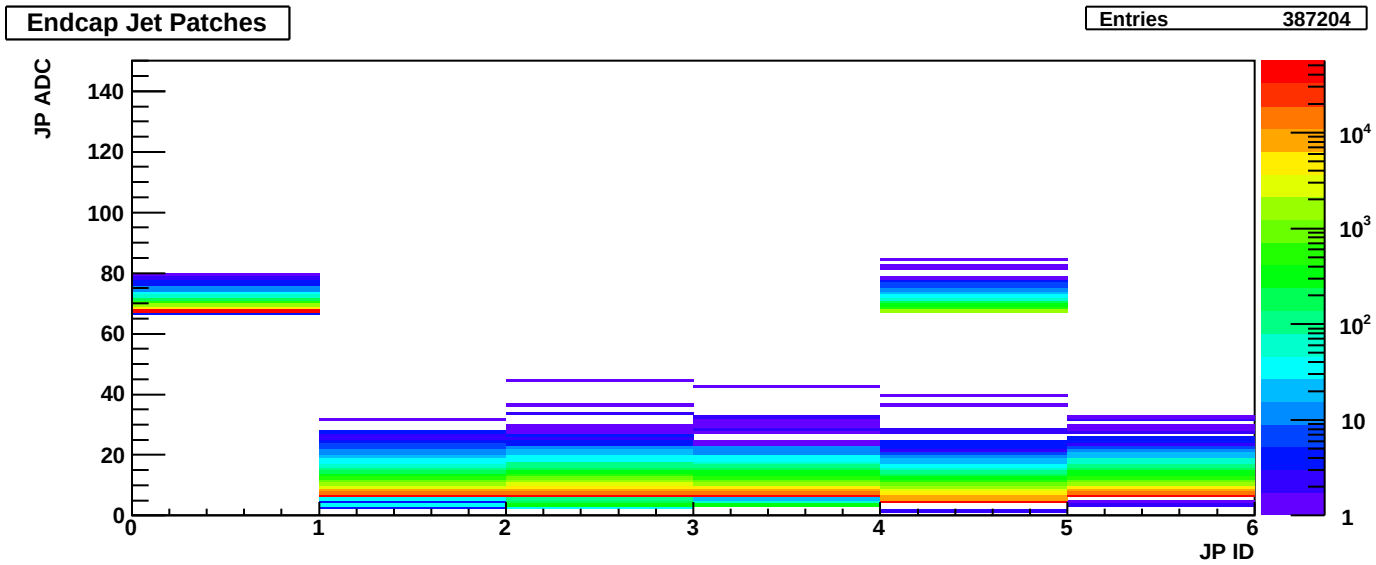

Barrel EMC L0 Input - High Tower
Entries 1.93602e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

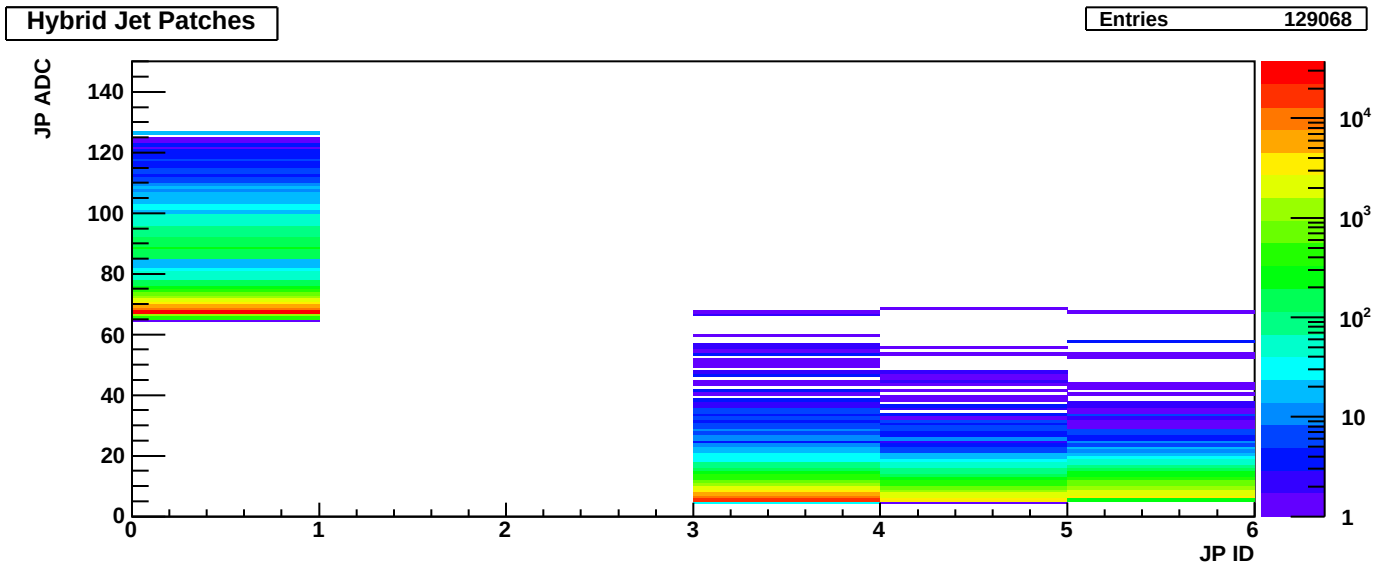

Barrel EMC L0 Input - Patch Sum
Entries 1.93602e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

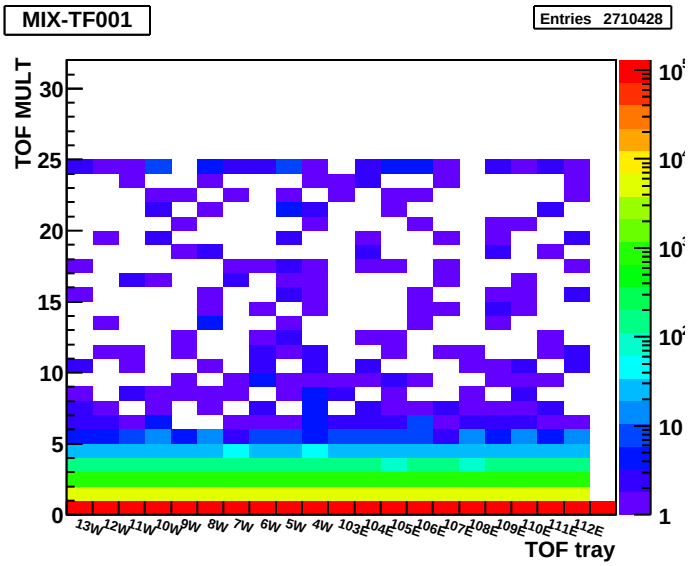

Endcap EMC L0 Input - High Tower
Entries 5808060
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

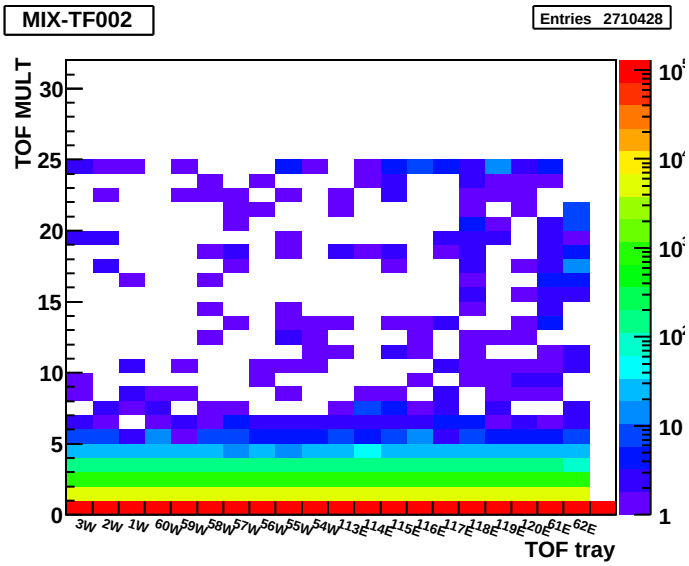

Endcap EMC L0 Input - Patch Sum
Entries 5808060
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

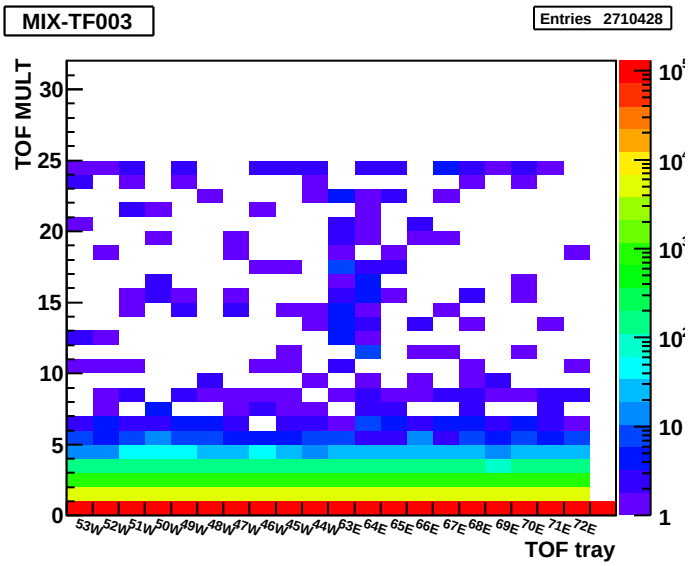

Barrel Jet Patches
Entries 1161612
JP ADC
JP ID

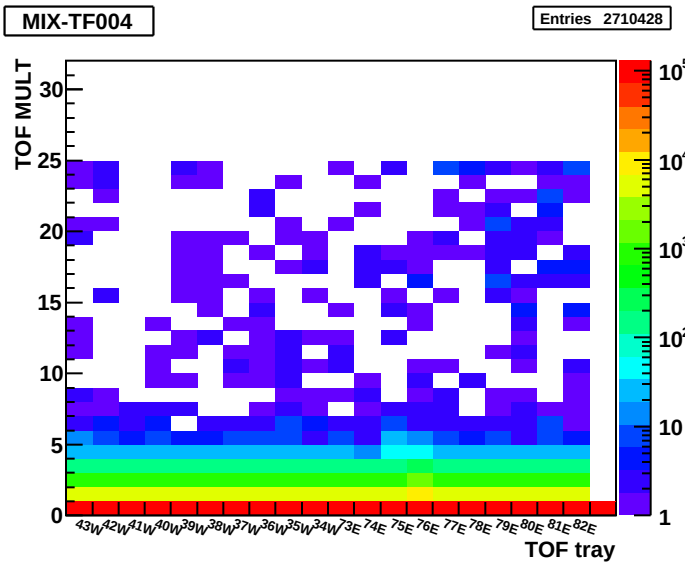

Endcap Jet Patches
Entries 387204
JP ADC
JP ID

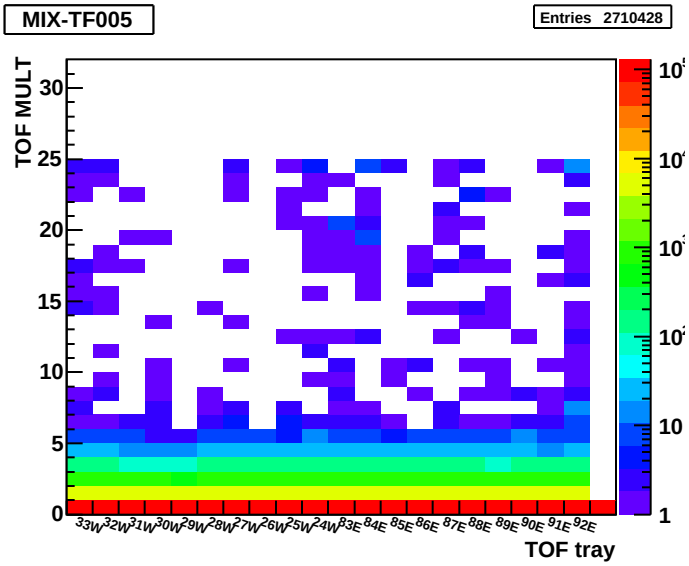

Hybrid Jet Patches
Entries 129068
JP ADC
JP ID

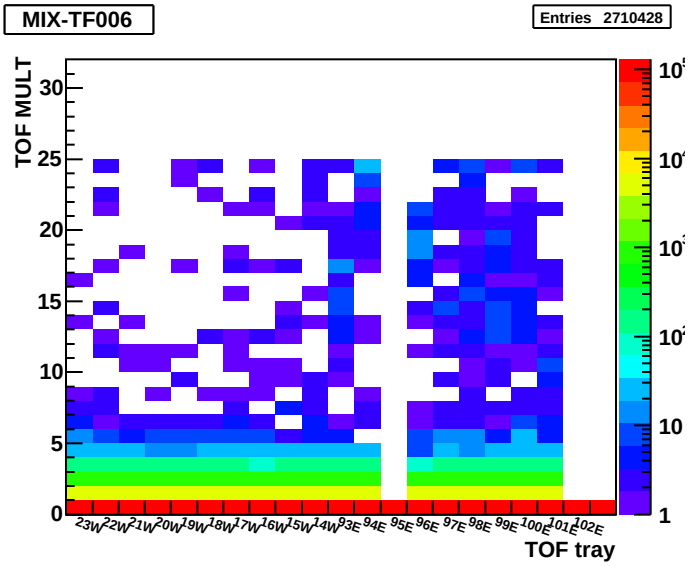

MIX-TF001
Entries 2710428
TOF MULT
TOF tray

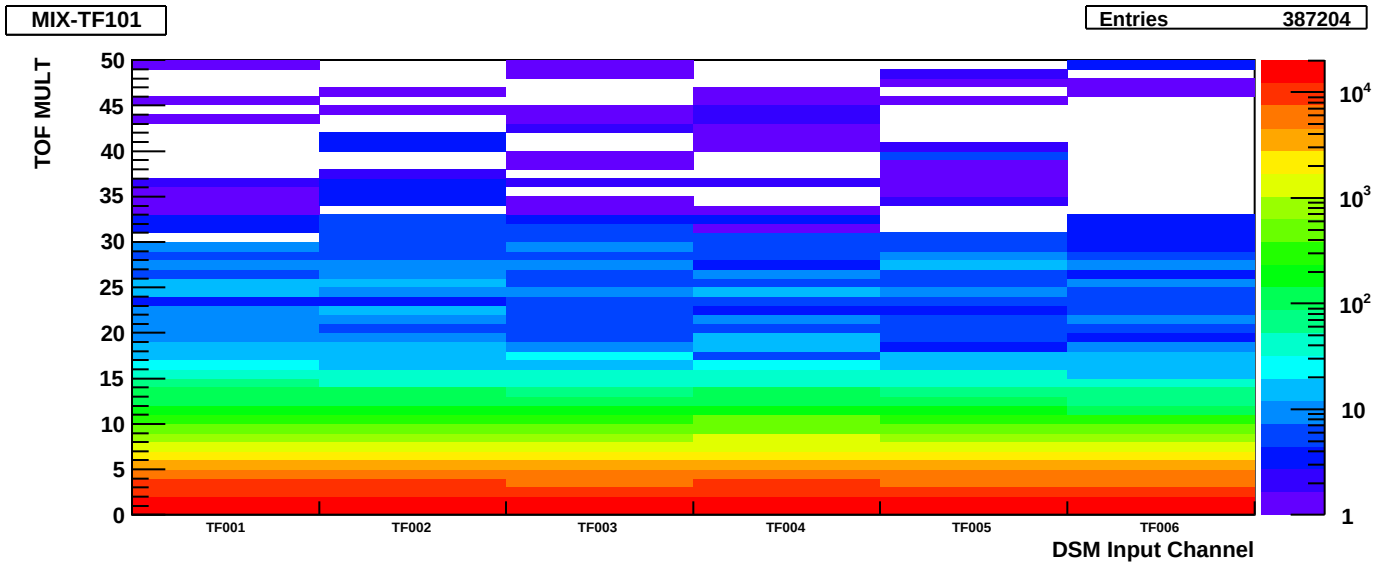

MIX-TF002
Entries 2710428
TOF MULT
TOF tray

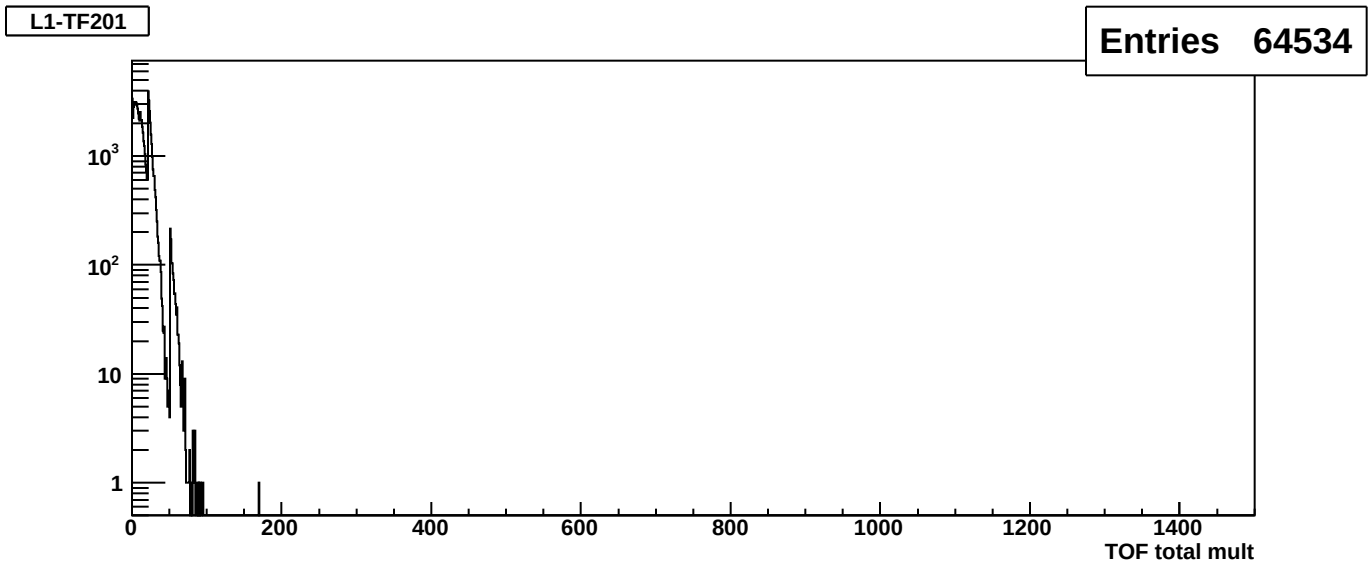

MIX-TF003
Entries 2710428
TOF MULT
TOF tray

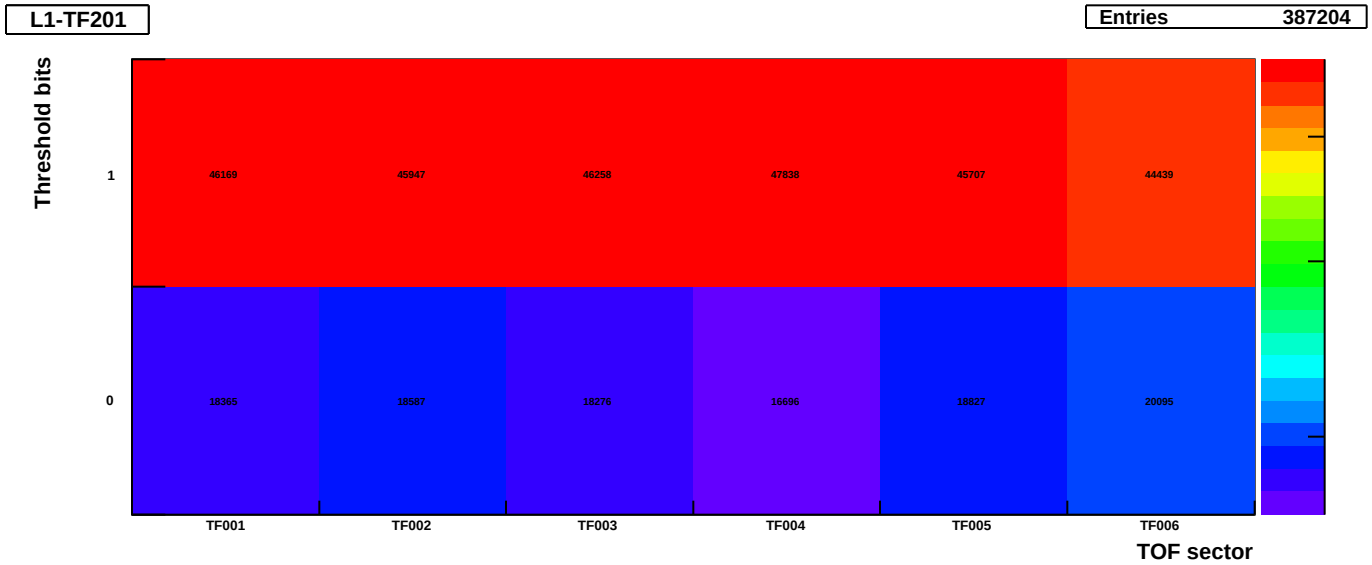

MIX-TF004
Entries 2710428
TOF MULT
TOF tray

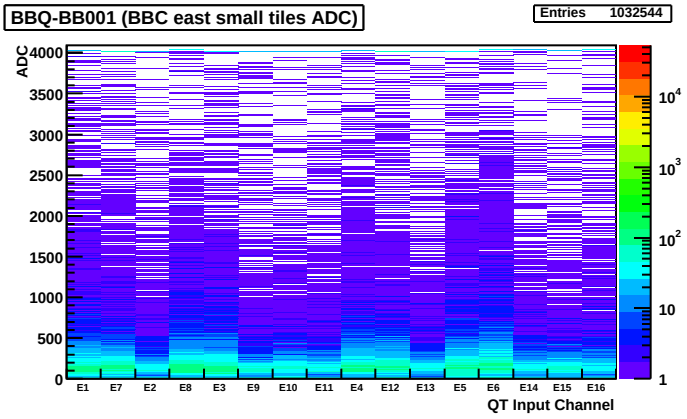

MIX-TF005
Entries 2710428
TOF MULT
TOF tray

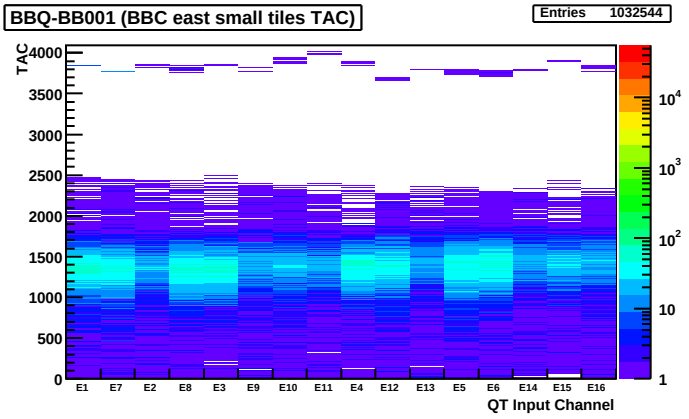

MIX-TF006
Entries 2710428
TOF MULT
TOF tray

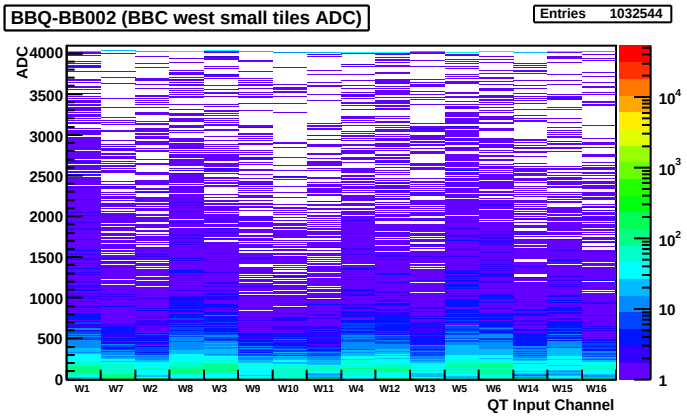

MIX-TF101
Entries 387204
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

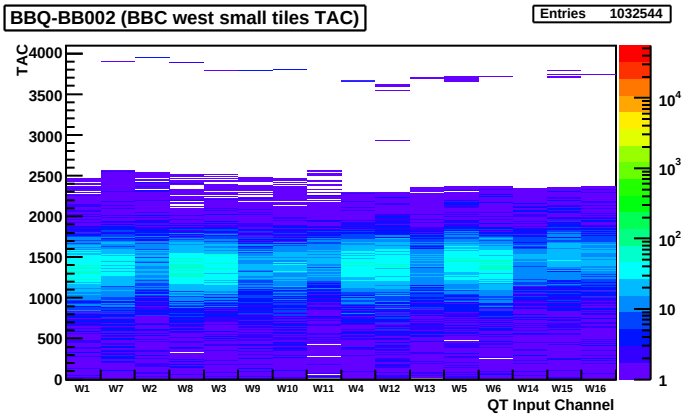

L1-TF201
Entries 64534
TOF total mult

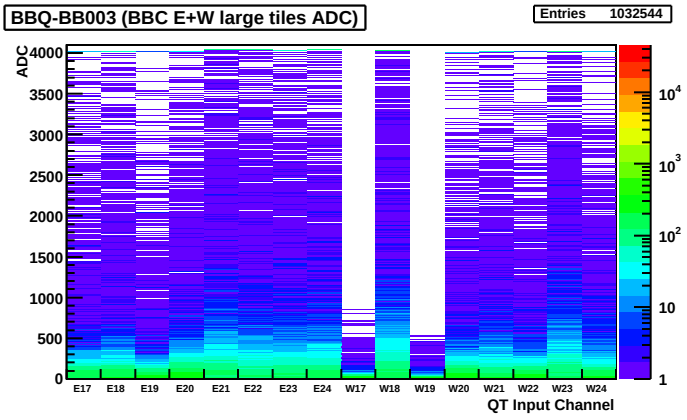

L1-TF201
Entries 387204
Threshold bits
46169
45947
46258
47838
45707
44439
18365
18587
18276
16696
18827
20095
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

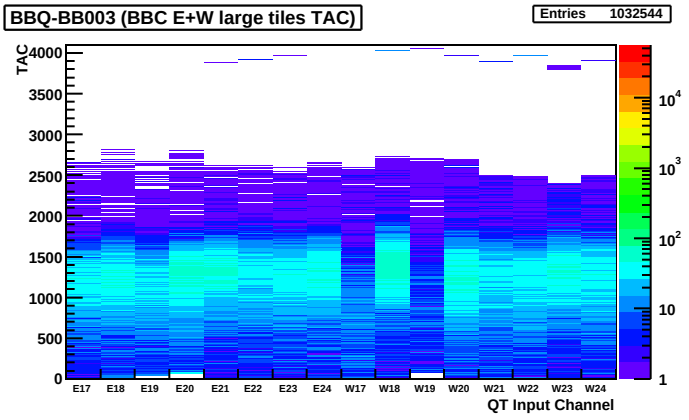

BBQ-BB001 (BBC east small tiles ADC)
Entries 1032544
ADC
QT Input Channel

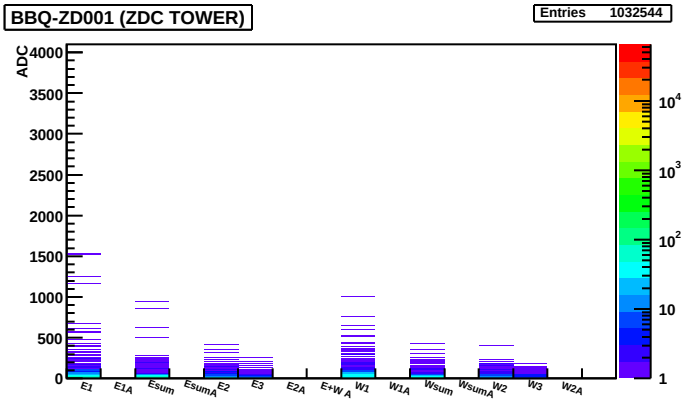

BBQ-BB001 (BBC east small tiles TAC)
Entries 1032544
TAC
QT Input Channel

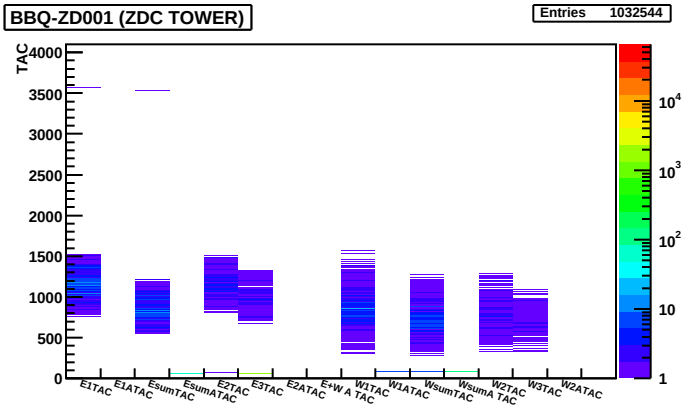

BBQ-BB002 (BBC west small tiles ADC)
Entries 1032544
ADC
QT Input Channel

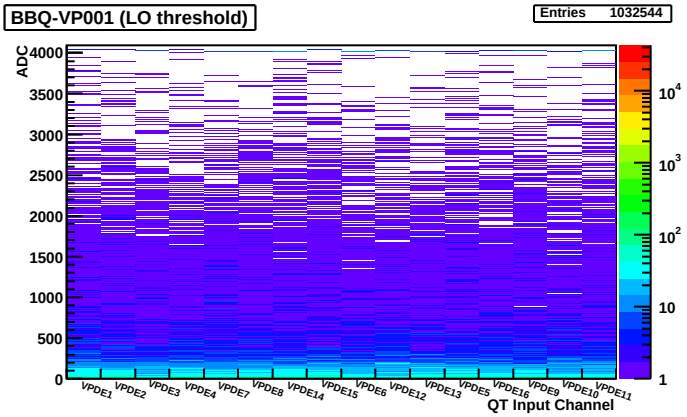

BBQ-BB002 (BBC west small tiles TAC)
Entries 1032544
TAC
QT Input Channel

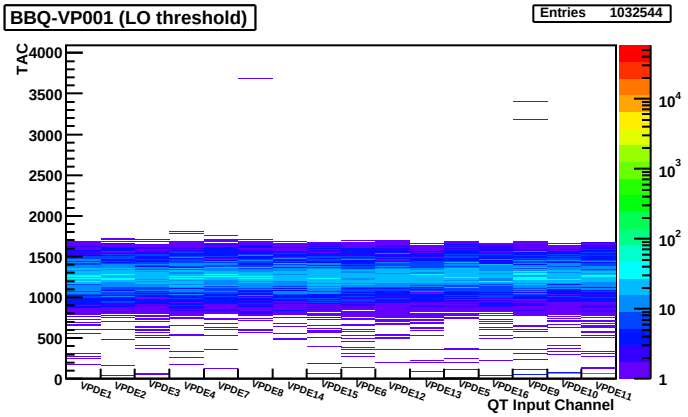

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1032544
ADC
QT Input Channel

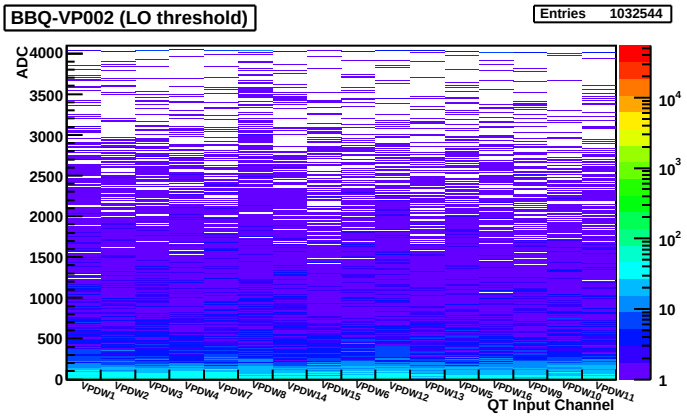

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1032544
TAC
QT Input Channel

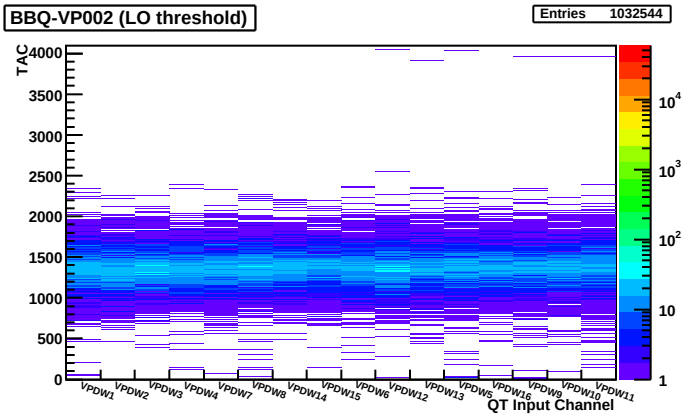

BBQ-ZD001 (ZDC TOWER)
Entries 1032544
ADC

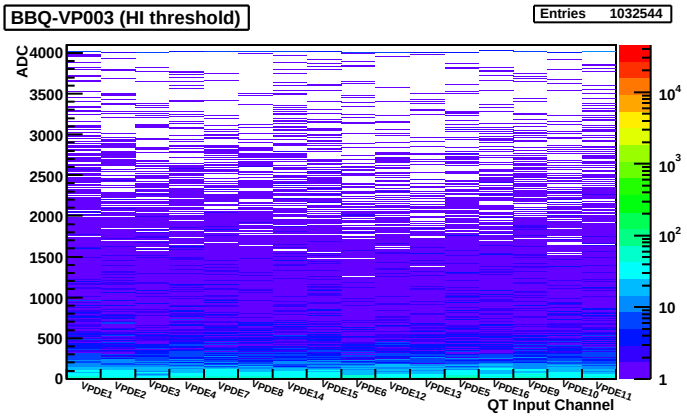

BBQ-ZD001 (ZDC TOWER)
Entries 1032544
TAC

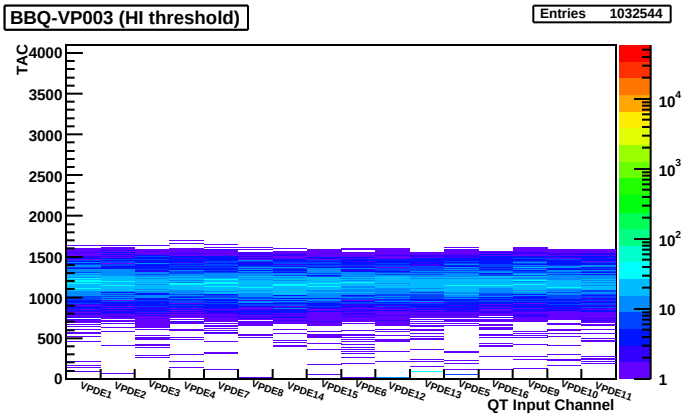

BBQ-VP001 (LO threshold)
Entries 1032544
ADC
QT Input Channel

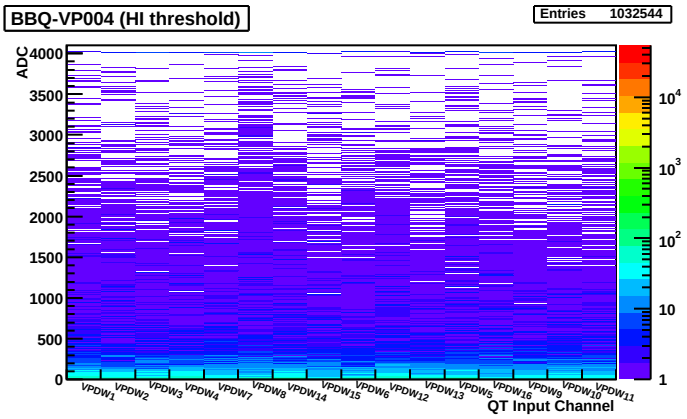

BBQ-VP001 (LO threshold)
Entries 1032544
TAC
QT Input Channel

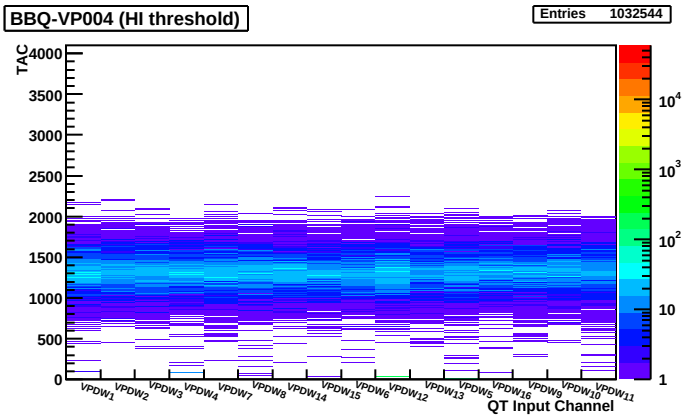

BBQ-VP002 (LO threshold)
Entries 1032544
ADC
QT Input Channel

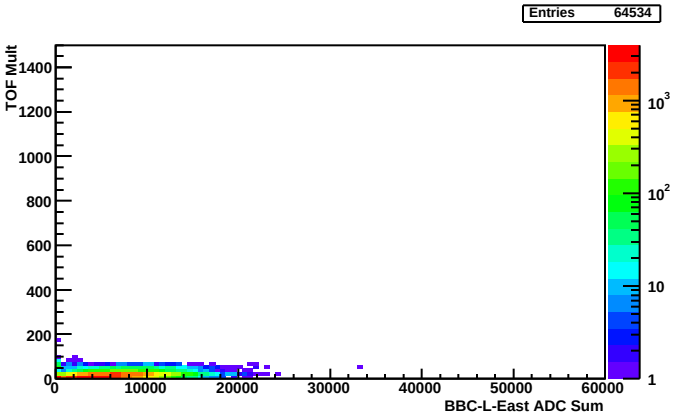

BBQ-VP002 (LO threshold)
Entries 1032544
TAC
QT Input Channel

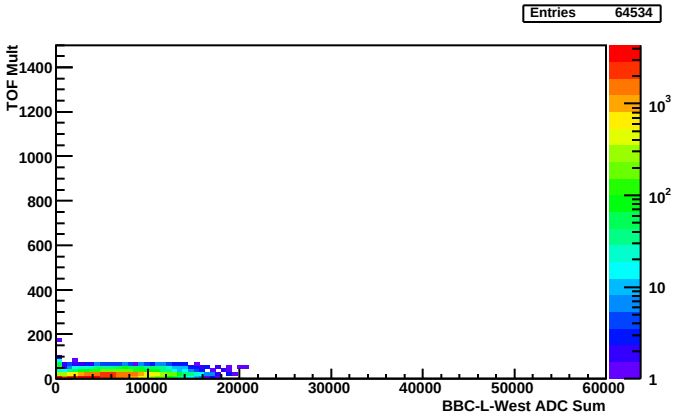

BBQ-VP003 (HI threshold)
Entries 1032544
ADC
QT Input Channel

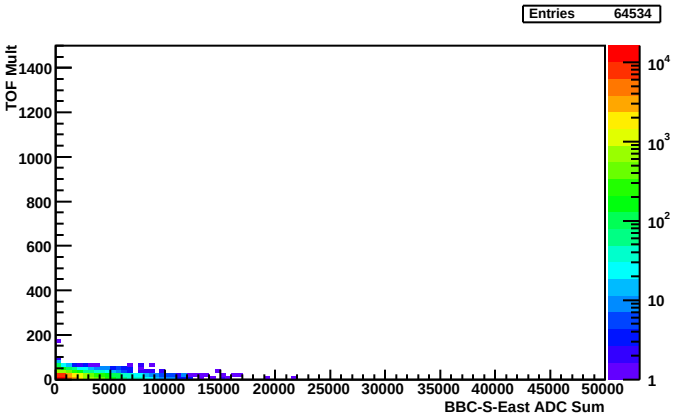

BBQ-VP003 (HI threshold)
Entries 1032544
TAC
QT Input Channel

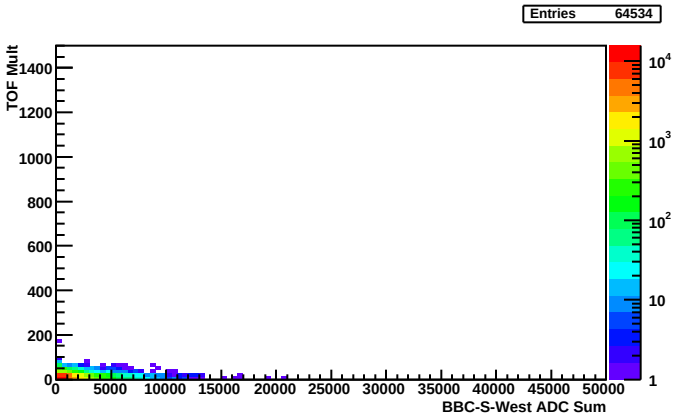

BBQ-VP004 (HI threshold)
Entries 1032544
ADC
QT Input Channel

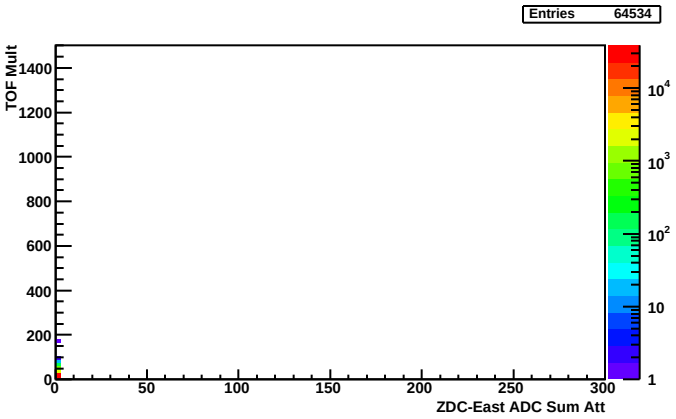

BBQ-VP004 (HI threshold)
Entries 1032544
TAC
QT Input Channel

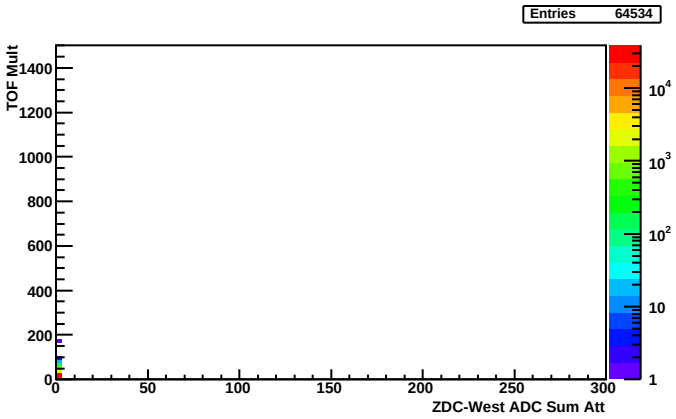

Entries 64534
TOF Mult
BBC-L-East ADC Sum

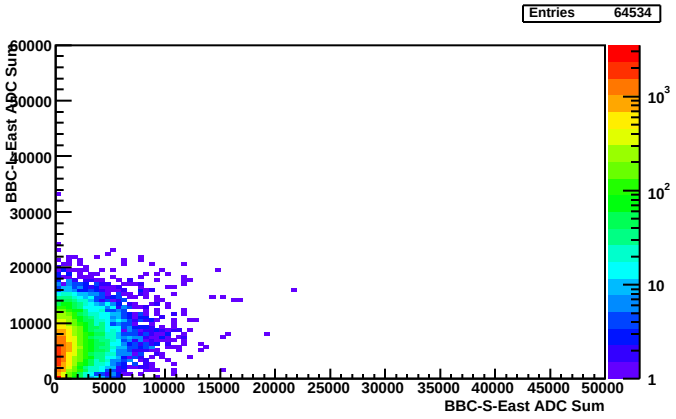

Entries 64534
TOF Mult
BBC-L-West ADC Sum

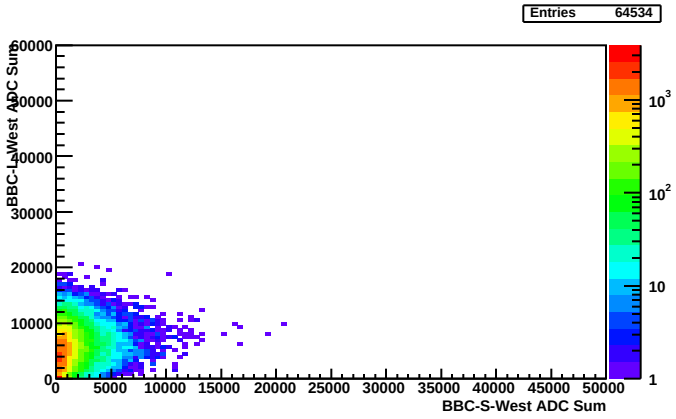

Entries 64534
TOF Mult
BBC-S-East ADC Sum

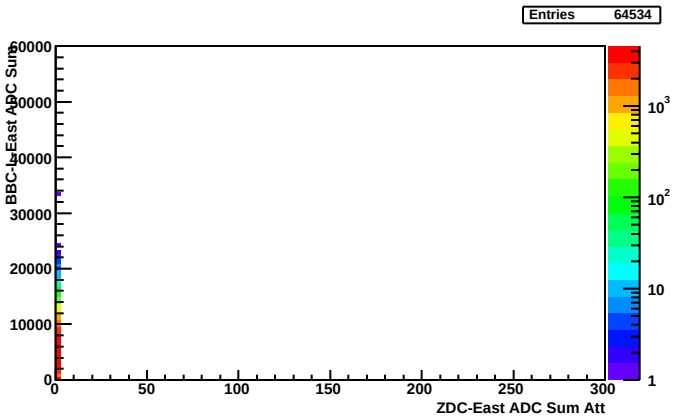

Entries 64534
TOF Mult
BBC-S-West ADC Sum

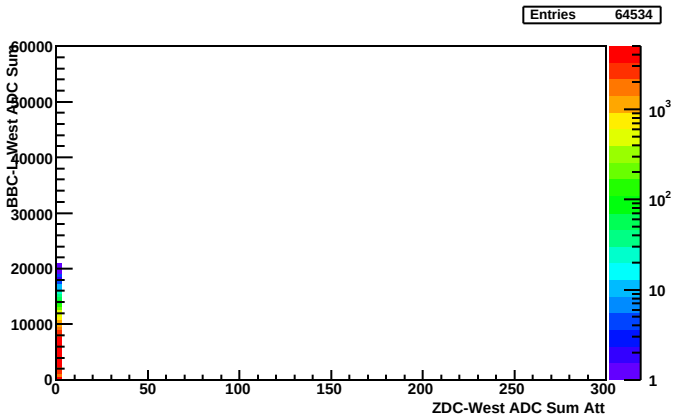

Entries 64534
TOF Mult
ZDC-East ADC Sum Att

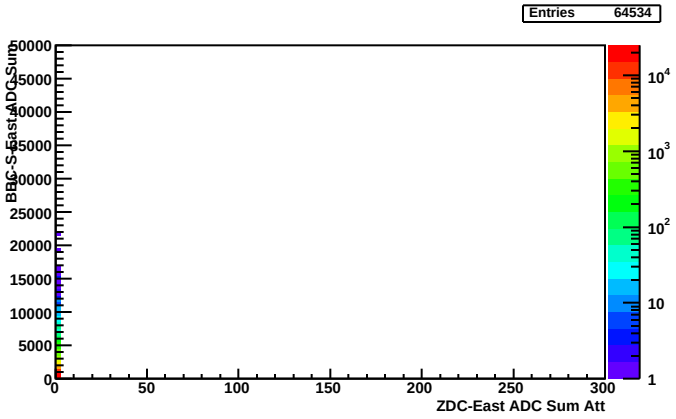

Entries 64534
TOF Mult
ZDC-West ADC Sum Att

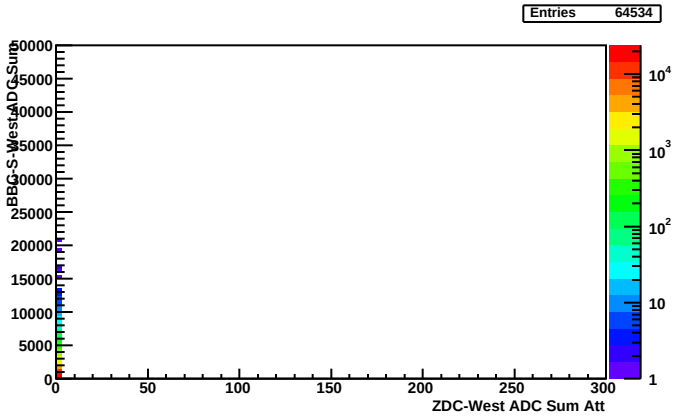

Entries 64534
BBC-L-East ADC Sum
BBC-S-East ADC Sum

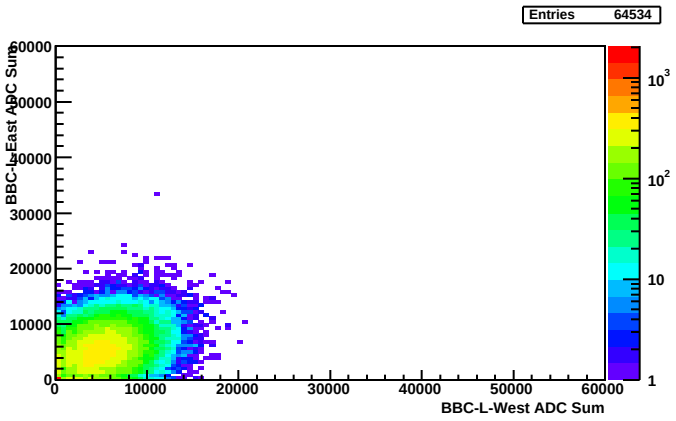

Entries 64534
BBC-L-West ADC Sum
BBC-S-West ADC Sum

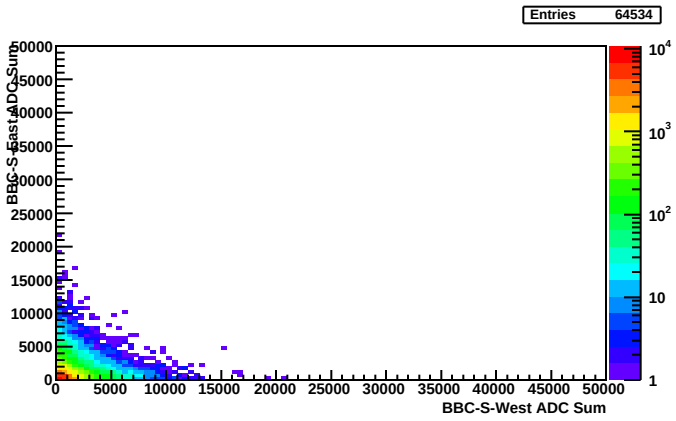

Entries 64534
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

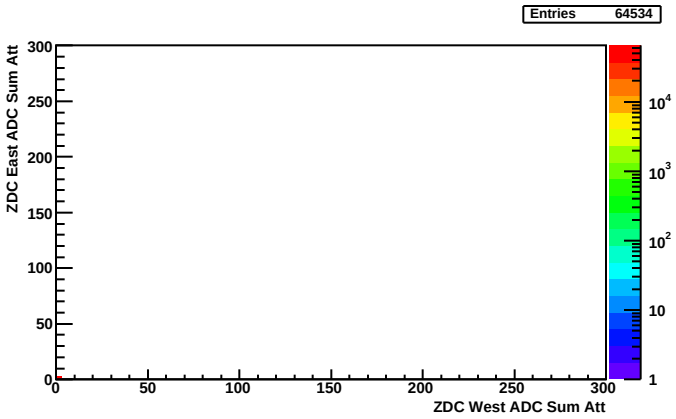

Entries 64534
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

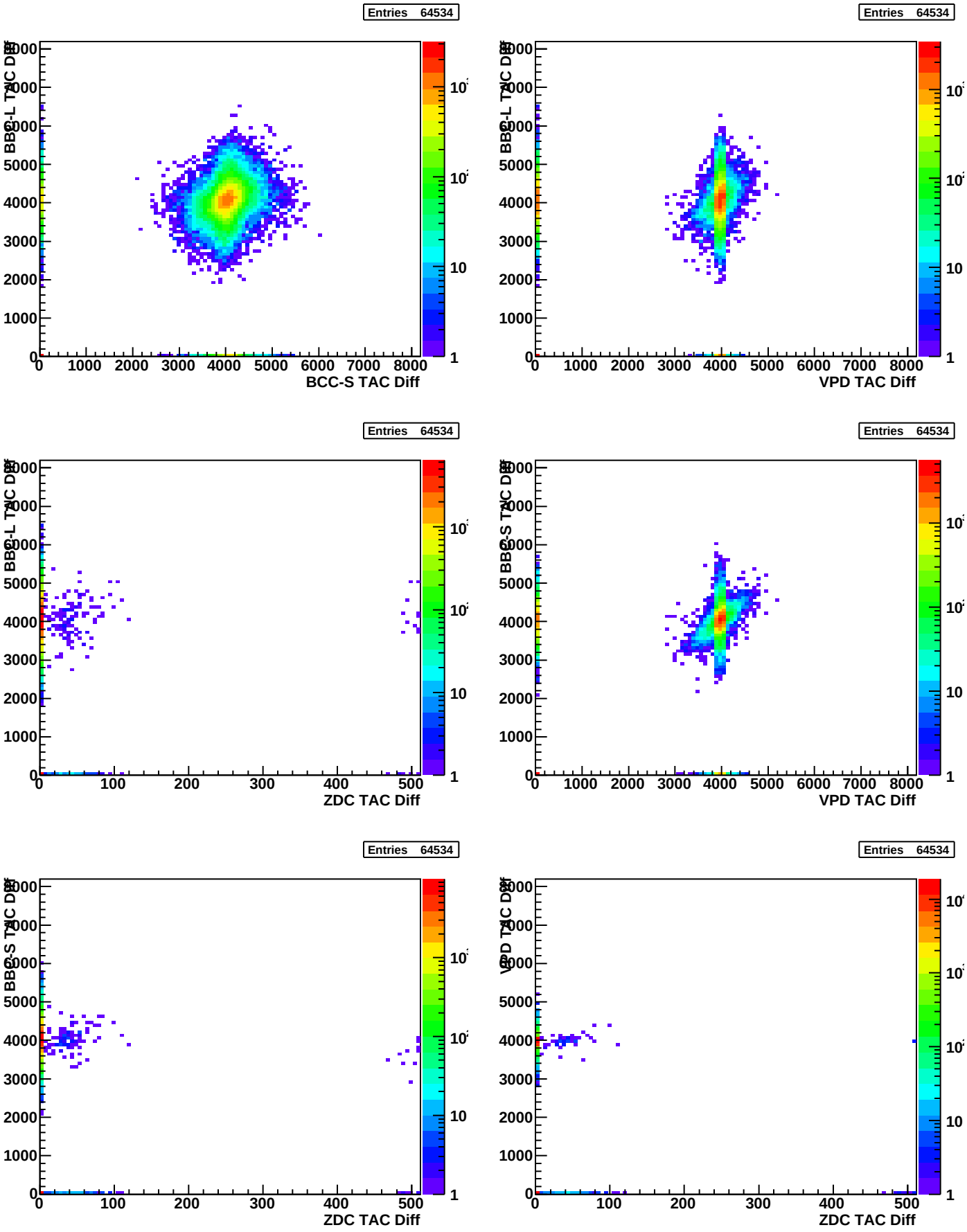

Entries 64534
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

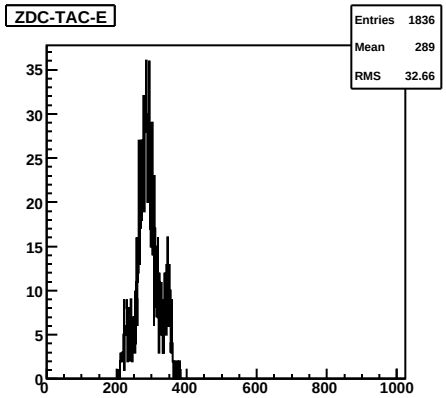

Entries 64534
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

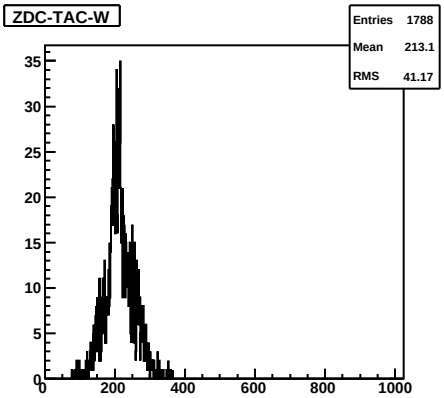

Entries 64534
BBC-L-East ADC Sum
BBC-L-West ADC Sum

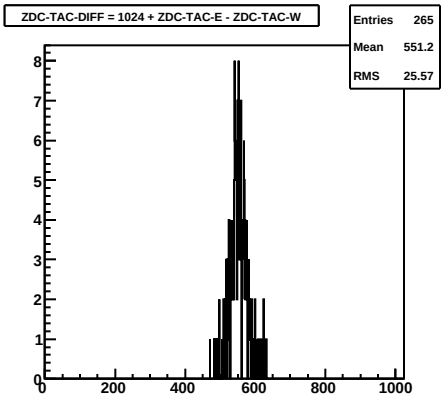

Entries 64534
BBC-S-East ADC Sum
BBC-S-West ADC Sum

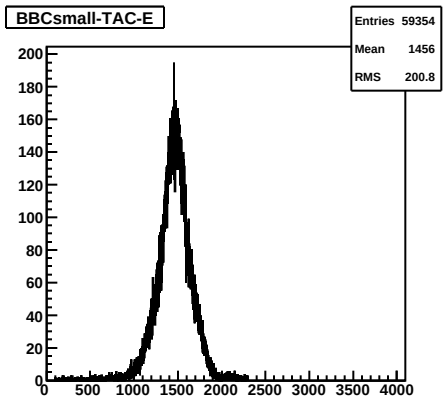

Entries 64534
ZDC East ADC Sum Att
ZDC West ADC Sum Att

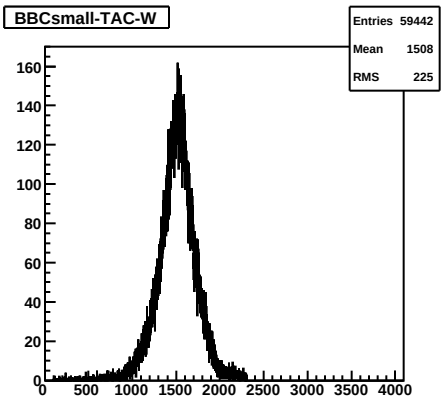

Entries 64534
BBC-L TAC Diff
BCC-S TAC Diff
Entries 64534
BBC-L TAC Diff
VPD TAC Diff
Entries 64534
BBC-L TAC Diff
ZDC TAC Diff
Entries 64534
BBC-S TAC Diff
VPD TAC Diff
Entries 64534
BBC-S TAC Diff
ZDC TAC Diff
Entries 64534
VPD TAC Diff
ZDC TAC Diff

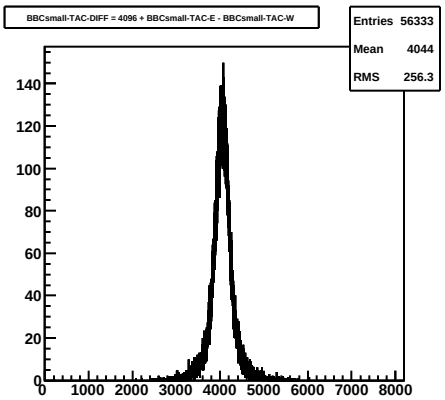

ZDC-TAC-E
Entries 1836
Mean 289
RMS 32.66

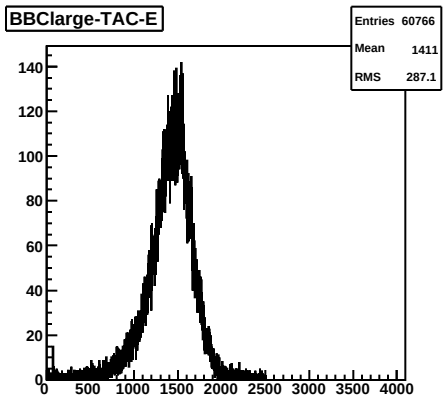

ZDC-TAC-W
Entries 1788
Mean 213.1
RMS 41.17

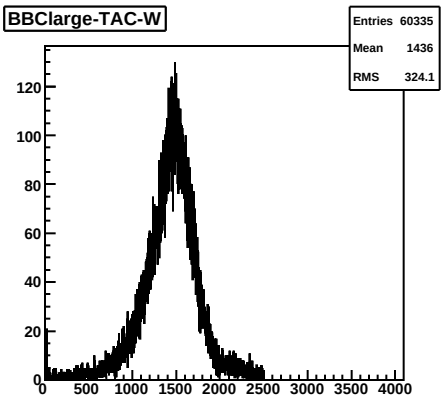

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 265
Mean 551.2
RMS 25.57

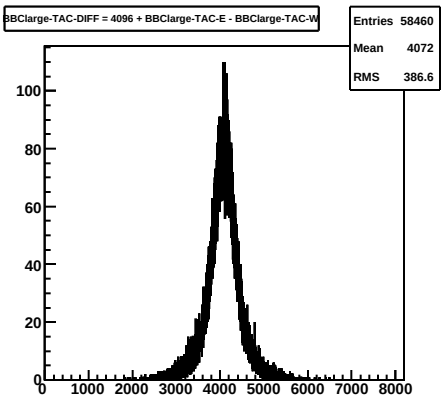

BBCsmall-TAC-E
Entries 59354
Mean 1456
RMS 200.8

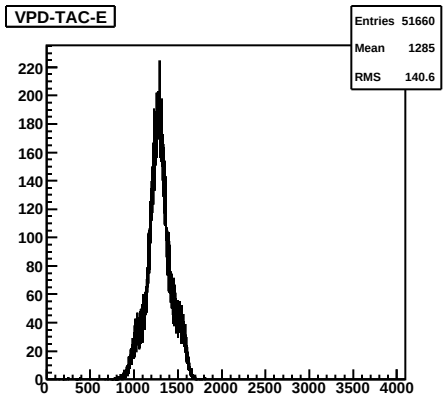

BBCsmall-TAC-W
Entries 59442
Mean 1508
RMS 225

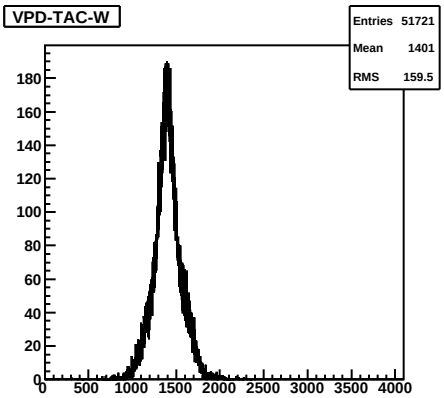

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 56333
Mean 4044
RMS 256.3

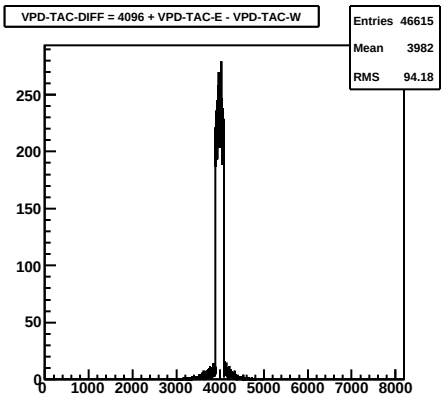

BBClarge-TAC-E
Entries 60766
Mean 1411
RMS 287.1

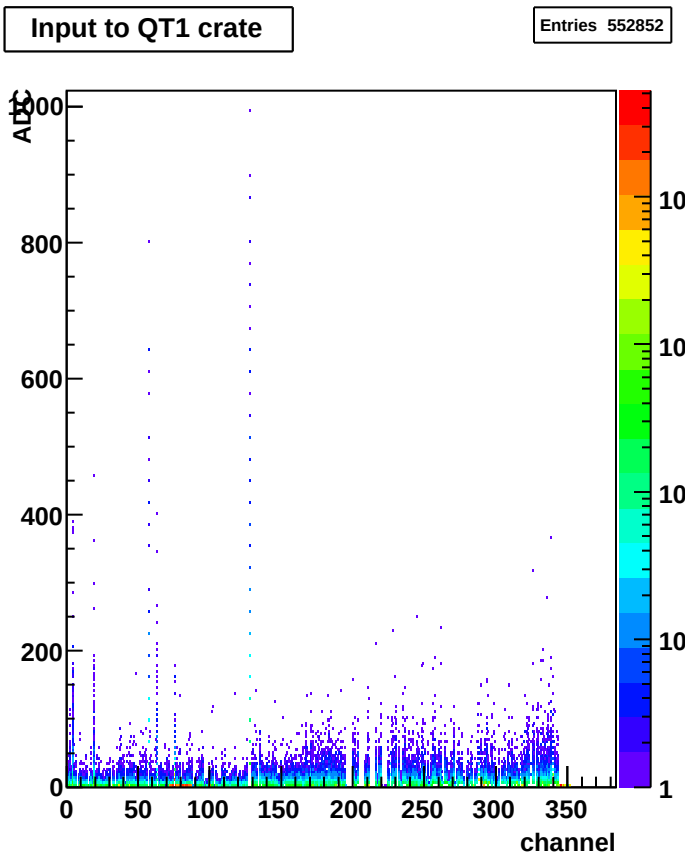

BBClarge-TAC-W
Entries 60335
Mean 1436
RMS 324.1

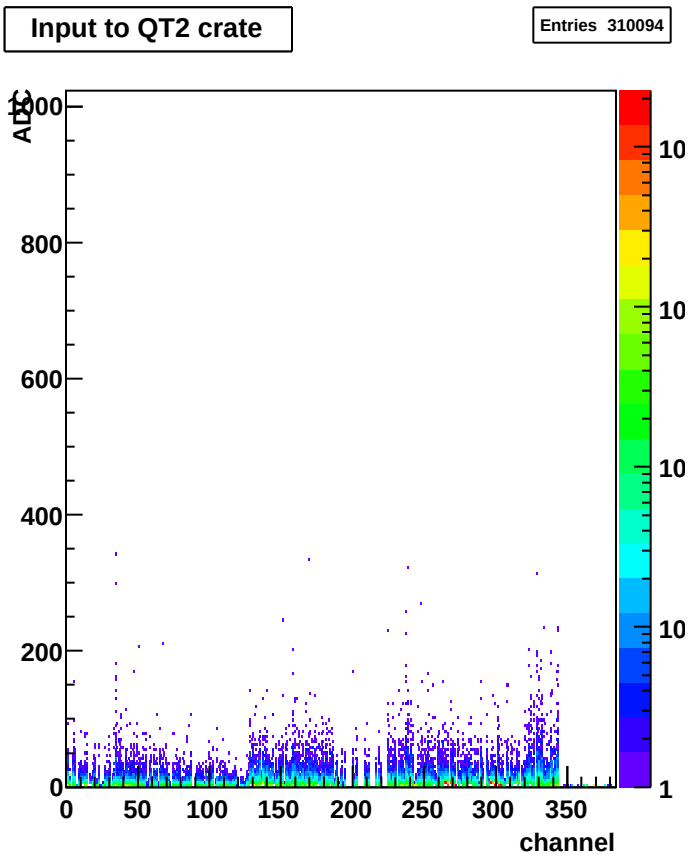

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 58460
Mean 4072
RMS 386.6

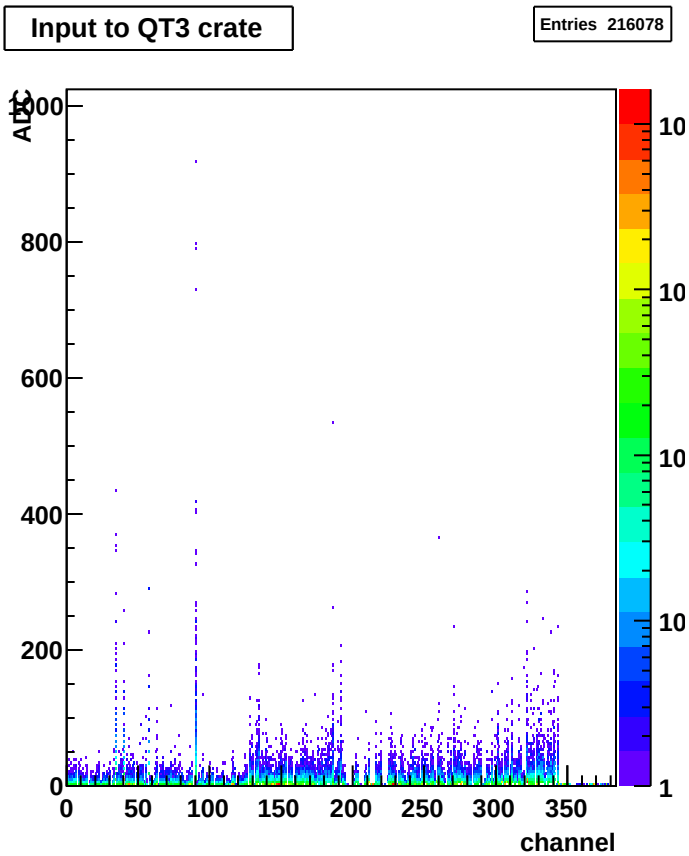

VPD-TAC-E
Entries 51660
Mean 1285
RMS 140.6

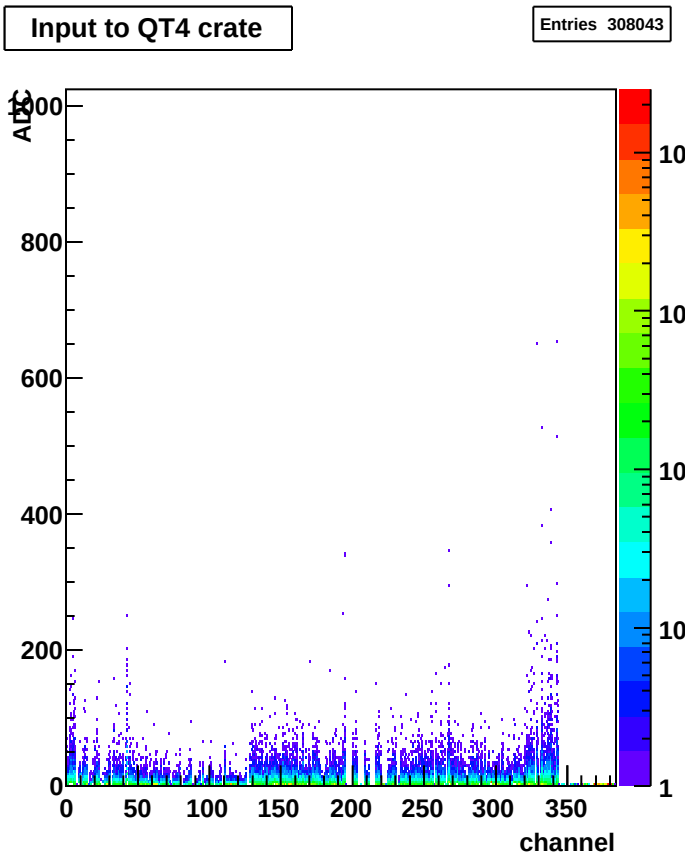

VPD-TAC-W
Entries 51721
Mean 1401
RMS 159.5

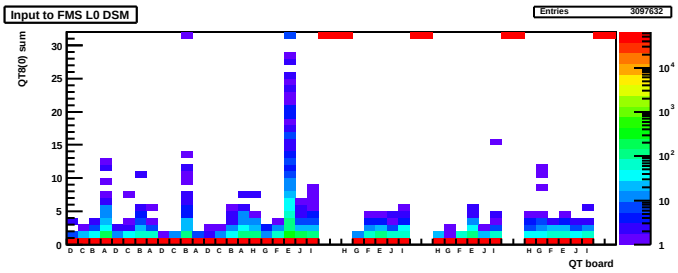

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 46615
Mean 3982
RMS 94.18

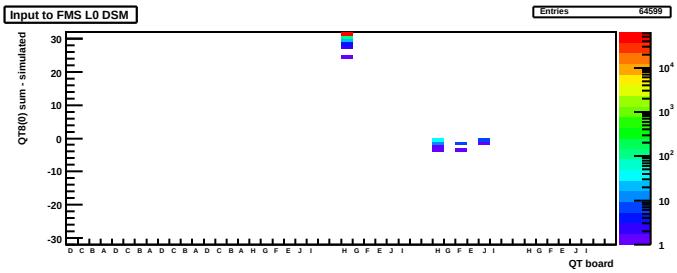

Input to QT1 crate
Entries 552852
ADC
channel

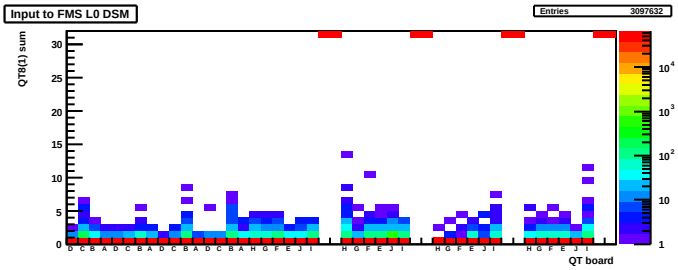

Input to QT2 crate
Entries 310094
ADC
channel

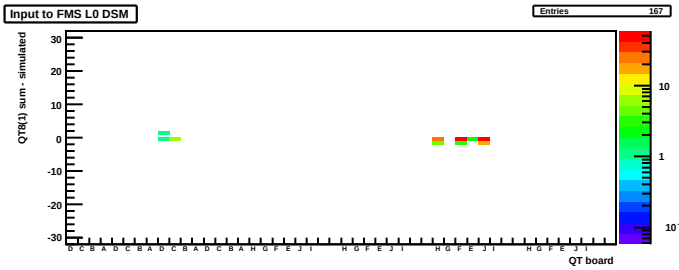

Input to QT3 crate
Entries 216078
ADC
channel

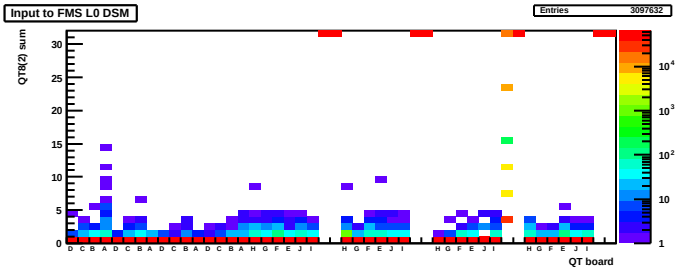

Input to QT4 crate
Entries 308043
ADC
channel

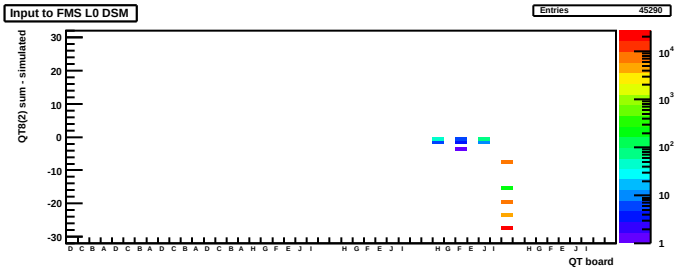

Input to FMS L0 DSM
Entries 3097632
QT8(0) sum
QT board

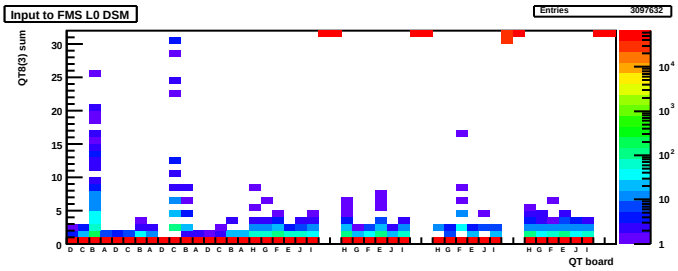

Input to FMS L0 DSM
Entries 64599
QT8(0) sum - simulated
QT board

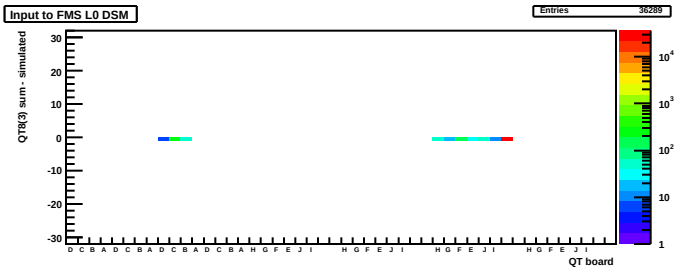

Input to FMS L0 DSM
Entries 3097632
QT8(1) sum
QT board

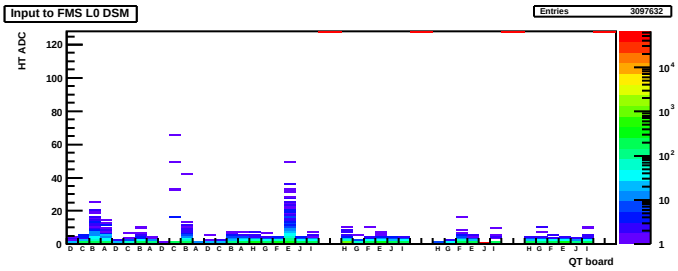

Input to FMS L0 DSM
Entries 167
QT8(1) sum - simulated
QT board

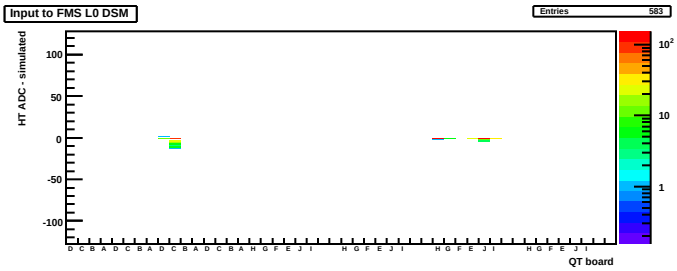

Input to FMS L0 DSM
Entries 3097632
QT8(2) sum
QT board

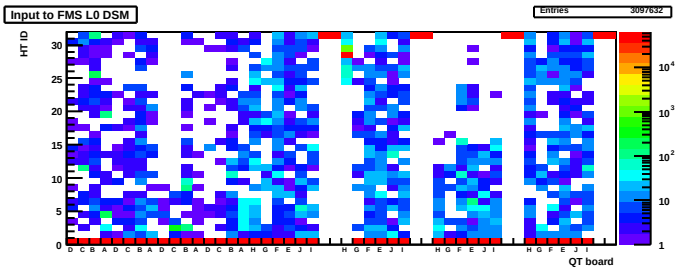

Input to FMS L0 DSM
Entries 45290
QT8(2) sum - simulated
QT board

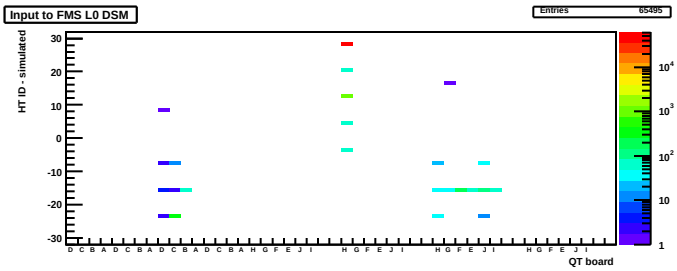

Input to FMS L0 DSM
Entries 3097632
QT8(3) sum
QT board

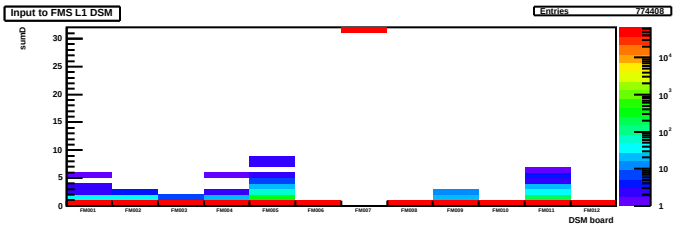

Input to FMS L0 DSM
Entries 36289
QT8(3) sum - simulated
QT board

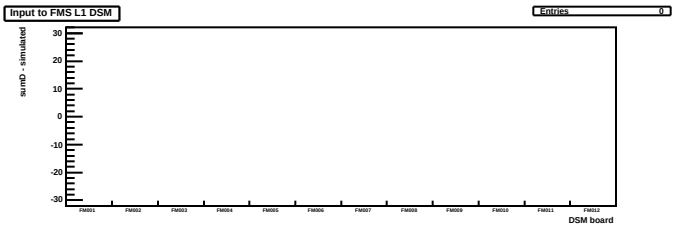

Input to FMS L0 DSM
Entries 3097632
HT ADC
QT board

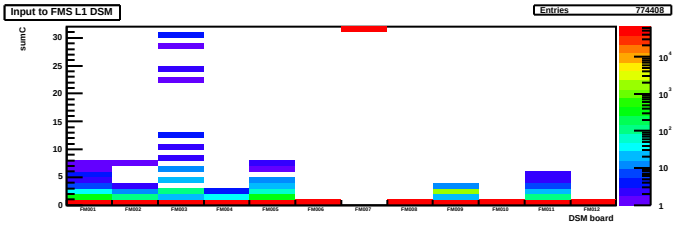

Input to FMS L0 DSM
Entries 583
HT ADC - simulated
QT board

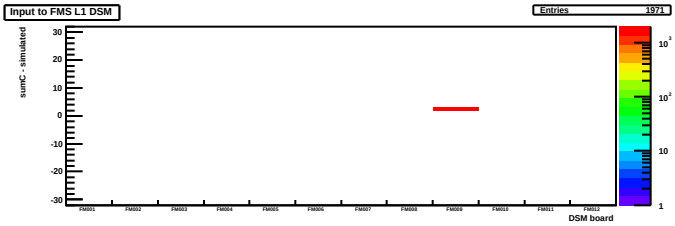

Input to FMS L0 DSM
Entries 3097632
HT ID
QT board

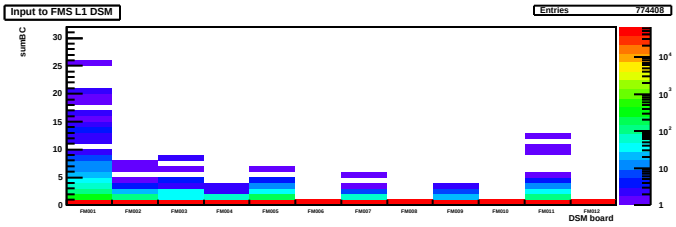

Input to FMS L0 DSM
Entries 65495
HT ID - simulated
QT board

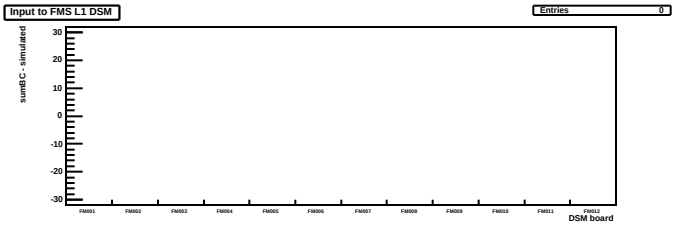

Input to FMS L1 DSM
Entries 774408
sumD
DSM board

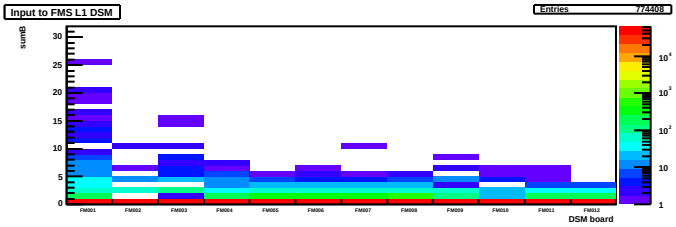

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

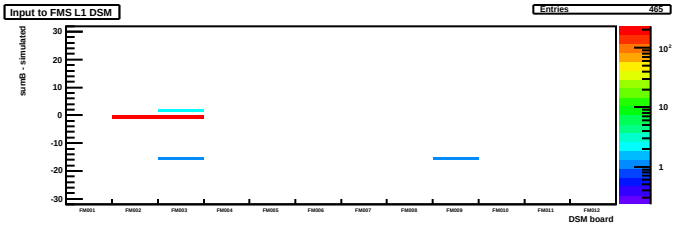

Input to FMS L1 DSM
Entries 774408
sumC
DSM board

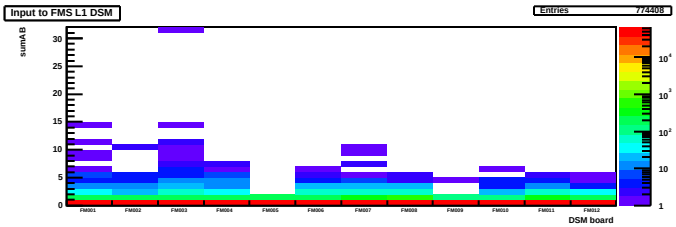

Input to FMS L1 DSM
Entries 1971
sumC - simulated
DSM board

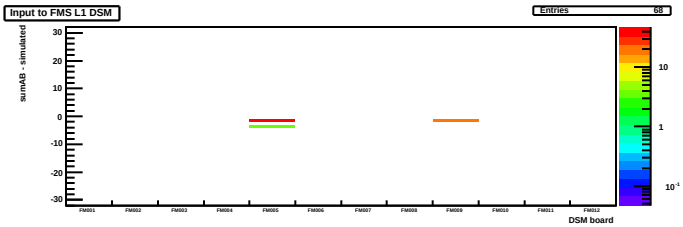

Input to FMS L1 DSM
Entries 774408
sumBC
DSM board

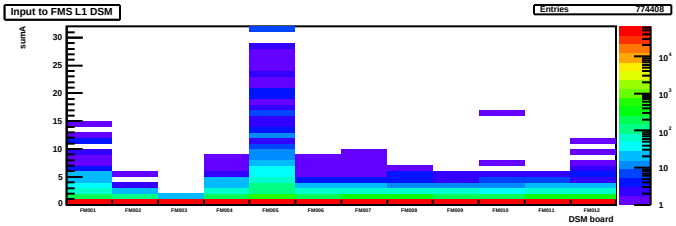

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

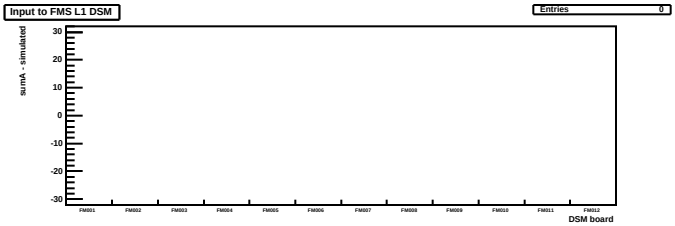

Input to FMS L1 DSM
Entries 774408
sumB
DSM board

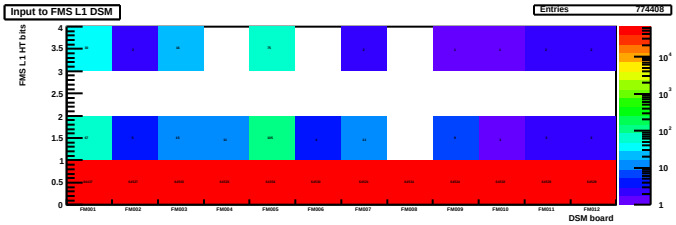

Input to FMS L1 DSM
Entries 465
sumB - simulated
DSM board

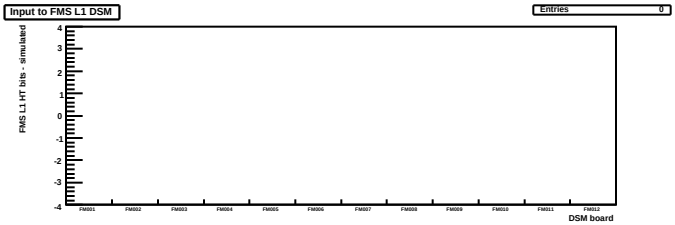

Input to FMS L1 DSM
Entries 774408
sumAB
DSM board

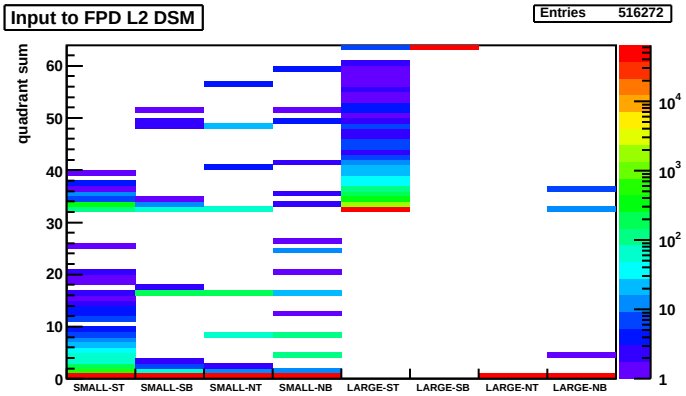

Input to FMS L1 DSM
Entries 68
sumAB - simulated
DSM board

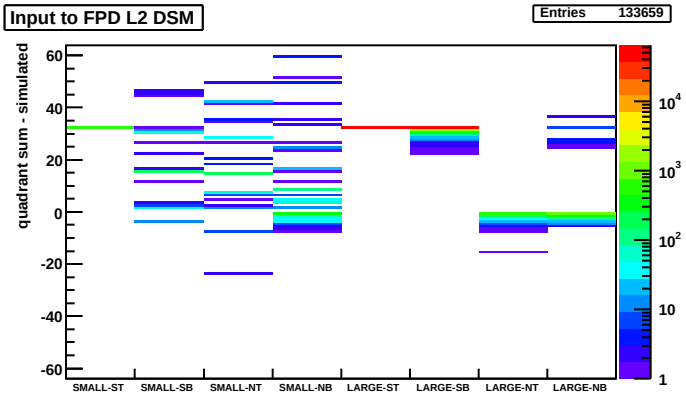

Input to FMS L1 DSM
Entries 774408
sumA
DSM board

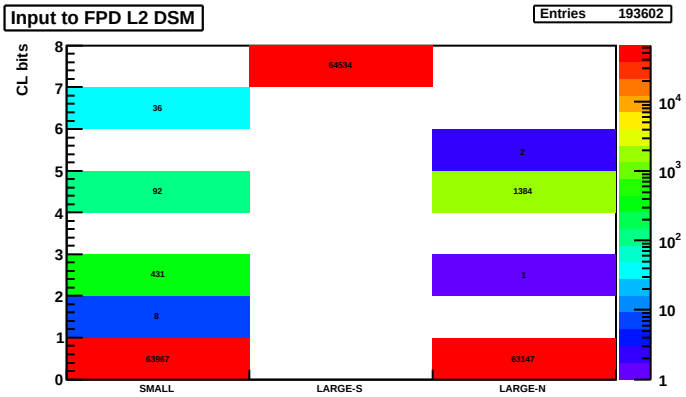

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

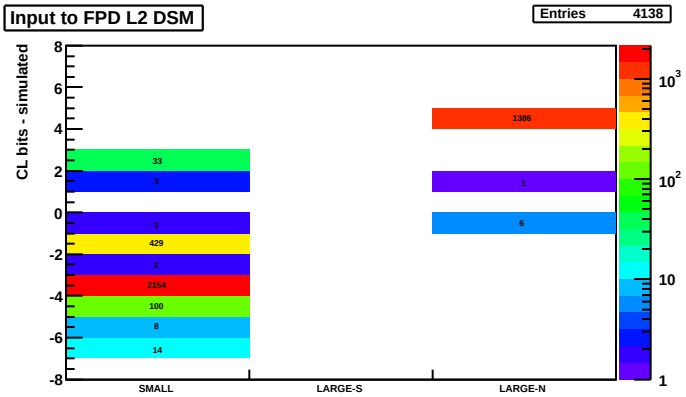

Input to FMS L1 DSM
Entries 774408
FMS L1 HT bits
DSM board

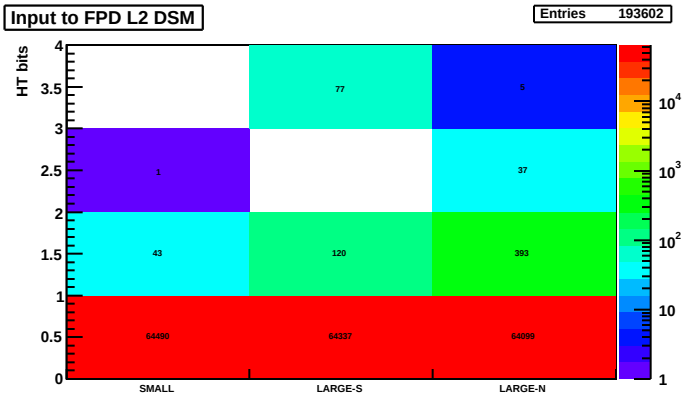

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

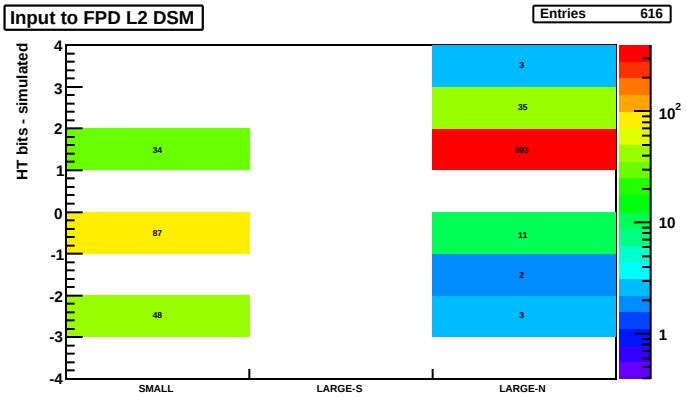

Input to FPD L2 DSM
Entries 516272
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

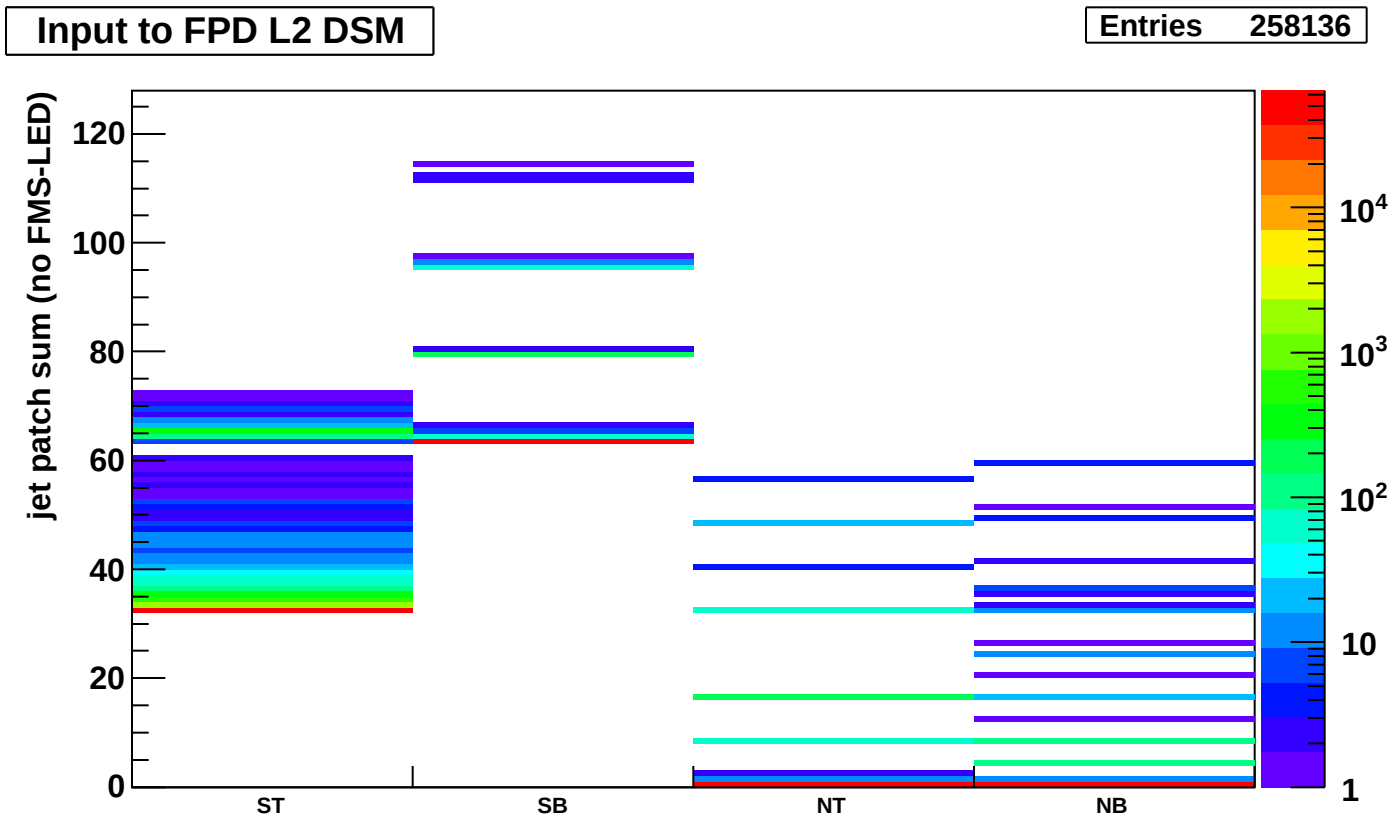

Input to FPD L2 DSM
Entries 133659
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

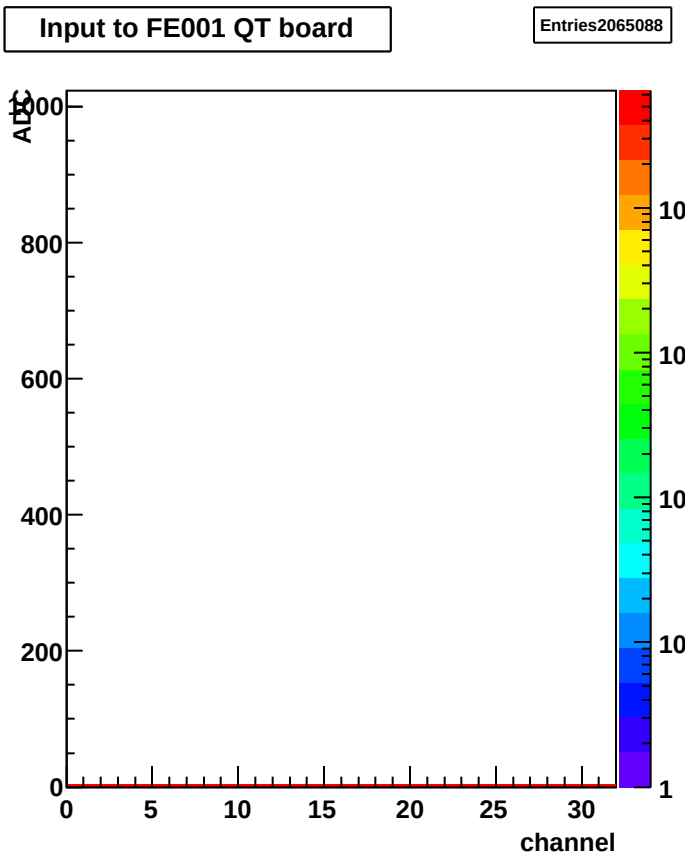

Input to FPD L2 DSM
Entries 193602
CL bits
SMALL
LARGE-S
LARGE-N

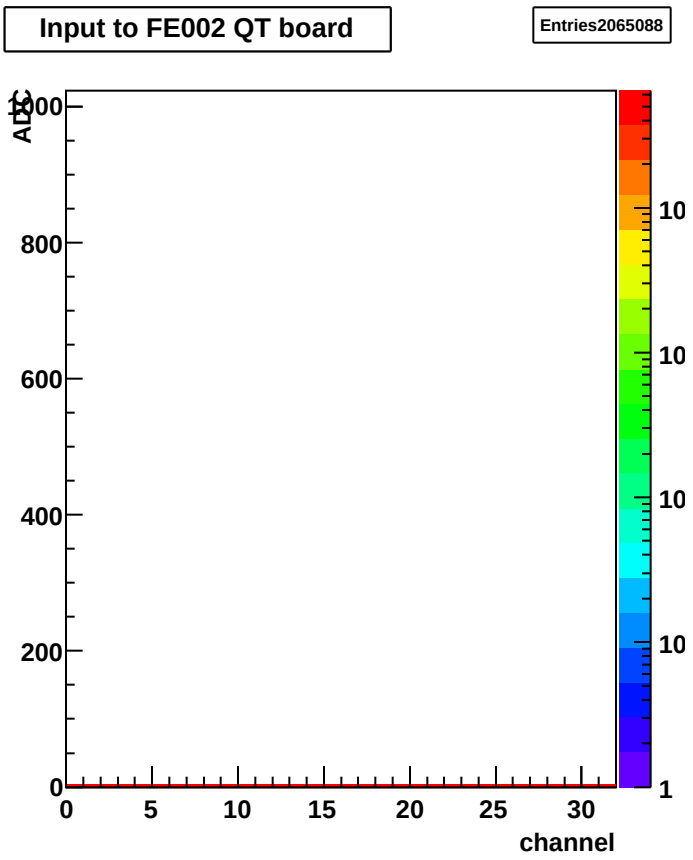

Input to FPD L2 DSM
Entries 4138
CL bits - simulated
SMALL
LARGE-S
LARGE-N

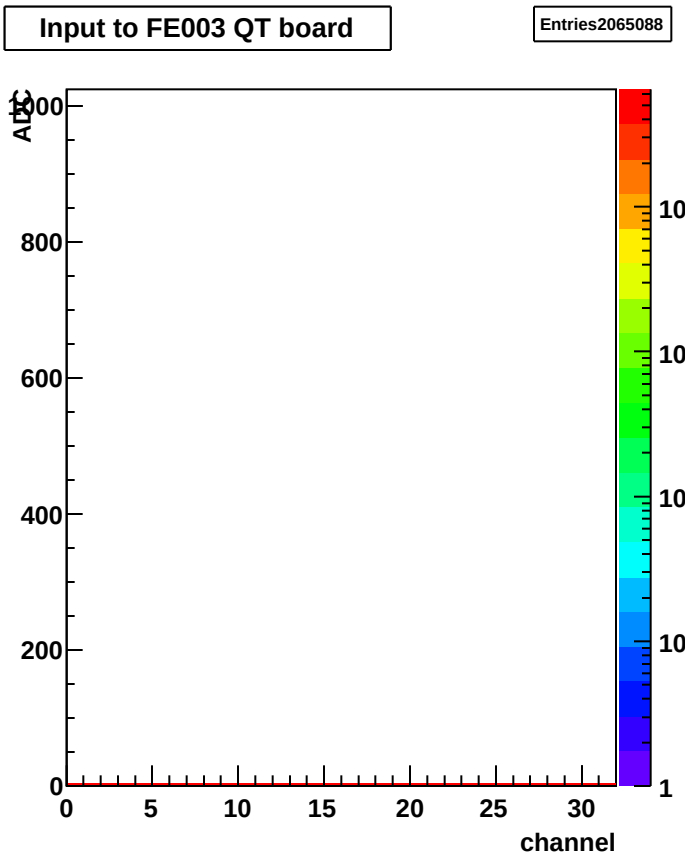

Input to FPD L2 DSM
Entries 193602
HT bits
SMALL
LARGE-S
LARGE-N

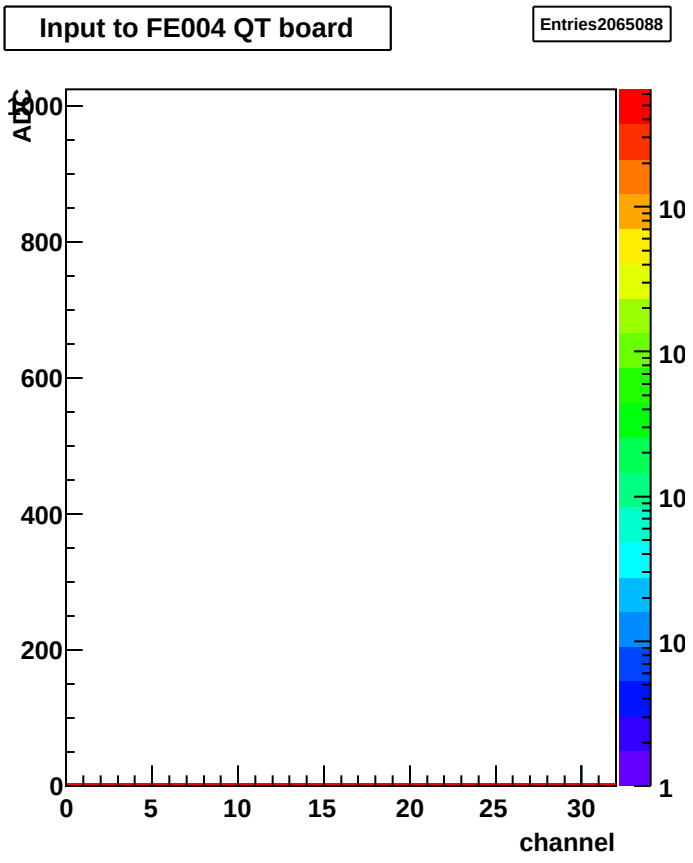

Input to FPD L2 DSM
Entries 616
HT bits - simulated
SMALL
LARGE-S
LARGE-N

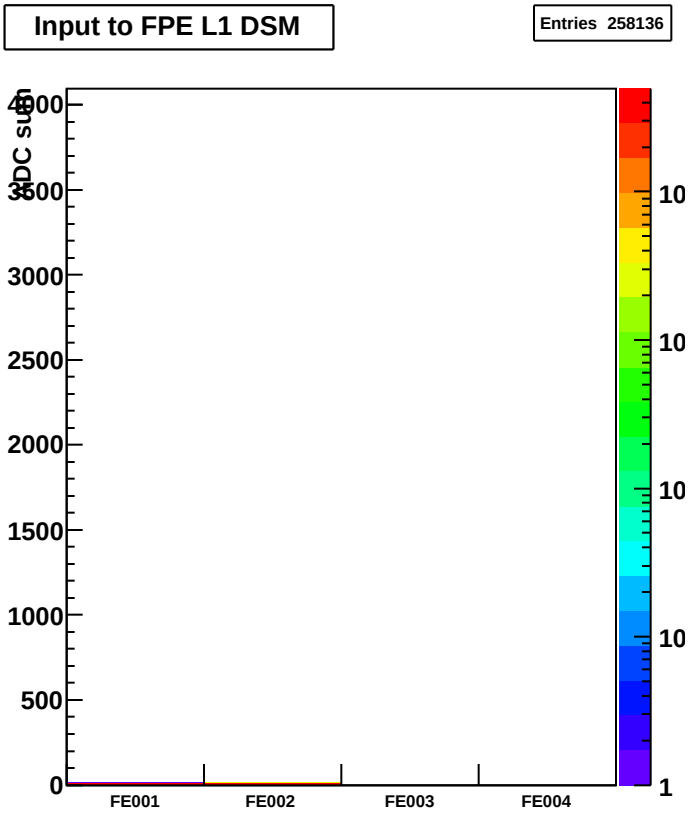

Input to FPD L2 DSM
Entries 258136
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

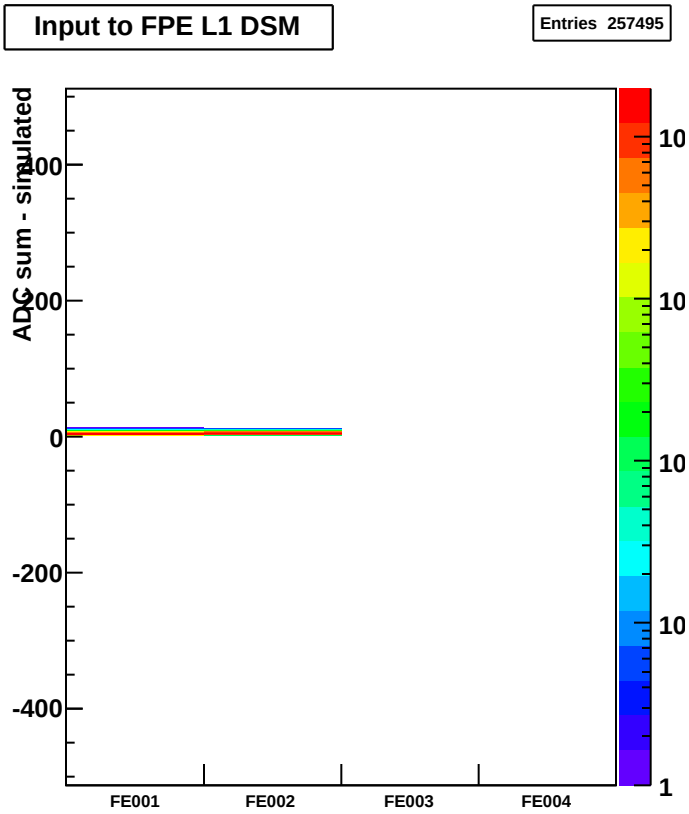

Input to FE001 QT board
Entries2065088
ADC
channel

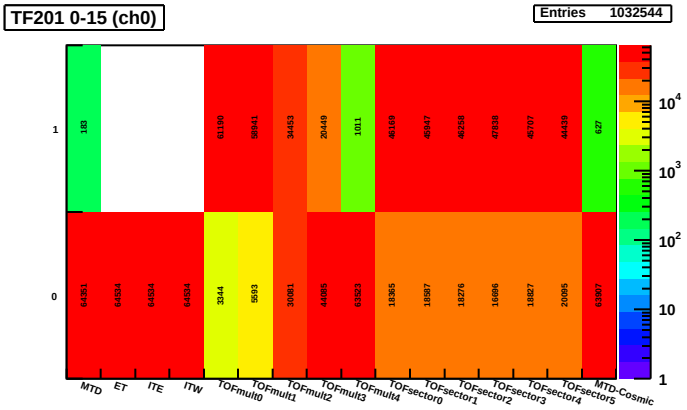

Input to FE002 QT board
Entries2065088
ADC
channel

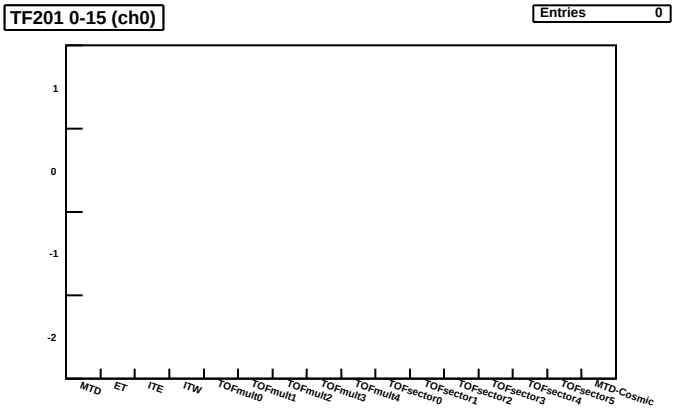

Input to FE003 QT board
Entries2065088
ADC
channel

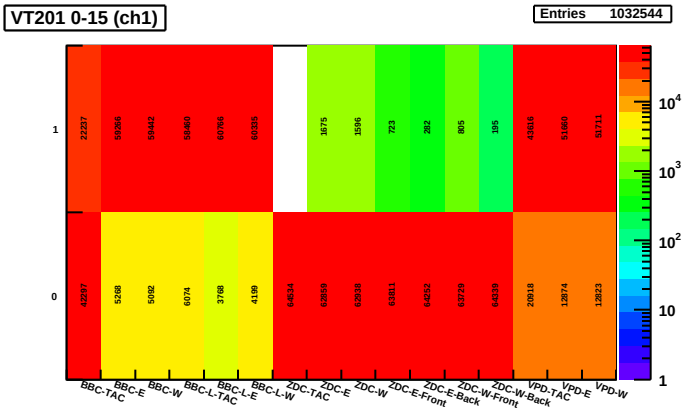

Input to FE004 QT board
Entries2065088
ADC
channel

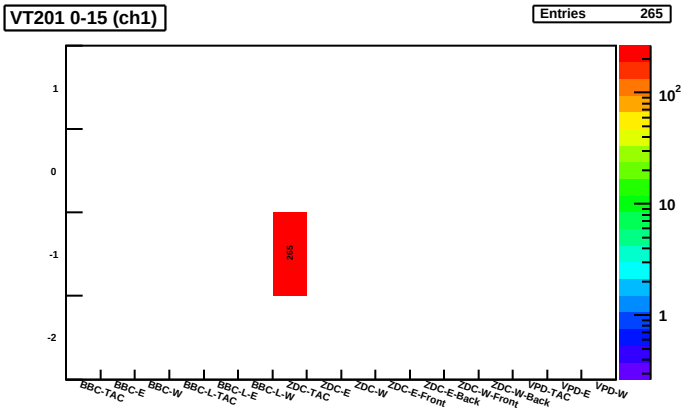

Input to FPE L1 DSM
Entries 258136
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

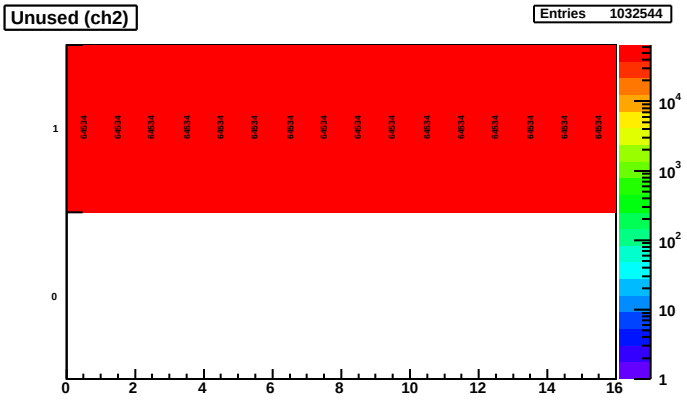

Input to FPE L1 DSM
Entries 257495
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

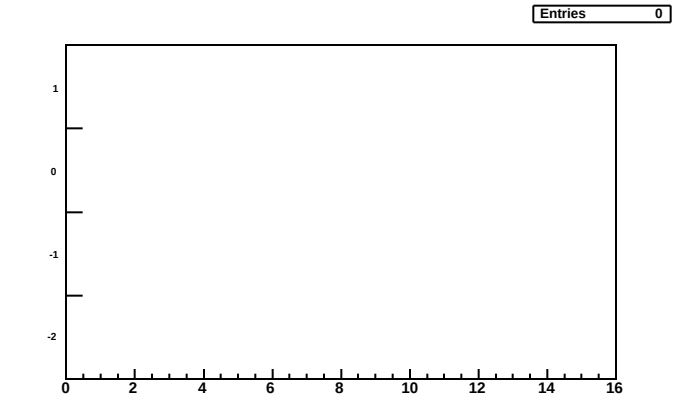

TF201 0-15 (ch0)
Entries 1032544
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

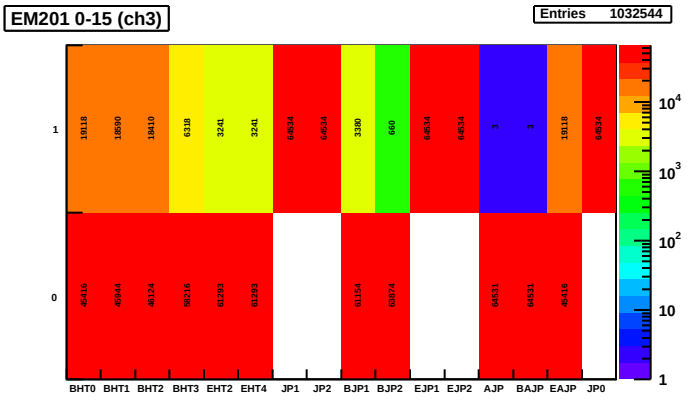

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

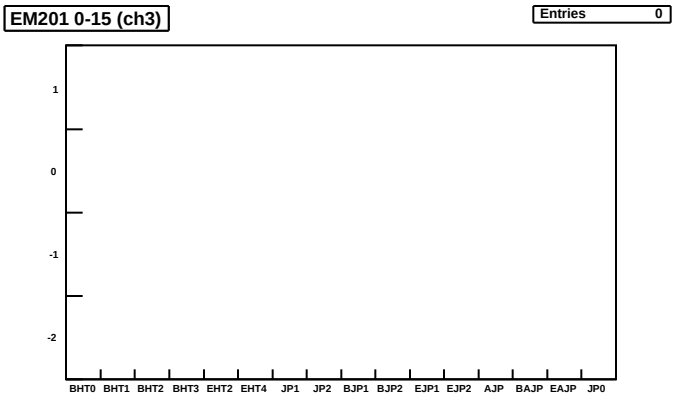

VT201 0-15 (ch1)
Entries 1032544
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 265
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

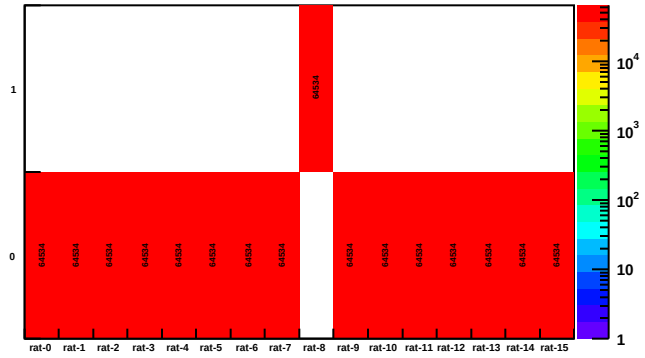

Unused (ch2)
Entries 1032544

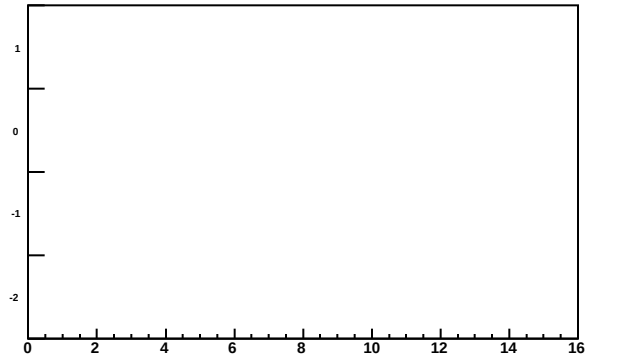

Entries 0


EM201 0-15 (ch3)
Entries 1032544
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

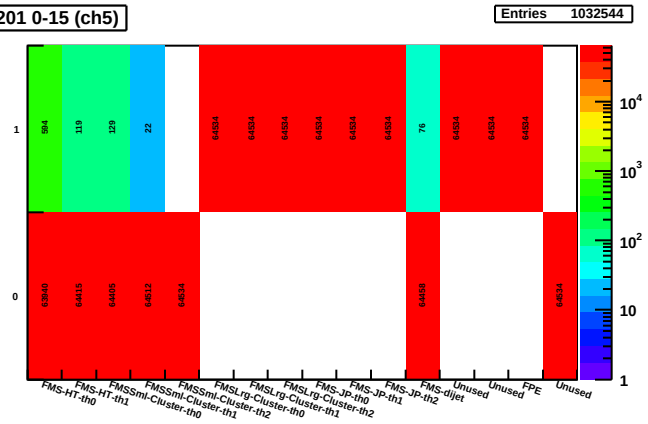

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

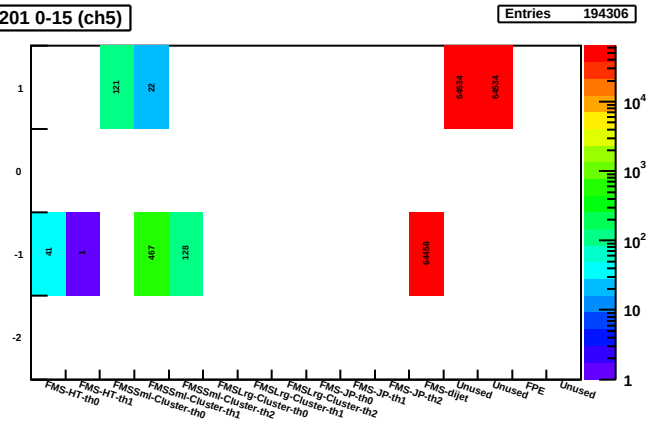




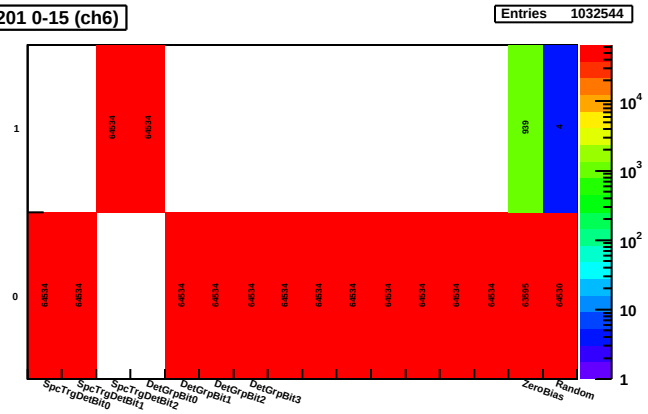
FP201 0-15 (ch5)

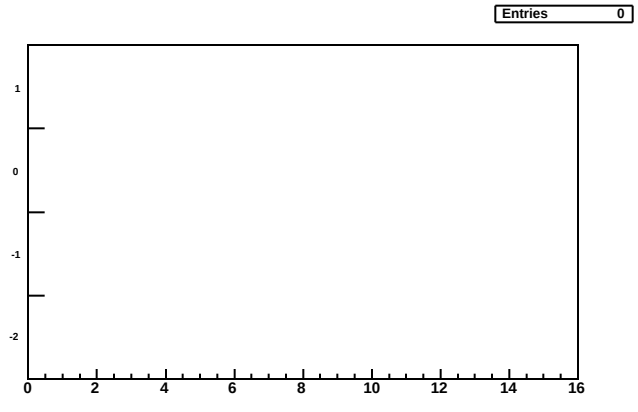
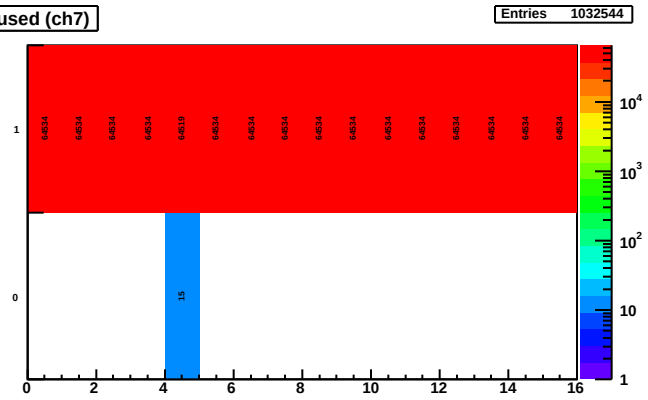
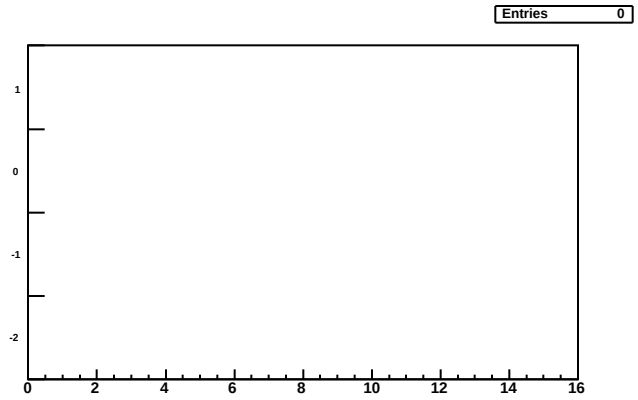


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)