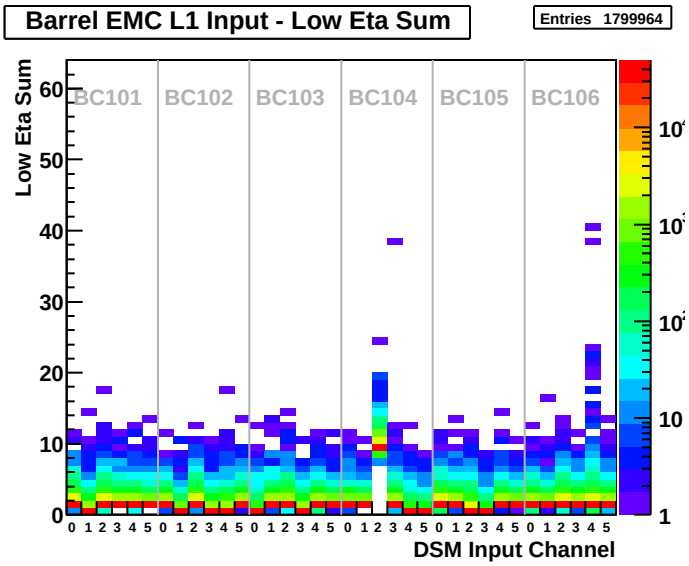
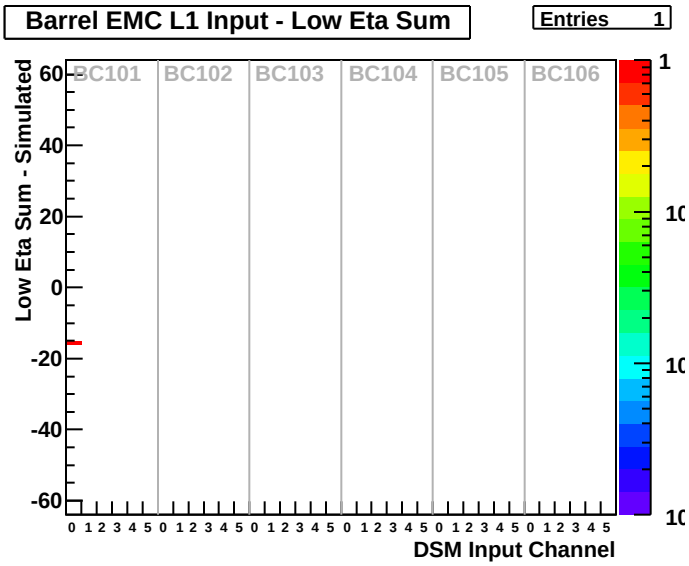
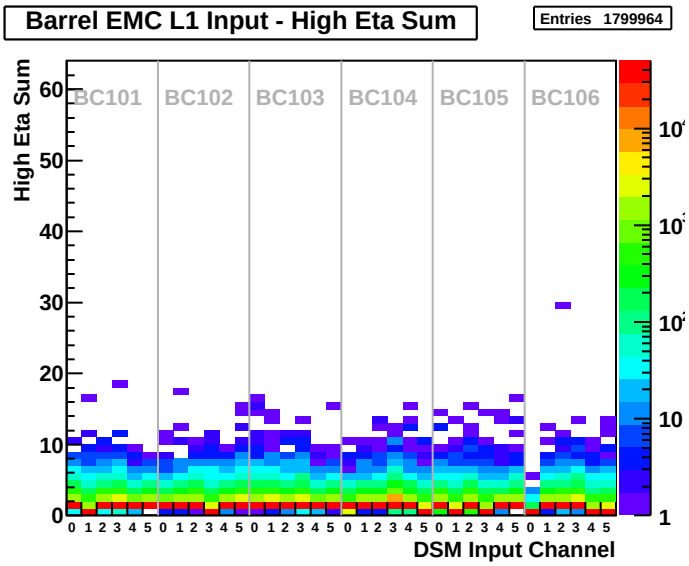
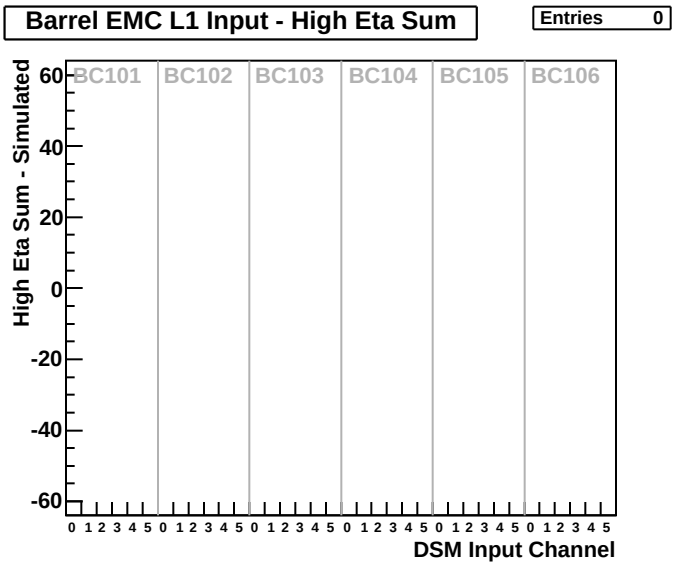
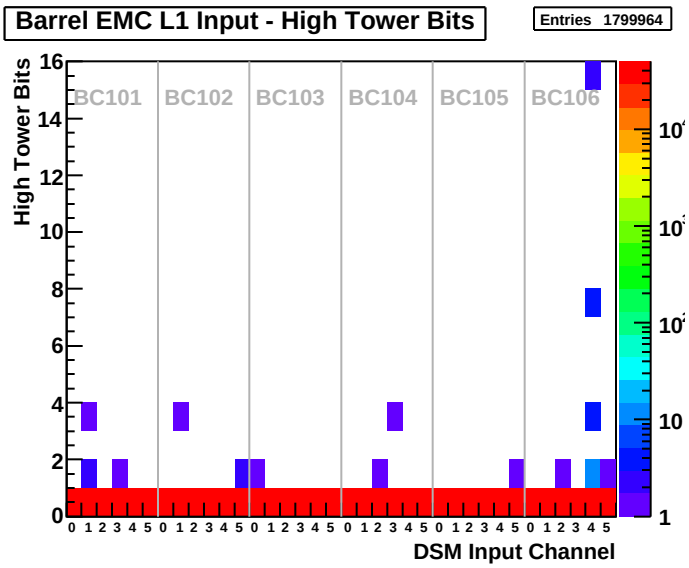
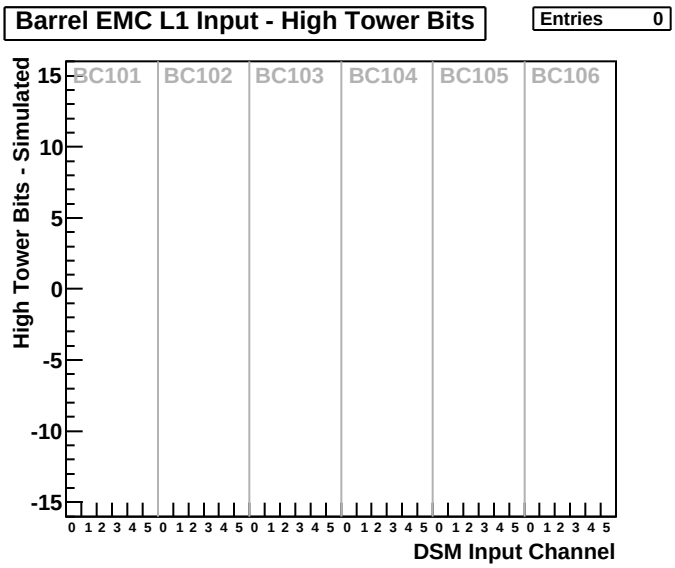

Barrel EMC L1 Input - Low Eta Sum
Entries 1799964
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

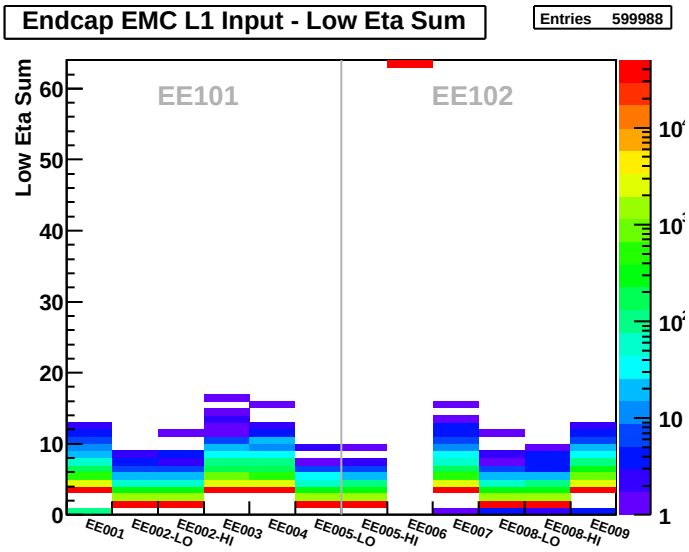

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

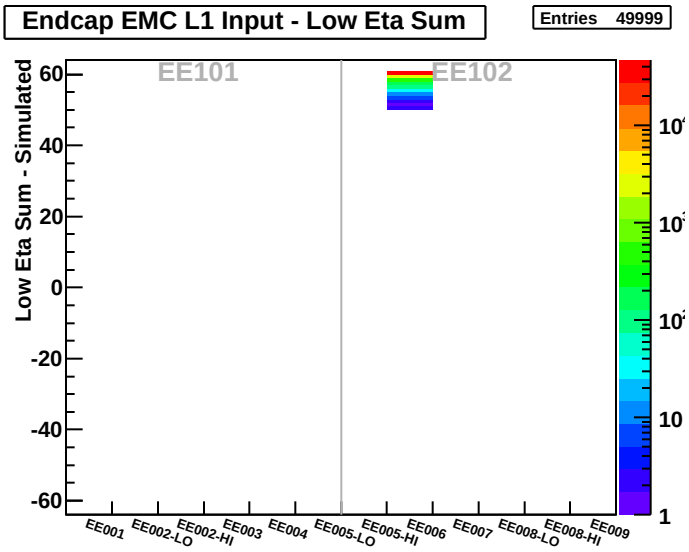

Barrel EMC L1 Input - High Eta Sum
Entries 1799964
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

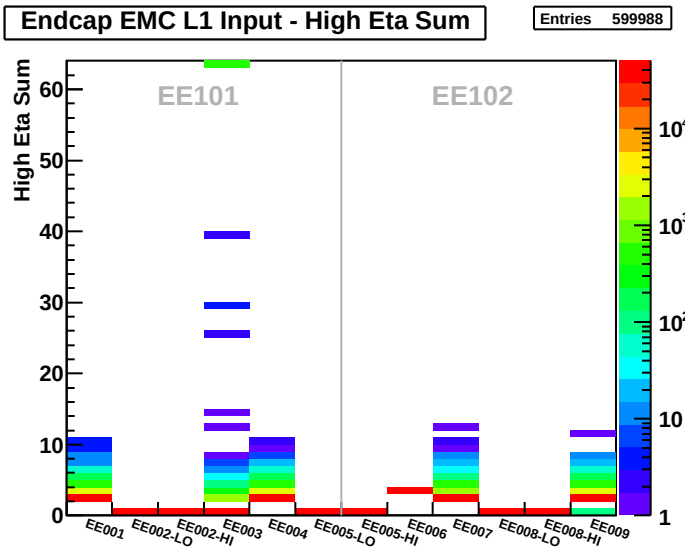

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

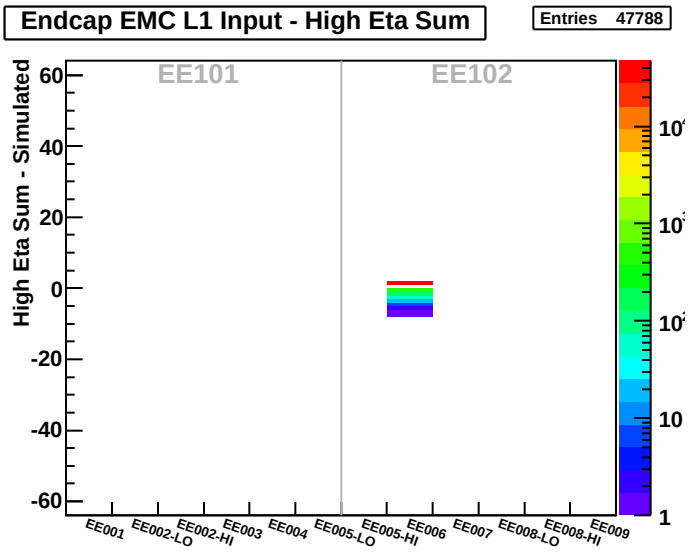

Barrel EMC L1 Input - High Tower Bits
Entries 1799964
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

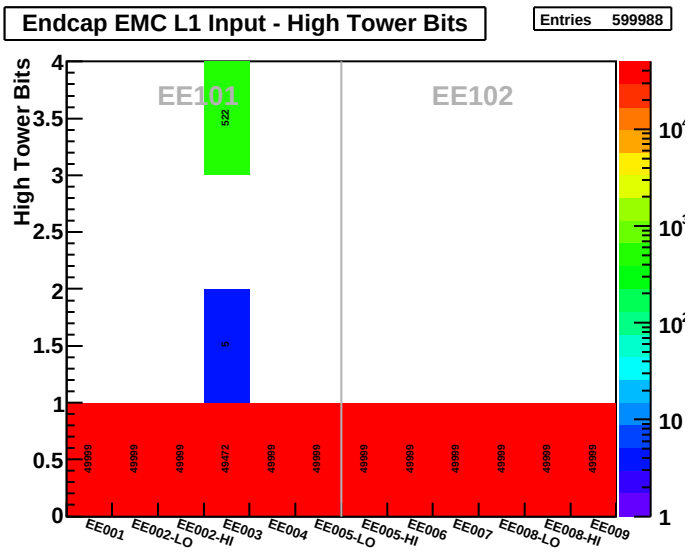

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

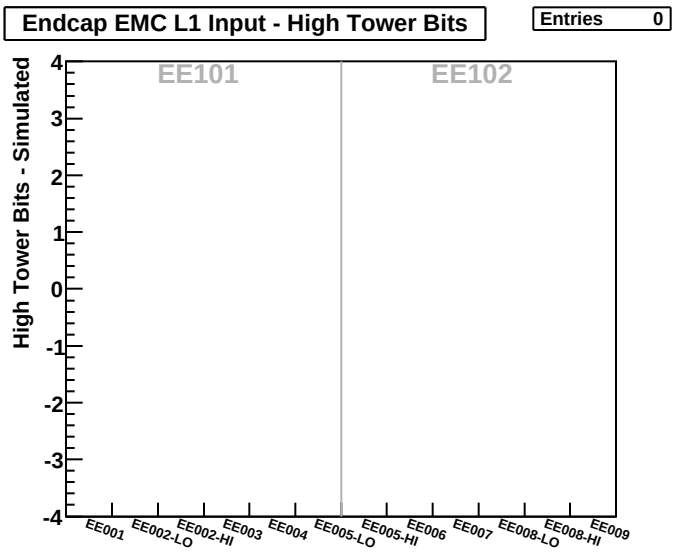

Endcap EMC L1 Input - Low Eta Sum
Entries 599988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

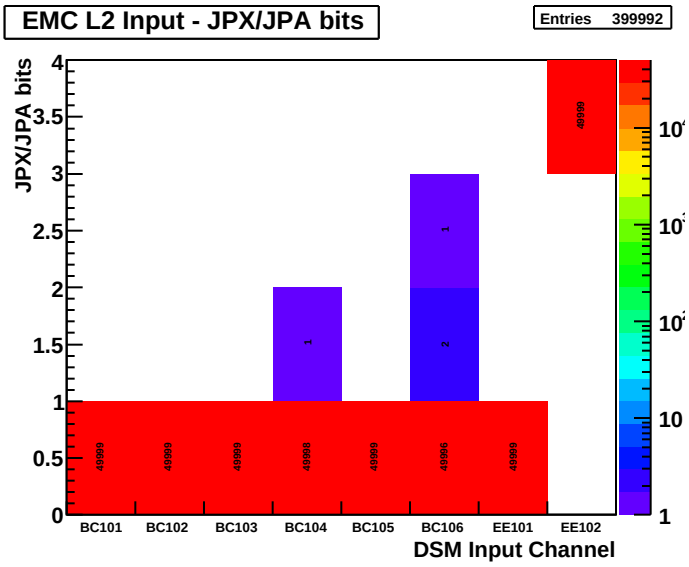

Endcap EMC L1 Input - Low Eta Sum
Entries 49999
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

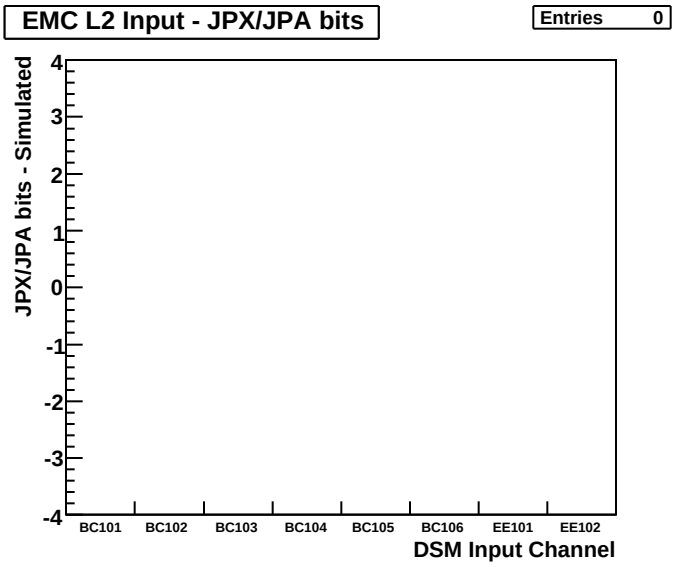

Endcap EMC L1 Input - High Eta Sum
Entries 599988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

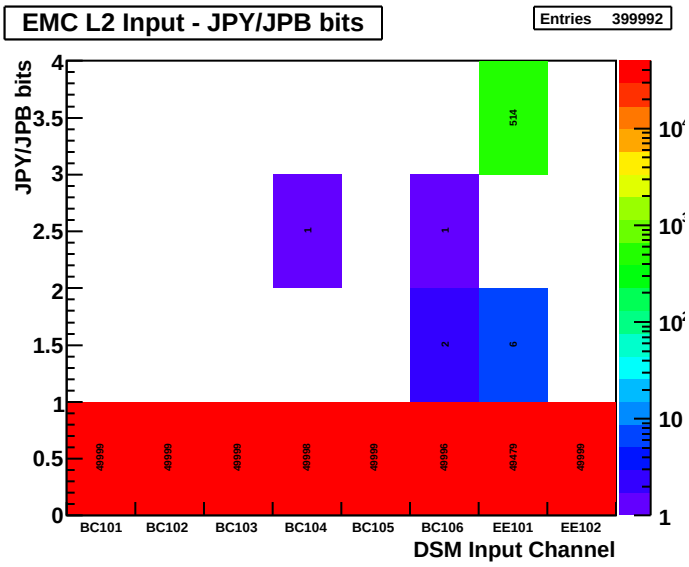

Endcap EMC L1 Input - High Eta Sum
Entries 47788
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

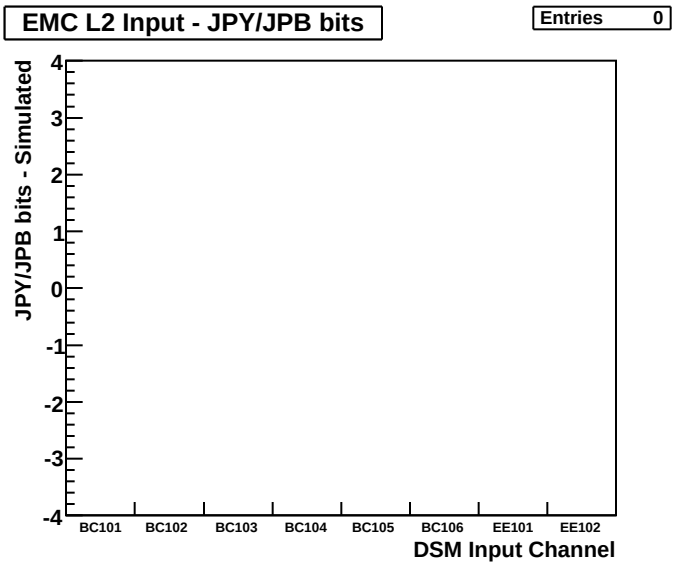

Endcap EMC L1 Input - High Tower Bits
Entries 599988
High Tower Bits
EE101
EE102
522
5
49999 49999 49999 49472 49999 49999 49999 49999 49999 49999 49999 49999
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

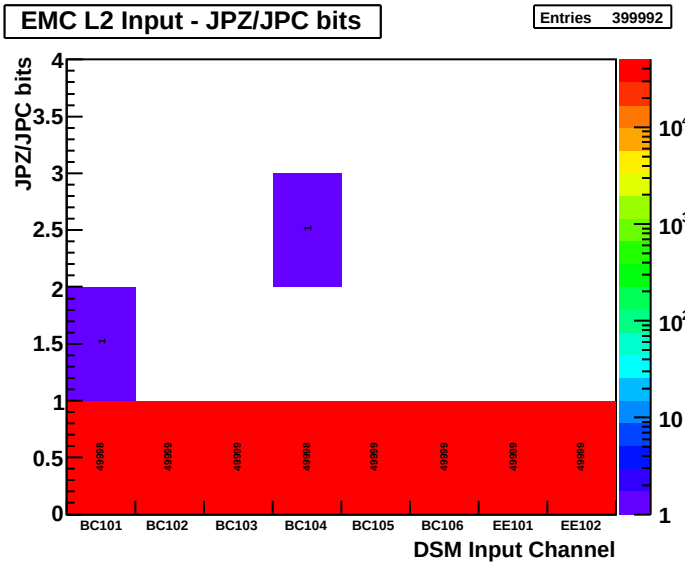

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

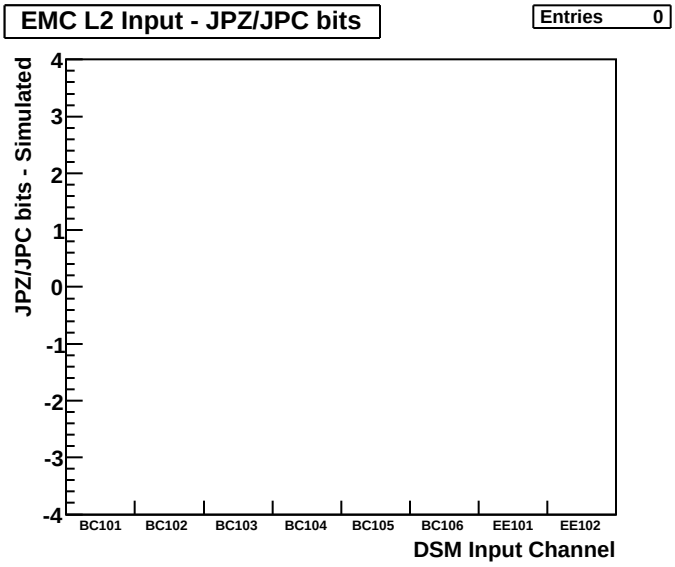

EMC L2 Input - JPX/JPA bits
Entries 399992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

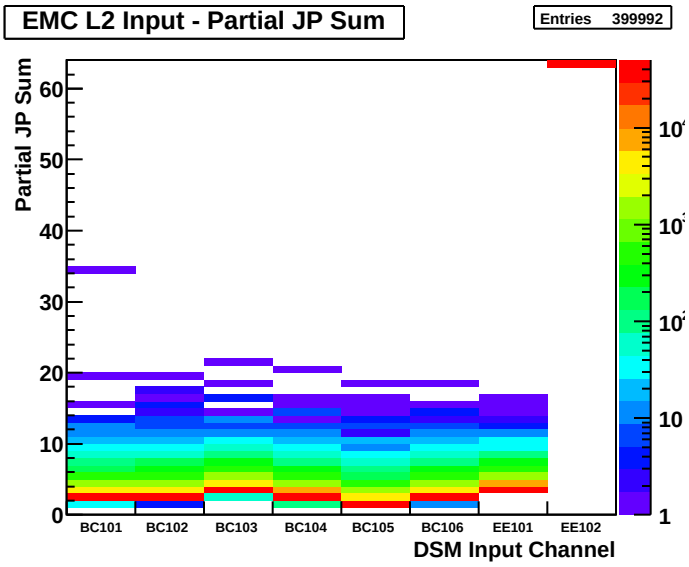

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

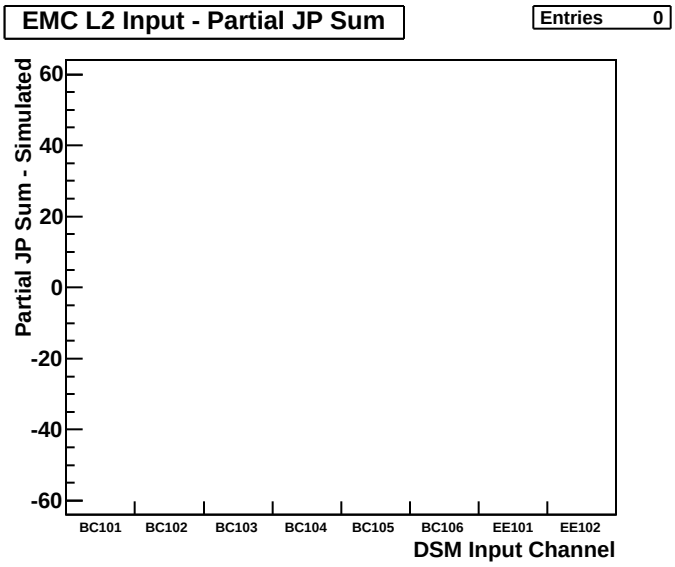

EMC L2 Input - JPY/JPB bits
Entries 399992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

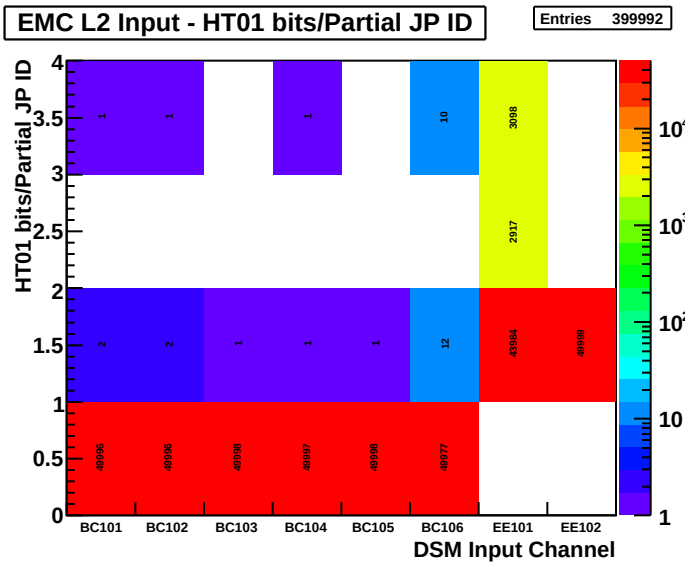

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

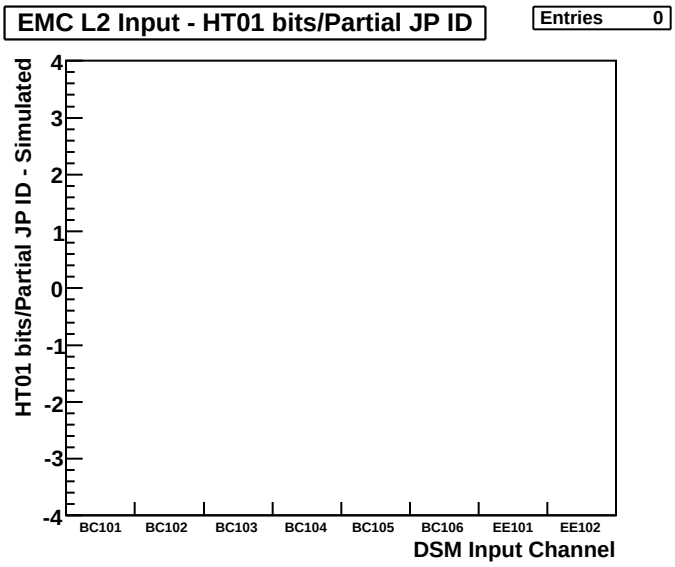

EMC L2 Input - JPZ/JPC bits
Entries 399992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

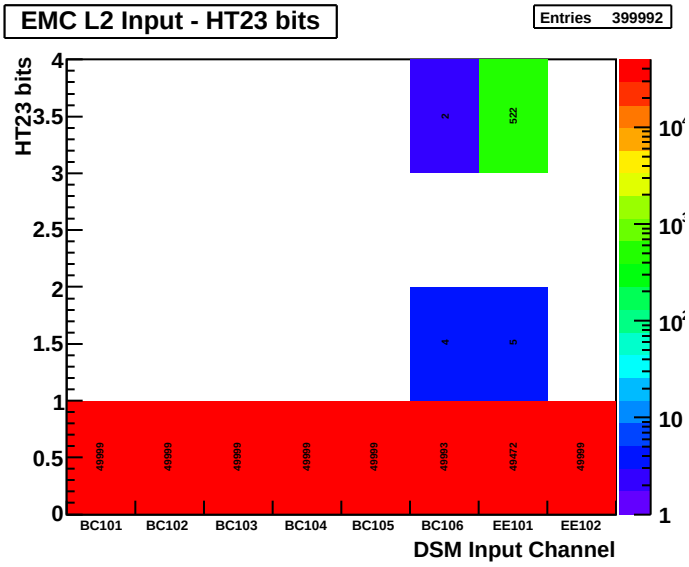

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

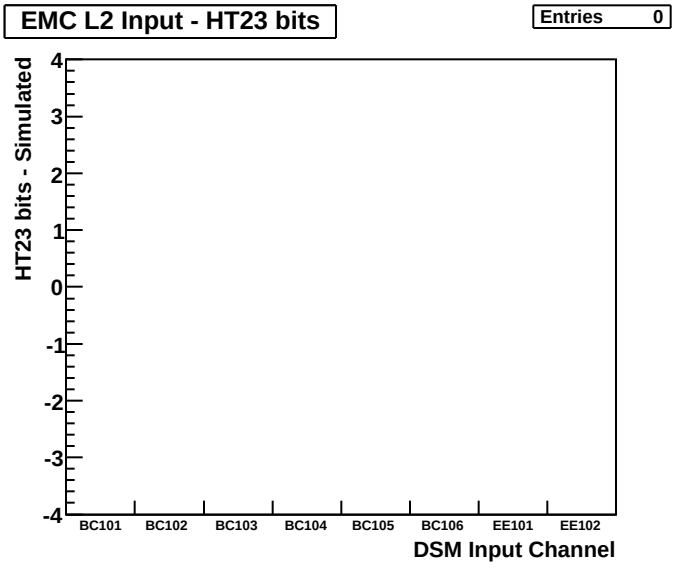

EMC L2 Input - Partial JP Sum
Entries 399992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

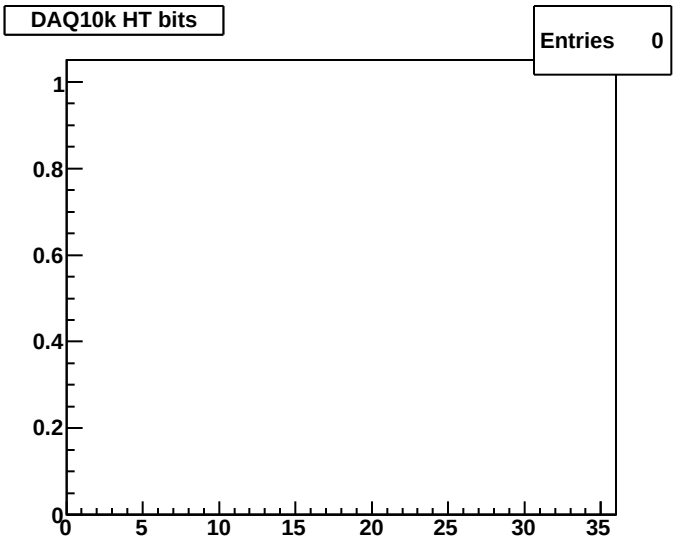

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

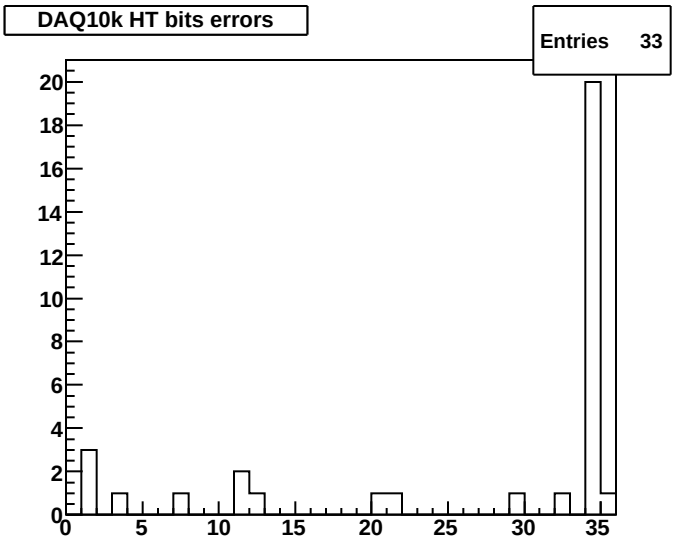

EMC L2 Input - HT01 bits/Partial JP ID
Entries 399992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

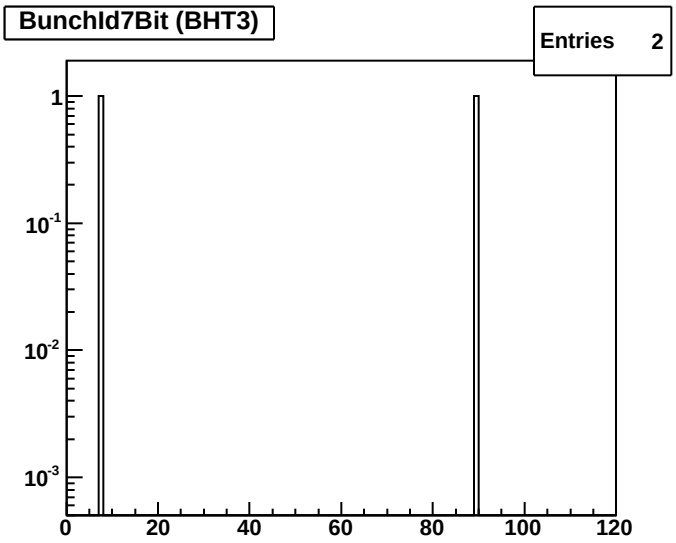

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

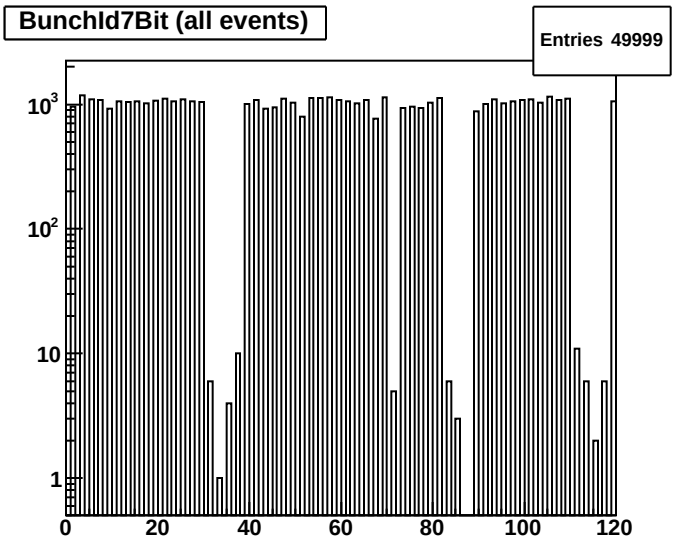

EMC L2 Input - HT23 bits
Entries 399992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

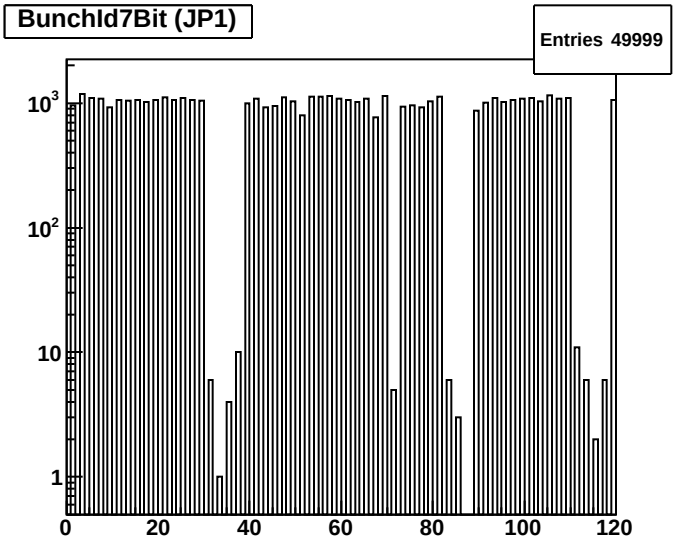

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

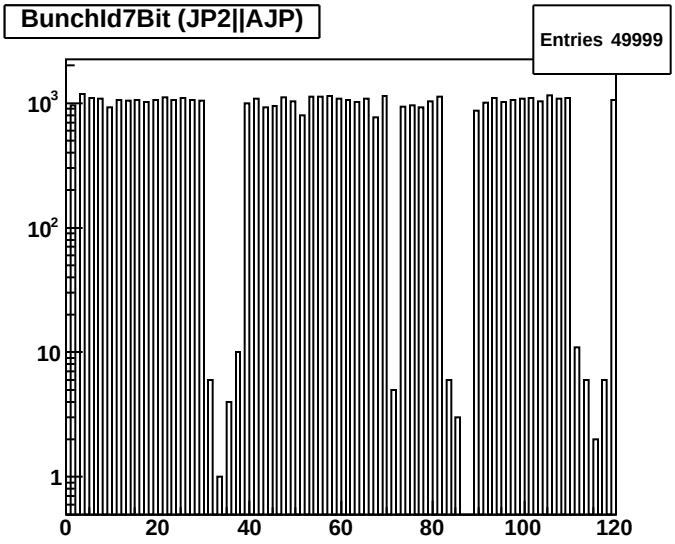

DAQ10k HT bits
Entries 0

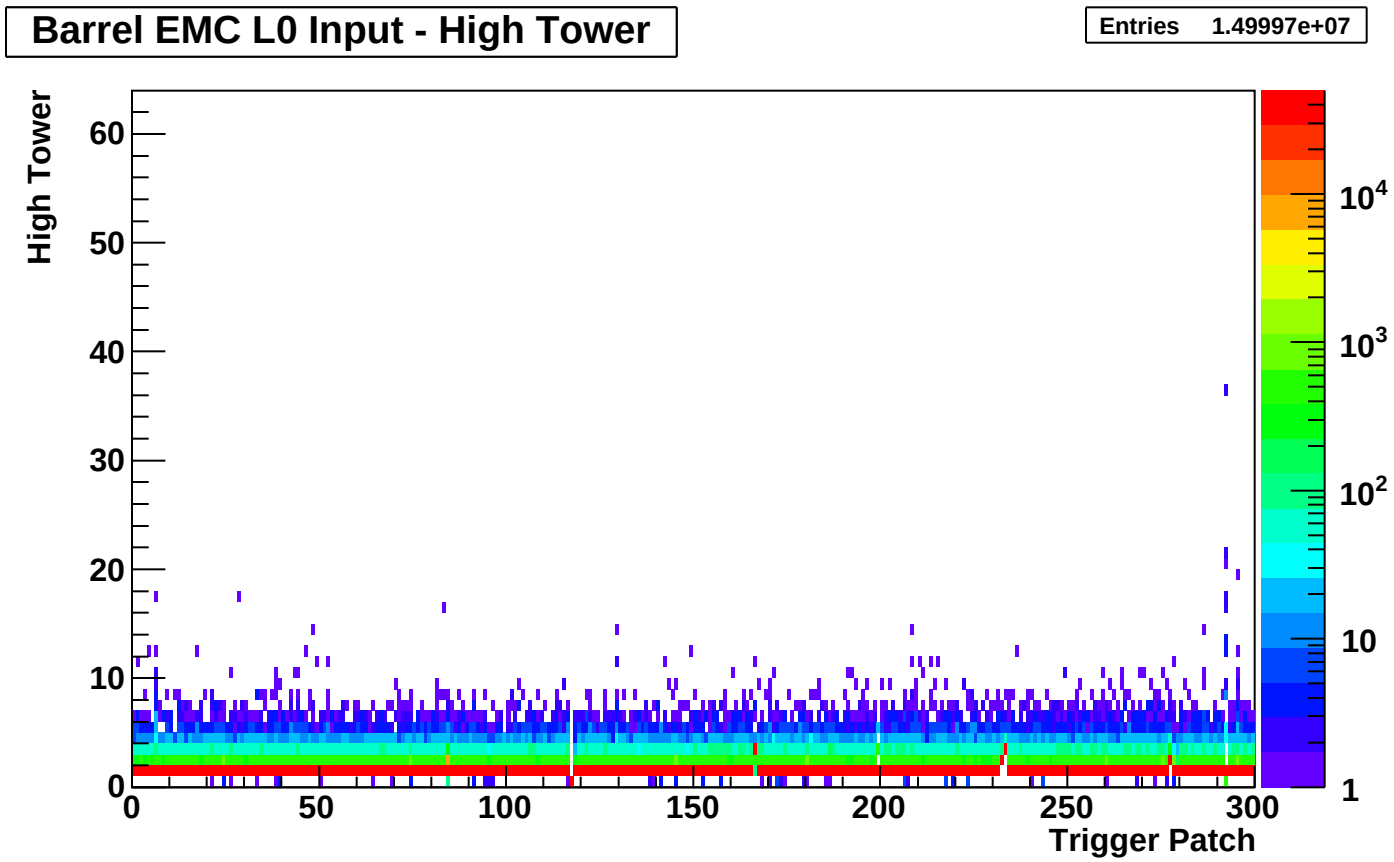

DAQ10k HT bits errors
Entries 33

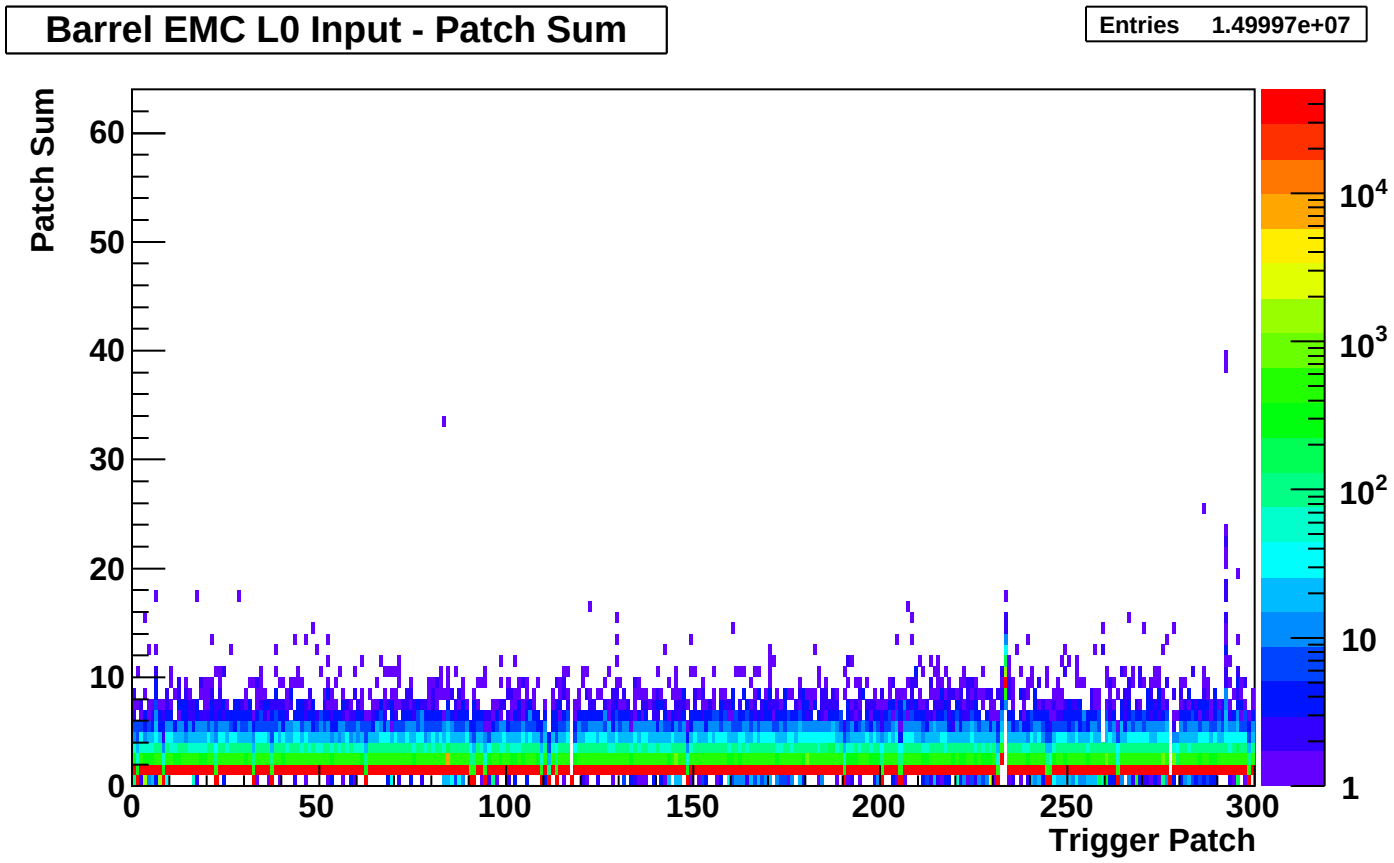

BunchId7Bit (BHT3)
Entries 2

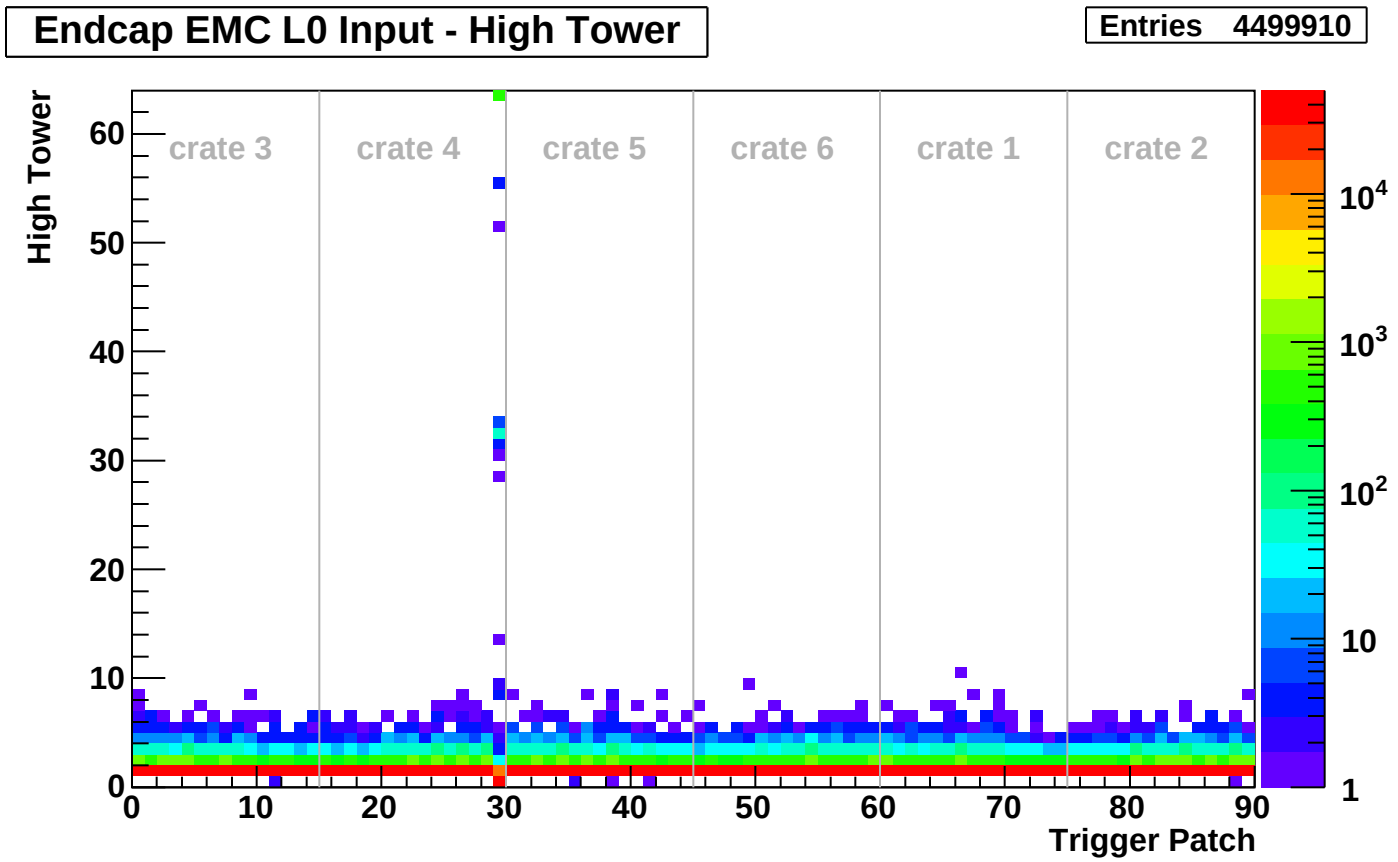

BunchId7Bit (all events)
Entries 49999

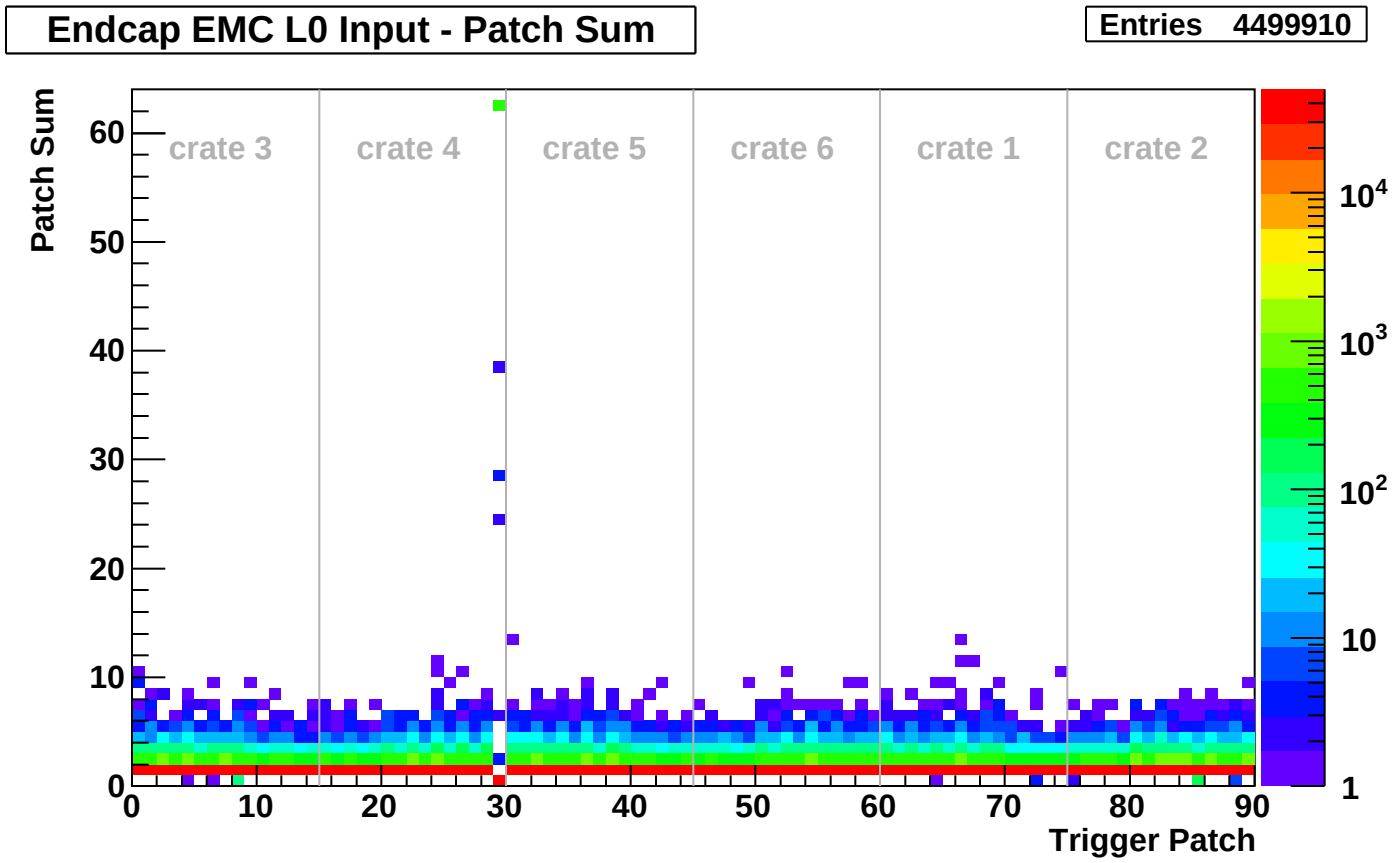

BunchId7Bit (JP1)
Entries 49999

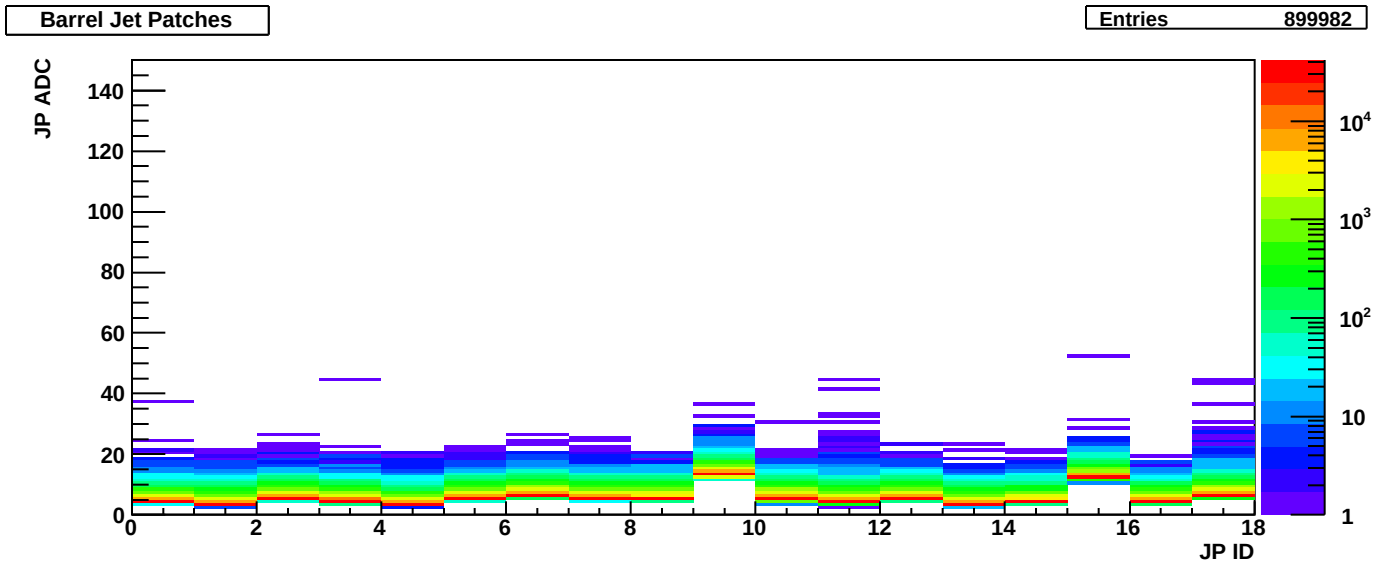

BunchId7Bit (JP2||AJP)
Entries 49999

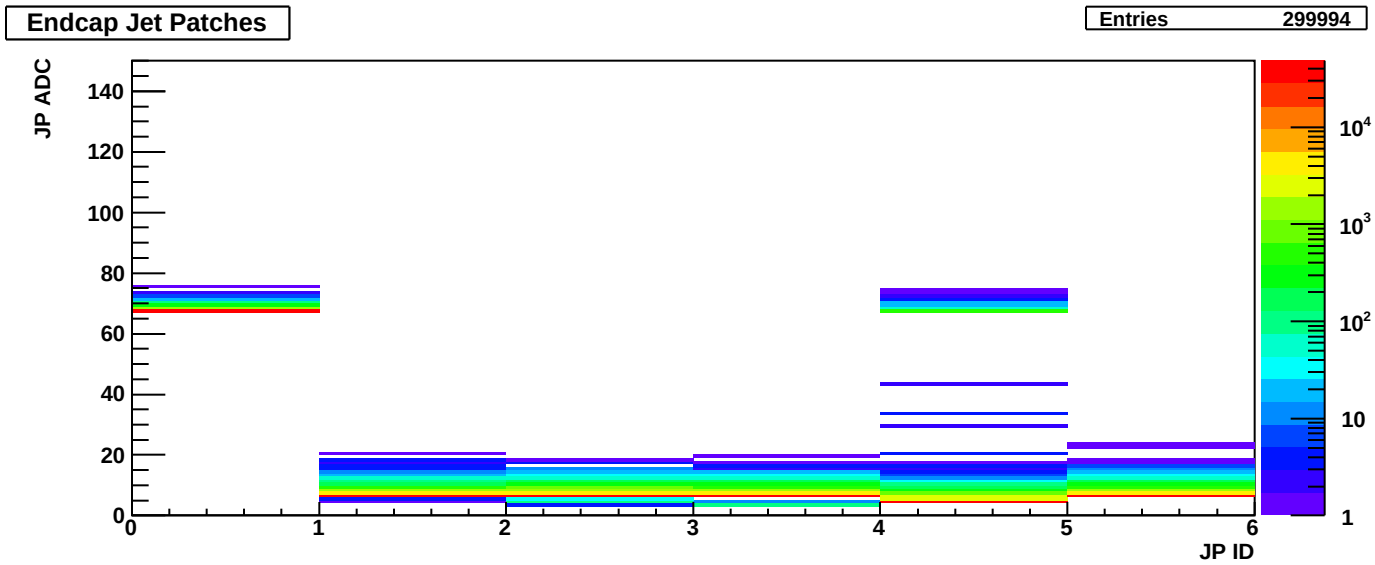

Barrel EMC L0 Input - High Tower
Entries 1.49997e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

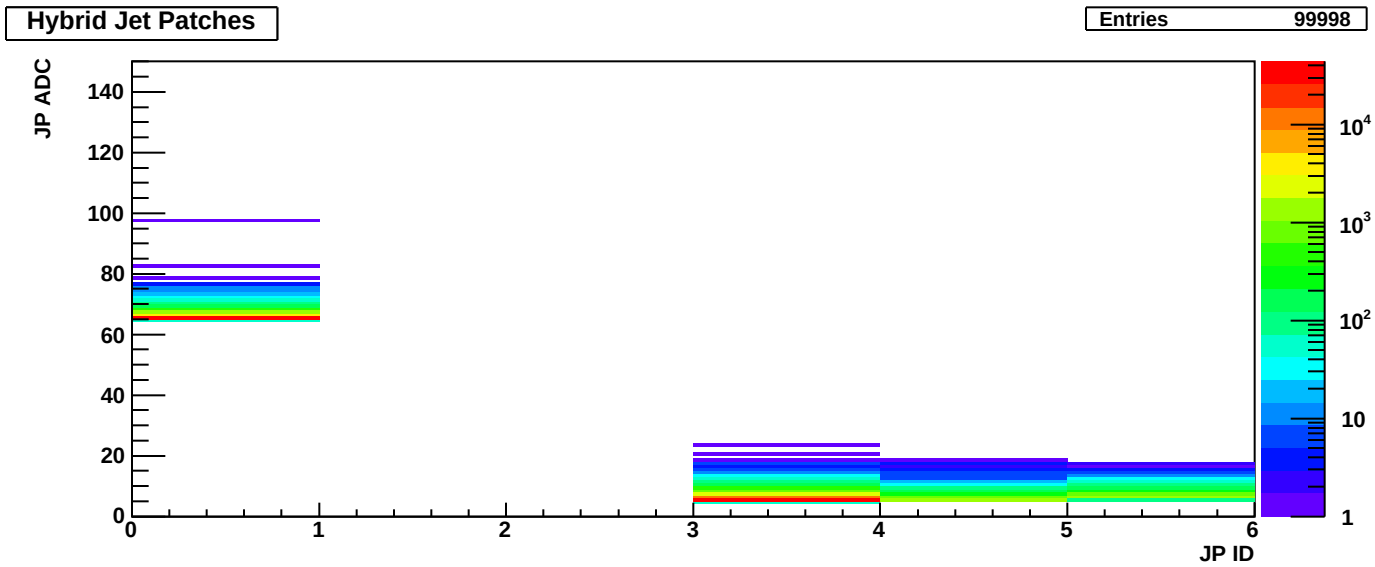

Barrel EMC L0 Input - Patch Sum
Entries 1.49997e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

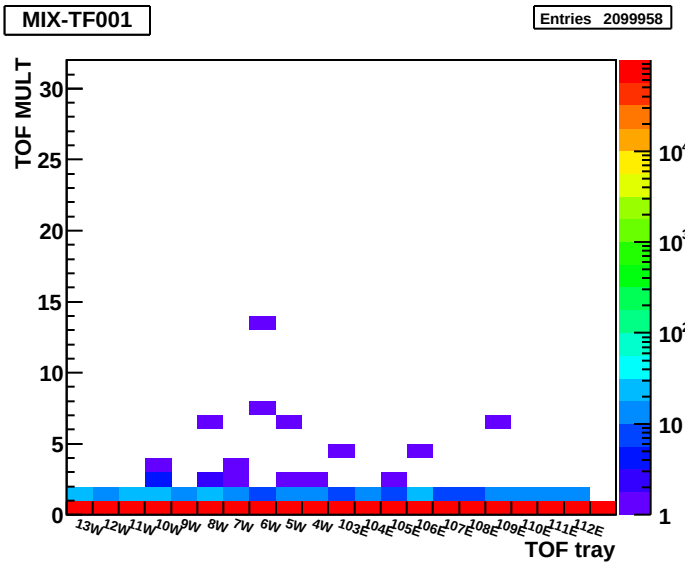

Endcap EMC L0 Input - High Tower
Entries 4499910
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

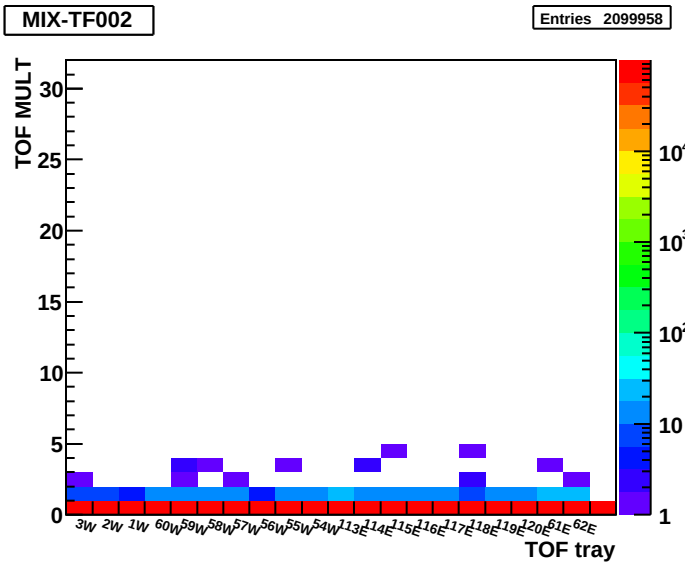

Endcap EMC L0 Input - Patch Sum
Entries 4499910
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

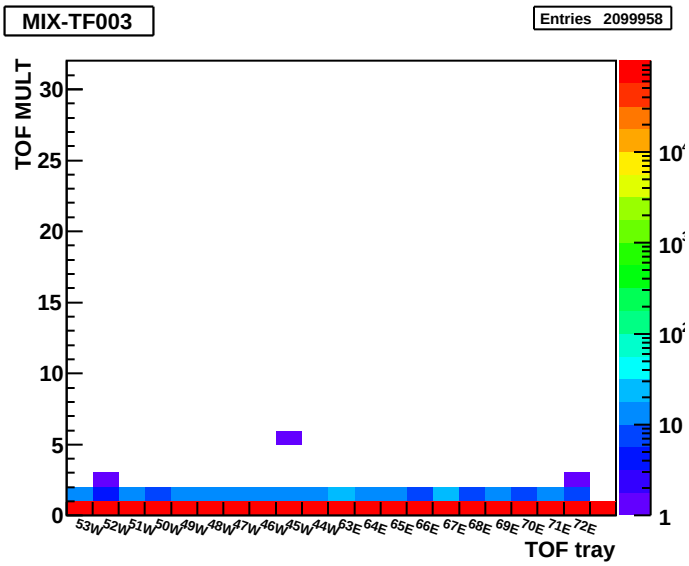

Barrel Jet Patches
Entries 899982
JP ADC
JP ID

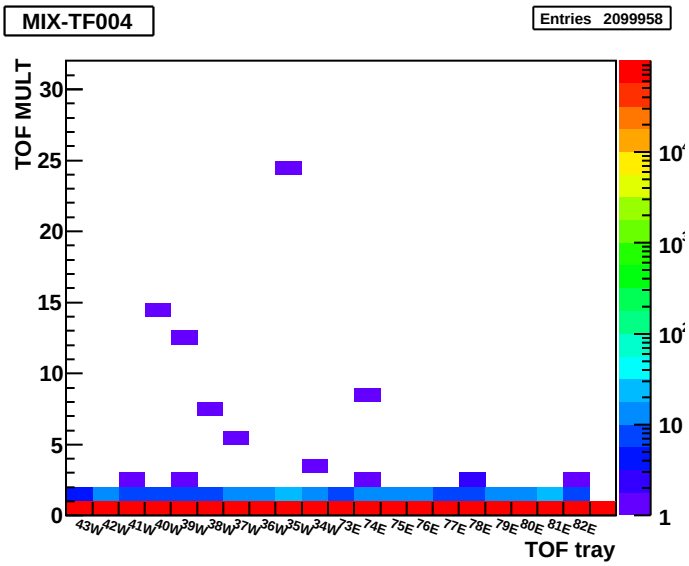

Endcap Jet Patches
Entries 299994
JP ADC
JP ID

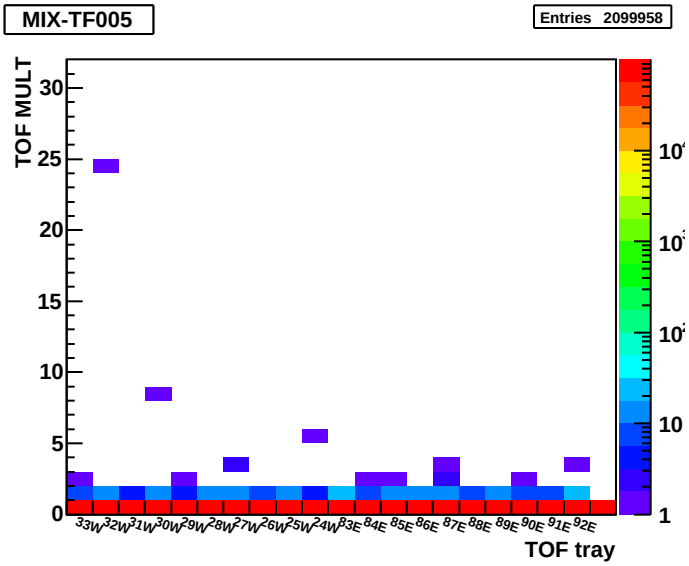

Hybrid Jet Patches
Entries 99998
JP ADC
JP ID

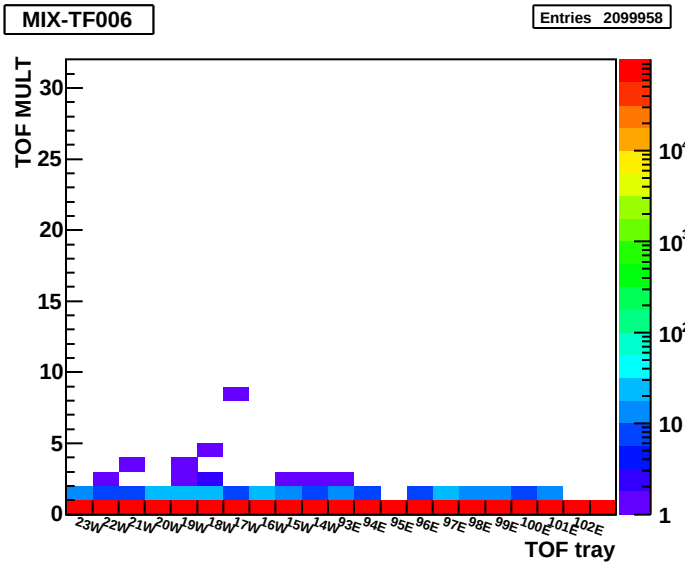

MIX-TF001
Entries 2099958
TOF MULT
TOF tray

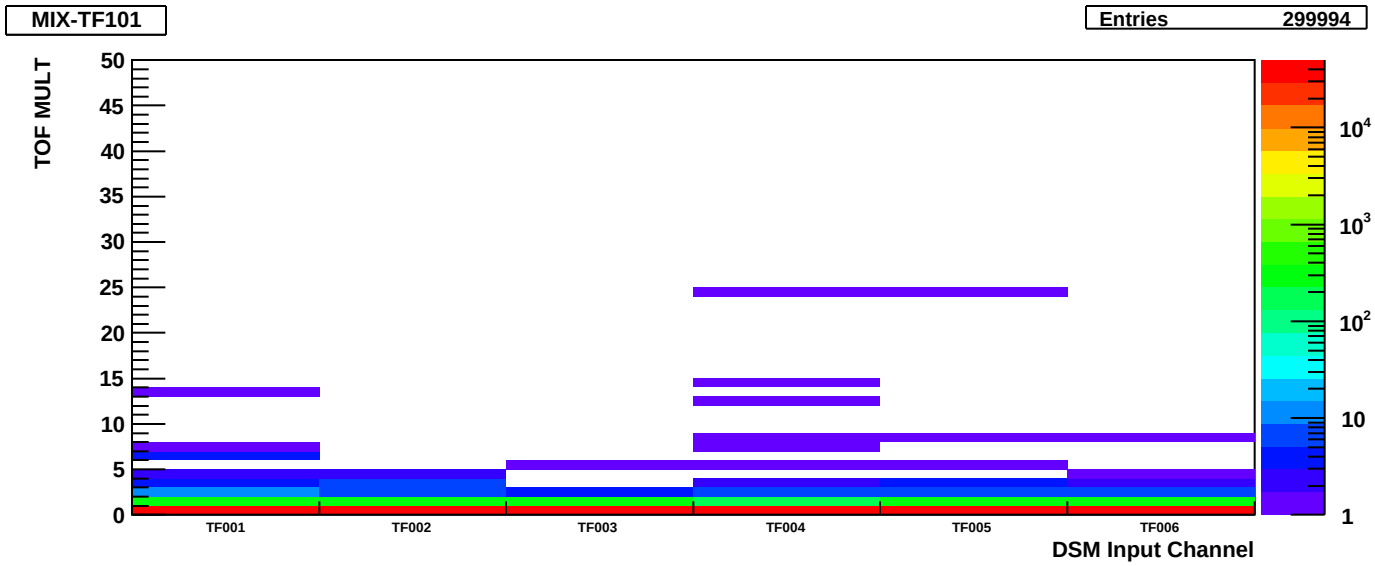

MIX-TF002
Entries 2099958
TOF MULT
TOF tray

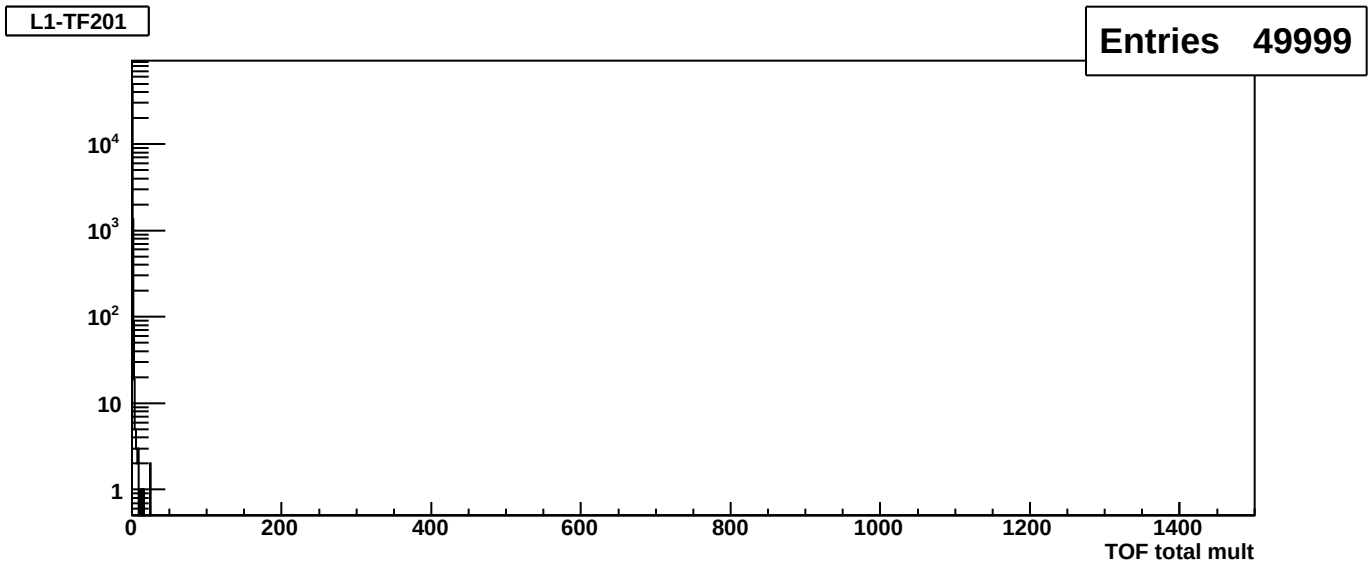

MIX-TF003
Entries 2099958
TOF MULT
TOF tray

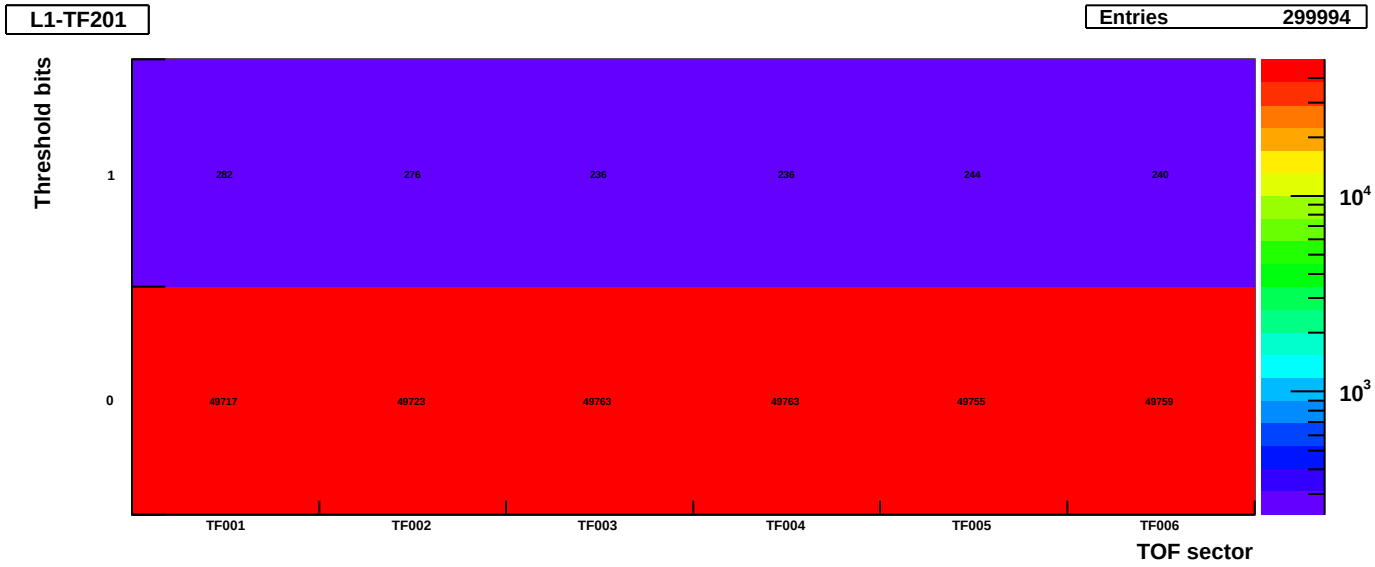

MIX-TF004
Entries 2099958
TOF MULT
TOF tray

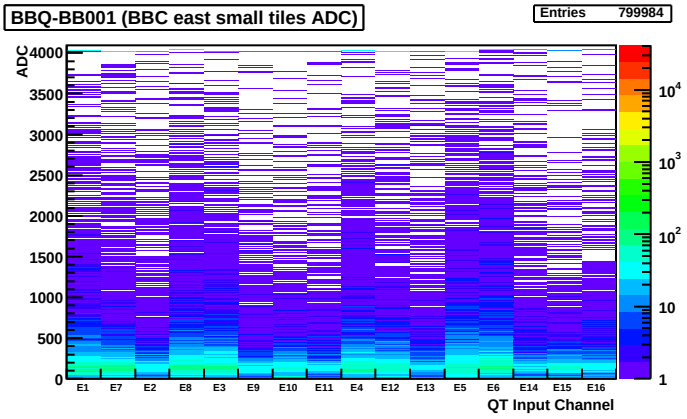

MIX-TF005
Entries 2099958
TOF MULT
TOF tray

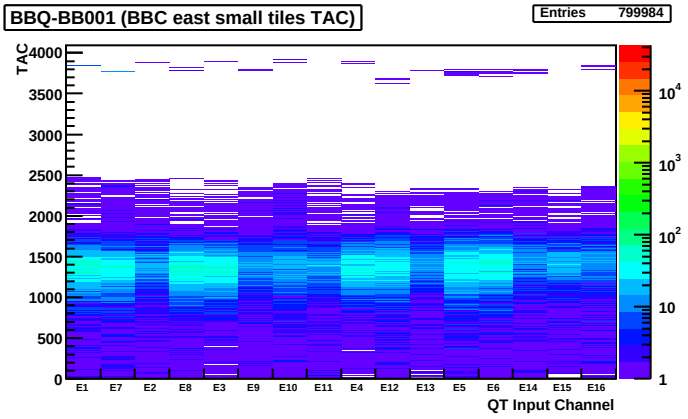

MIX-TF006
Entries 2099958
TOF MULT
TOF tray

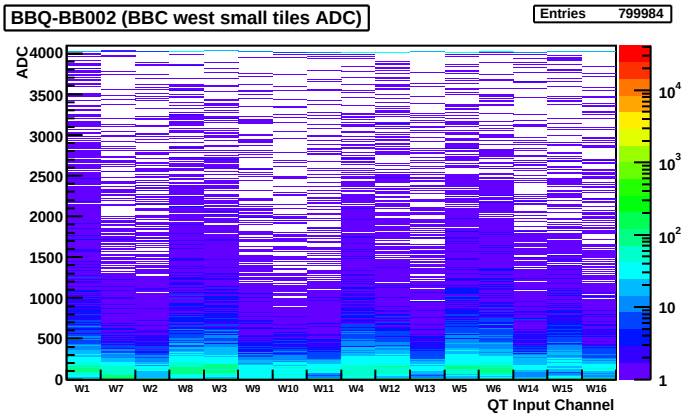

MIX-TF101
Entries 299994
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

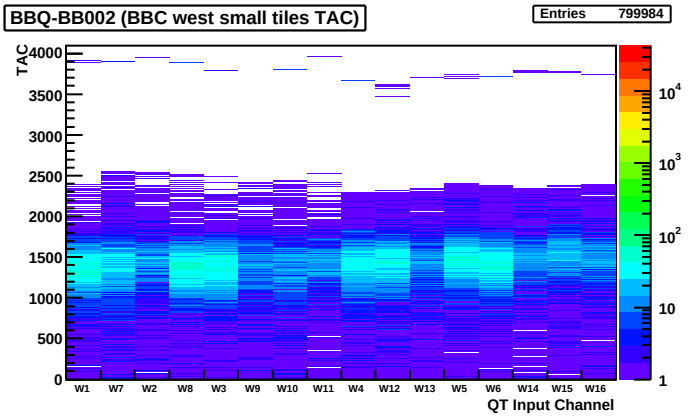

L1-TF201
Entries 49999
TOF total mult

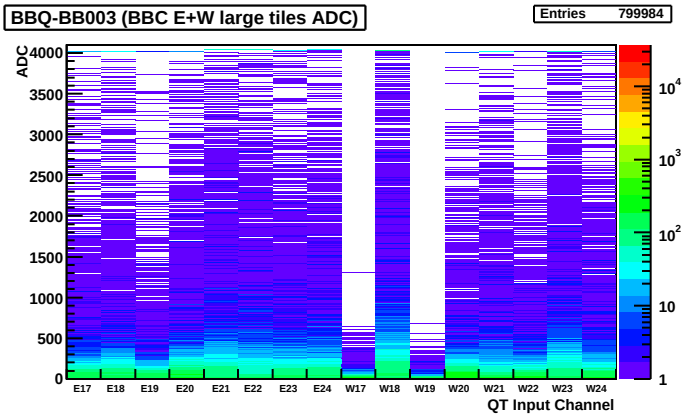

L1-TF201
Entries 299994
Threshold bits
282
276
236
236
244
240
49717
49723
49763
49763
49755
49759
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

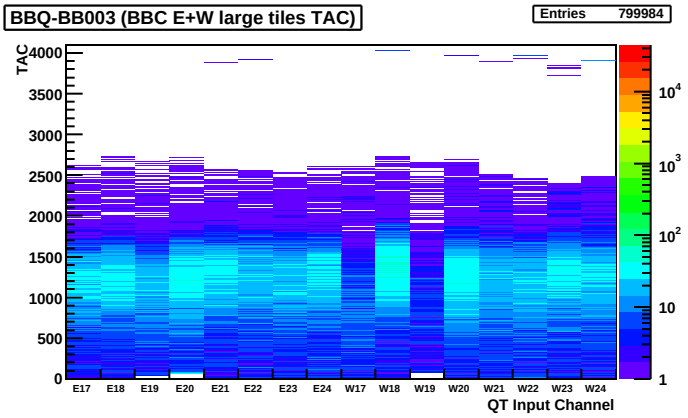

BBQ-BB001 (BBC east small tiles ADC)
Entries 799984
ADC
QT Input Channel

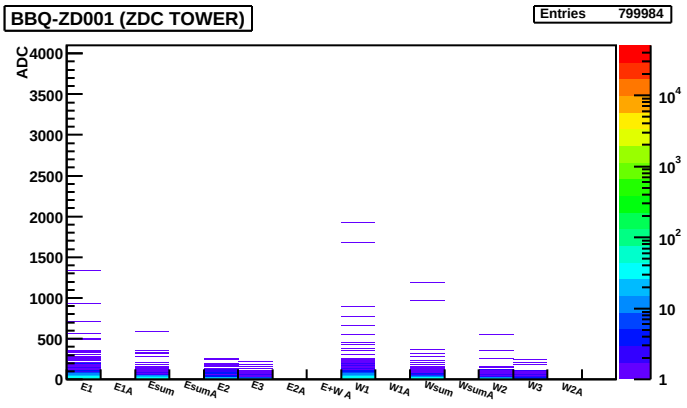

BBQ-BB001 (BBC east small tiles TAC)
Entries 799984
TAC
QT Input Channel

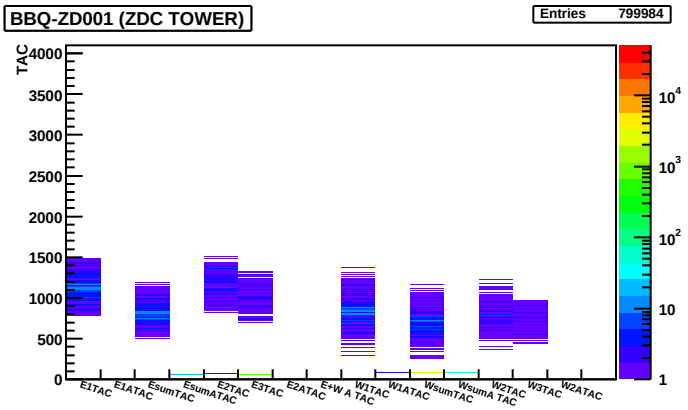

BBQ-BB002 (BBC west small tiles ADC)
Entries 799984
ADC
QT Input Channel

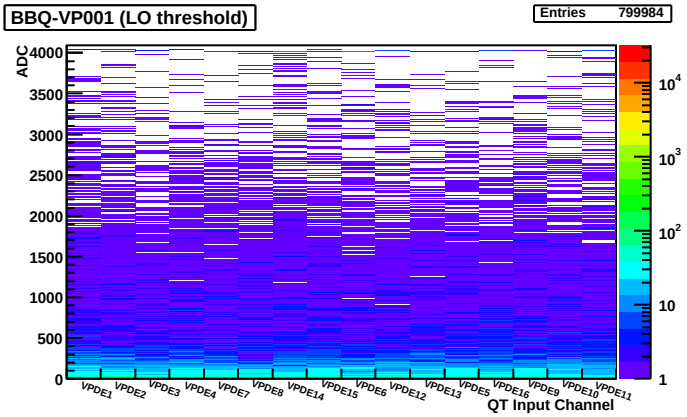

BBQ-BB002 (BBC west small tiles TAC)
Entries 799984
TAC
QT Input Channel

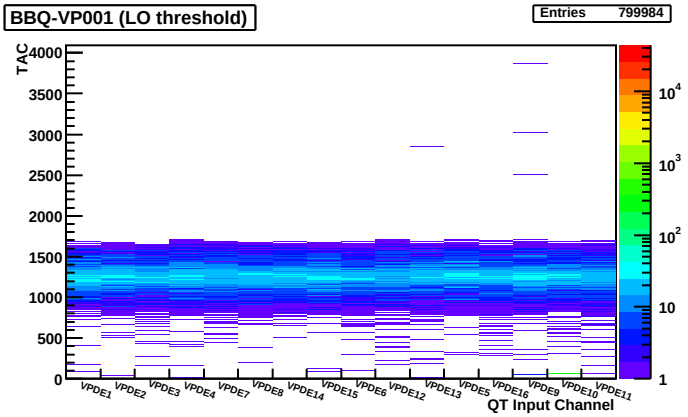

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 799984
ADC
QT Input Channel

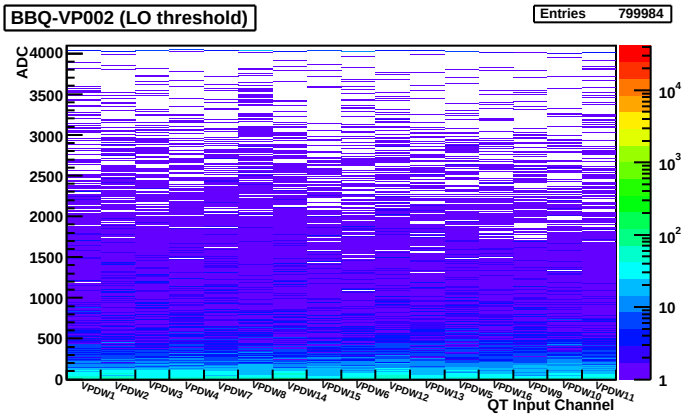

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 799984
TAC
QT Input Channel

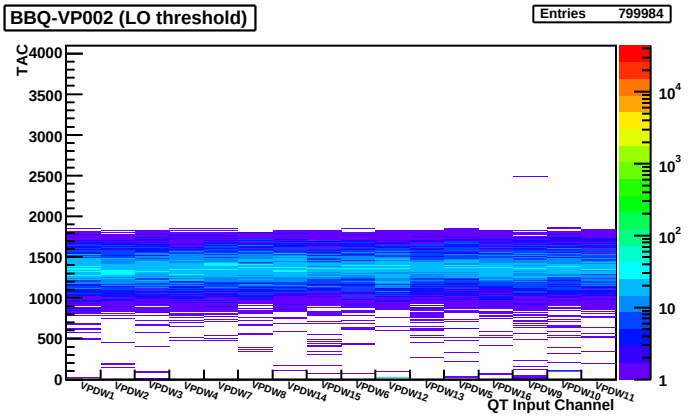

BBQ-ZD001 (ZDC TOWER)
Entries 799984
ADC

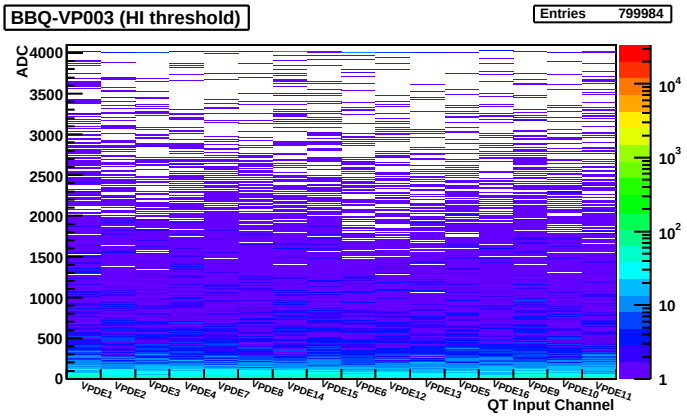

BBQ-ZD001 (ZDC TOWER)
Entries 799984
TAC

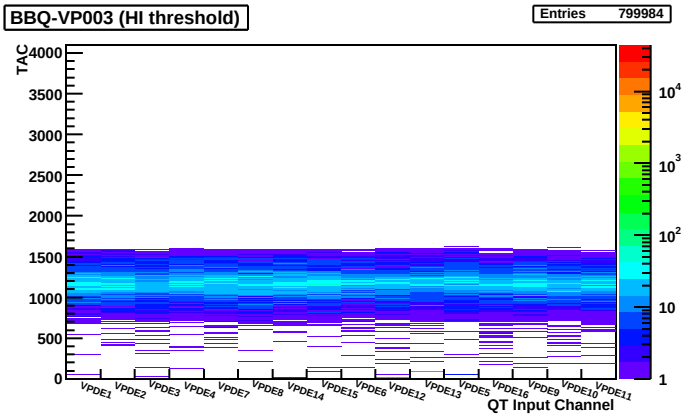

BBQ-VP001 (LO threshold)
Entries 799984
ADC
QT Input Channel

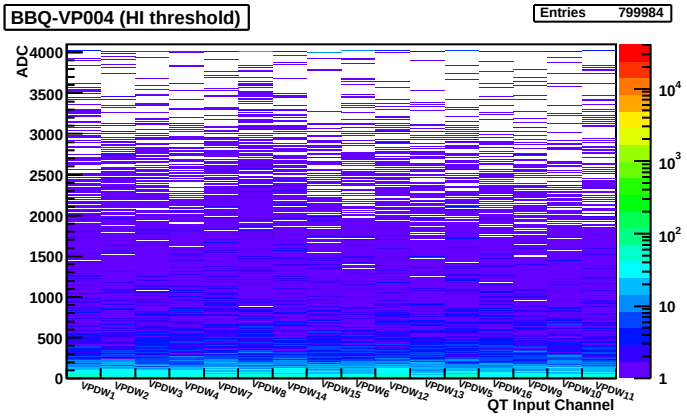

BBQ-VP001 (LO threshold)
Entries 799984
TAC
QT Input Channel

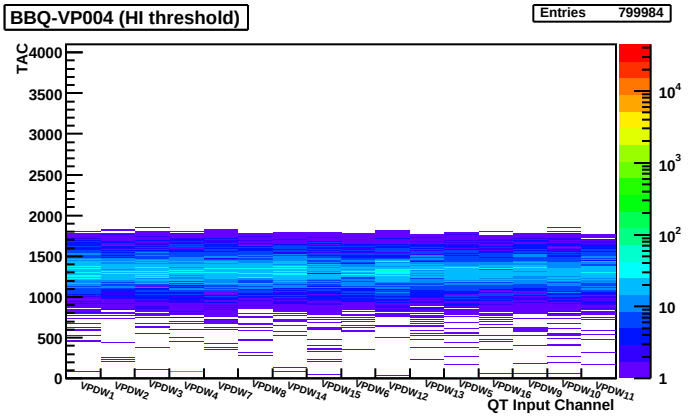

BBQ-VP002 (LO threshold)
Entries 799984
ADC
QT Input Channel

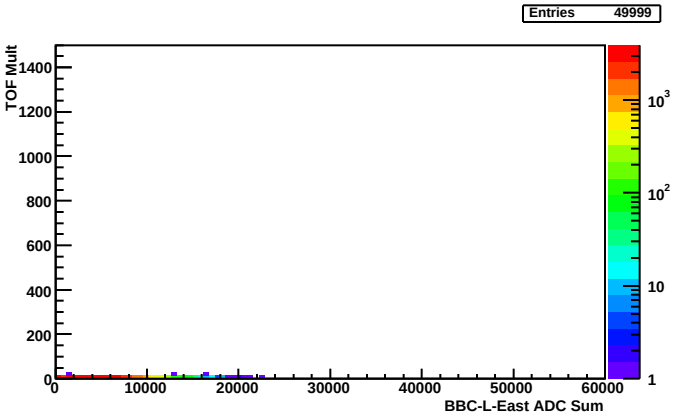

BBQ-VP002 (LO threshold)
Entries 799984
TAC
QT Input Channel

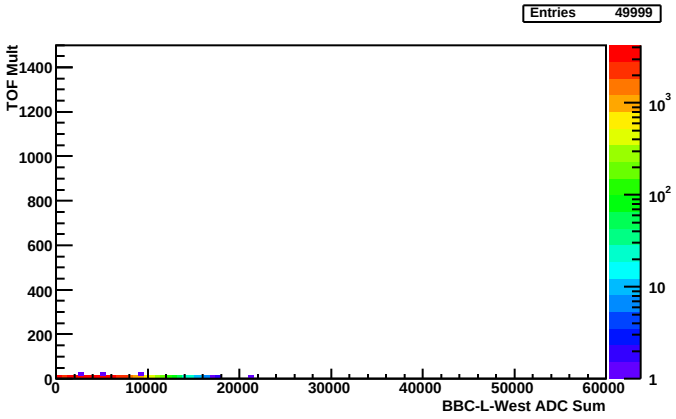

BBQ-VP003 (HI threshold)
Entries 799984
ADC
QT Input Channel

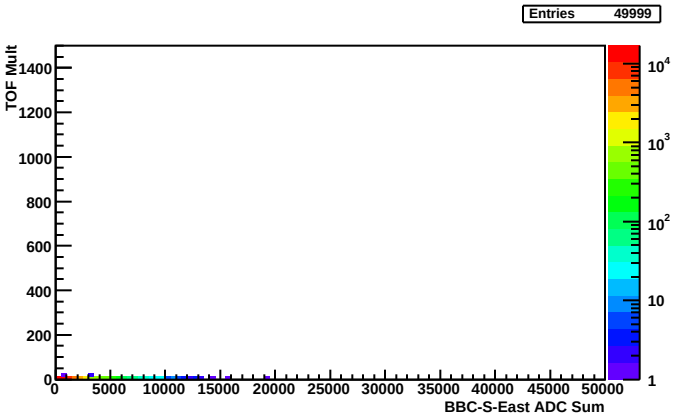

BBQ-VP003 (HI threshold)
Entries 799984
TAC
QT Input Channel

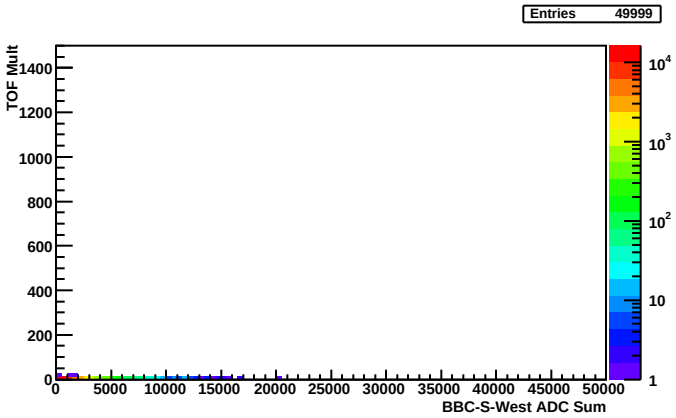

BBQ-VP004 (HI threshold)
Entries 799984
ADC
QT Input Channel

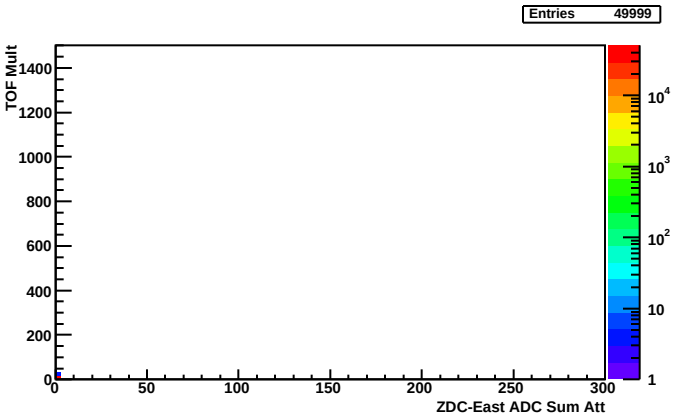

BBQ-VP004 (HI threshold)
Entries 799984
TAC
QT Input Channel

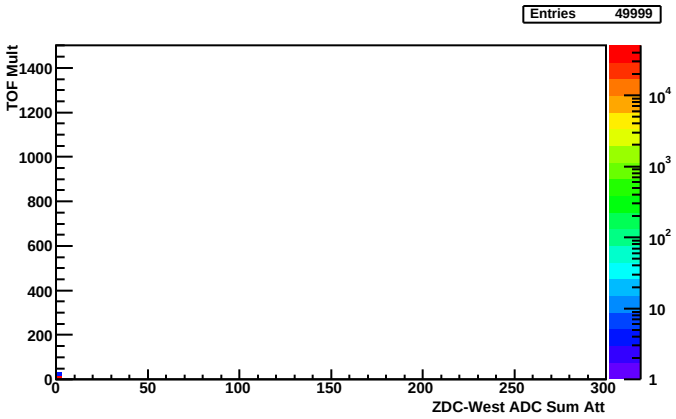

Entries 49999
TOF Mult
BBC-L-East ADC Sum

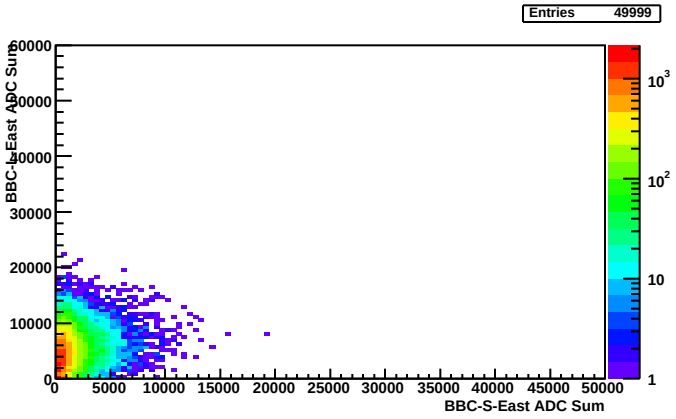

Entries 49999
TOF Mult
BBC-L-West ADC Sum

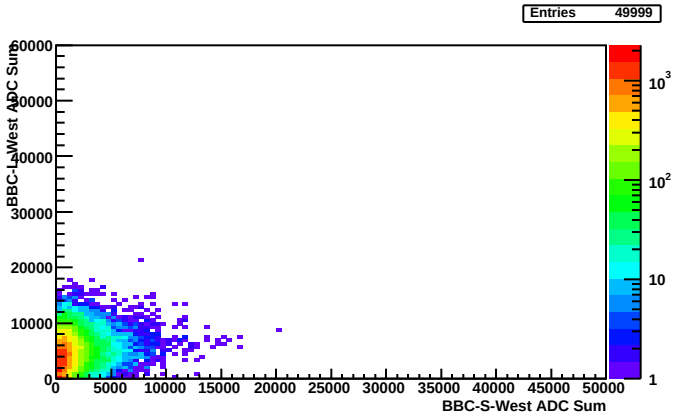

Entries 49999
TOF Mult
BBC-S-East ADC Sum

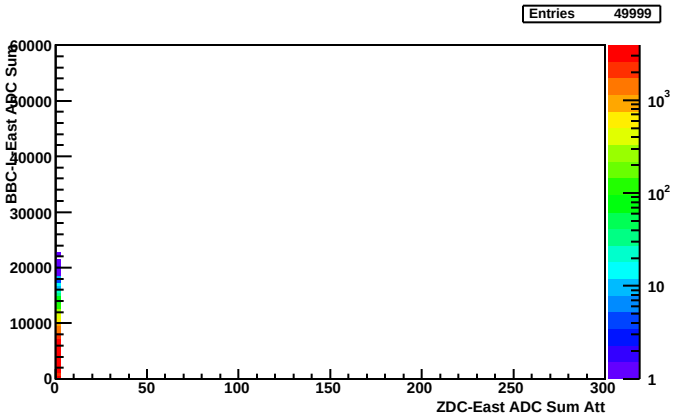

Entries 49999
TOF Mult
BBC-S-West ADC Sum

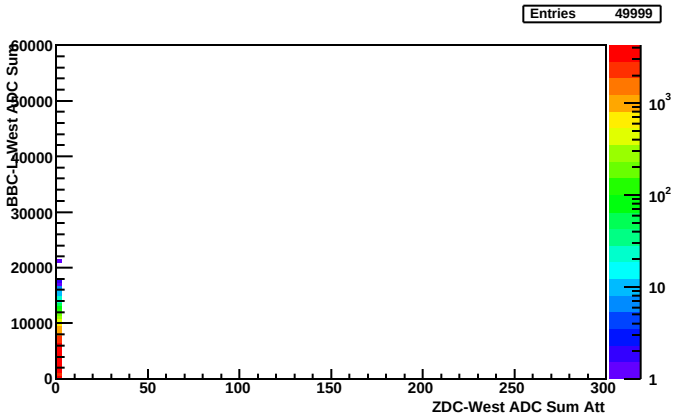

Entries 49999
TOF Mult
ZDC-East ADC Sum Att

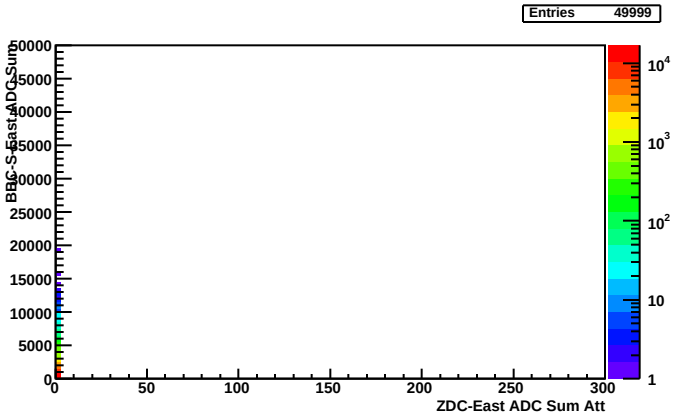

Entries 49999
TOF Mult
ZDC-West ADC Sum Att

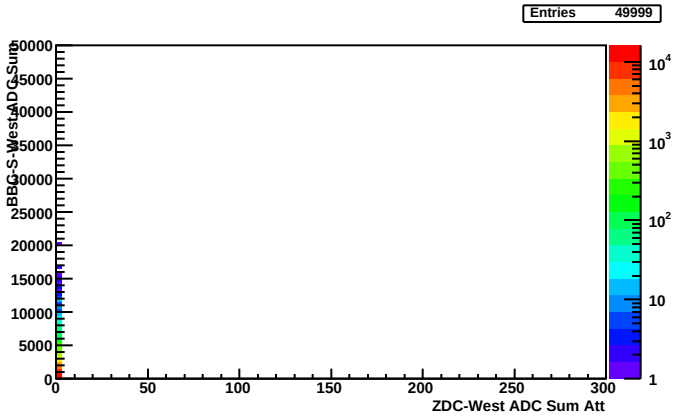

Entries 49999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

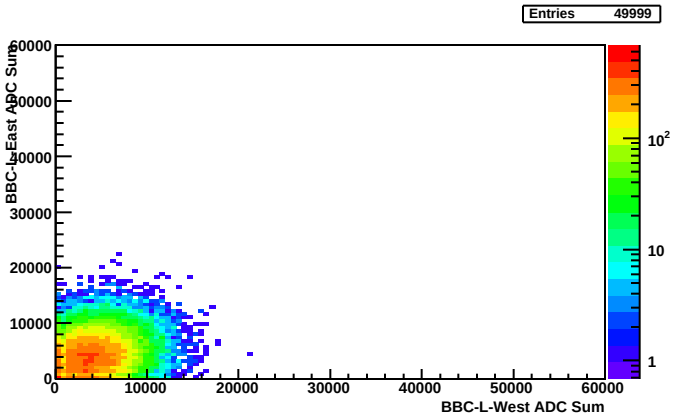

Entries 49999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

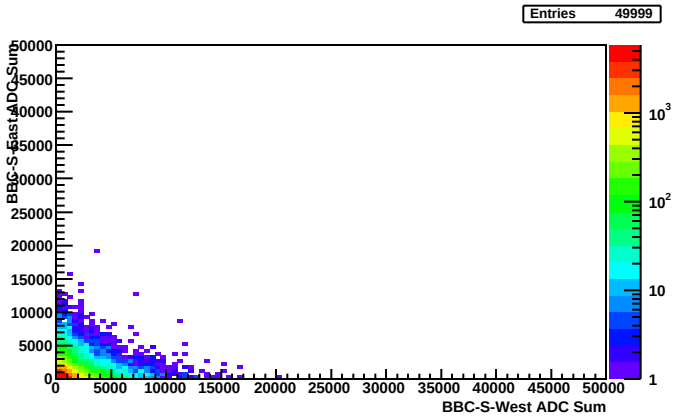

Entries 49999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

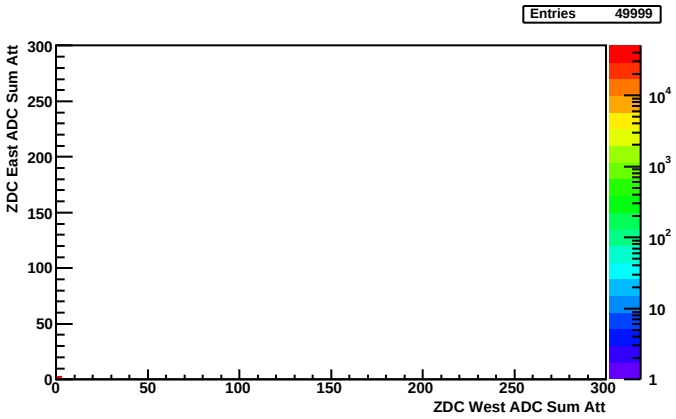

Entries 49999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

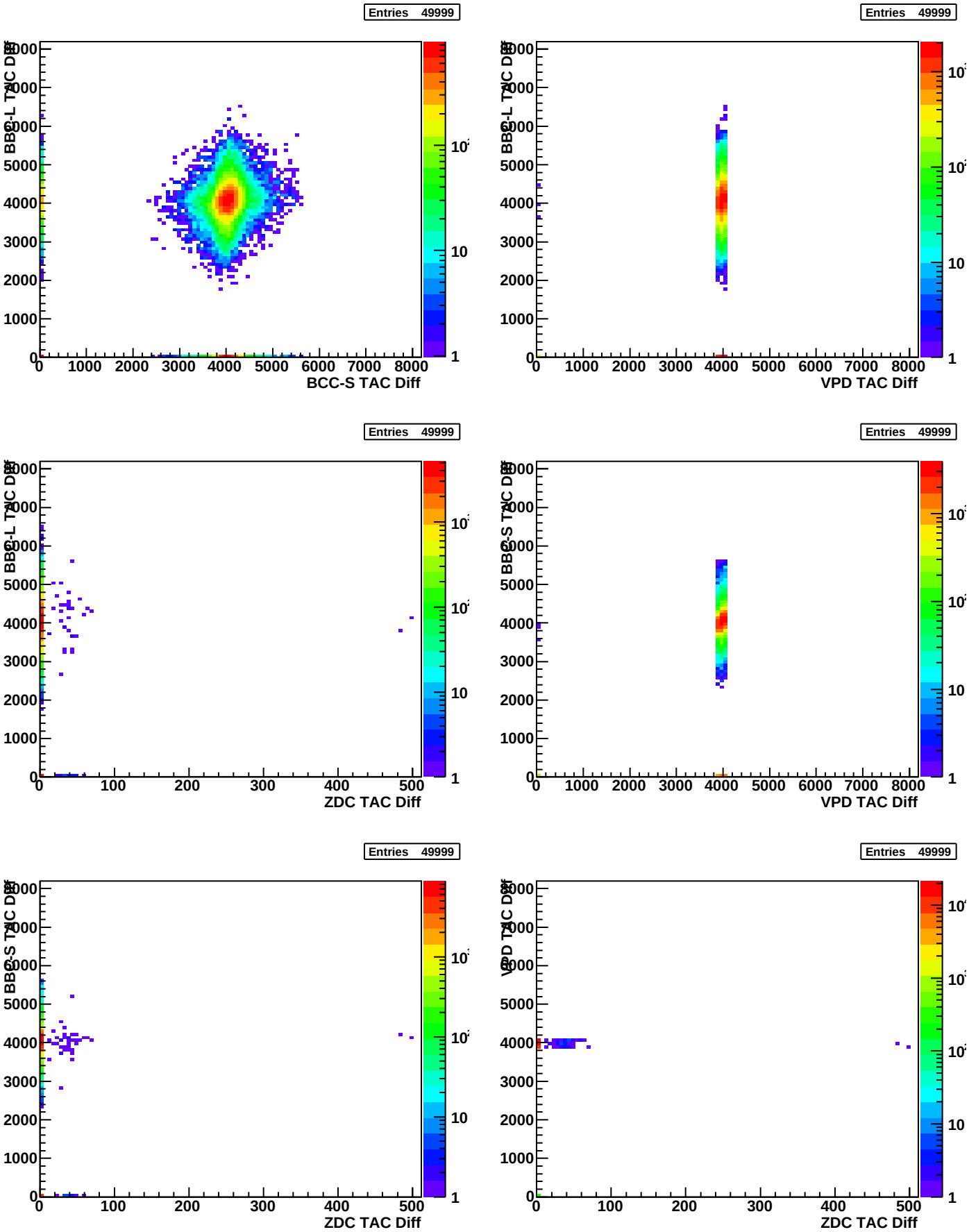

Entries 49999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

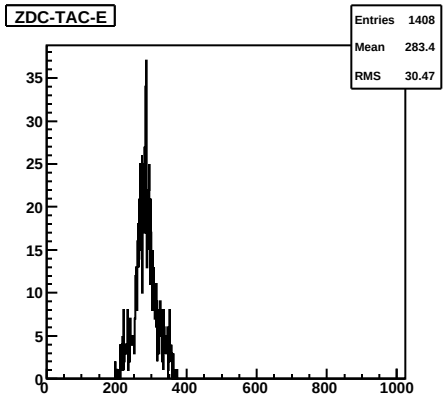

Entries 49999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

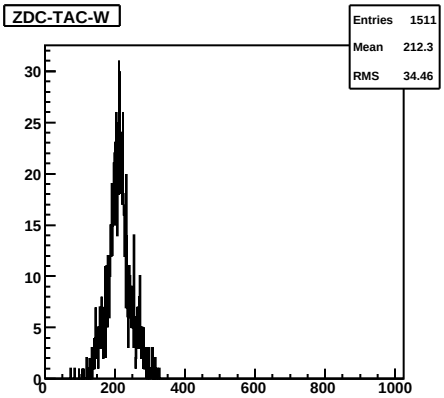

Entries 49999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

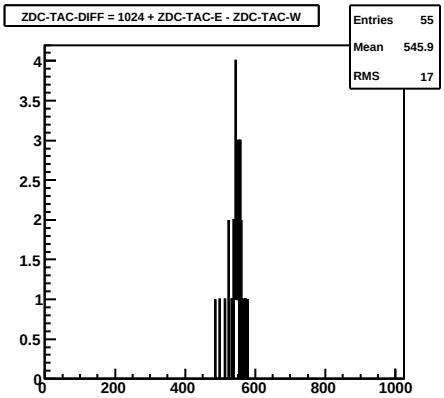

Entries 49999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

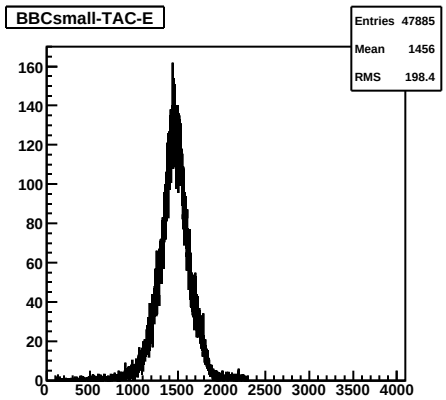

Entries 49999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

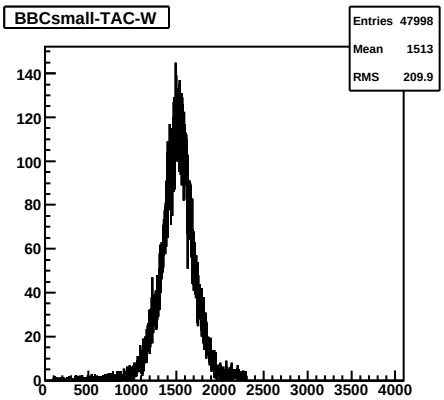

Entries 49999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 49999
BBC-L TAC Diff
VPD TAC Diff
Entries 49999
BBC-L TAC Diff
ZDC TAC Diff
Entries 49999
BBC-S TAC Diff
VPD TAC Diff
Entries 49999
BBC-S TAC Diff
ZDC TAC Diff
Entries 49999
VPD TAC Diff
ZDC TAC Diff

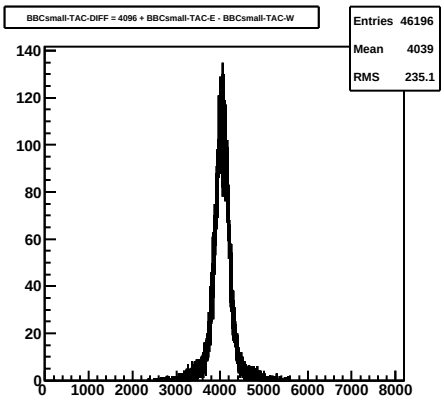

ZDC-TAC-E
Entries 1408
Mean 283.4
RMS 30.47

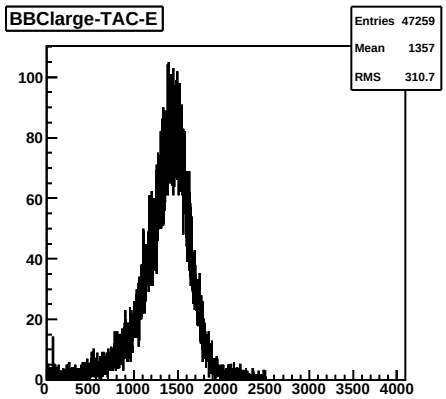

ZDC-TAC-W
Entries 1511
Mean 212.3
RMS 34.46

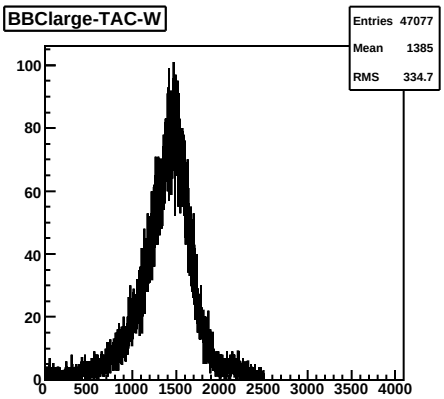

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 55
Mean 545.9
RMS 17

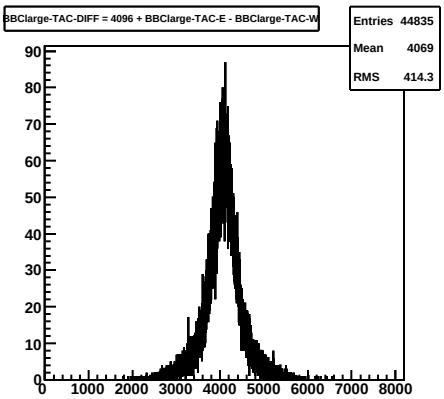

BBCsmall-TAC-E
Entries 47885
Mean 1456
RMS 198.4

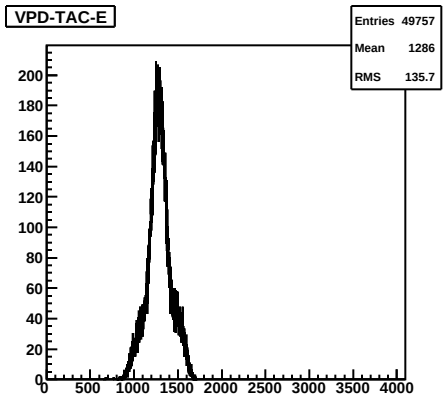

BBCsmall-TAC-W
Entries 47998
Mean 1513
RMS 209.9

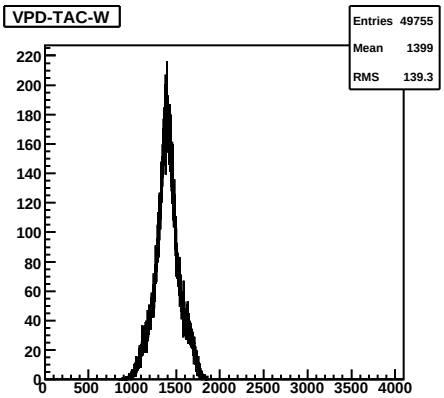

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 46196
Mean 4039
RMS 235.1

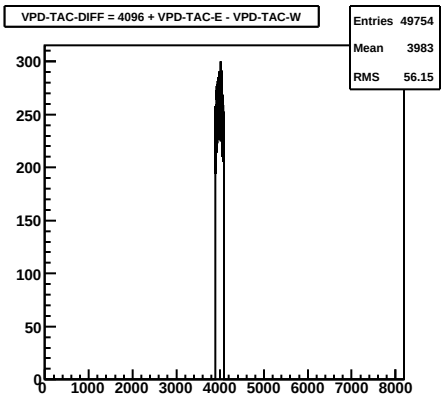

BBClarge-TAC-E
Entries 47259
Mean 1357
RMS 310.7

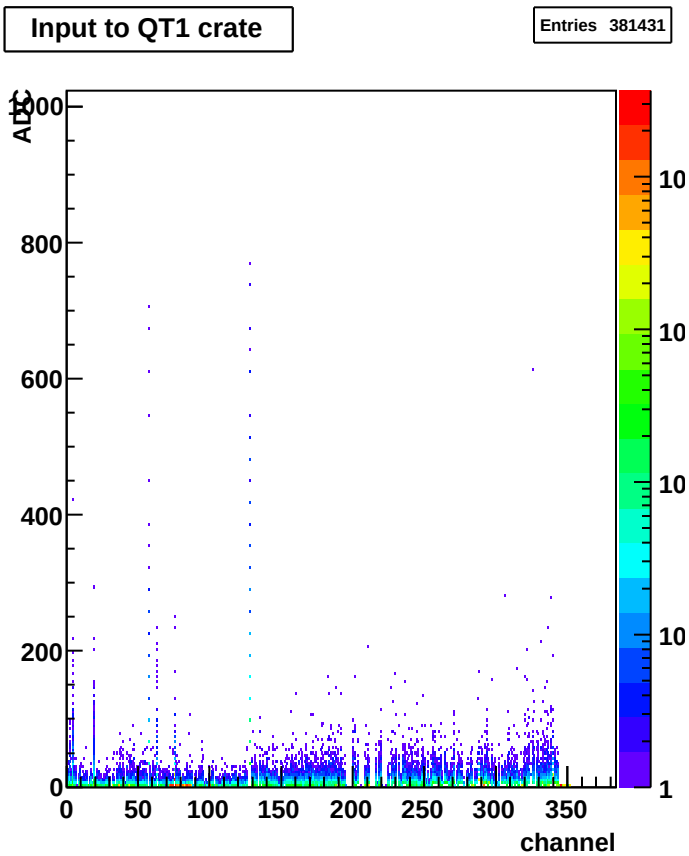

BBClarge-TAC-W
Entries 47077
Mean 1385
RMS 334.7

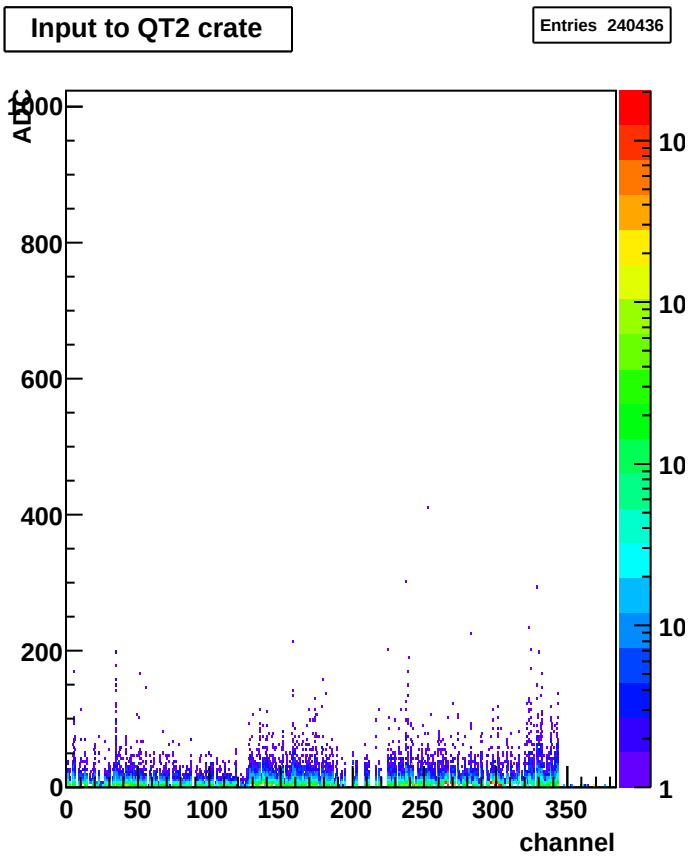

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 44835
Mean 4069
RMS 414.3

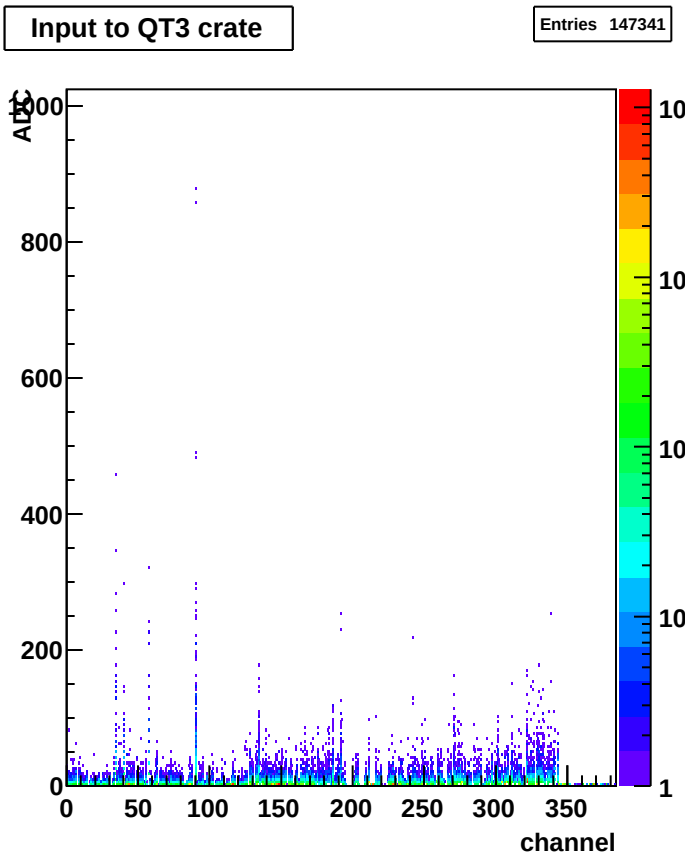

VPD-TAC-E
Entries 49757
Mean 1286
RMS 135.7

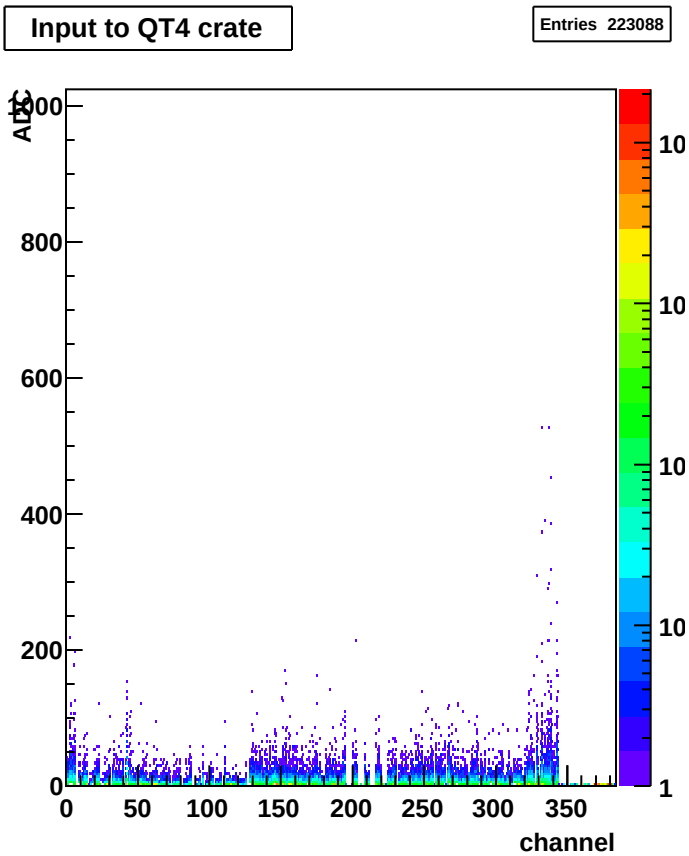

VPD-TAC-W
Entries 49755
Mean 1399
RMS 139.3

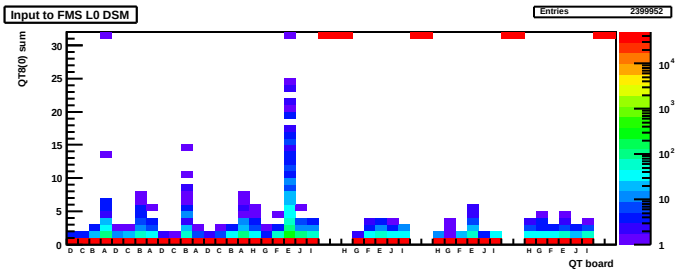

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 49754
Mean 3983
RMS 56.15

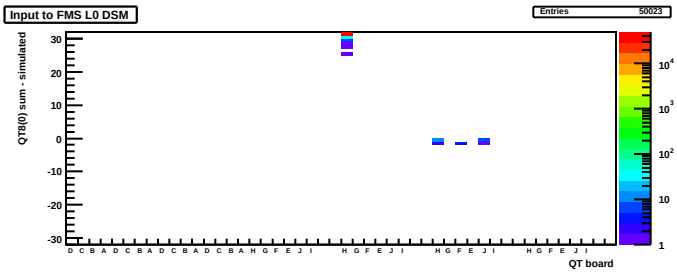

Input to QT1 crate
Entries 381431
ADC
channel

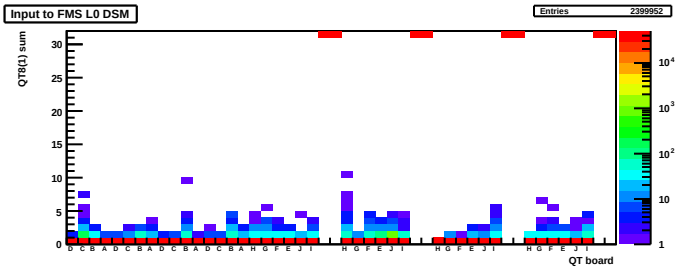

Input to QT2 crate
Entries 240436
ADC
channel

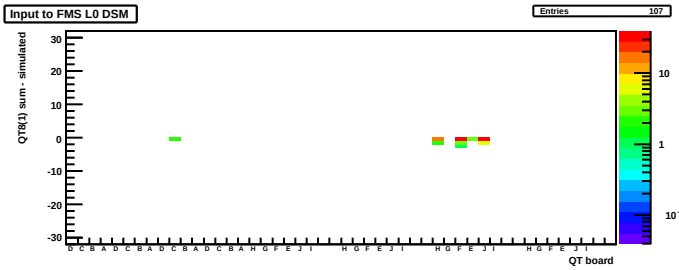

Input to QT3 crate
Entries 147341
ADC
channel

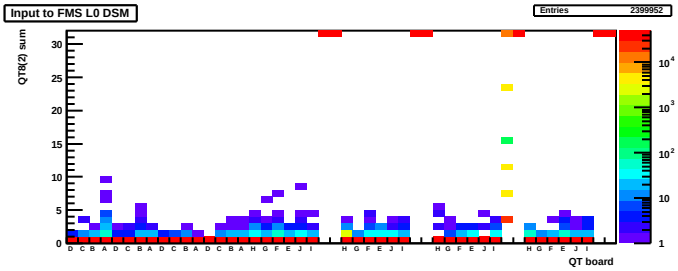

Input to QT4 crate
Entries 223088
ADC
channel

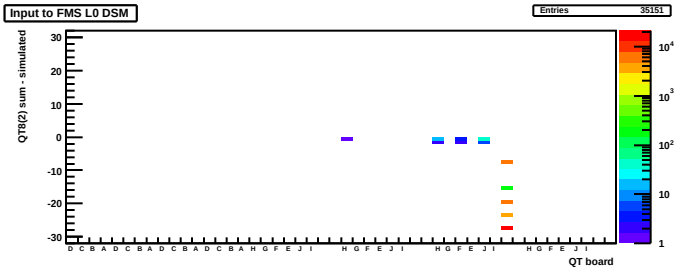

Input to FMS L0 DSM
Entries 2399952
QT8(0) sum
QT board

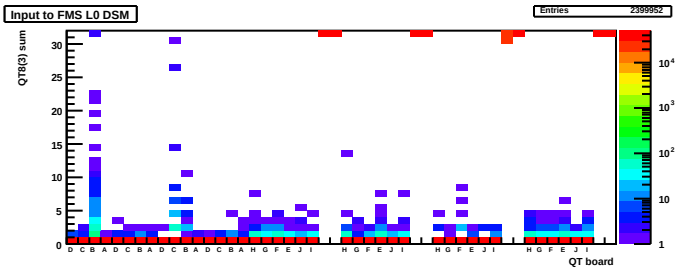

Input to FMS L0 DSM
Entries 50023
QT8(0) sum - simulated
QT board

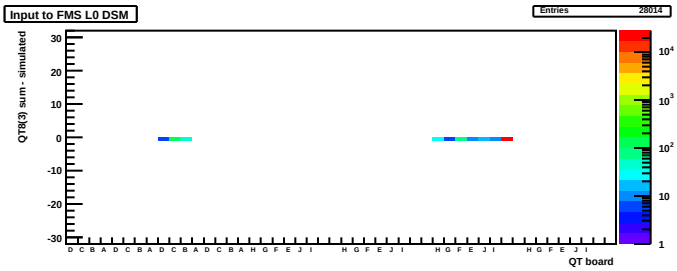

Input to FMS L0 DSM
Entries 2399952
QT8(1) sum
QT board

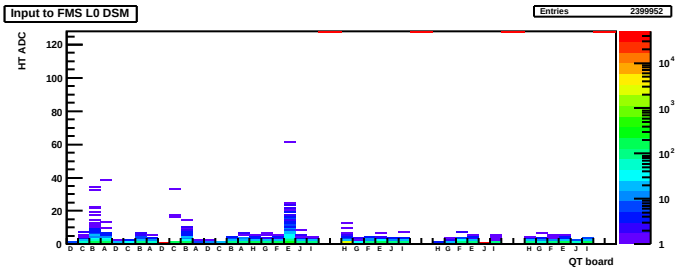

Input to FMS L0 DSM
Entries 107
QT8(1) sum - simulated
QT board

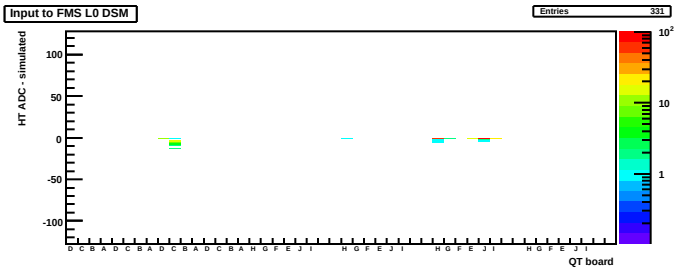

Input to FMS L0 DSM
Entries 2399952
QT8(2) sum
QT board

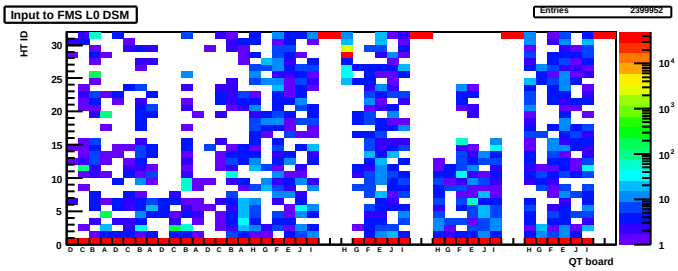

Input to FMS L0 DSM
Entries 35151
QT8(2) sum - simulated
QT board

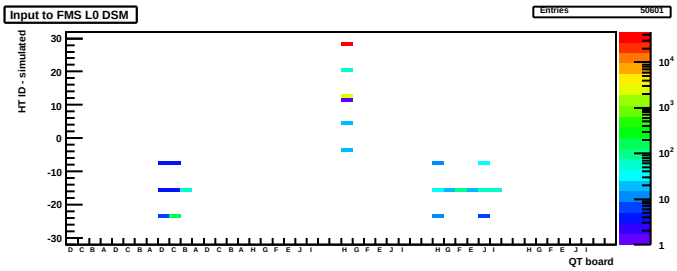

Input to FMS L0 DSM
Entries 2399952
QT8(3) sum
QT board

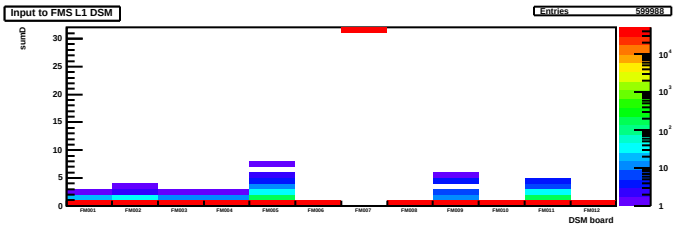

Input to FMS L0 DSM
Entries 28014
QT8(3) sum - simulated
QT board

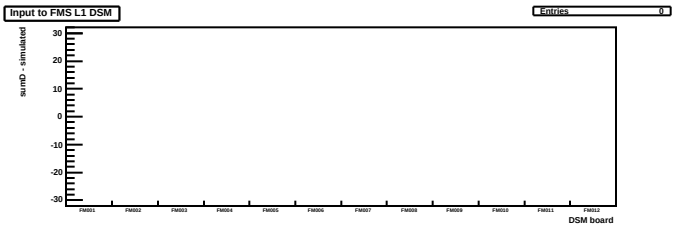

Input to FMS L0 DSM
Entries 2399952
HT ADC
QT board

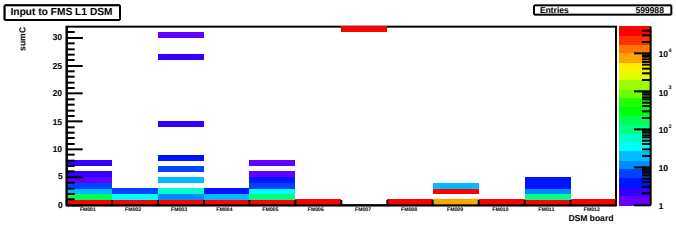

Input to FMS L0 DSM
Entries 331
HT ADC - simulated
QT board

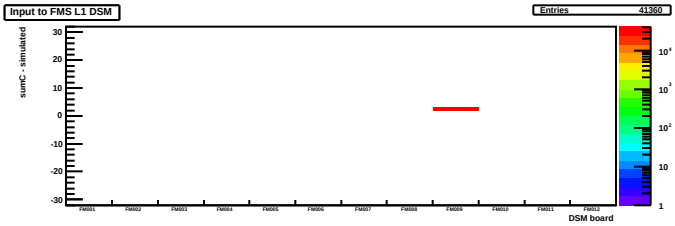

Input to FMS L0 DSM
Entries 2399952
HT ID
QT board

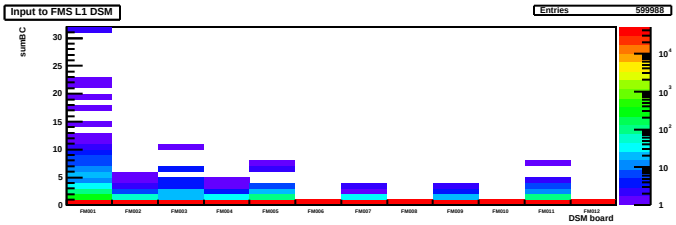

Input to FMS L0 DSM
Entries 50601
HT ID - simulated
QT board

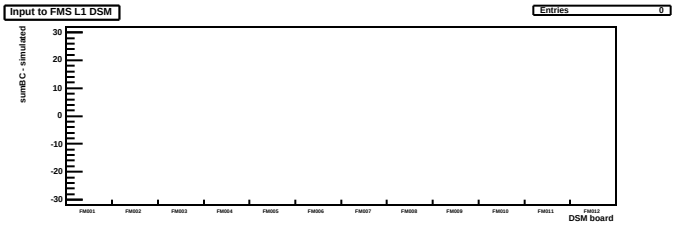

Input to FMS L1 DSM
Entries 599988
sumD
DSM board

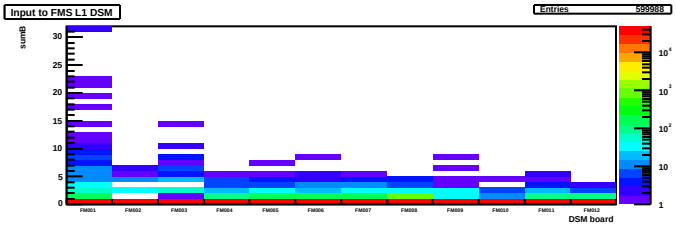

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

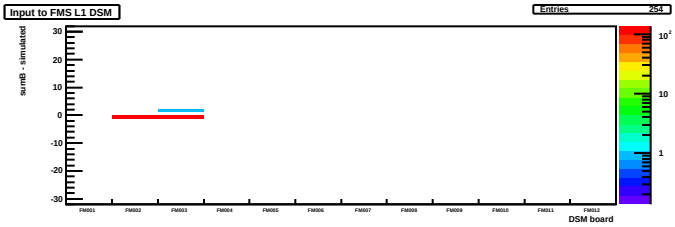

Input to FMS L1 DSM
Entries 599988
sumC
DSM board

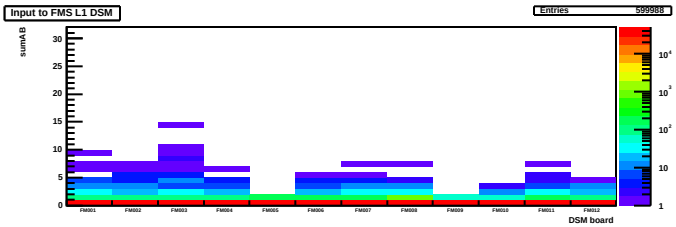

Input to FMS L1 DSM
Entries 41360
sumC - simulated
DSM board

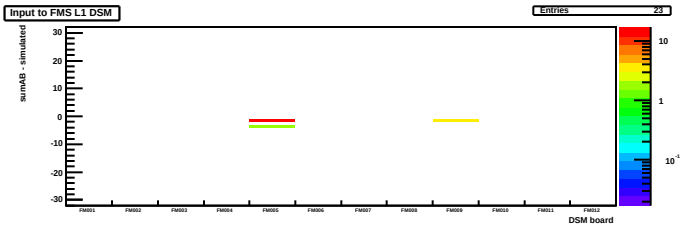

Input to FMS L1 DSM
Entries 599988
sumBC
DSM board

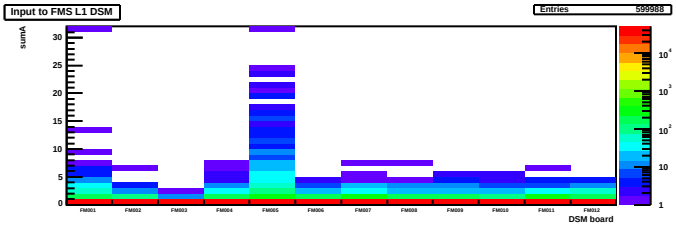

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

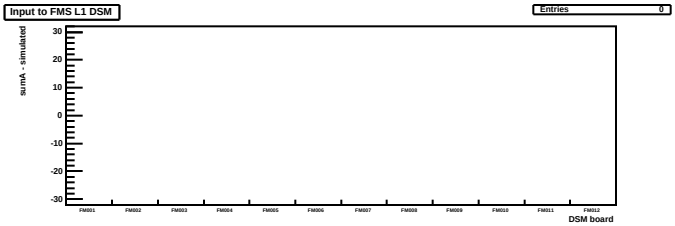

Input to FMS L1 DSM
Entries 599988
sumB
DSM board

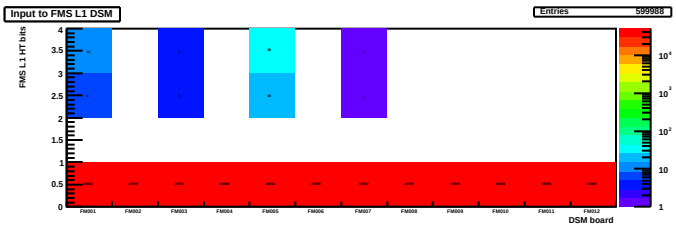

Input to FMS L1 DSM
Entries 254
sumB - simulated
DSM board

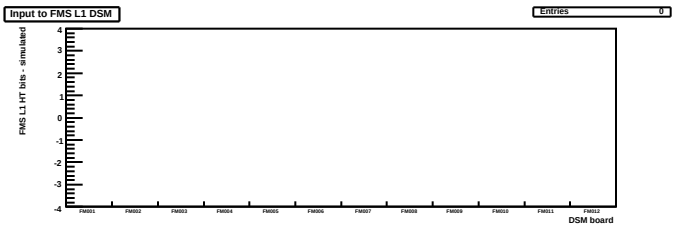

Input to FMS L1 DSM
Entries 599988
sumAB
DSM board

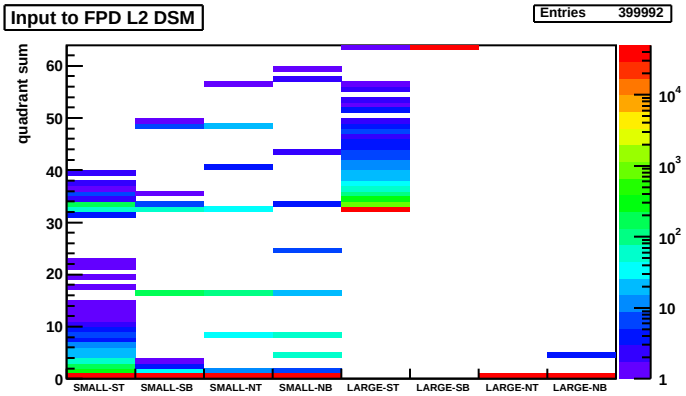

Input to FMS L1 DSM
Entries 23
sumAB - simulated
DSM board

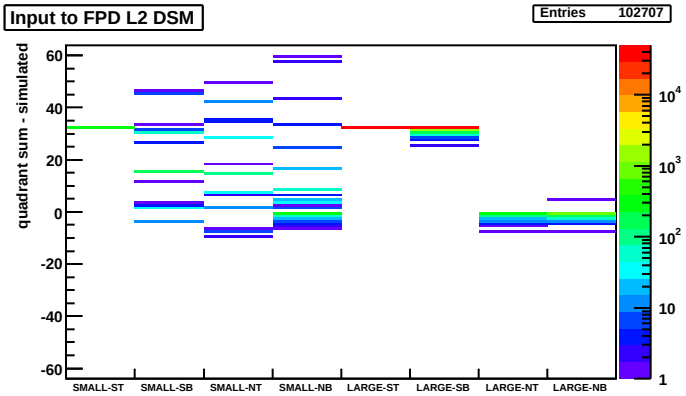

Input to FMS L1 DSM
Entries 599988
sumA
DSM board

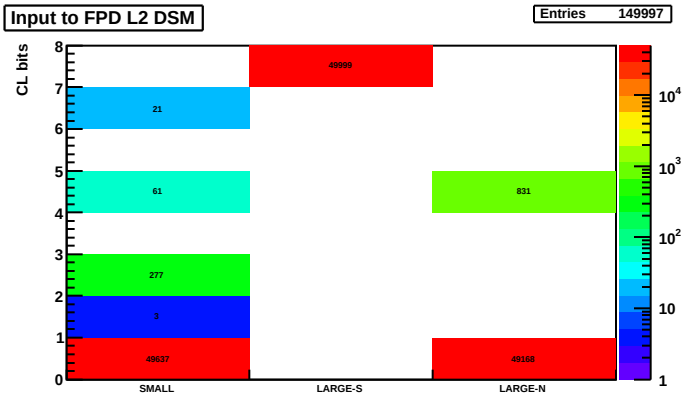

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

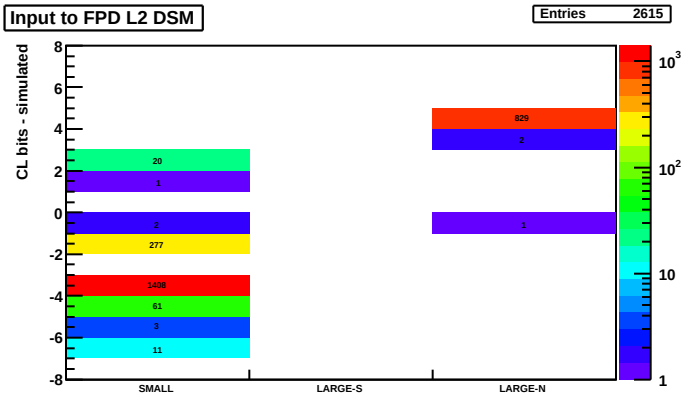

Input to FMS L1 DSM
Entries 599988
FMS L1 HT bits
DSM board

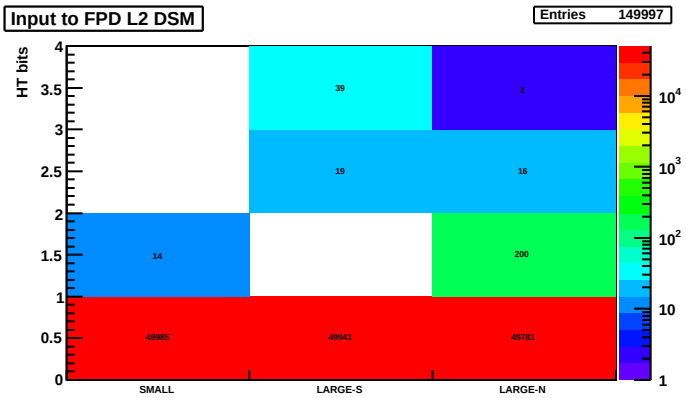

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

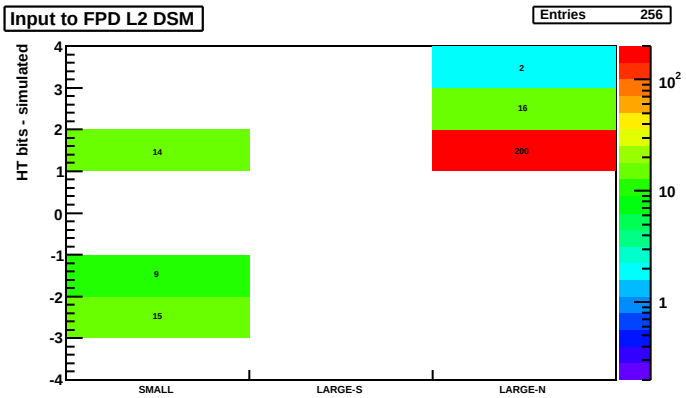

Input to FPD L2 DSM
Entries 399992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

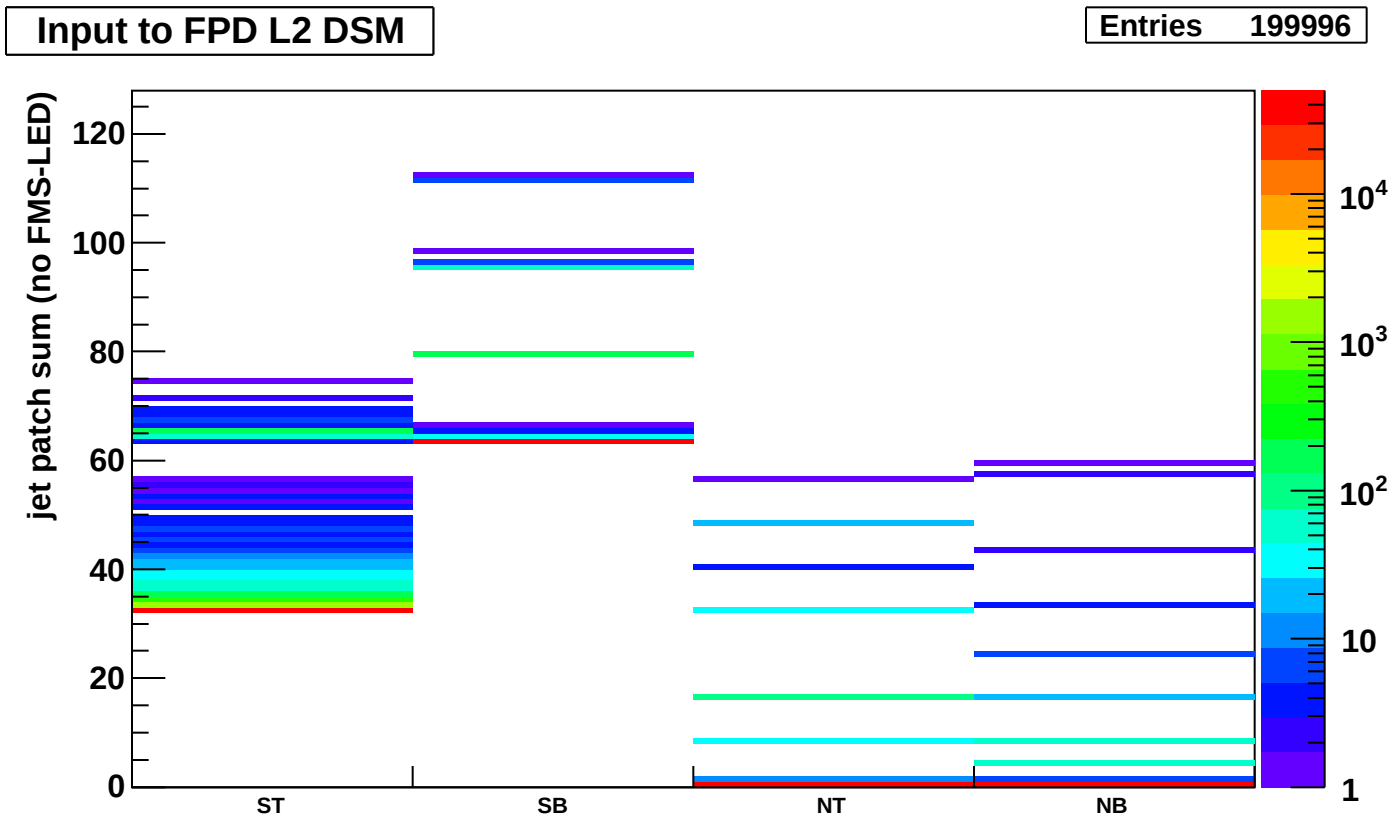

Input to FPD L2 DSM
Entries 102707
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

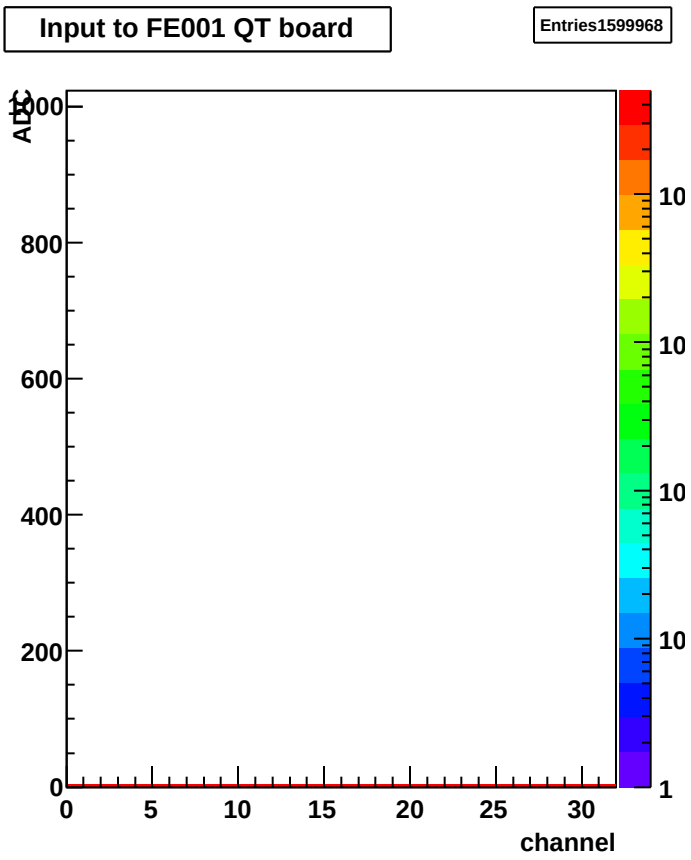

Input to FPD L2 DSM
Entries 149997
CL bits
49999
21
61
831
277
3
49637
49168
SMALL
LARGE-S
LARGE-N

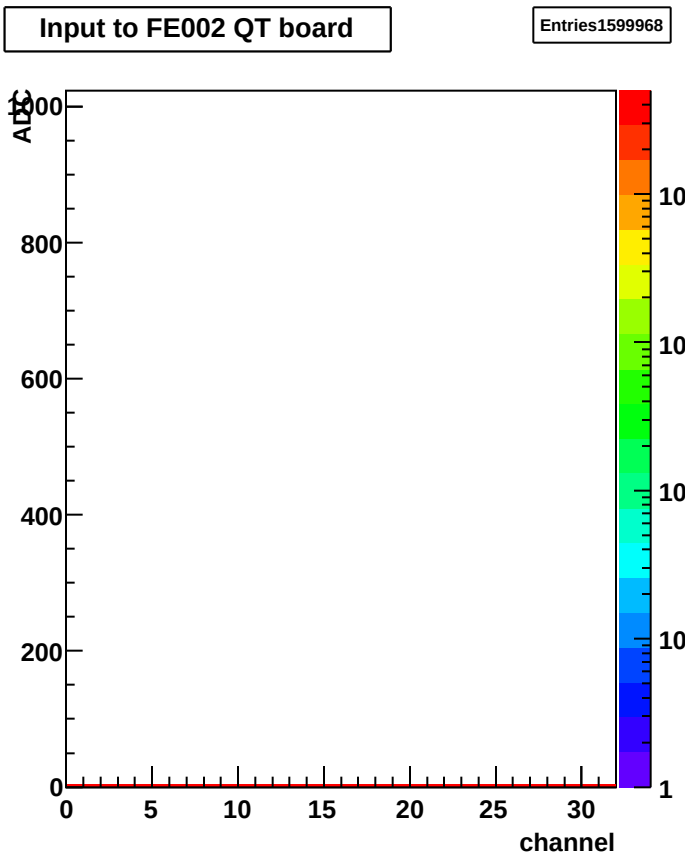

Input to FPD L2 DSM
Entries 2615
CL bits - simulated
829
2
20
1
2
277
1408
61
3
11
1
SMALL
LARGE-S
LARGE-N

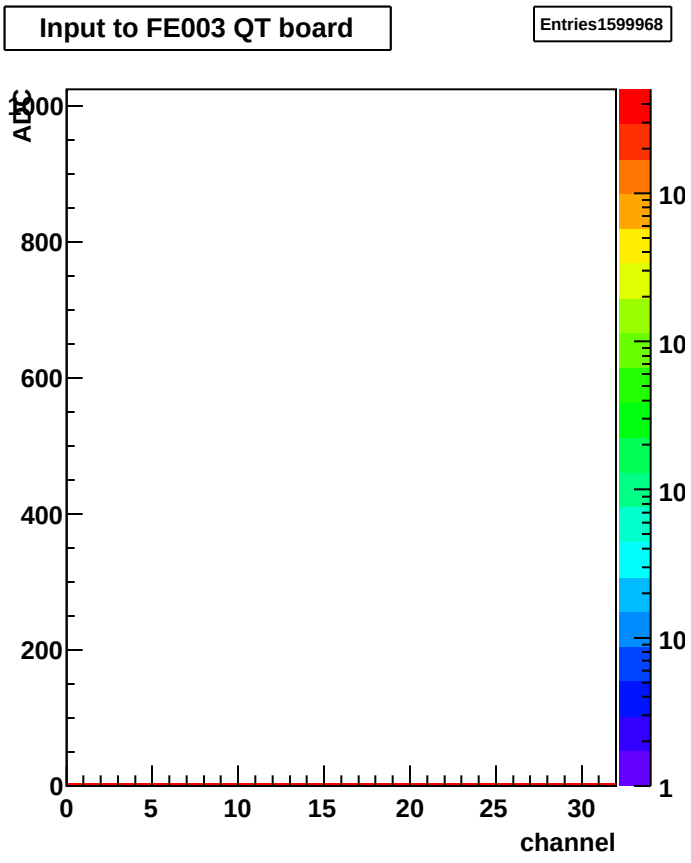

Input to FPD L2 DSM
Entries 149997
HT bits
39
2
19
16
14
200
49985
49941
49781
SMALL
LARGE-S
LARGE-N

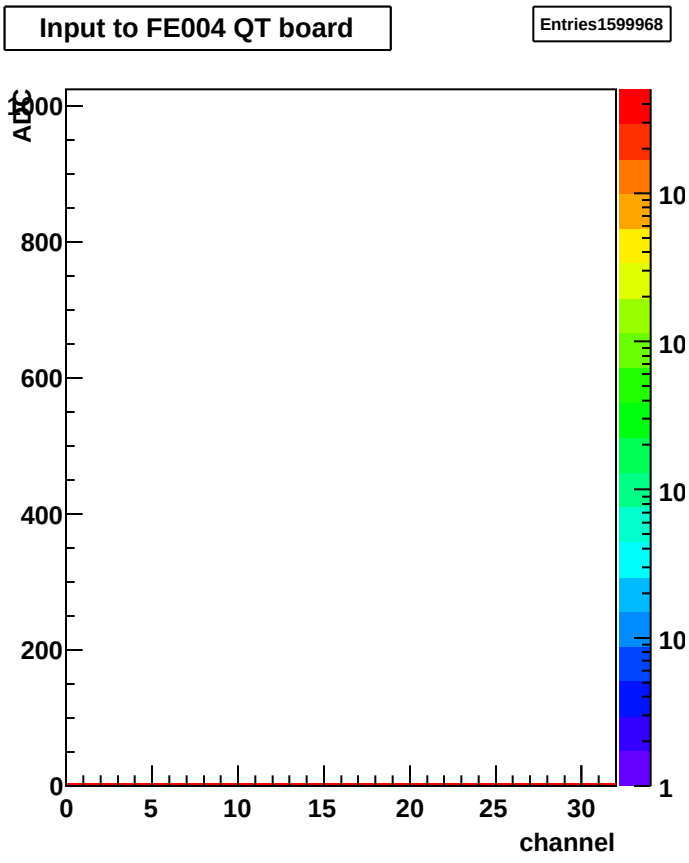

Input to FPD L2 DSM
Entries 256
HT bits - simulated
2
16
14
200
9
15
SMALL
LARGE-S
LARGE-N

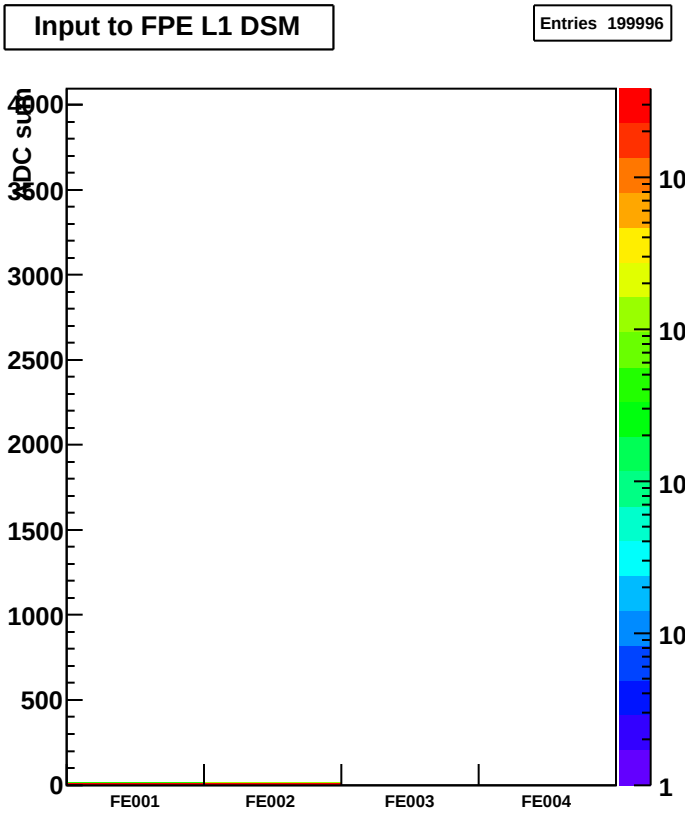

Input to FPD L2 DSM
Entries 199996
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

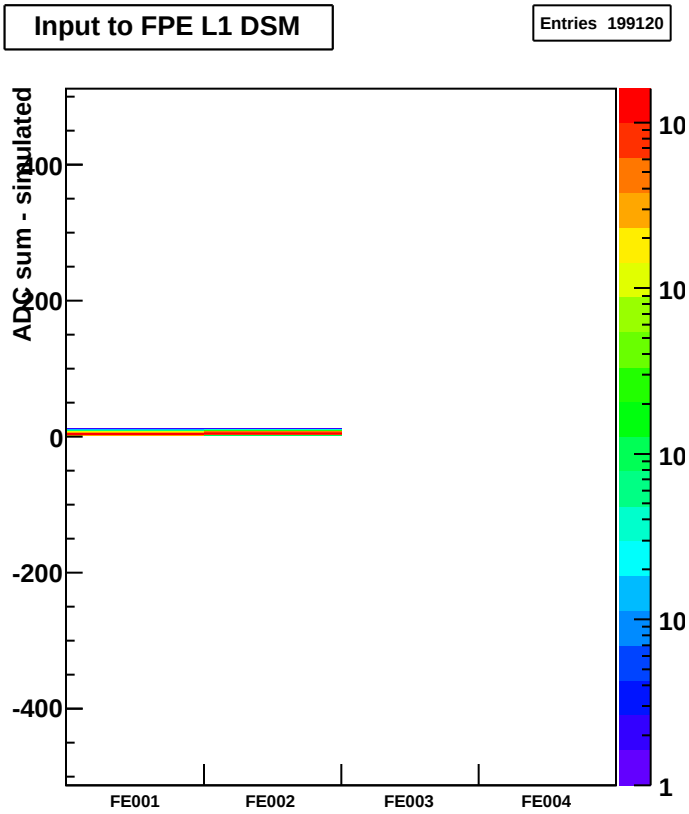

Input to FE001 QT board
Entries1599968
ADC
channel


Input to FE002 QT board
Entries1599968
ADC
channel

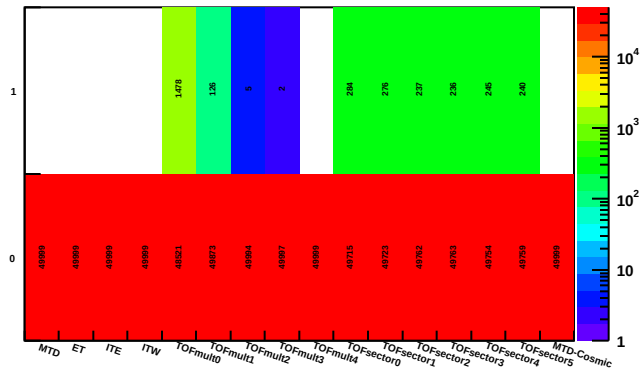

Input to FE003 QT board
Entries1599968
ADC
channel


Input to FE004 QT board
Entries1599968
ADC
channel

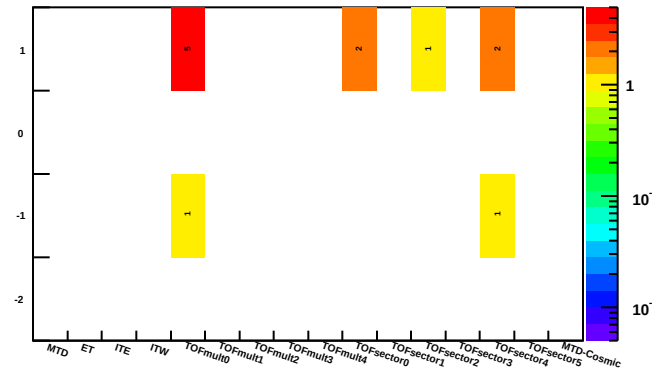

Input to FPE L1 DSM
Entries 199996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 199120
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

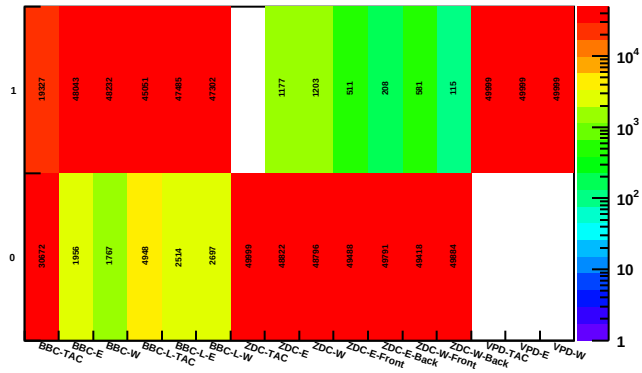
TF201 0-15 (ch0)



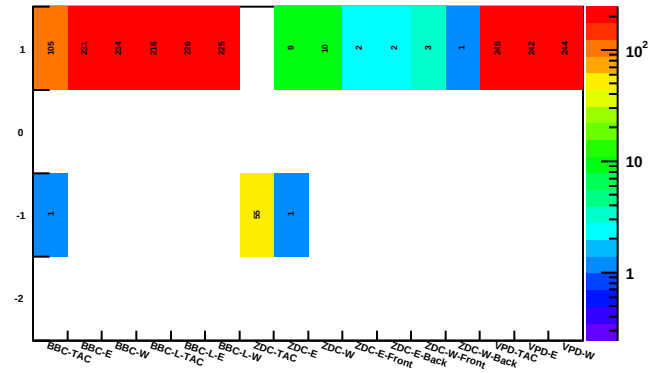
TF201 0-15 (ch0)



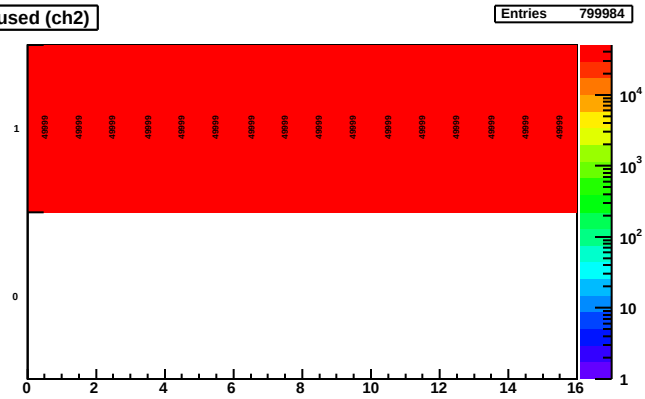
VT201 0-15 (ch1)

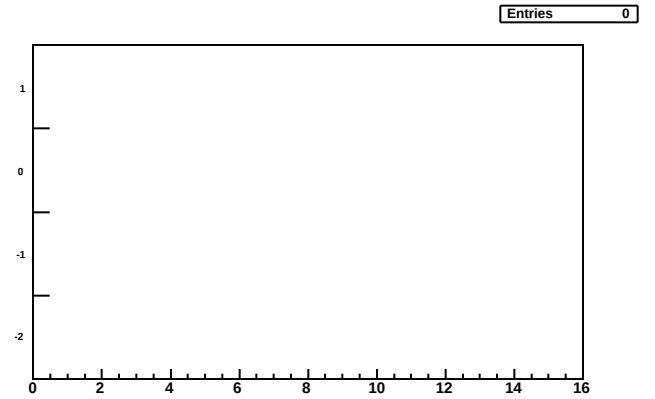


VT201 0-15 (ch1)

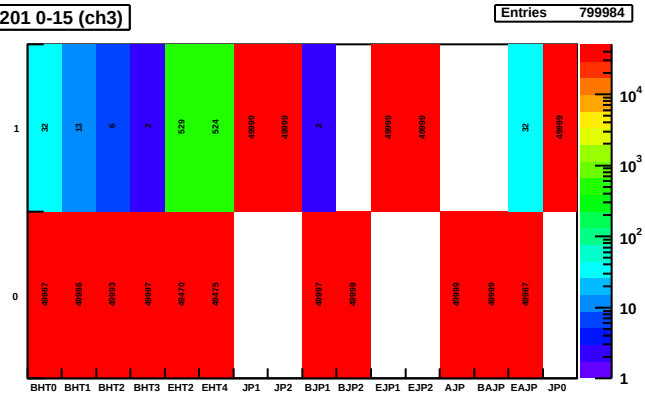


Unused (ch2)

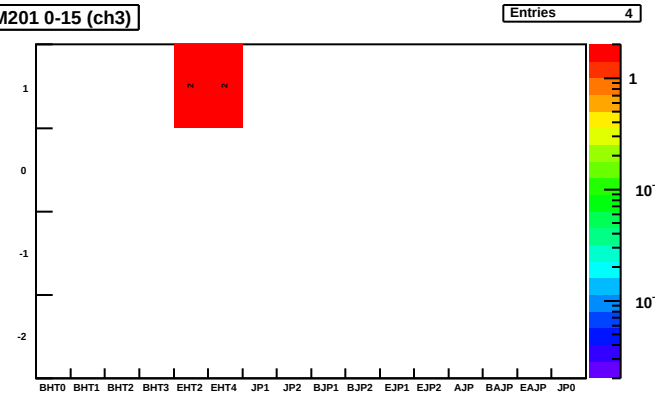




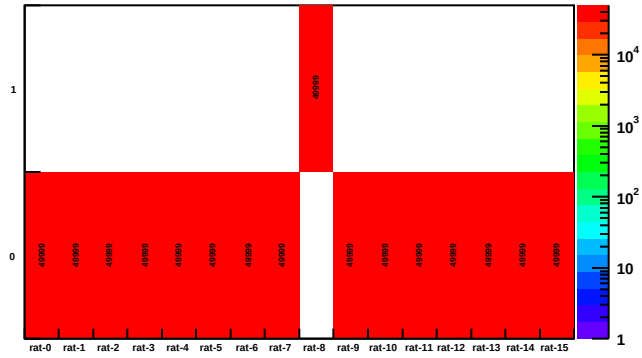
EM201 0-15 (ch3)

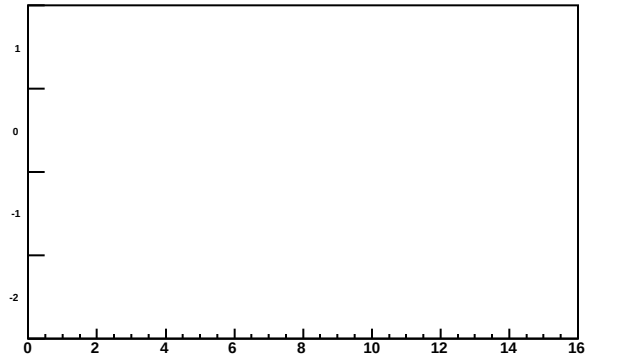


EM201 0-15 (ch3)

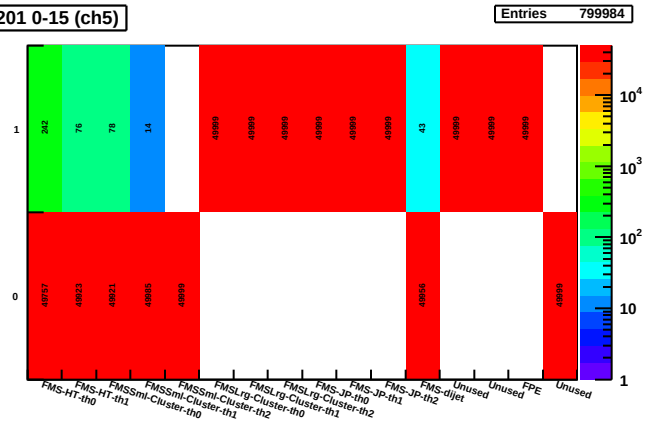


RAT board (ch4)

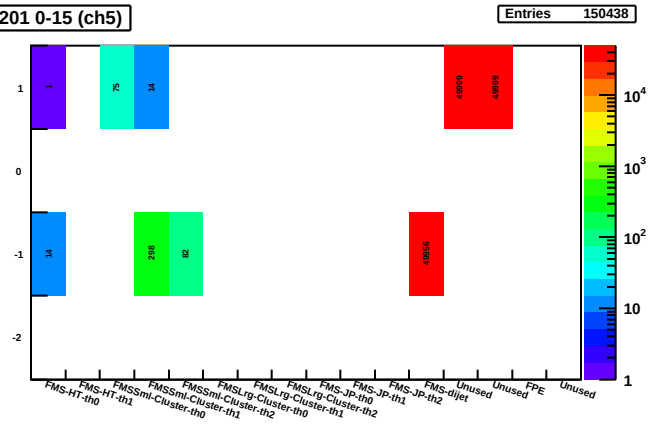




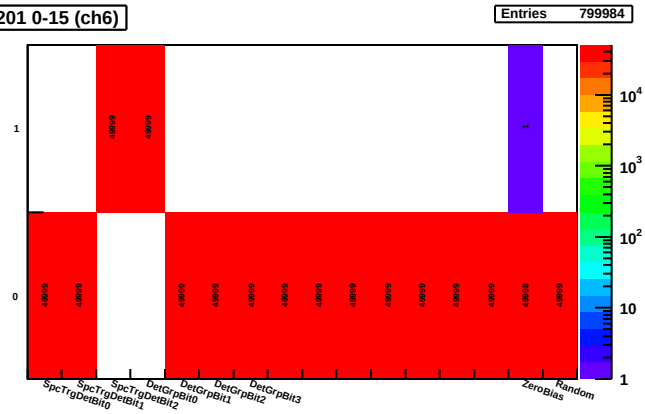
FP201 0-15 (ch5)

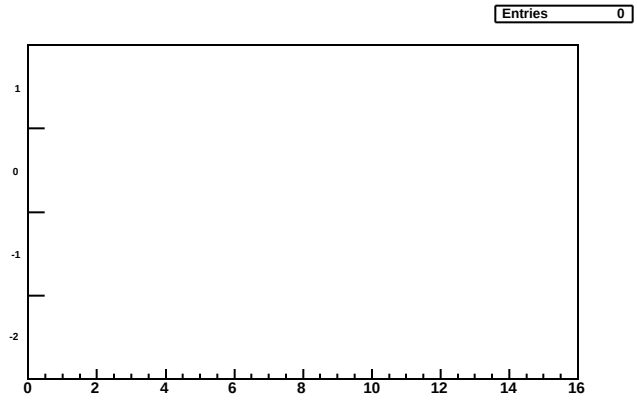
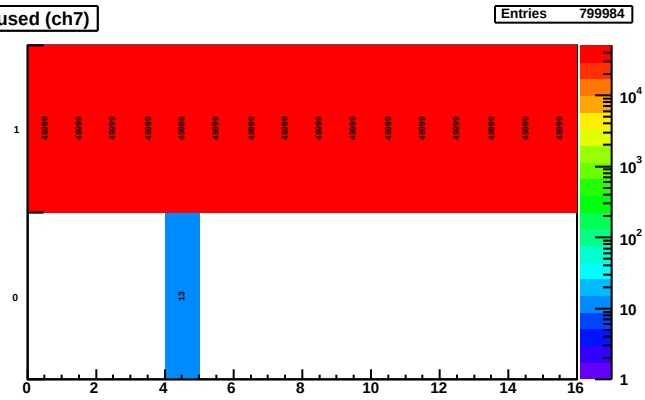
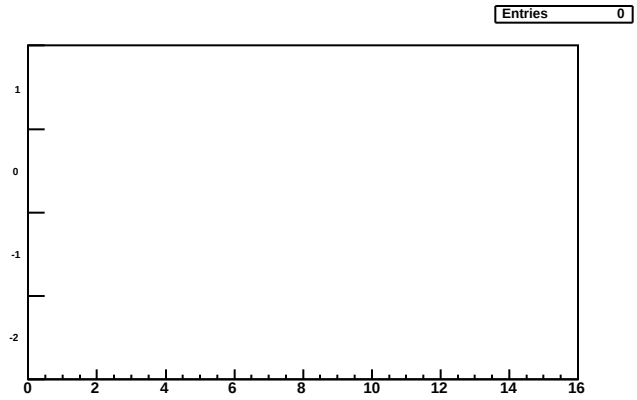


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)