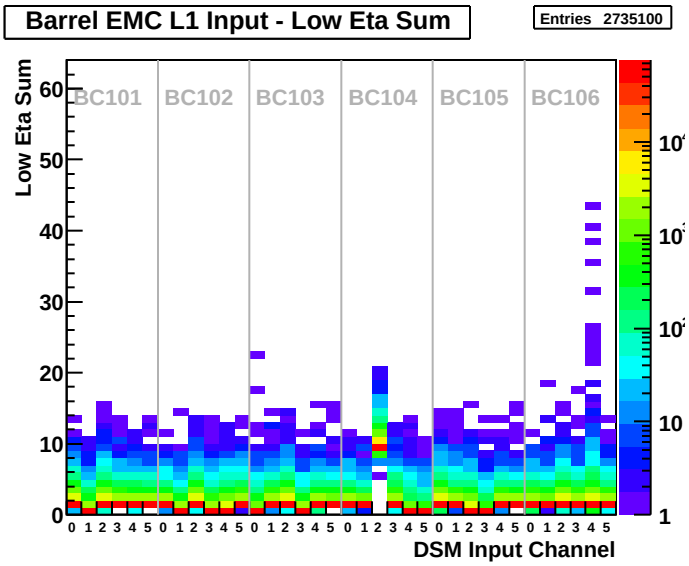
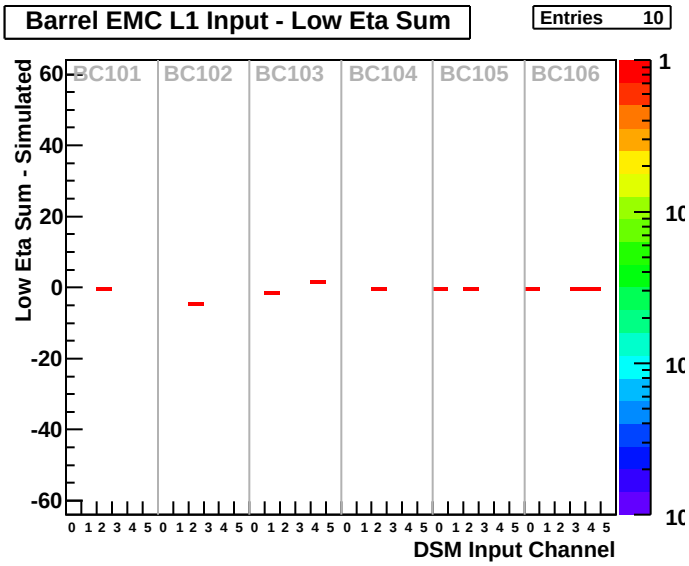
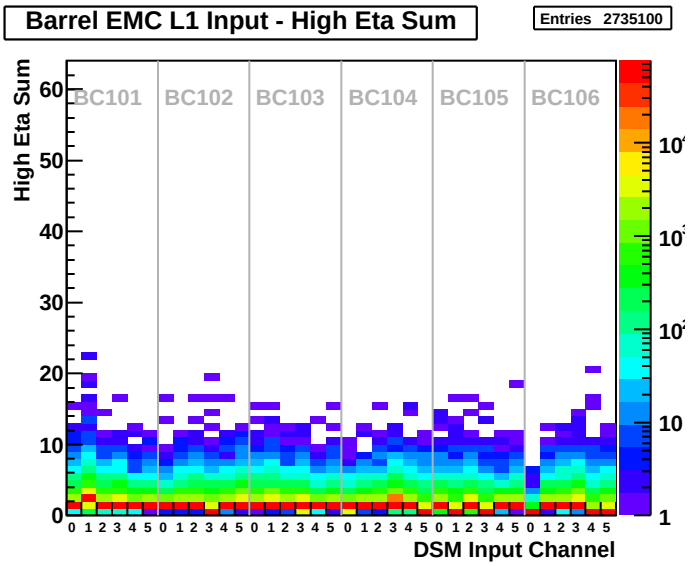
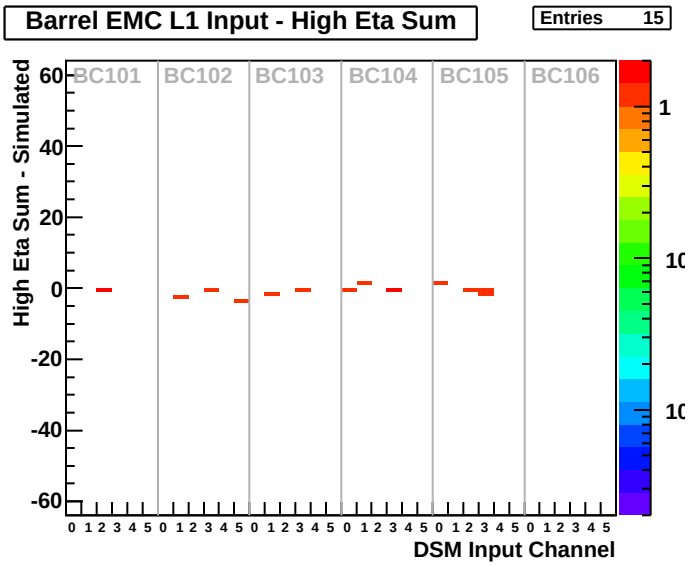
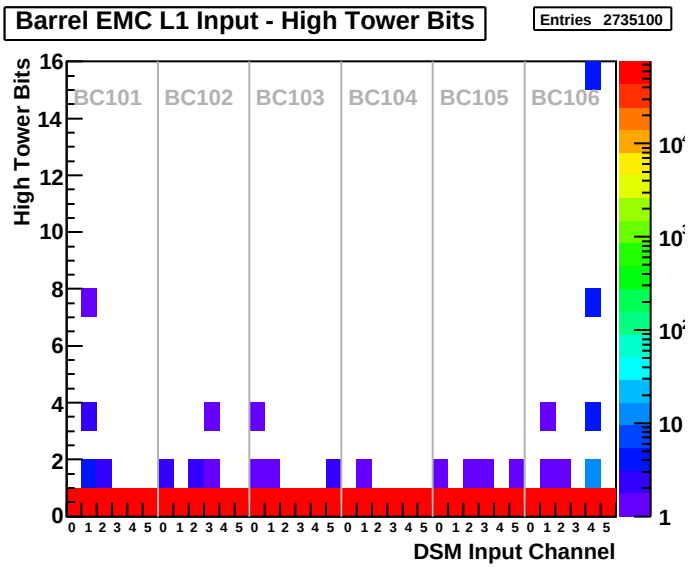
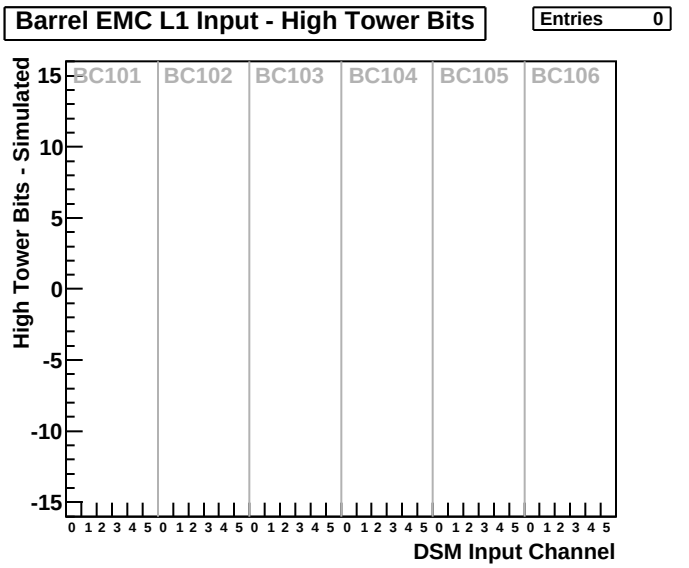

Barrel EMC L1 Input - Low Eta Sum
Entries 2735100
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

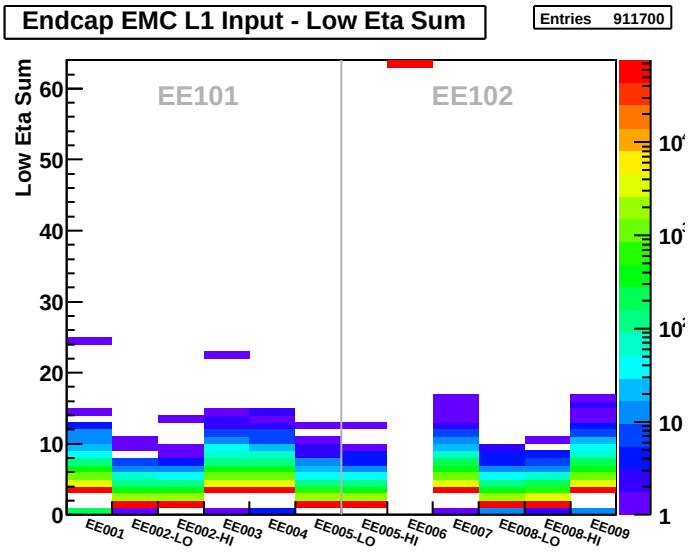

Barrel EMC L1 Input - Low Eta Sum
Entries 10
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

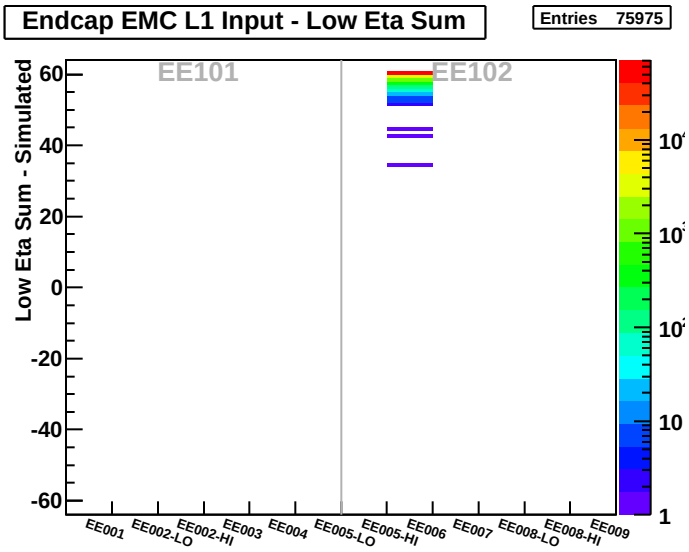

Barrel EMC L1 Input - High Eta Sum
Entries 2735100
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

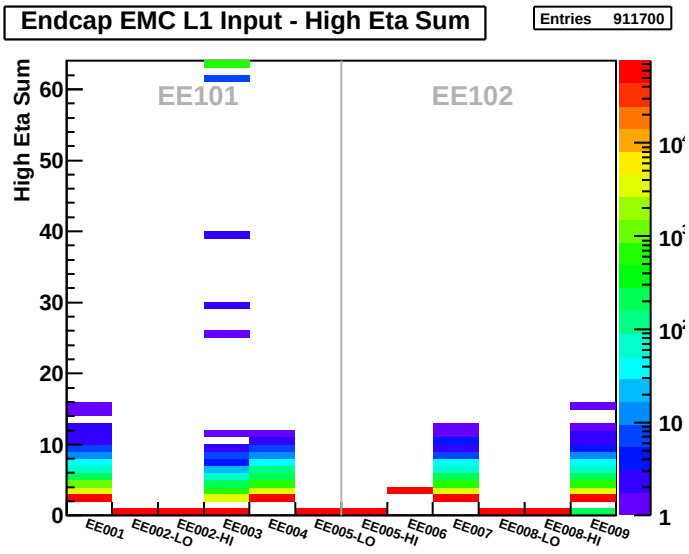

Barrel EMC L1 Input - High Eta Sum
Entries 15
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

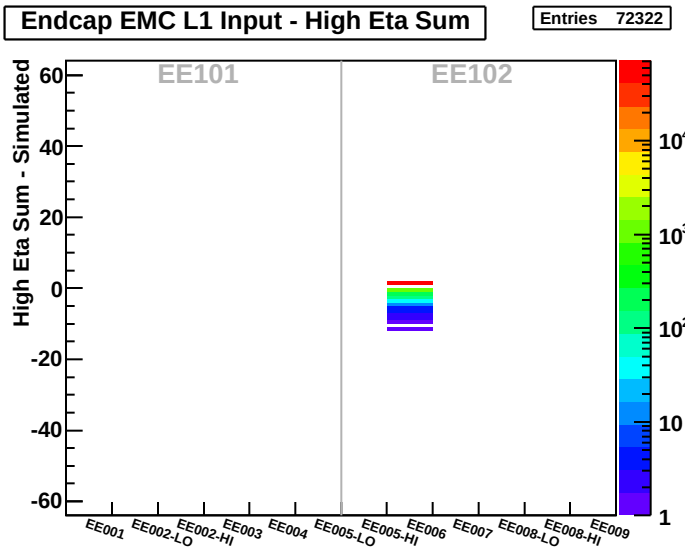

Barrel EMC L1 Input - High Tower Bits
Entries 2735100
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

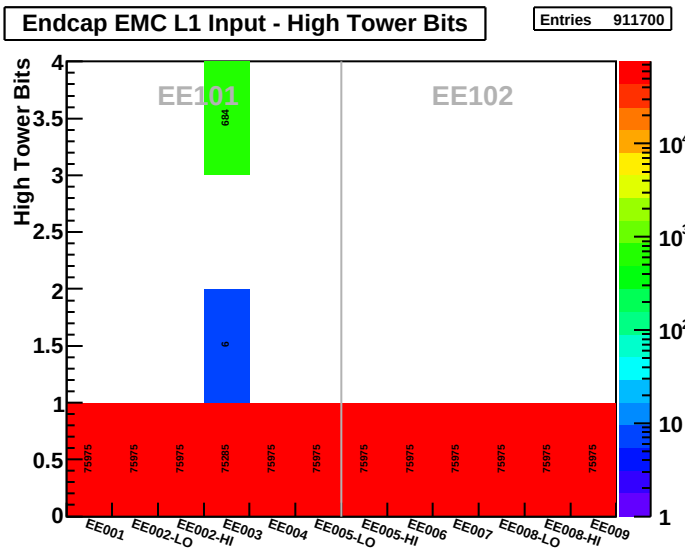

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

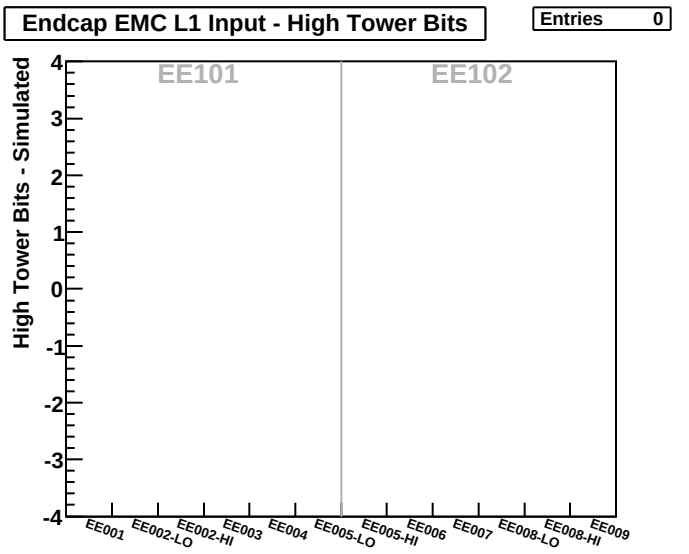

Endcap EMC L1 Input - Low Eta Sum
Entries 911700
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

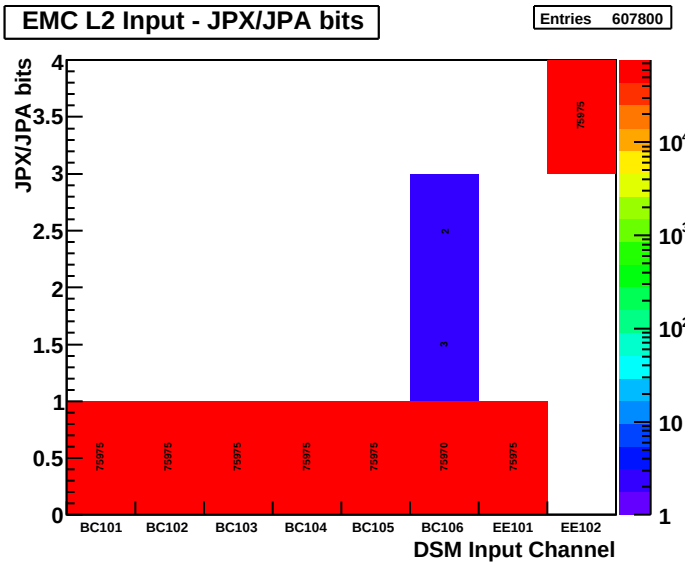

Endcap EMC L1 Input - Low Eta Sum
Entries 75975
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

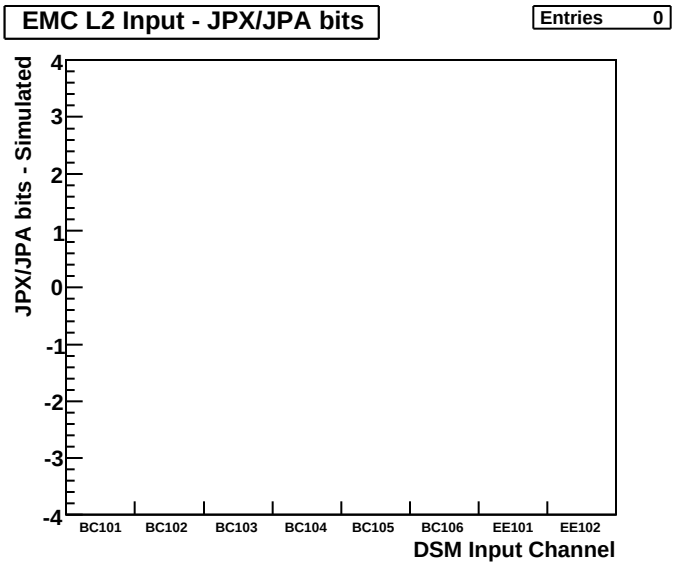

Endcap EMC L1 Input - High Eta Sum
Entries 911700
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

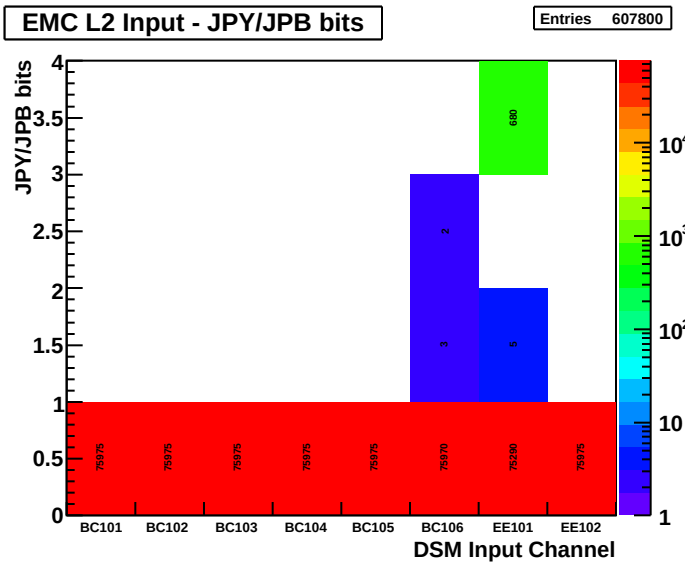

Endcap EMC L1 Input - High Eta Sum
Entries 72322
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

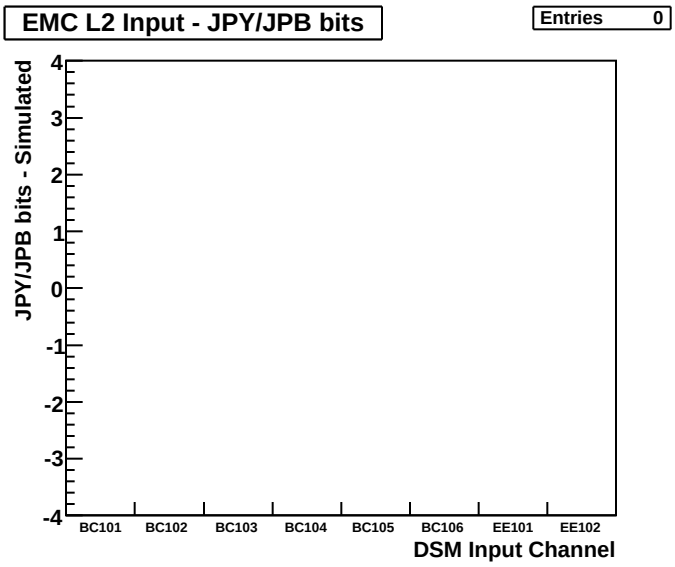

Endcap EMC L1 Input - High Tower Bits
Entries 911700
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

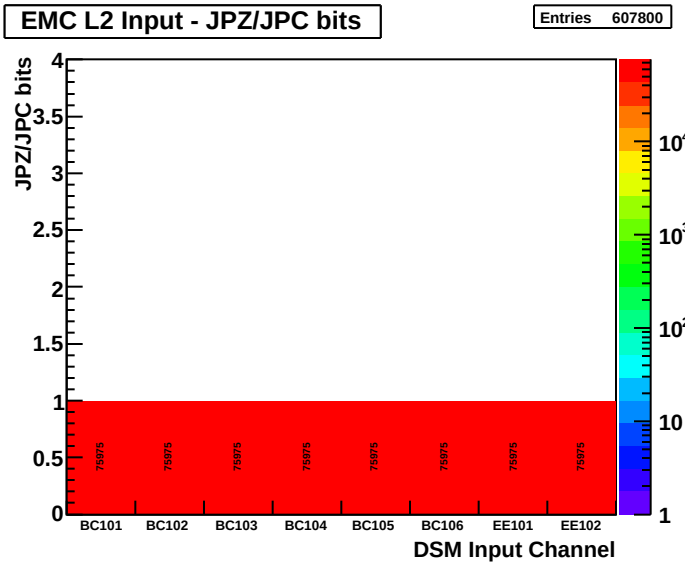

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

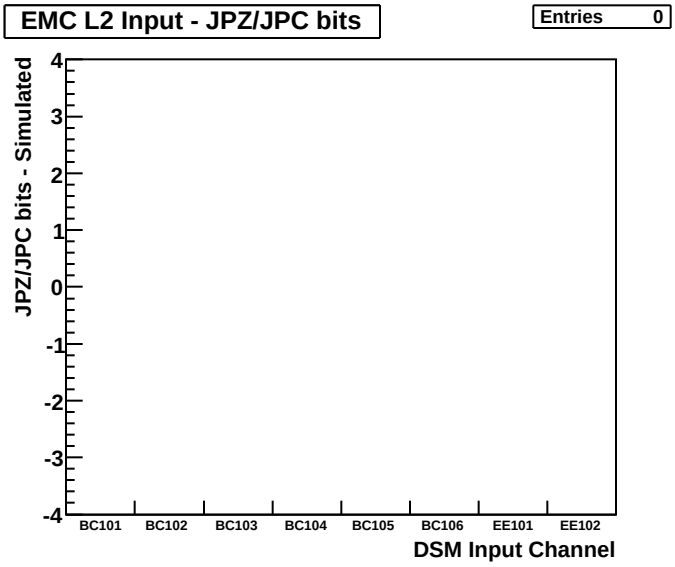

EMC L2 Input - JPX/JPA bits
Entries 607800
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

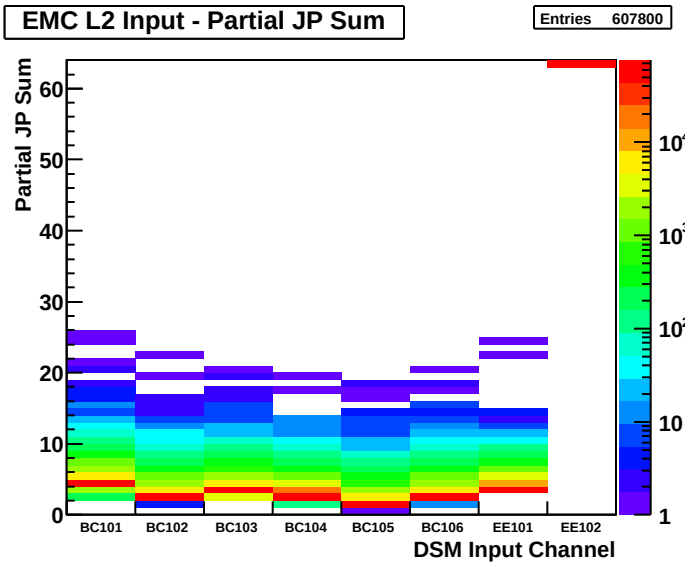

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

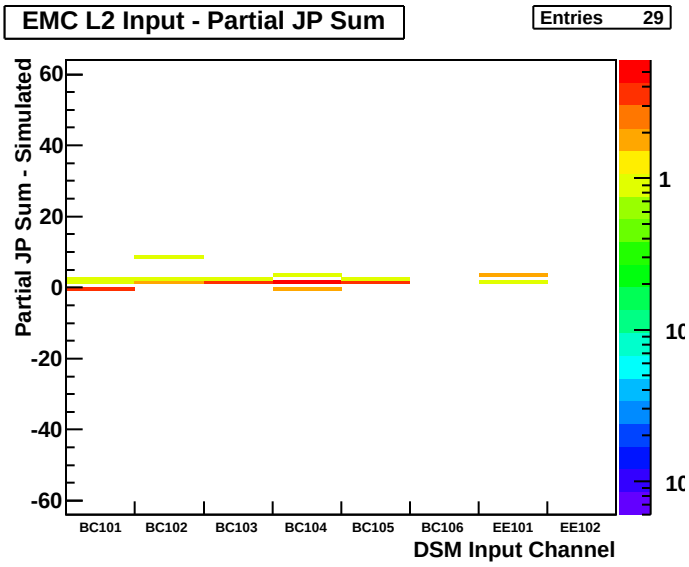

EMC L2 Input - JPY/JPB bits
Entries 607800
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

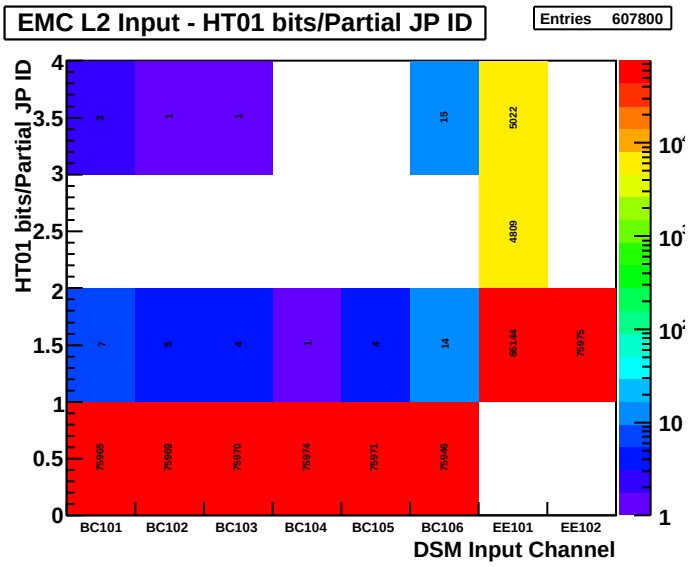

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

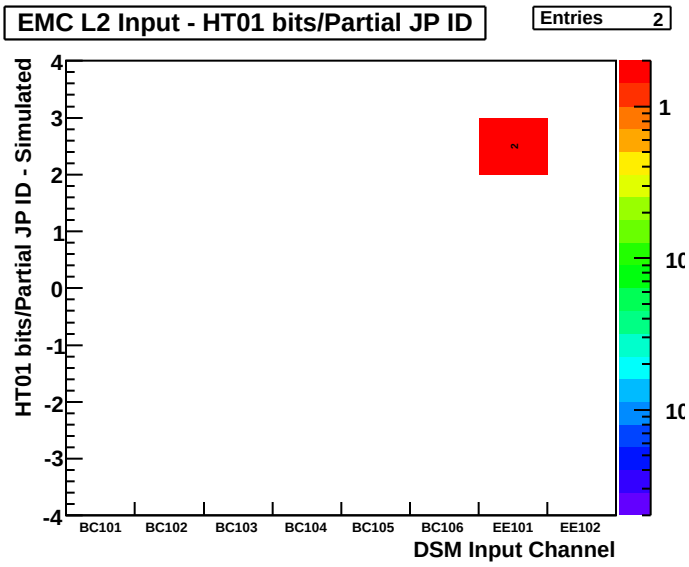

EMC L2 Input - JPZ/JPC bits
Entries 607800
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

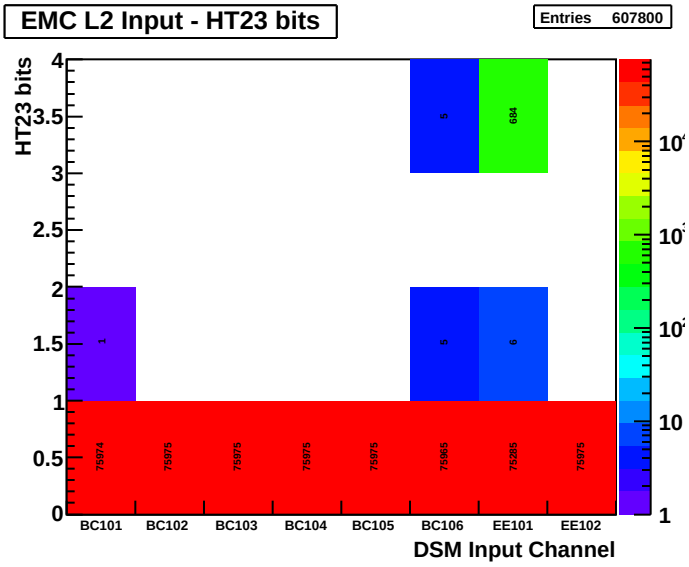

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

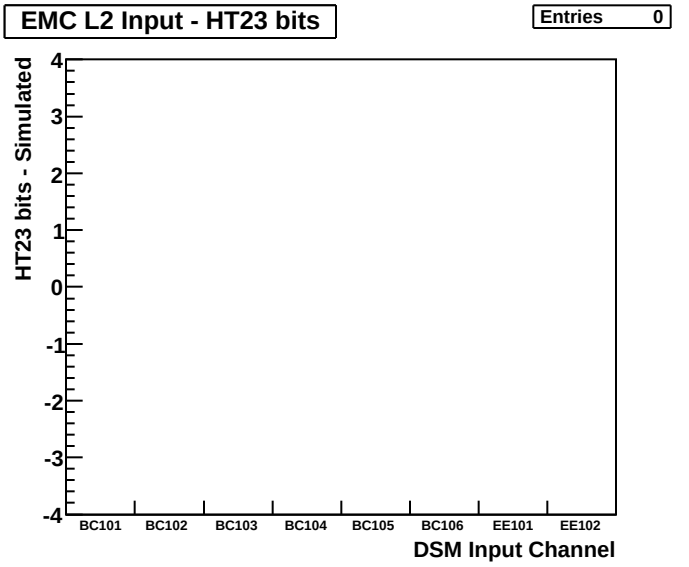

EMC L2 Input - Partial JP Sum
Entries 607800
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

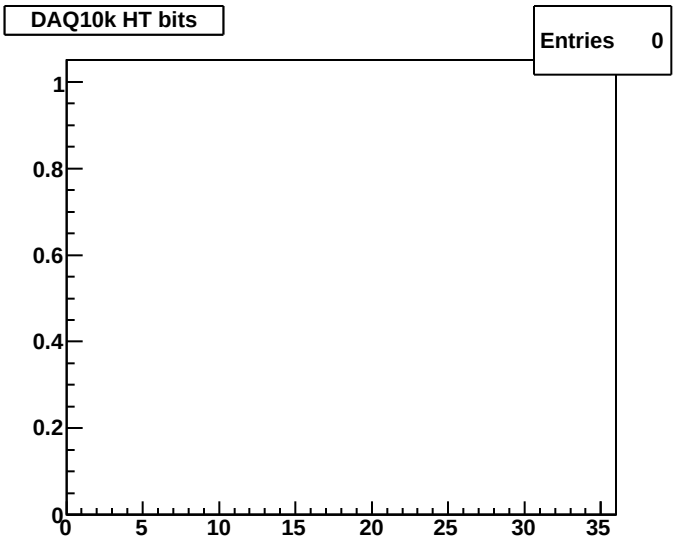

EMC L2 Input - Partial JP Sum
Entries 29
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

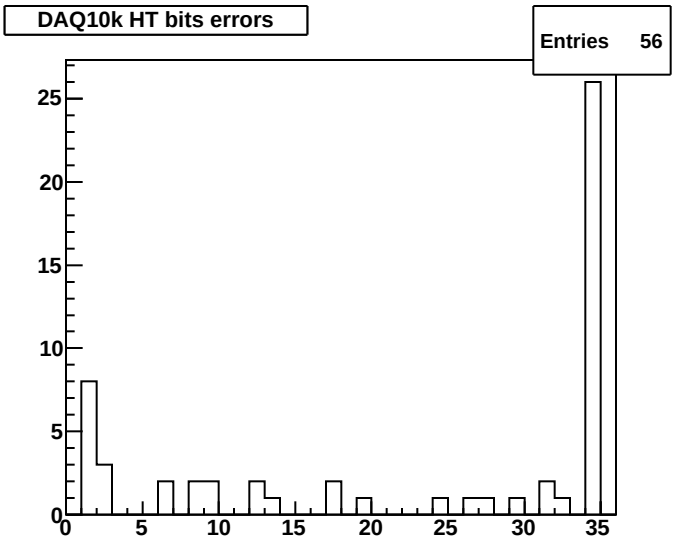

EMC L2 Input - HT01 bits/Partial JP ID
Entries 607800
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

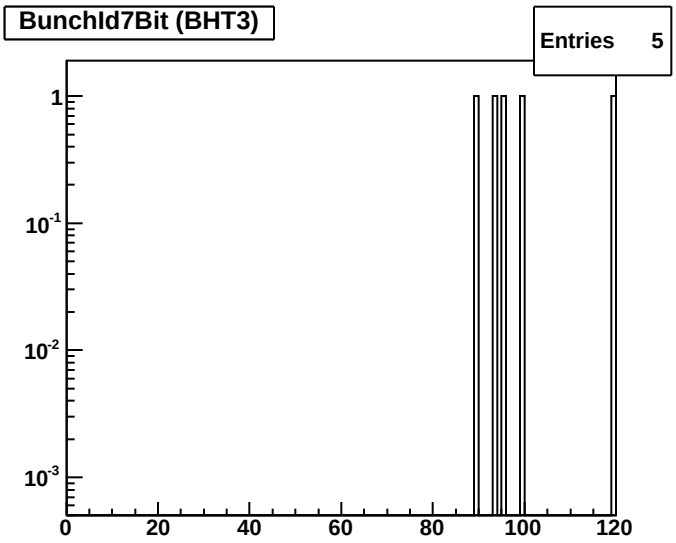

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

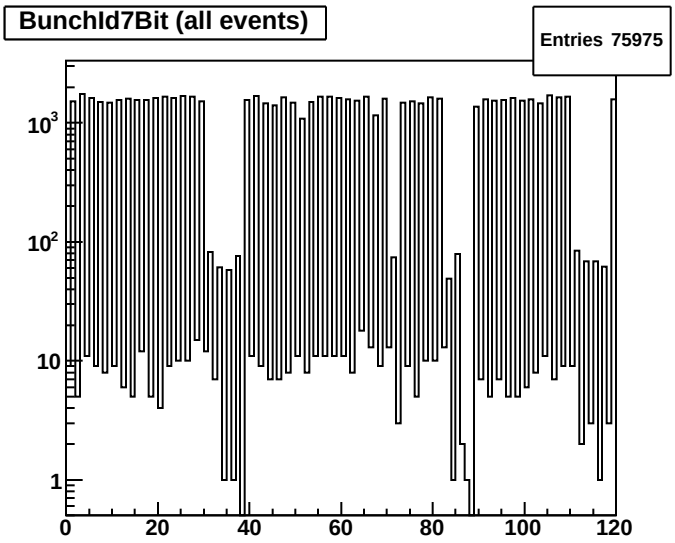

EMC L2 Input - HT23 bits
Entries 607800
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

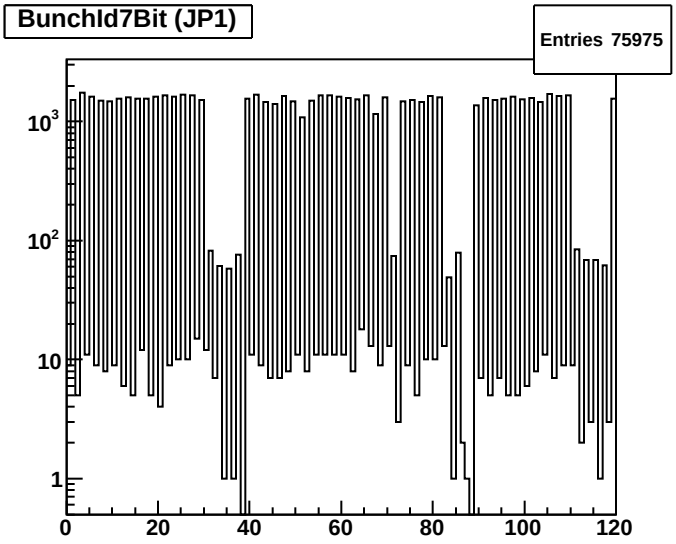

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

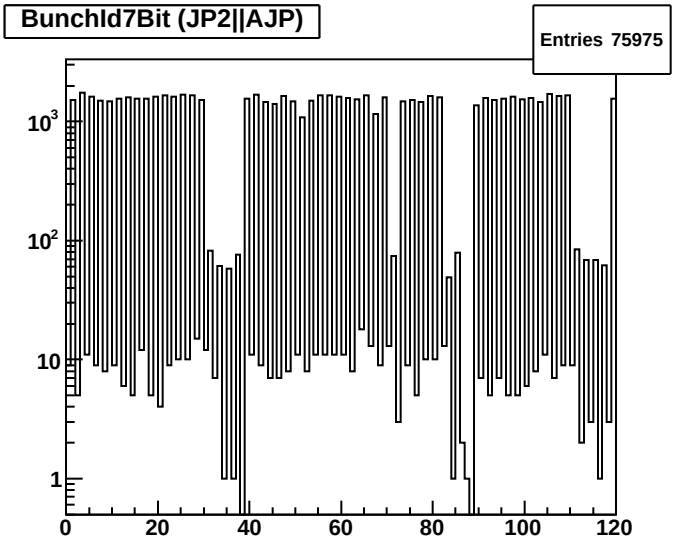

DAQ10k HT bits
Entries 0

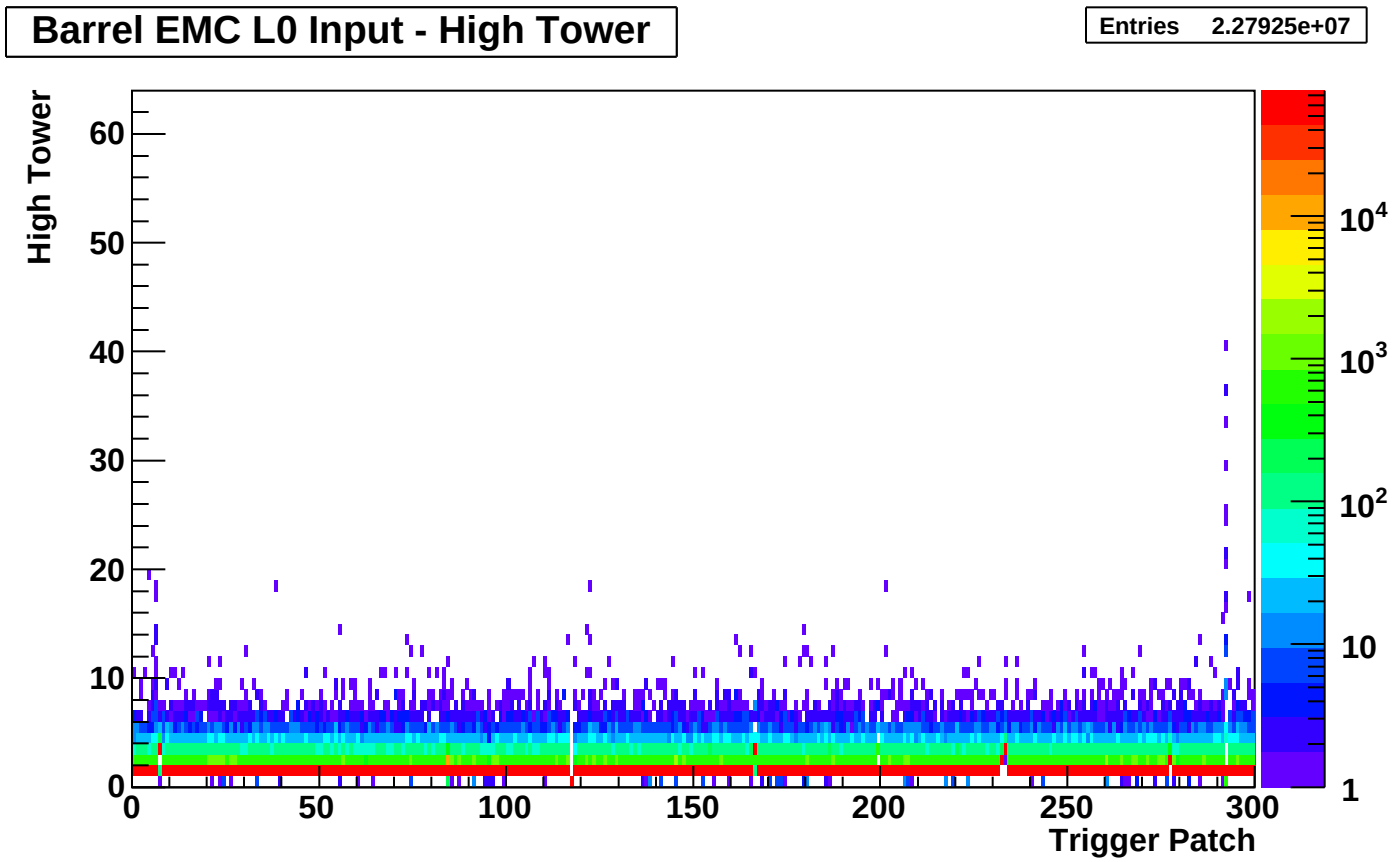

DAQ10k HT bits errors
Entries 56

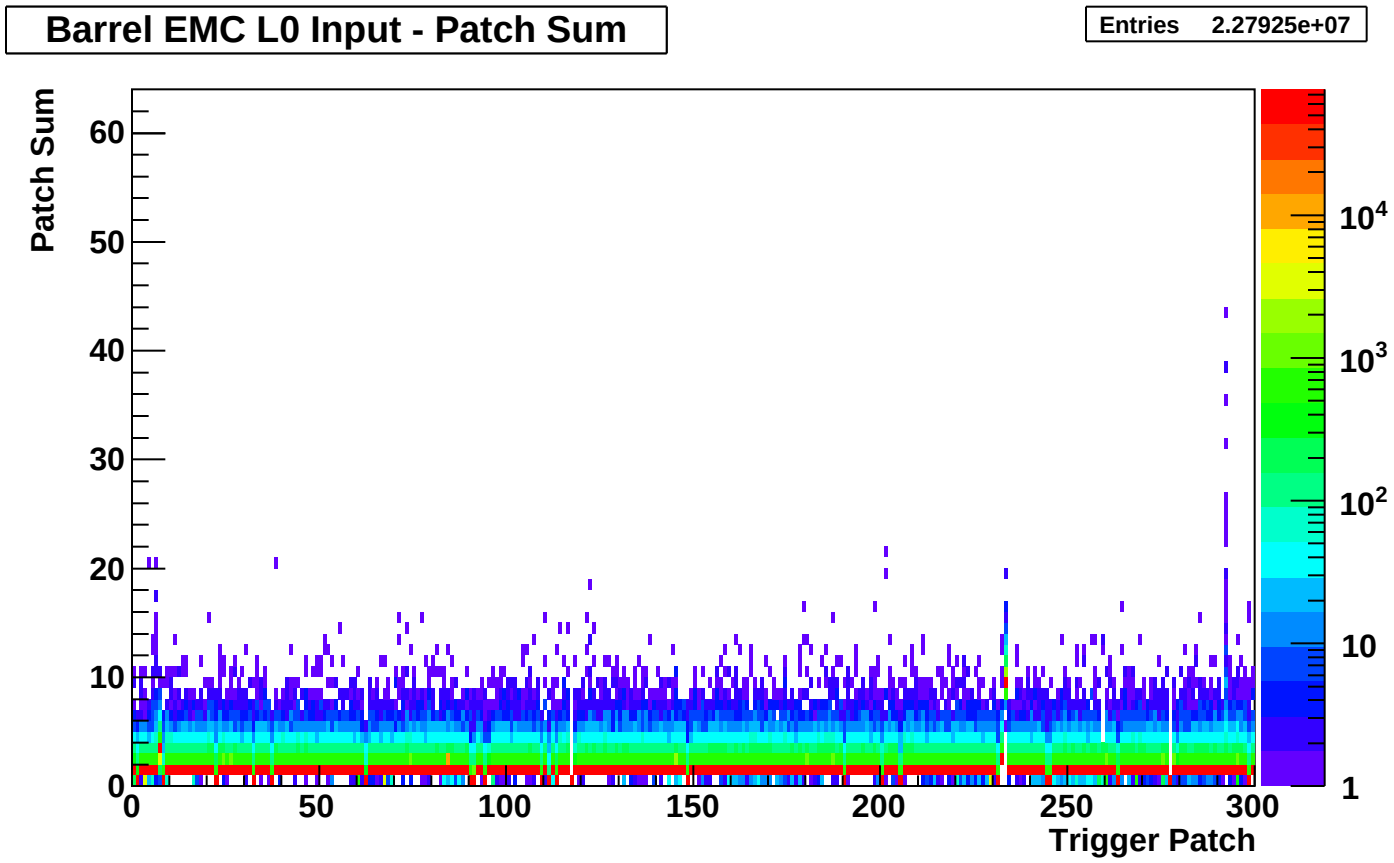

BunchId7Bit (BHT3)
Entries 5

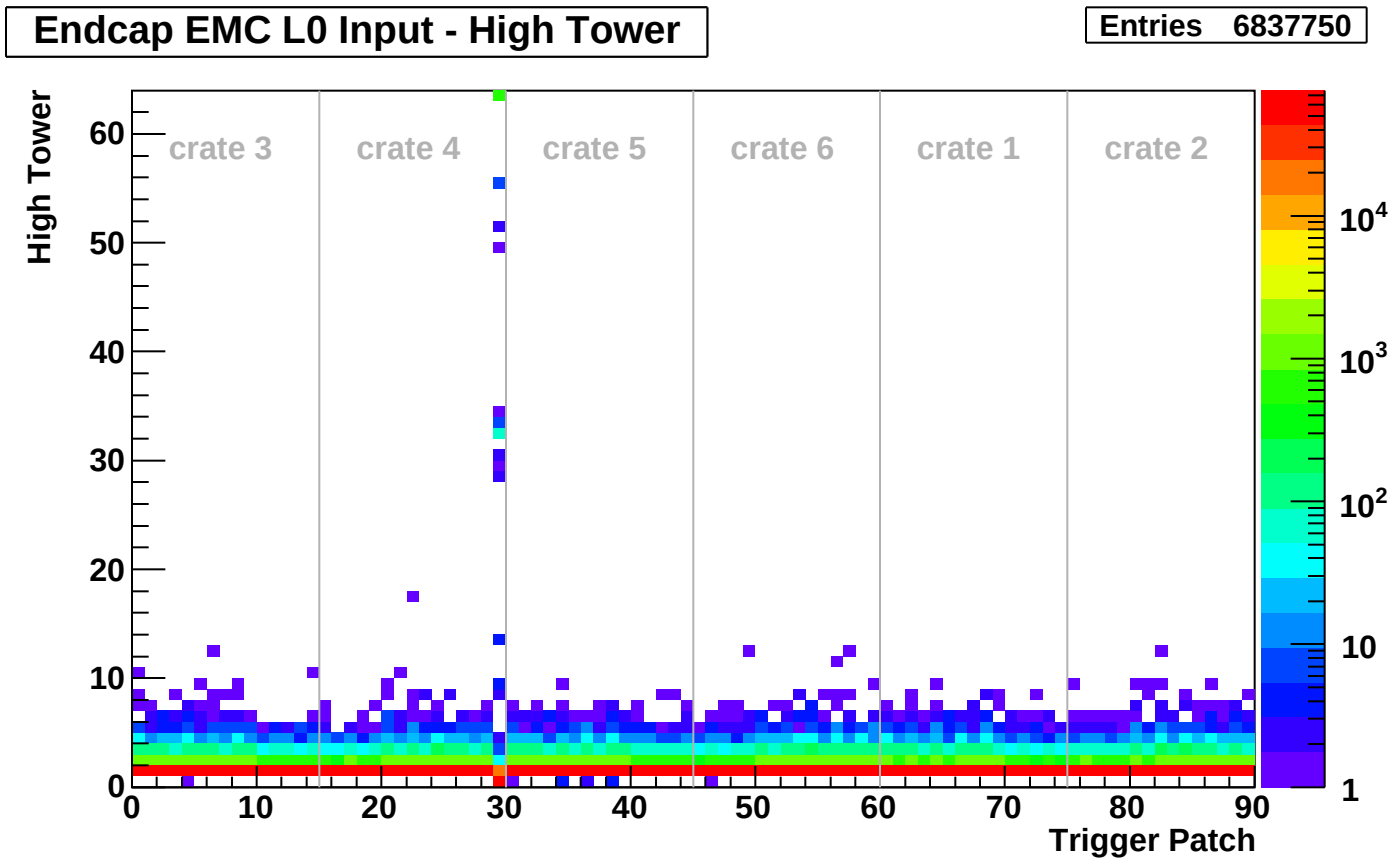

BunchId7Bit (all events)
Entries 75975

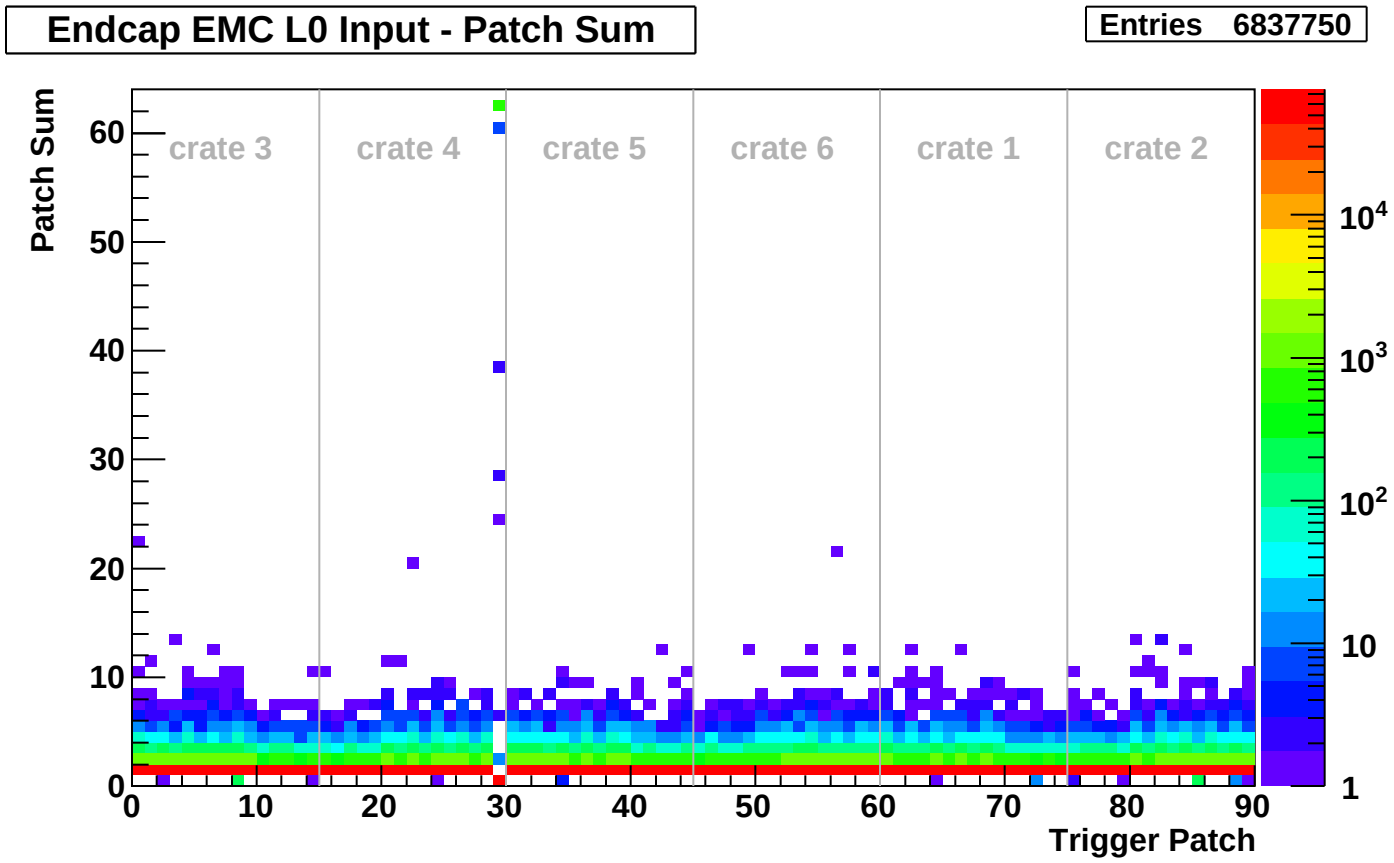

BunchId7Bit (JP1)
Entries 75975

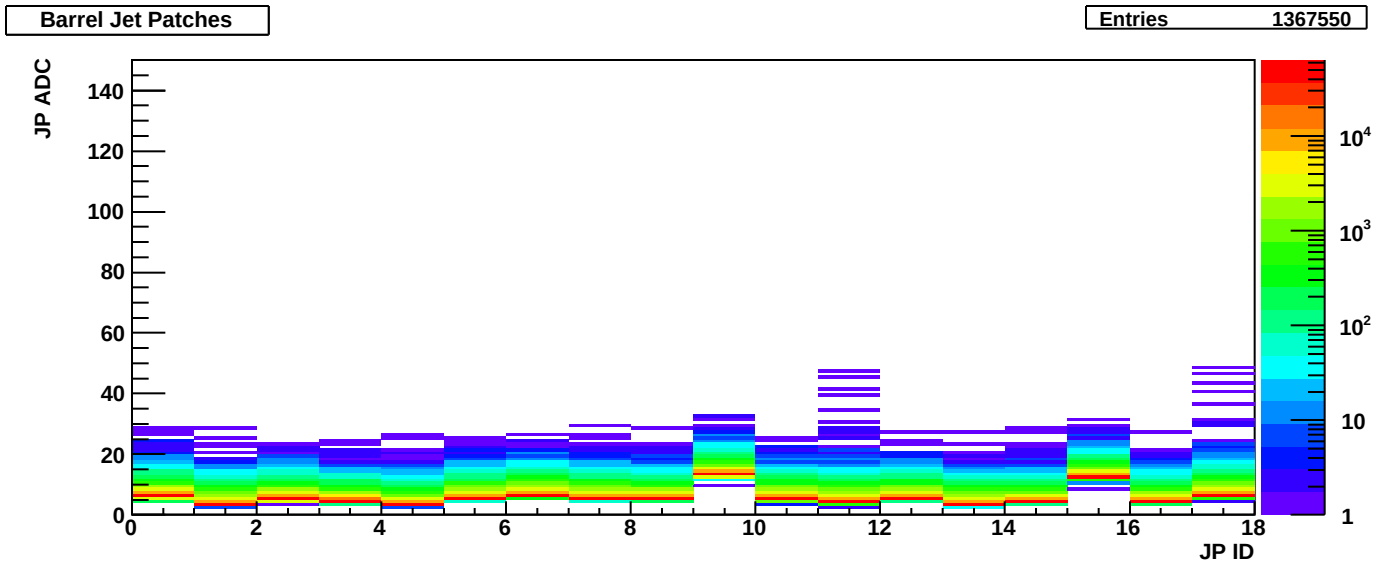

BunchId7Bit (JP2||AJP)
Entries 75975

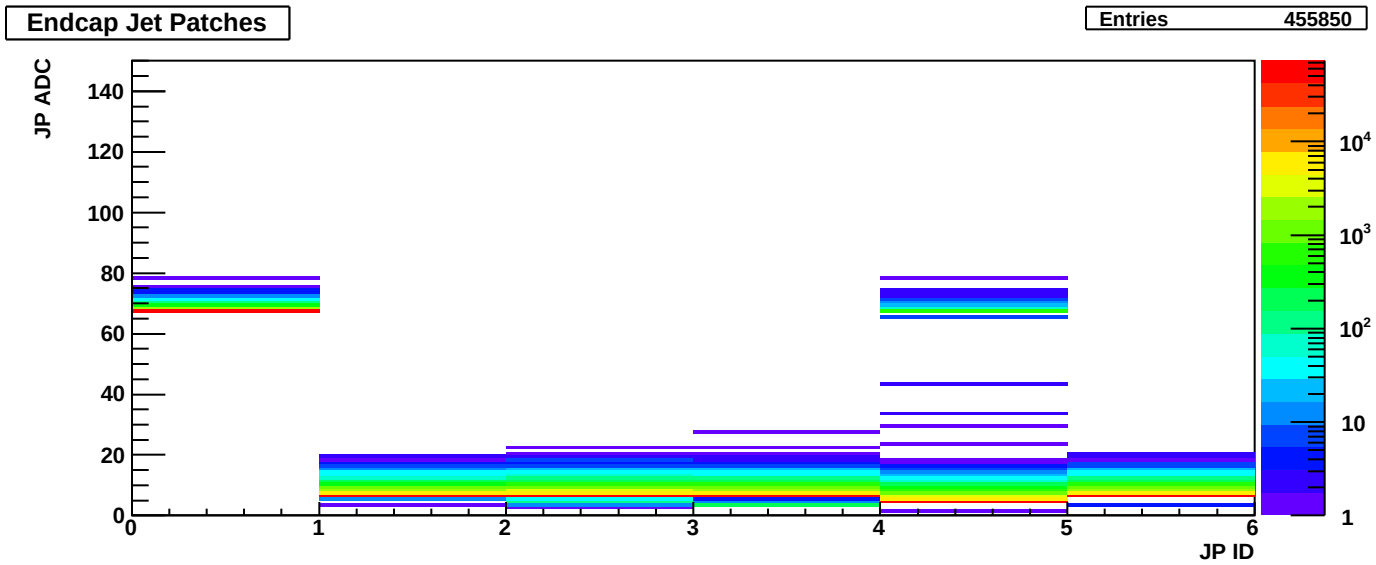

Barrel EMC L0 Input - High Tower
Entries 2.27925e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

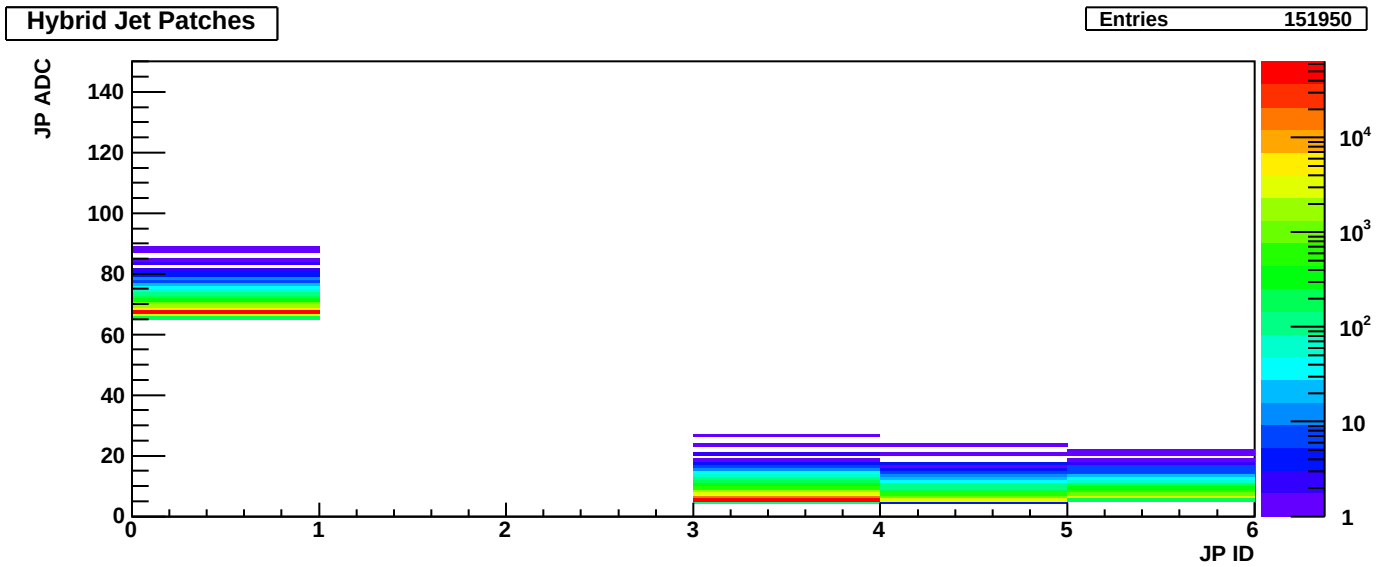

Barrel EMC L0 Input - Patch Sum
Entries 2.27925e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

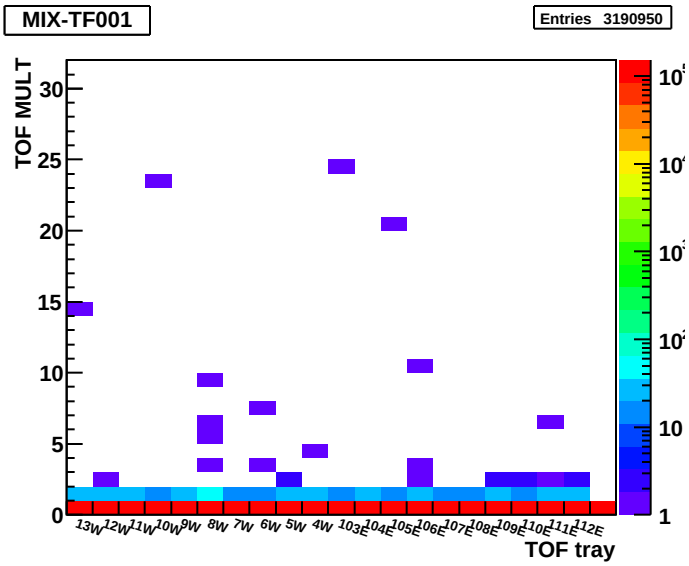

Endcap EMC L0 Input - High Tower
Entries 6837750
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

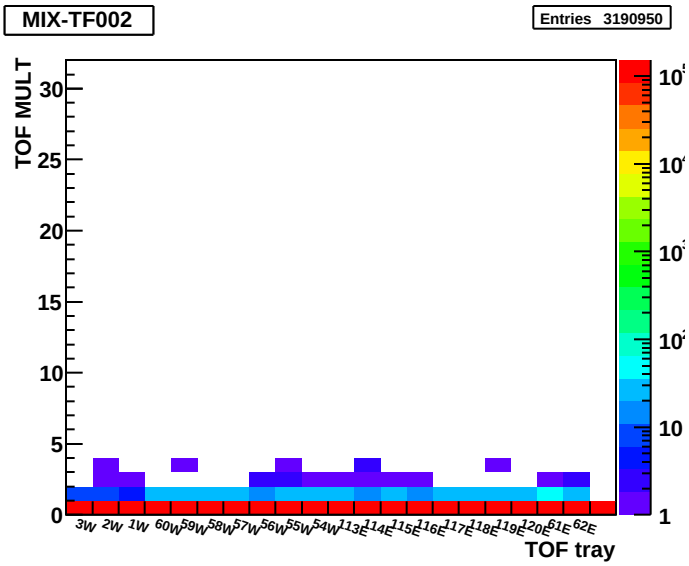

Endcap EMC L0 Input - Patch Sum
Entries 6837750
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

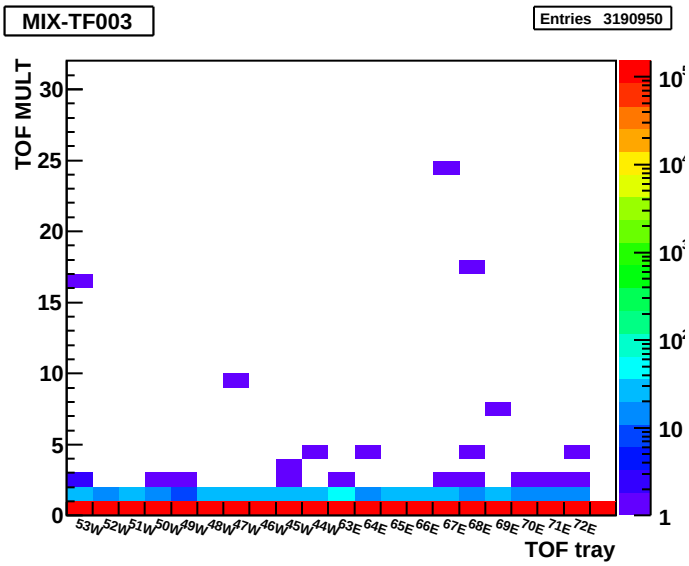

Barrel Jet Patches
Entries 1367550
JP ADC
JP ID

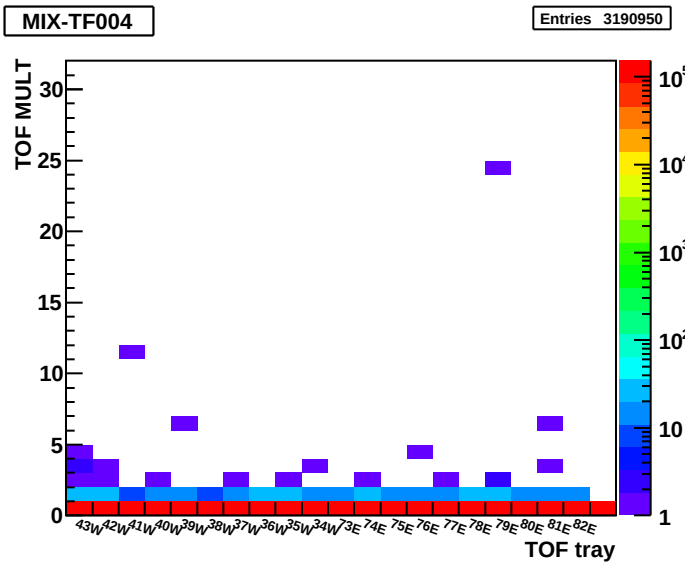

Endcap Jet Patches
Entries 455850
JP ADC
JP ID

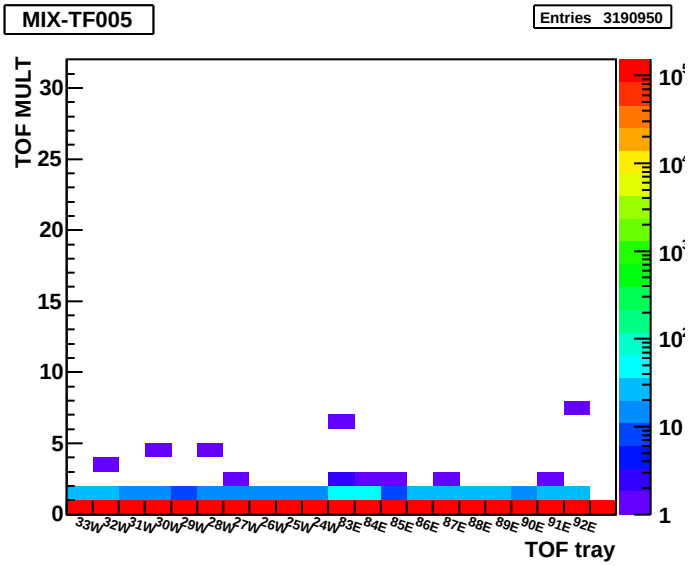

Hybrid Jet Patches
Entries 151950
JP ADC
JP ID

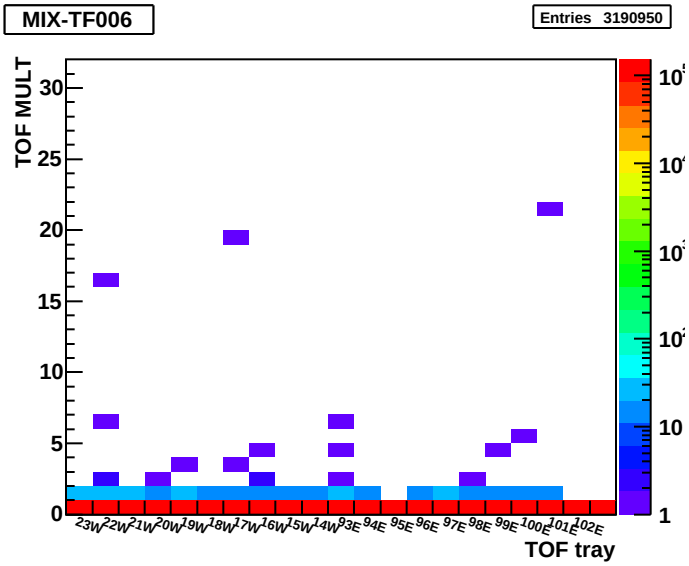

MIX-TF001
Entries 3190950
TOF MULT
TOF tray

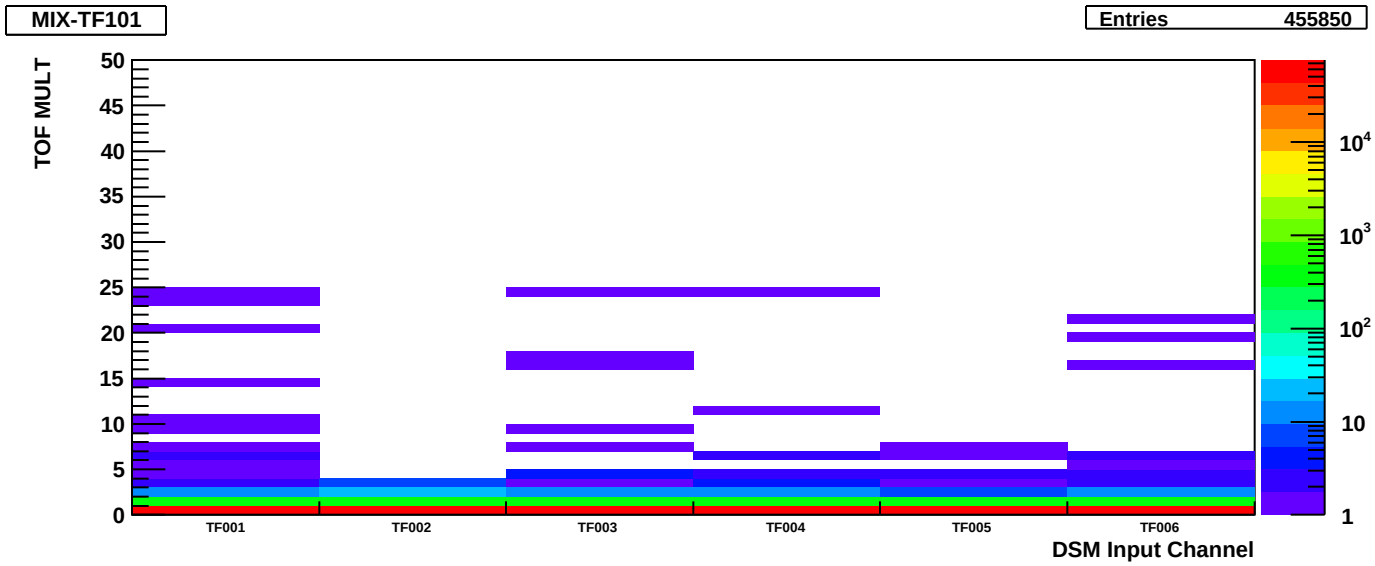

MIX-TF002
Entries 3190950
TOF MULT
TOF tray

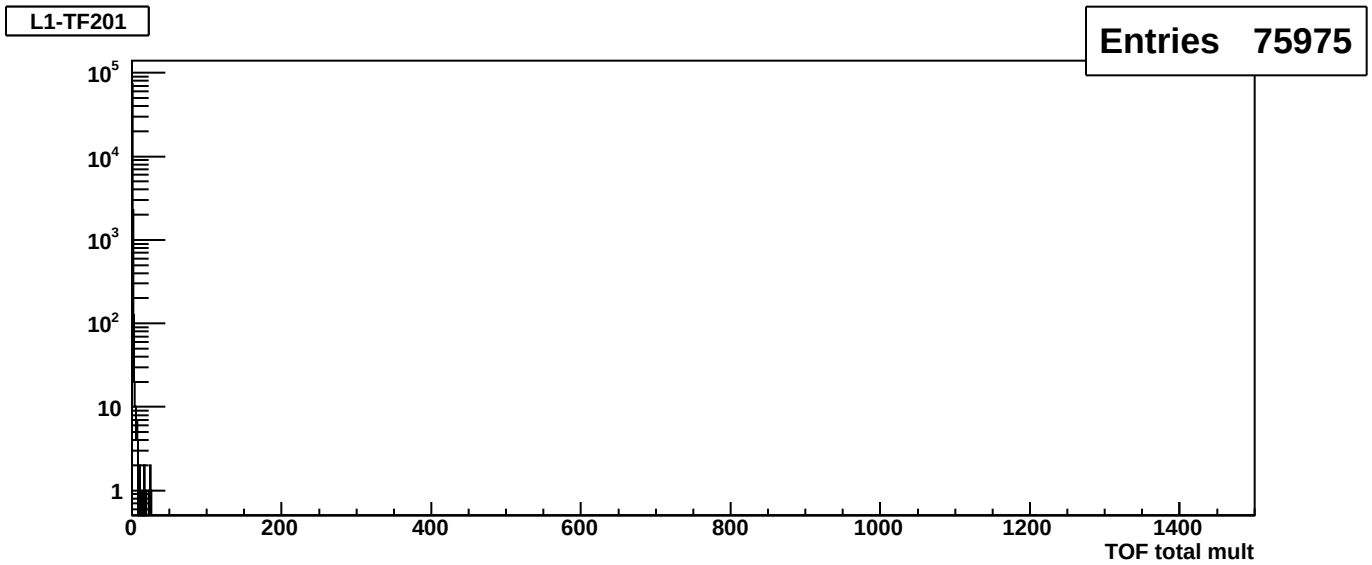

MIX-TF003
Entries 3190950
TOF MULT
TOF tray

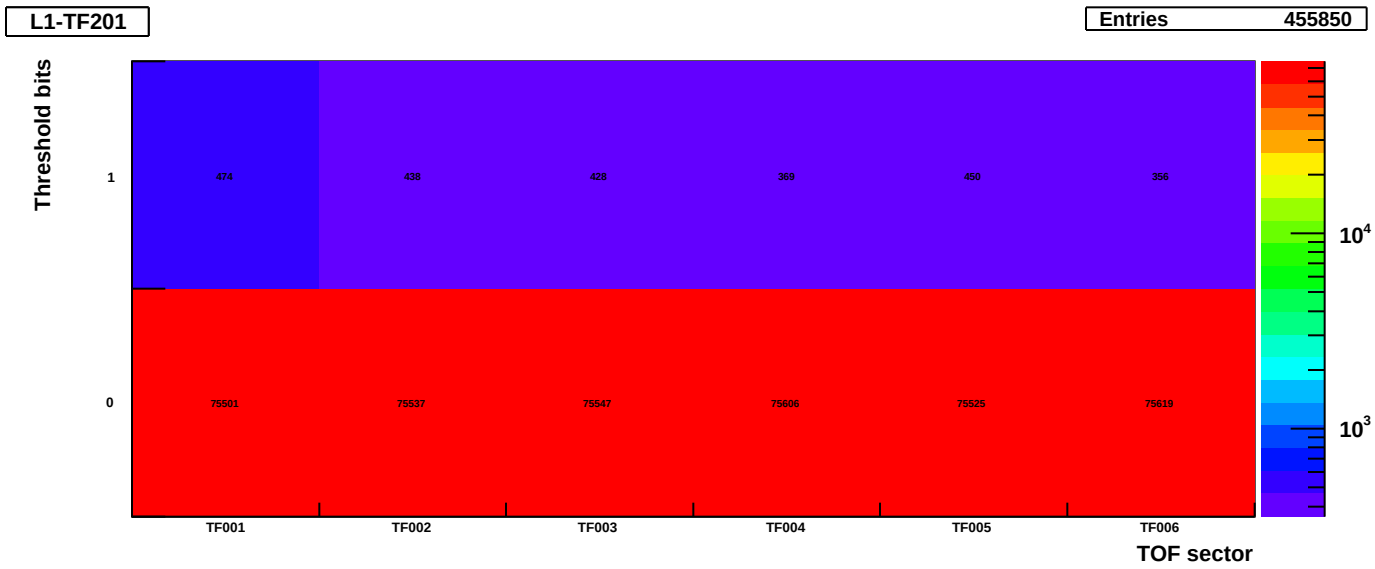

MIX-TF004
Entries 3190950
TOF MULT
TOF tray

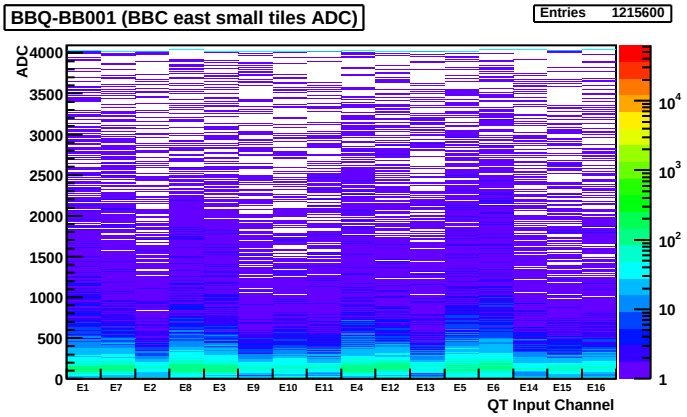

MIX-TF005
Entries 3190950
TOF MULT
TOF tray

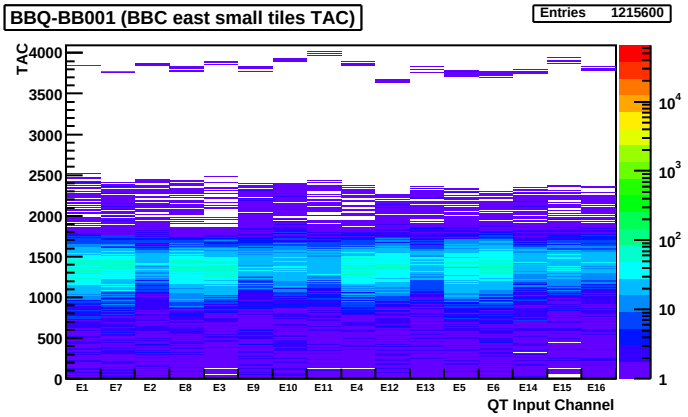

MIX-TF006
Entries 3190950
TOF MULT
TOF tray

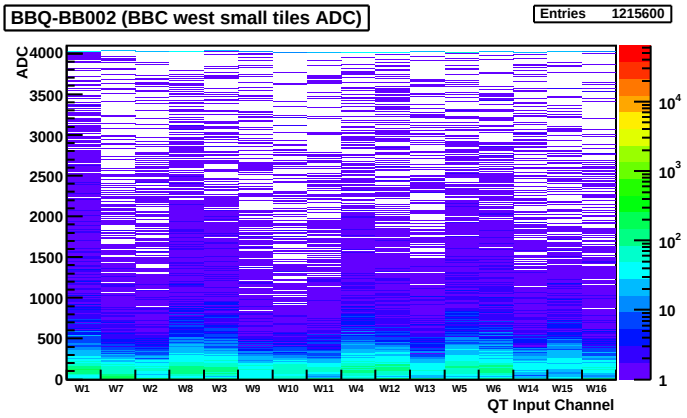

MIX-TF101
Entries 455850
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

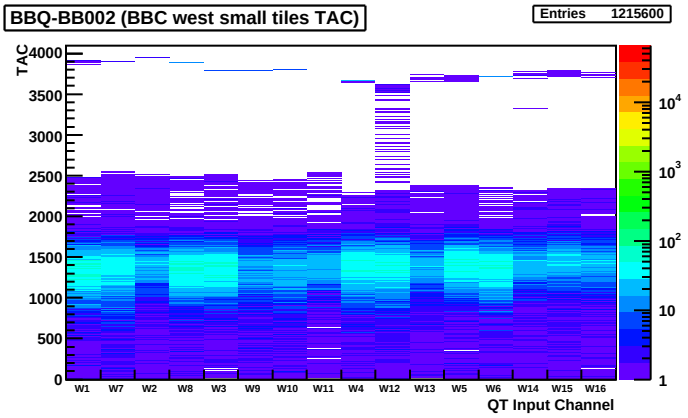

L1-TF201
Entries 75975
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

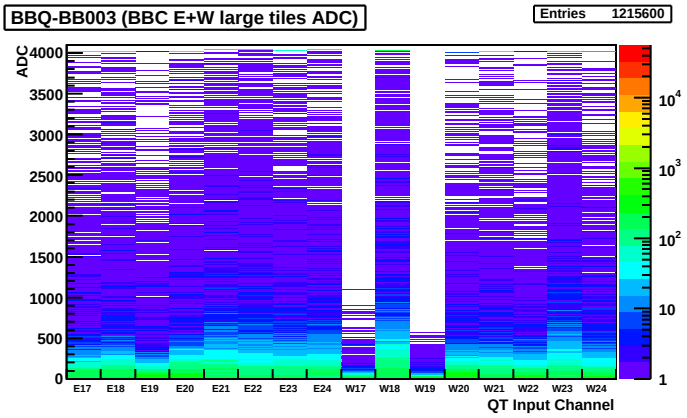

L1-TF201
Entries 455850
Threshold bits
1
0
474
438
428
369
450
356
75501
75537
75547
75606
75525
75619
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

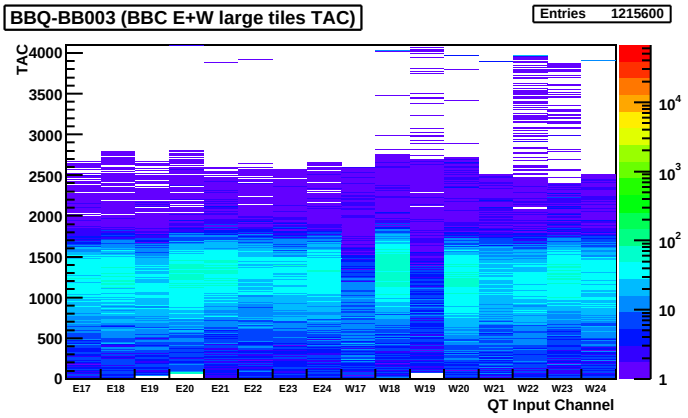

BBQ-BB001 (BBC east small tiles ADC)
Entries 1215600
ADC
QT Input Channel

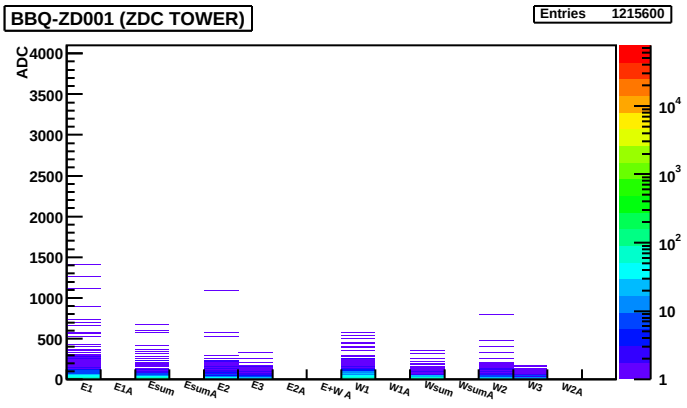

BBQ-BB001 (BBC east small tiles TAC)
Entries 1215600
TAC
QT Input Channel

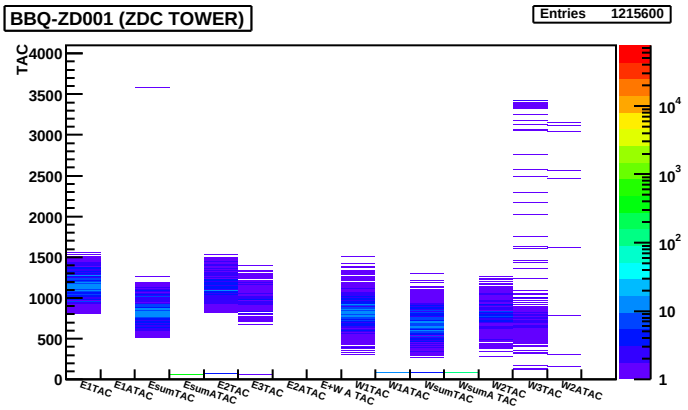

BBQ-BB002 (BBC west small tiles ADC)
Entries 1215600
ADC
QT Input Channel

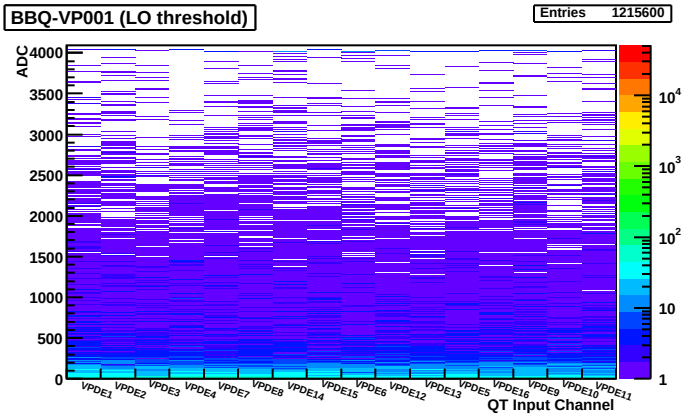

BBQ-BB002 (BBC west small tiles TAC)
Entries 1215600
TAC
QT Input Channel

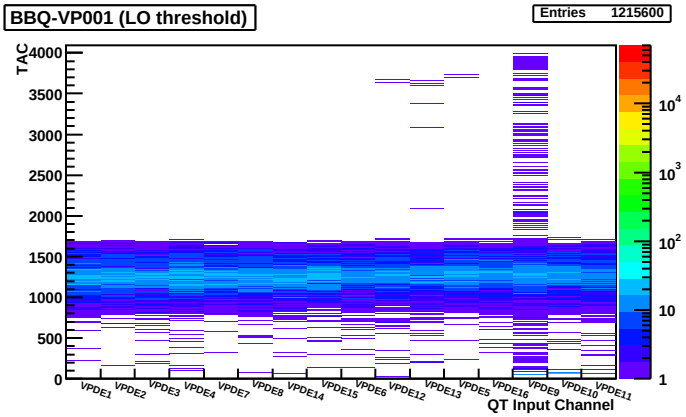

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1215600
ADC
QT Input Channel

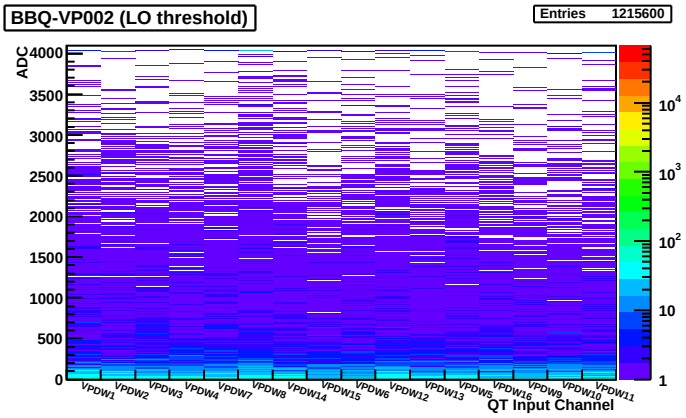

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1215600
TAC
QT Input Channel

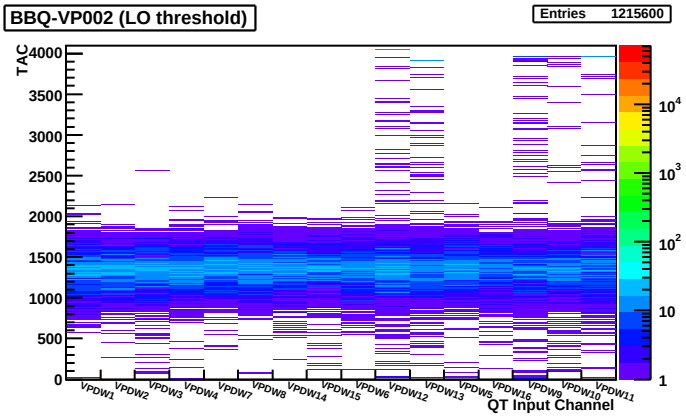

BBQ-ZD001 (ZDC TOWER)
Entries 1215600
ADC

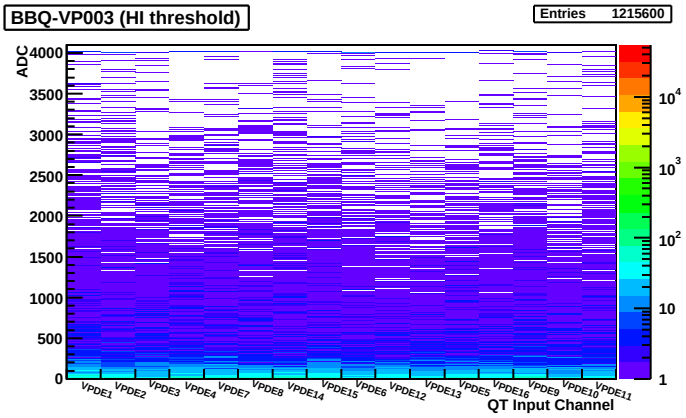

BBQ-ZD001 (ZDC TOWER)
Entries 1215600
TAC

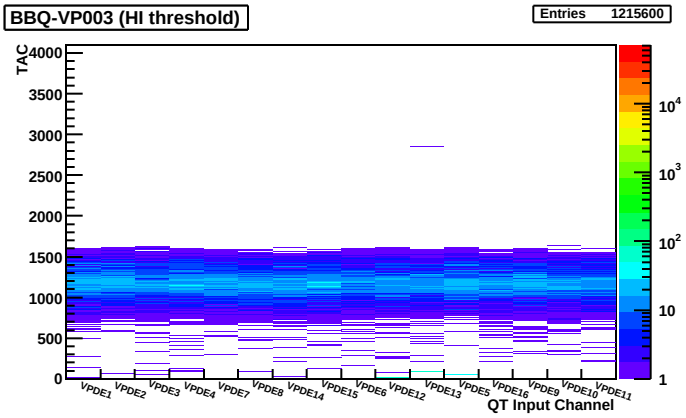

BBQ-VP001 (LO threshold)
Entries 1215600
ADC
QT Input Channel

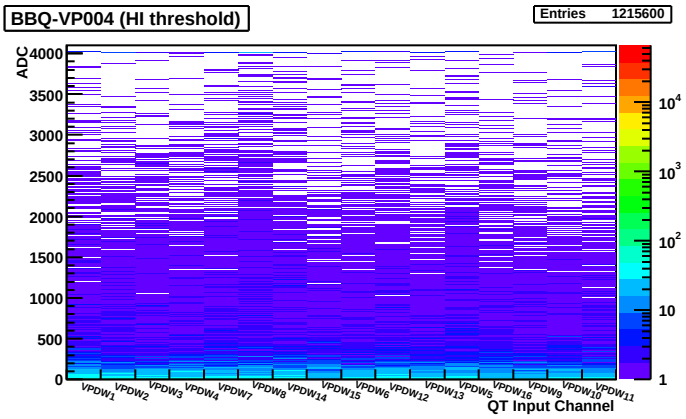

BBQ-VP001 (LO threshold)
Entries 1215600
TAC
QT Input Channel

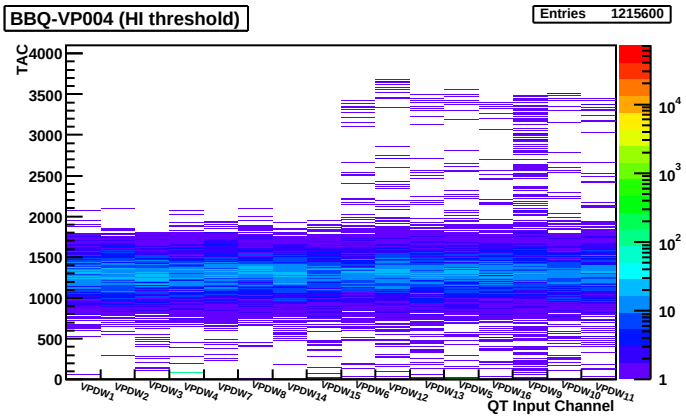

BBQ-VP002 (LO threshold)
Entries 1215600
ADC
QT Input Channel

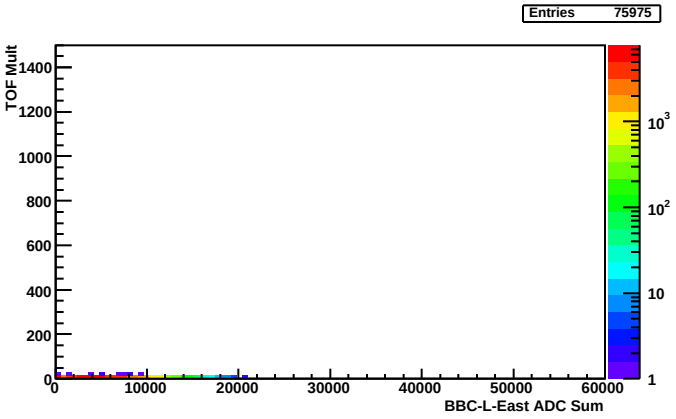

BBQ-VP002 (LO threshold)
Entries 1215600
TAC
QT Input Channel

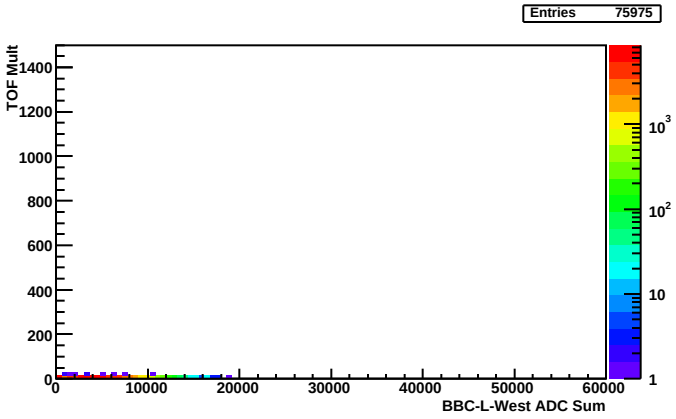

BBQ-VP003 (HI threshold)
Entries 1215600
ADC
QT Input Channel

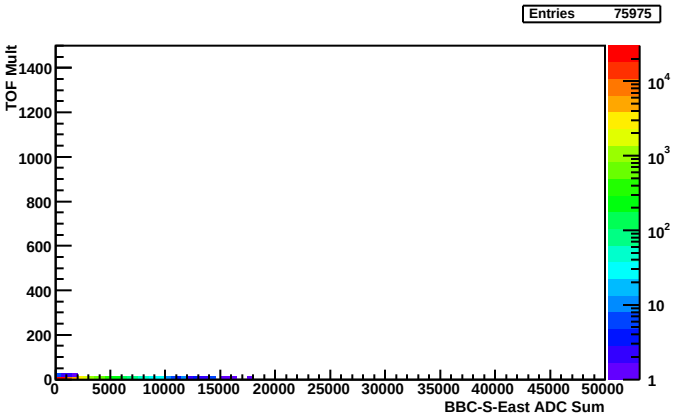

BBQ-VP003 (HI threshold)
Entries 1215600
TAC
QT Input Channel

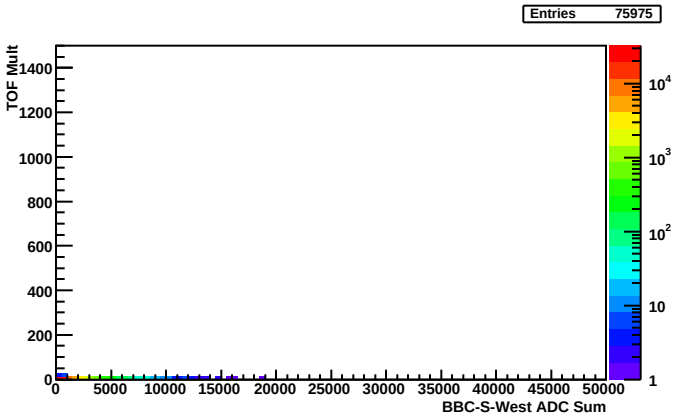

BBQ-VP004 (HI threshold)
Entries 1215600
ADC
QT Input Channel

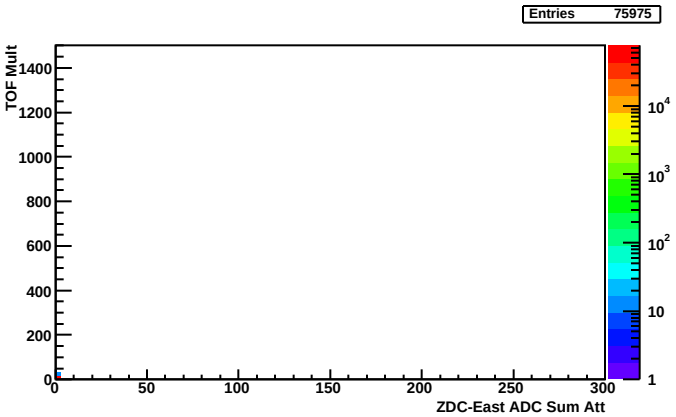

BBQ-VP004 (HI threshold)
Entries 1215600
TAC
QT Input Channel

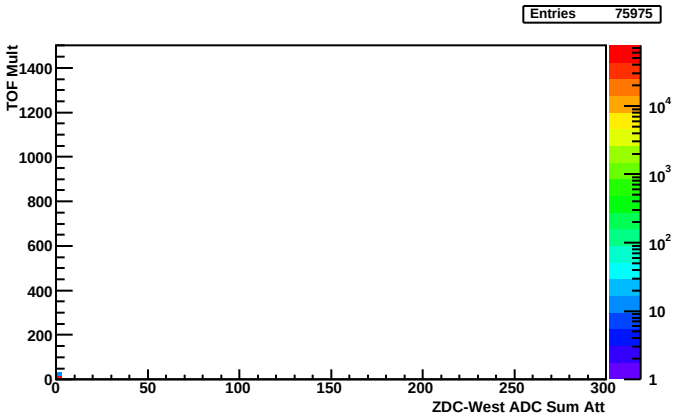

Entries 75975
TOF Mult
BBC-L-East ADC Sum

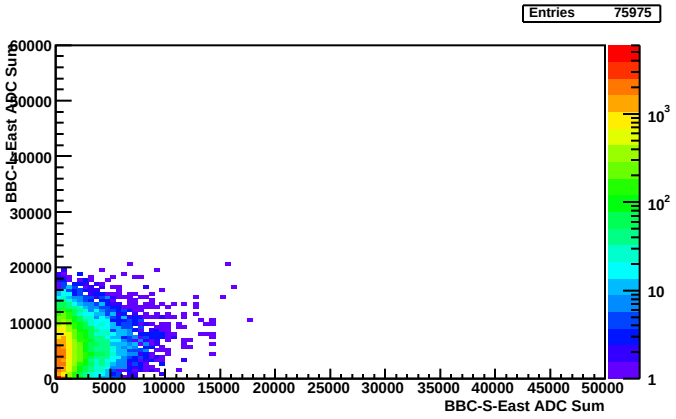

Entries 75975
TOF Mult
BBC-L-West ADC Sum

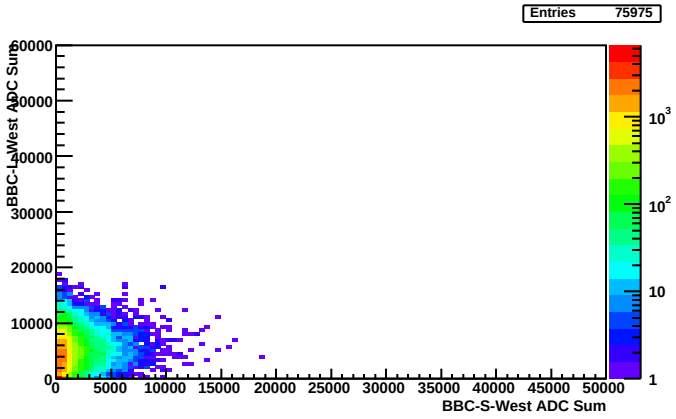

Entries 75975
TOF Mult
BBC-S-East ADC Sum

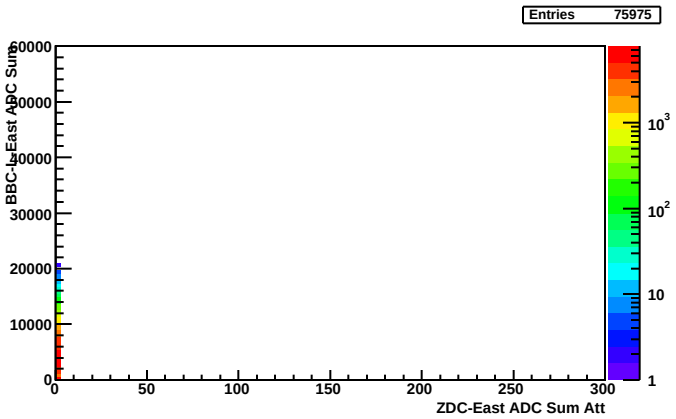

Entries 75975
TOF Mult
BBC-S-West ADC Sum

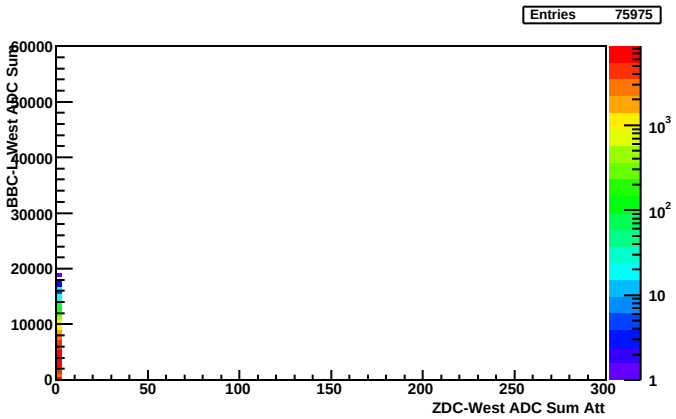

Entries 75975
TOF Mult
ZDC-East ADC Sum Att

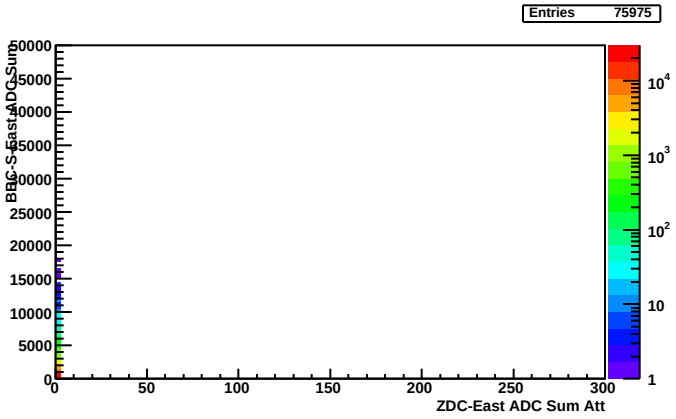

Entries 75975
TOF Mult
ZDC-West ADC Sum Att

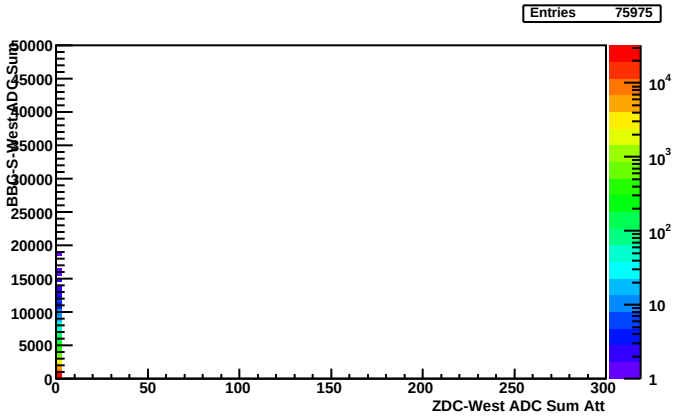

Entries 75975
BBC-L-East ADC Sum
BBC-S-East ADC Sum

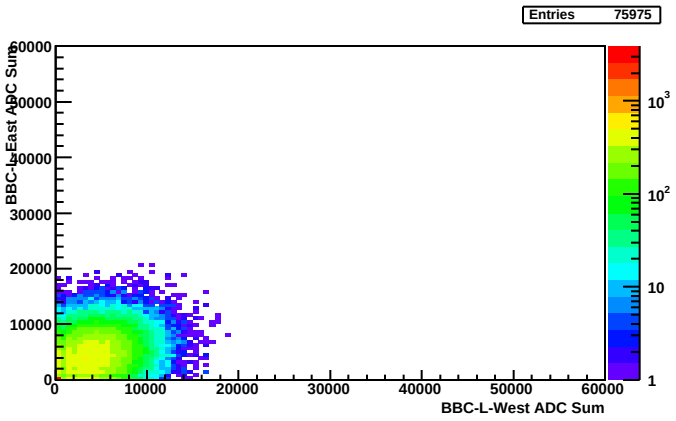

Entries 75975
BBC-L-West ADC Sum
BBC-S-West ADC Sum

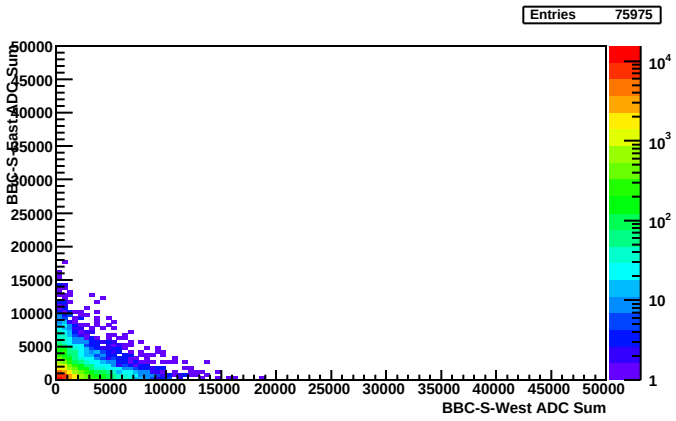

Entries 75975
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

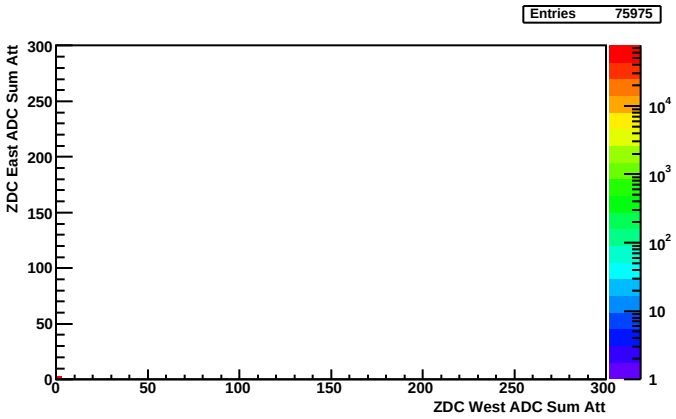

Entries 75975
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

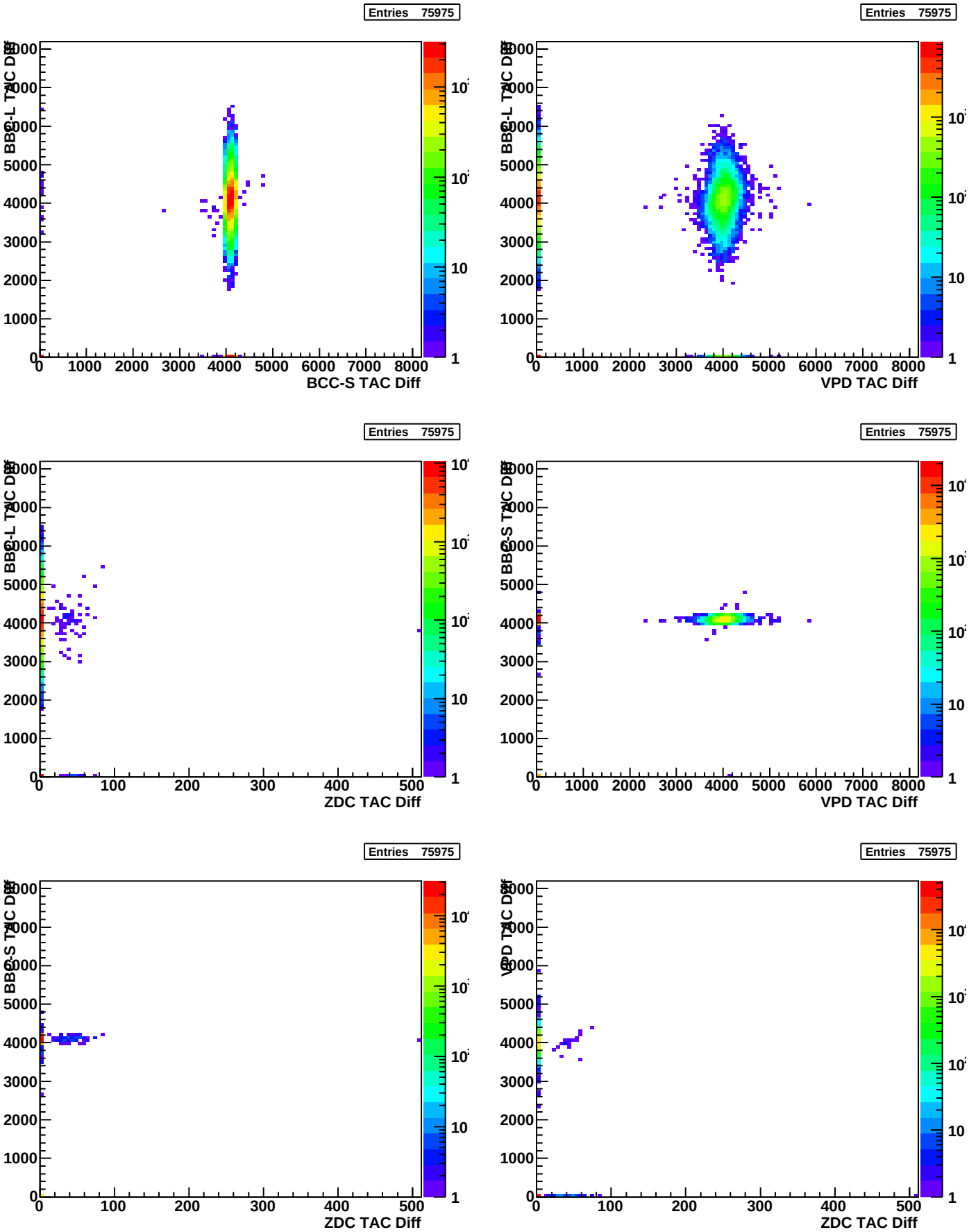

Entries 75975
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

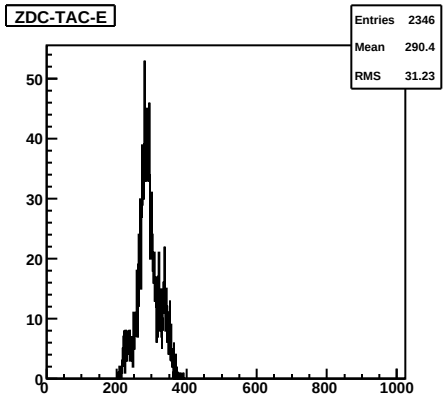

Entries 75975
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

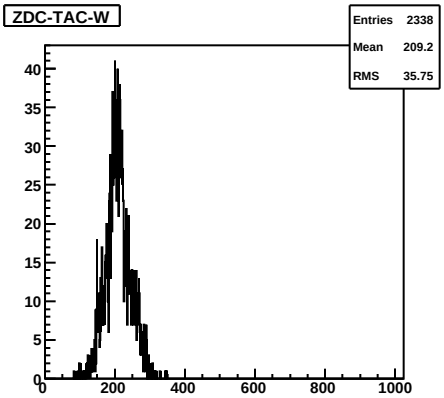

Entries 75975
BBC-L-East ADC Sum
BBC-L-West ADC Sum

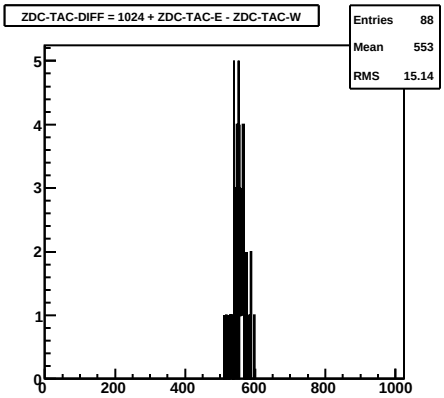

Entries 75975
BBC-S-East ADC Sum
BBC-S-West ADC Sum

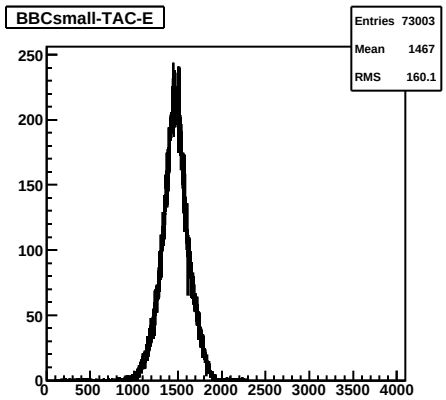

Entries 75975
ZDC East ADC Sum Att
ZDC West ADC Sum Att

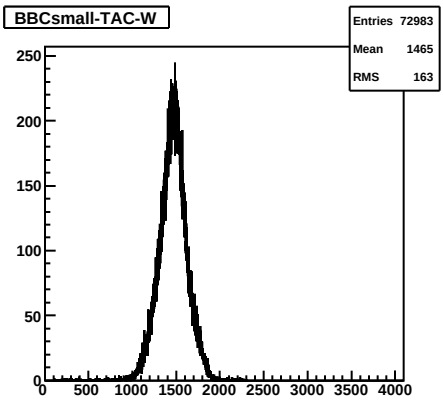

Entries 75975
BBC-L TAC Diff
BCC-S TAC Diff
Entries 75975
BBC-L TAC Diff
VPD TAC Diff
Entries 75975
BBC-L TAC Diff
ZDC TAC Diff
Entries 75975
BBC-S TAC Diff
VPD TAC Diff
Entries 75975
BBC-S TAC Diff
ZDC TAC Diff
Entries 75975
VPD TAC Diff
ZDC TAC Diff

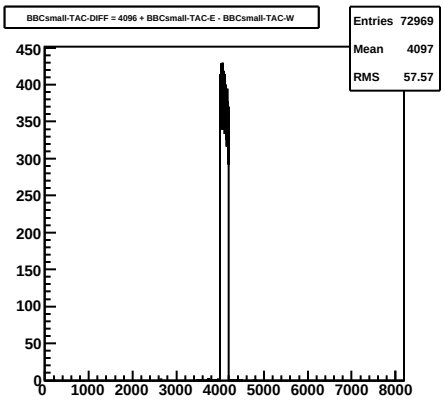

ZDC-TAC-E
Entries 2346
Mean 290.4
RMS 31.23

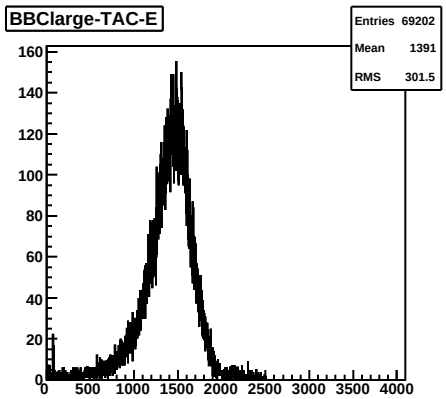

ZDC-TAC-W
Entries 2338
Mean 209.2
RMS 35.75

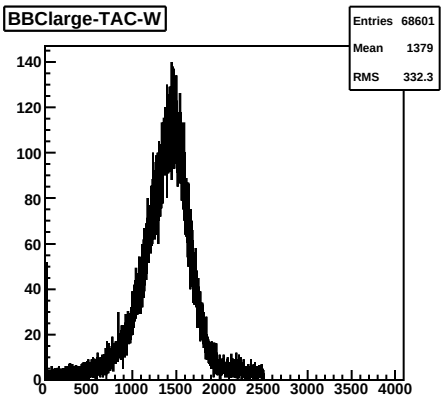

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 88
Mean 553
RMS 15.14

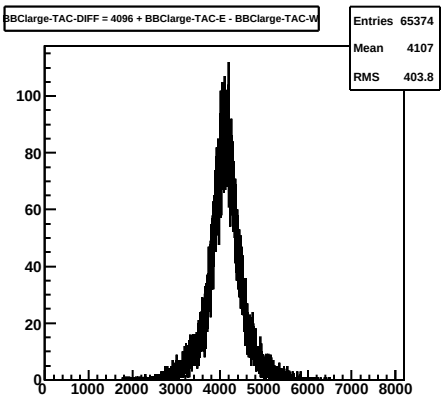

BBCsmall-TAC-E
Entries 73003
Mean 1467
RMS 160.1

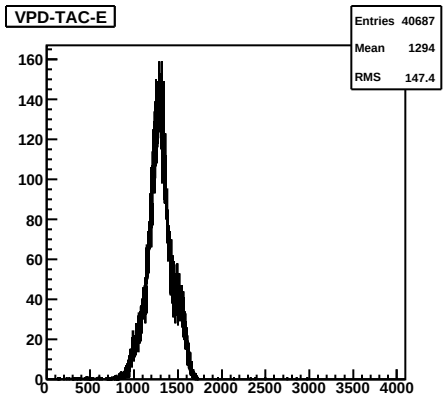

BBCsmall-TAC-W
Entries 72983
Mean 1465
RMS 163

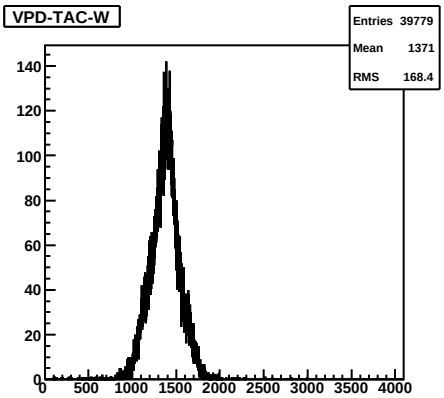

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 72969
Mean 4097
RMS 57.57

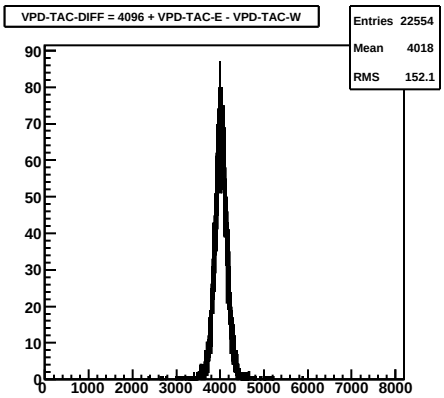

BBClarge-TAC-E
Entries 69202
Mean 1391
RMS 301.5

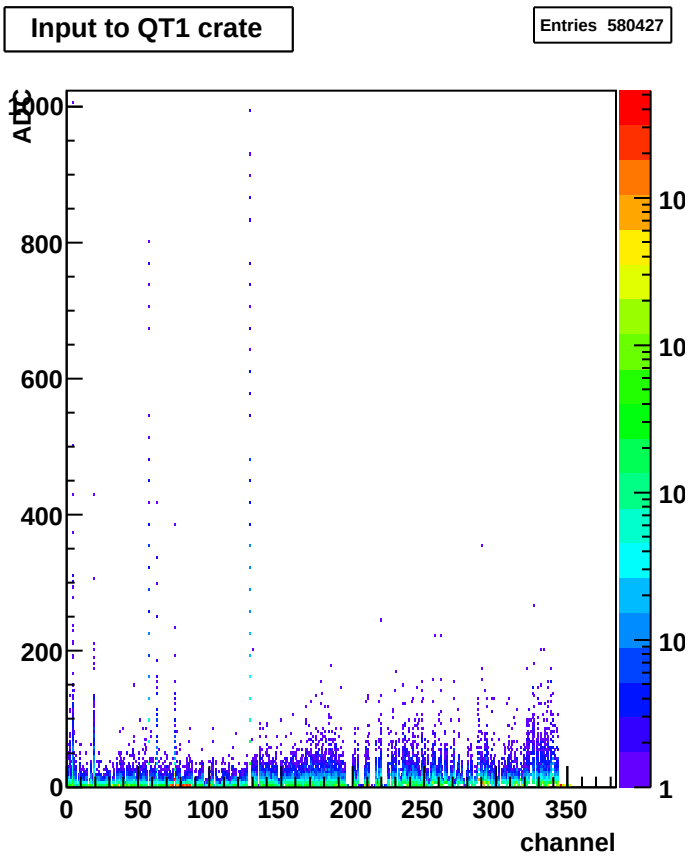

BBClarge-TAC-W
Entries 68601
Mean 1379
RMS 332.3

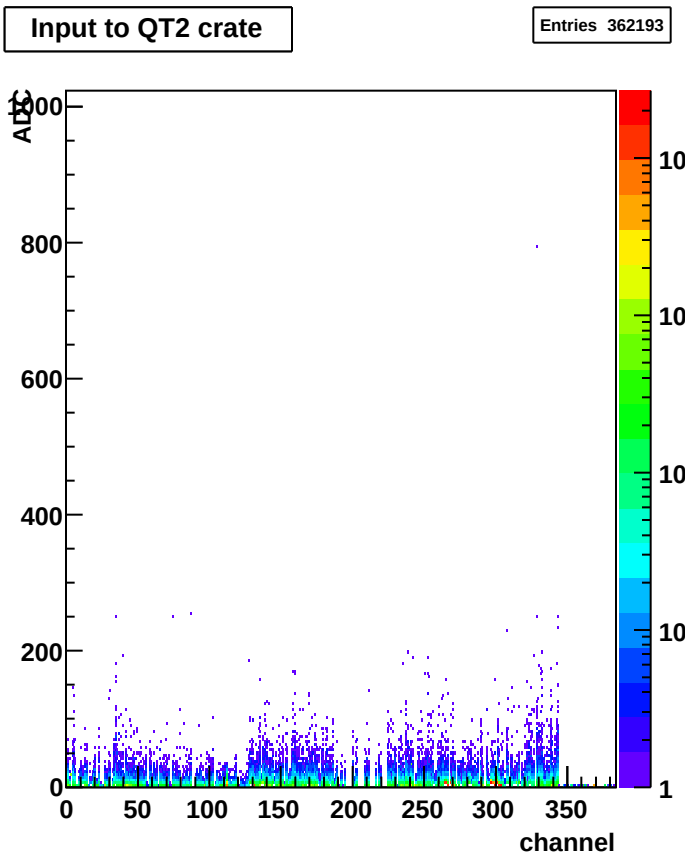

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 65374
Mean 4107
RMS 403.8

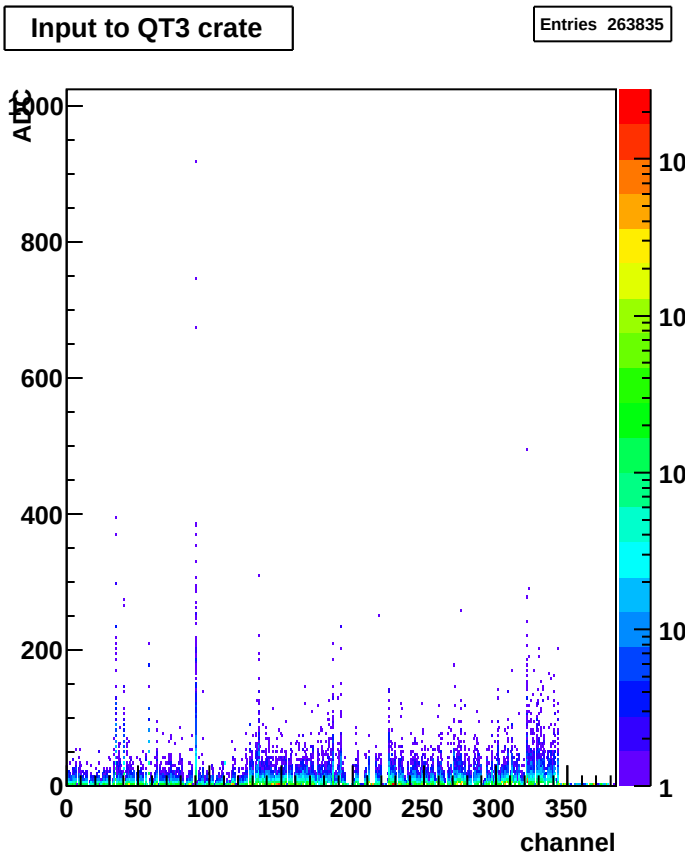

VPD-TAC-E
Entries 40687
Mean 1294
RMS 147.4

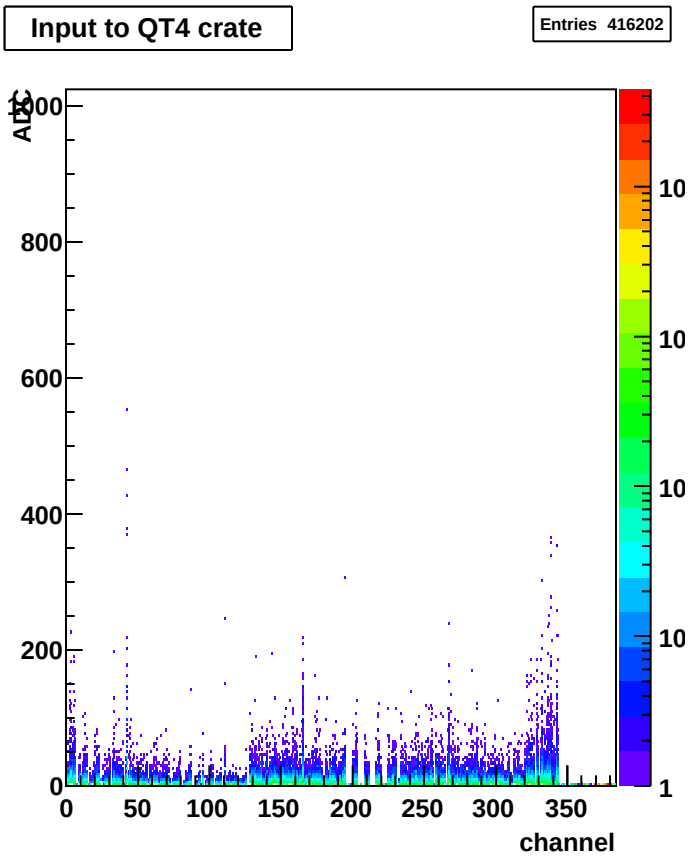

VPD-TAC-W
Entries 39779
Mean 1371
RMS 168.4

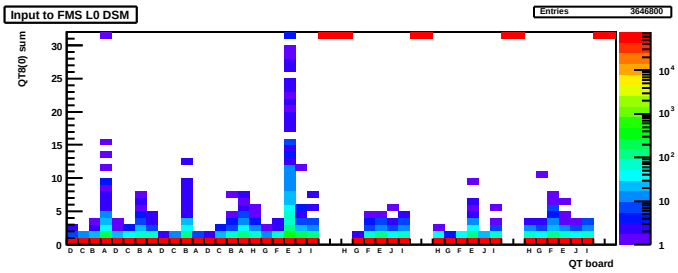

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 22554
Mean 4018
RMS 152.1

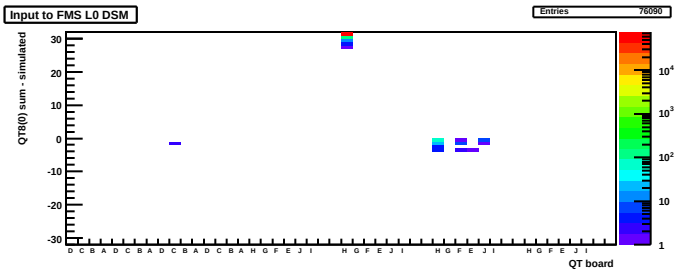

Input to QT1 crate
Entries 580427
ADC
channel

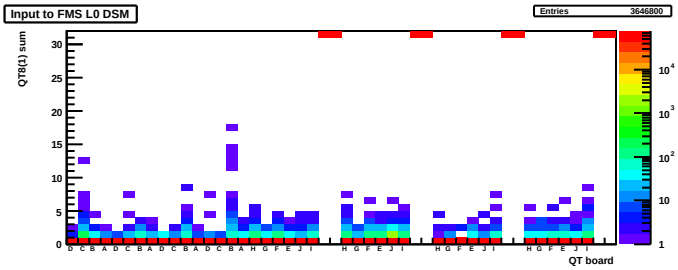

Input to QT2 crate
Entries 362193
ADC
channel

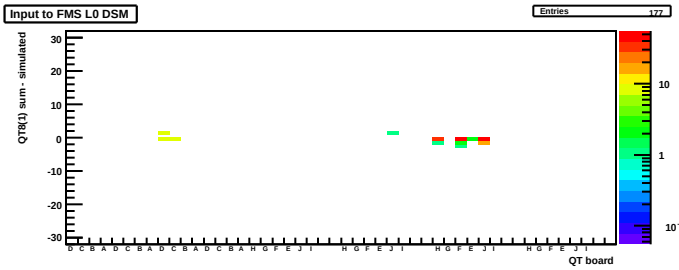

Input to QT3 crate
Entries 263835
ADC
channel

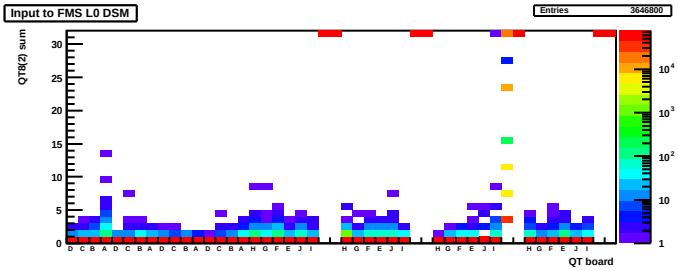

Input to QT4 crate
Entries 416202
ADC
channel

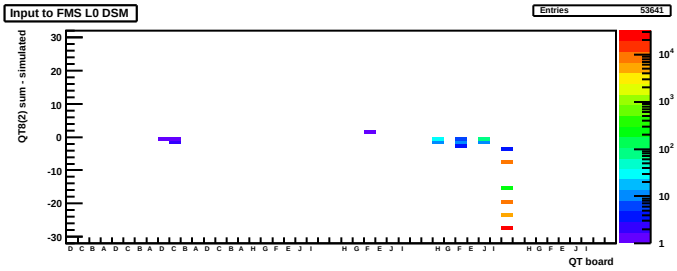

Input to FMS L0 DSM
Entries 3646800
QT8(0) sum
QT board

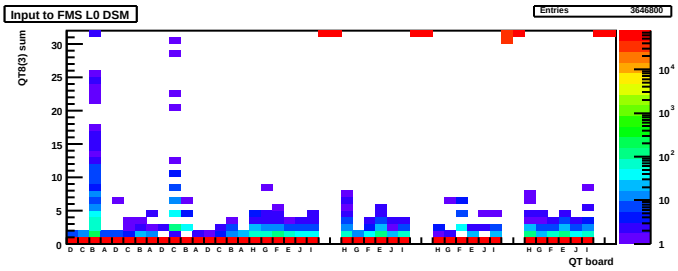

Input to FMS L0 DSM
Entries 76090
QT8(0) sum - simulated
QT board

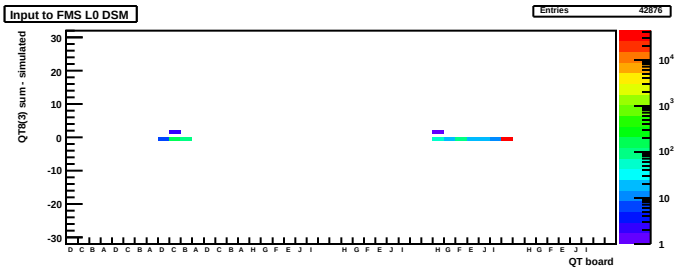

Input to FMS L0 DSM
Entries 3646800
QT8(1) sum
QT board

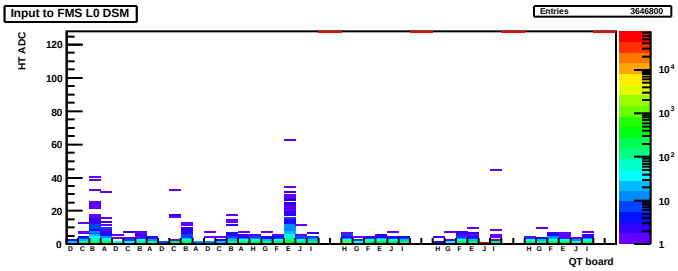

Input to FMS L0 DSM
Entries 177
QT8(1) sum - simulated
QT board

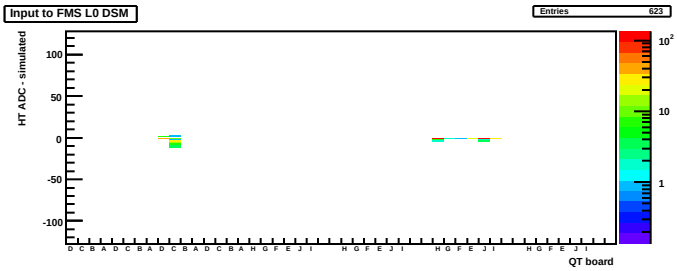

Input to FMS L0 DSM
Entries 3646800
QT8(2) sum
QT board

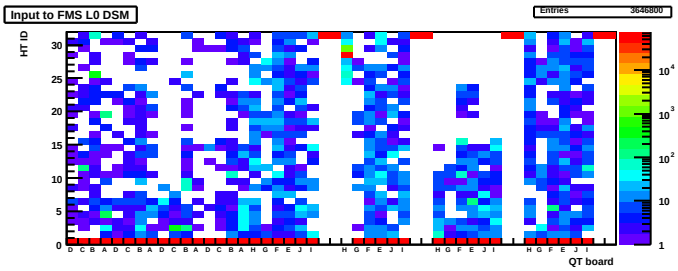

Input to FMS L0 DSM
Entries 53641
QT8(2) sum - simulated
QT board

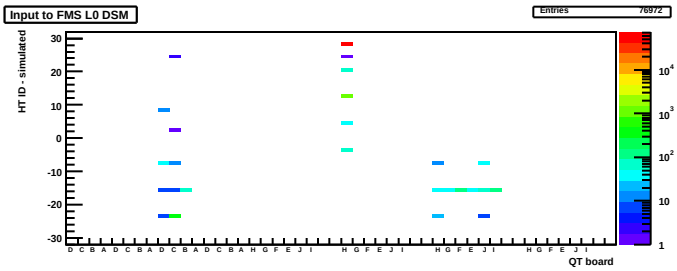

Input to FMS L0 DSM
Entries 3646800
QT8(3) sum
QT board

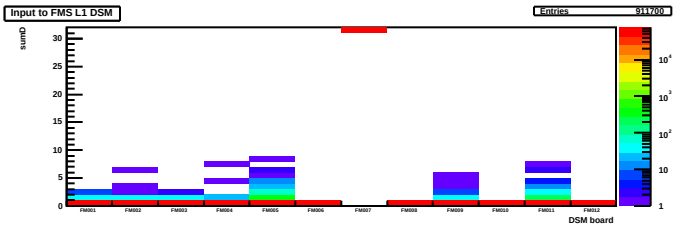

Input to FMS L0 DSM
Entries 42876
QT8(3) sum - simulated
QT board

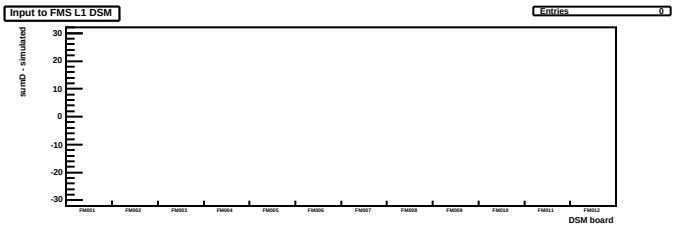

Input to FMS L0 DSM
Entries 3646800
HT ADC
QT board

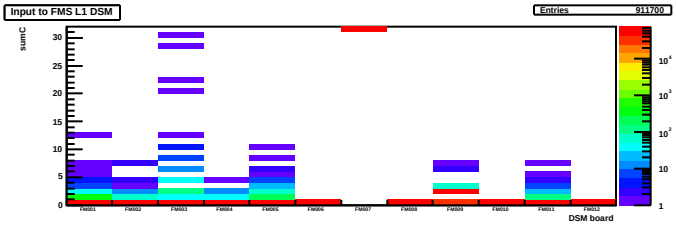

Input to FMS L0 DSM
Entries 623
HT ADC - simulated
QT board

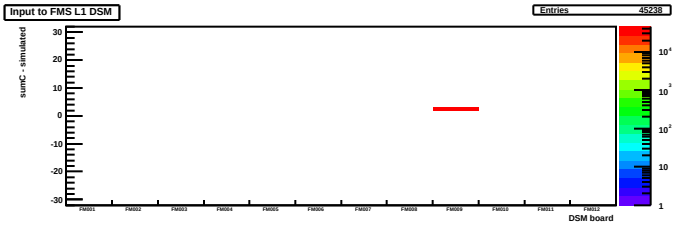

Input to FMS L0 DSM
Entries 3646800
HT ID
QT board

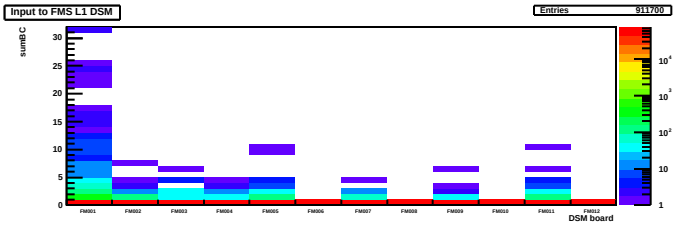

Input to FMS L0 DSM
Entries 76972
HT ID - simulated
QT board

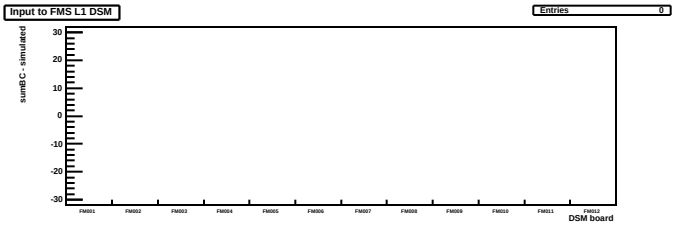

Input to FMS L1 DSM
Entries 911700
sumD
DSM board

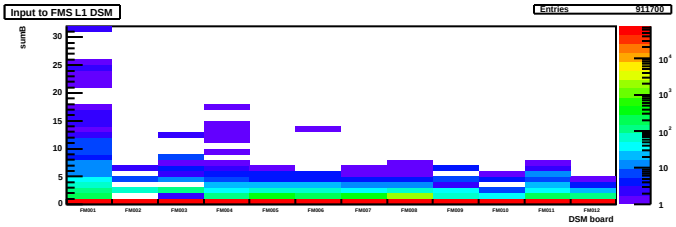

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

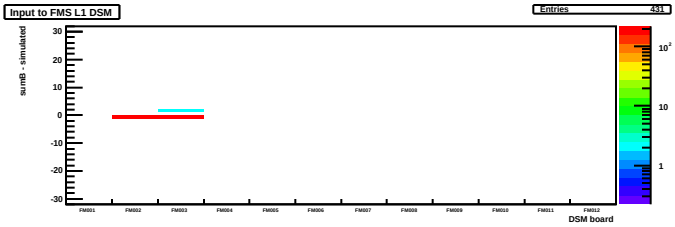

Input to FMS L1 DSM
Entries 911700
sumC
DSM board

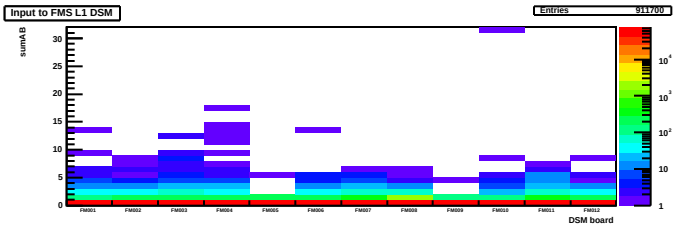

Input to FMS L1 DSM
Entries 45238
sumC - simulated
DSM board

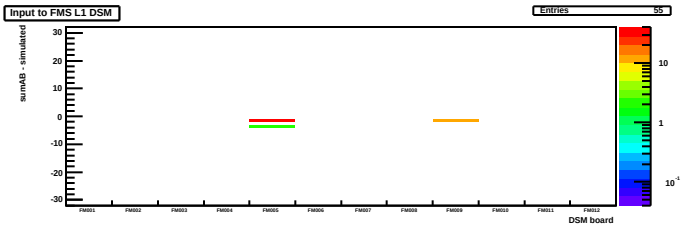

Input to FMS L1 DSM
Entries 911700
sumBC
DSM board

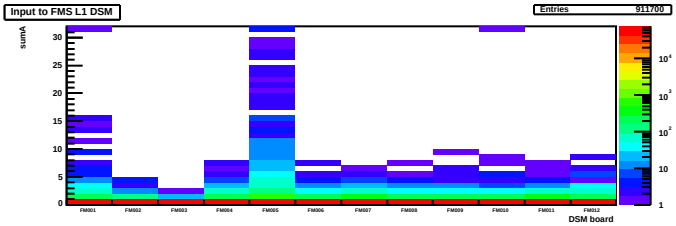

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

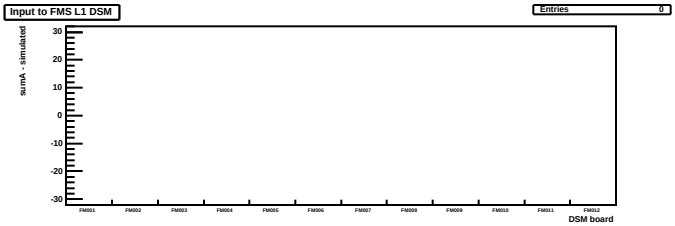

Input to FMS L1 DSM
Entries 911700
sumB
DSM board

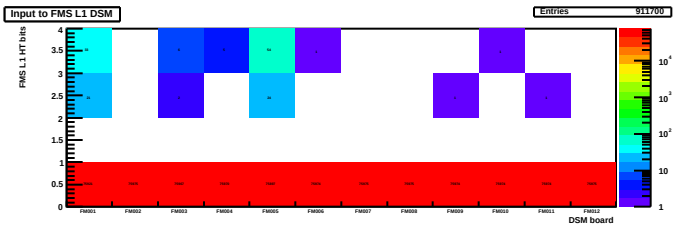

Input to FMS L1 DSM
Entries 431
sumB - simulated
DSM board

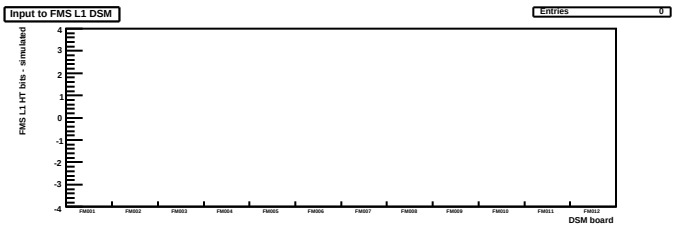

Input to FMS L1 DSM
Entries 911700
sumAB
DSM board

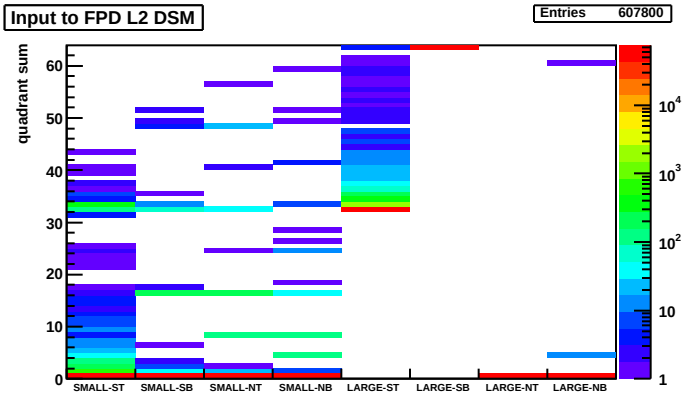

Input to FMS L1 DSM
Entries 55
sumAB - simulated
DSM board

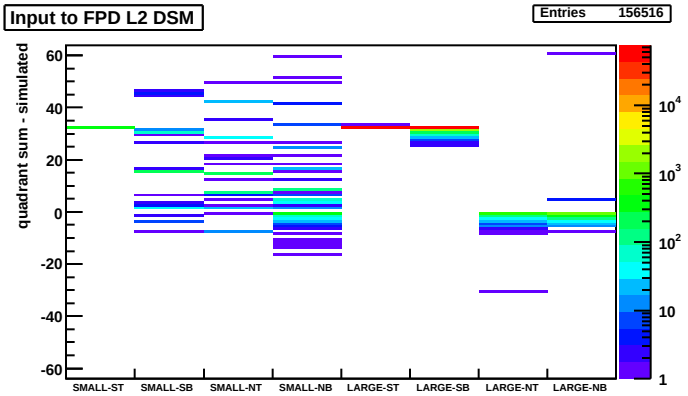

Input to FMS L1 DSM
Entries 911700
sumA
DSM board

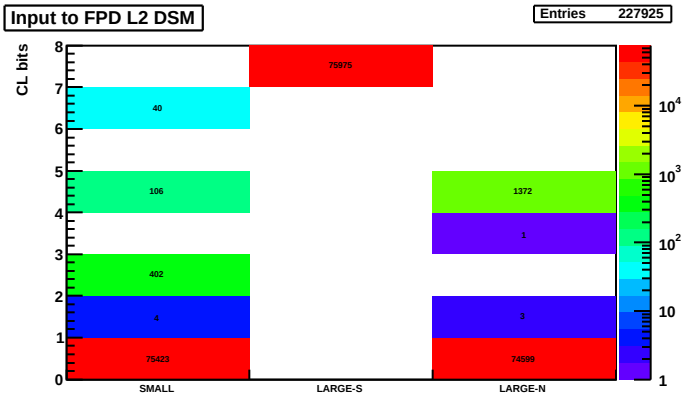

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

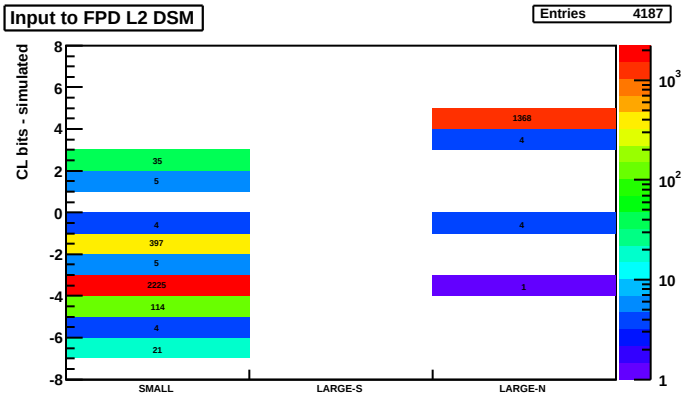

Input to FMS L1 DSM
Entries 911700
FMS L1 HT bits
DSM board

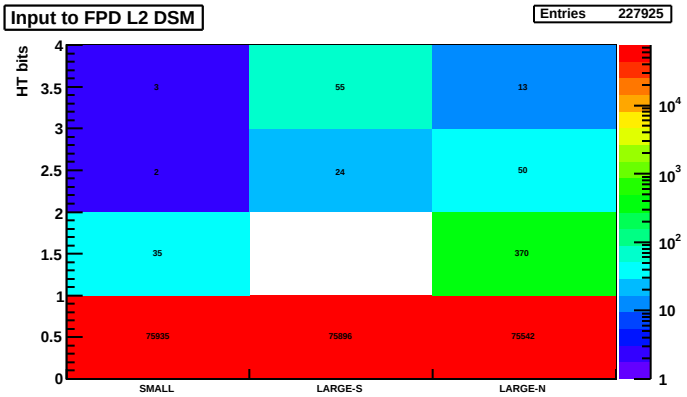

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

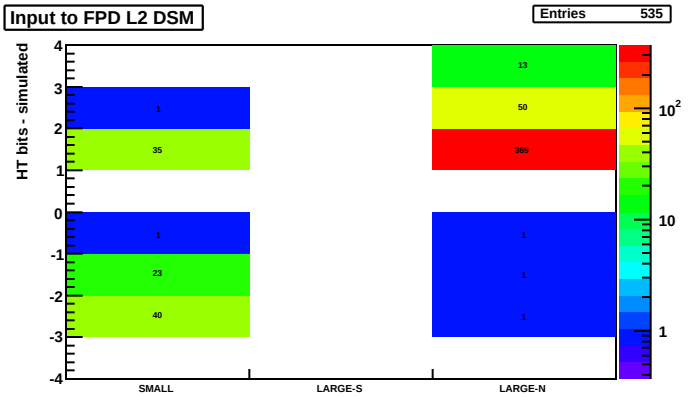

Input to FPD L2 DSM
Entries 607800
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

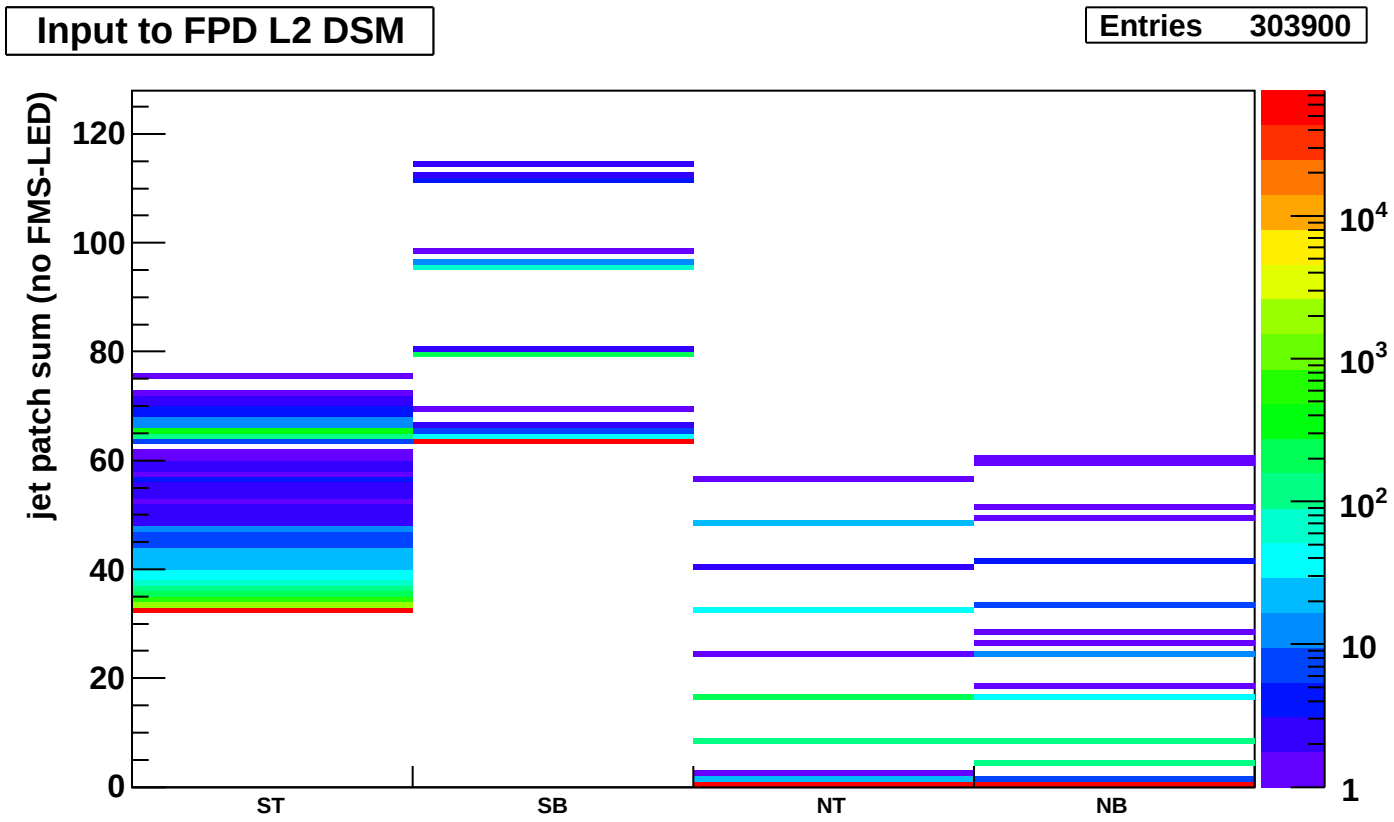

Input to FPD L2 DSM
Entries 156516
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

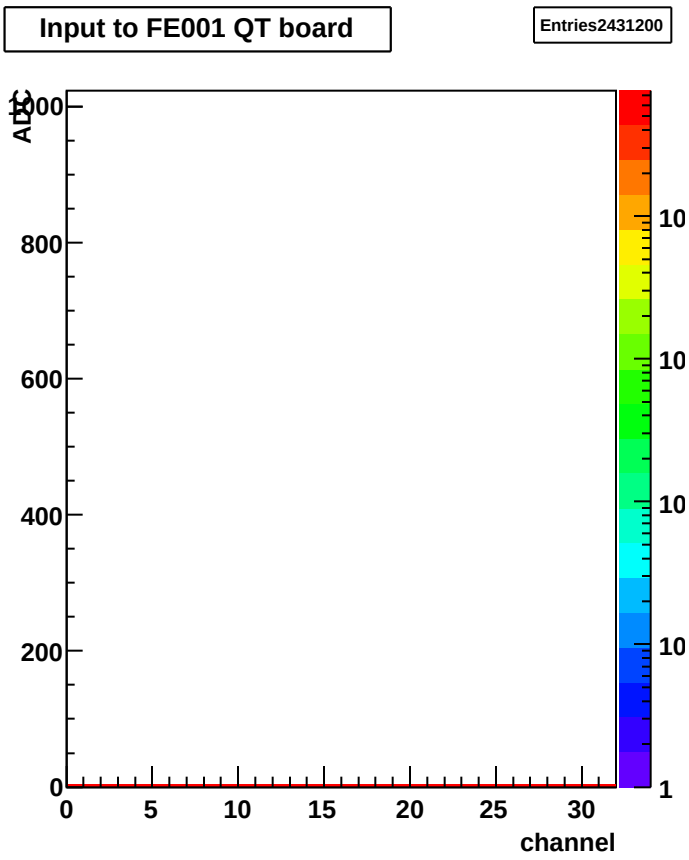

Input to FPD L2 DSM
Entries 227925
CL bits
75975
40
106
1372
1
402
4
3
75423
74599
SMALL
LARGE-S
LARGE-N

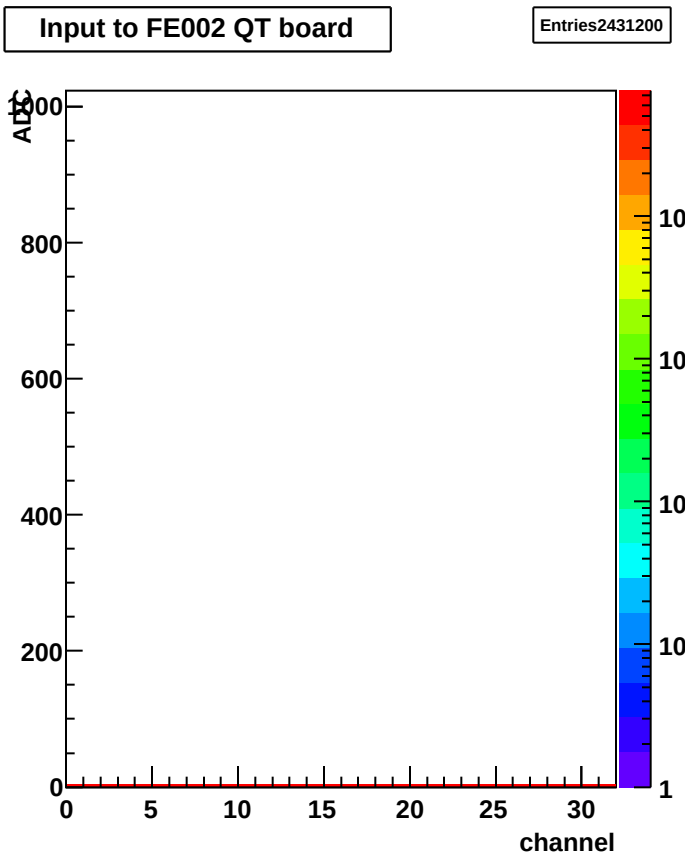

Input to FPD L2 DSM
Entries 4187
CL bits - simulated
1368
4
35
5
4
397
5
2225
114
4
21
4
1
SMALL
LARGE-S
LARGE-N

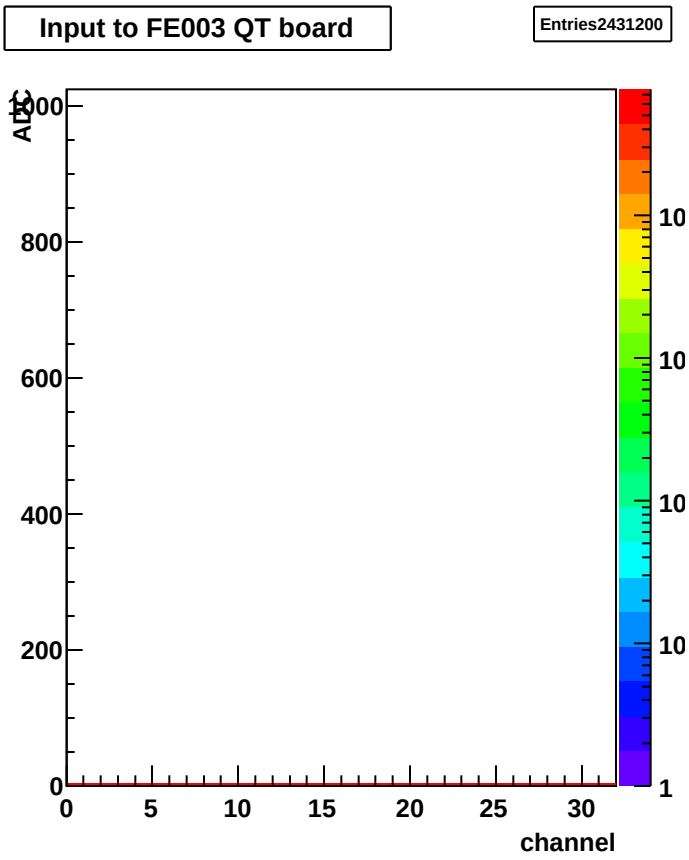

Input to FPD L2 DSM
Entries 227925
HT bits
3
55
13
2
24
50
35
370
75935
75896
75542
SMALL
LARGE-S
LARGE-N

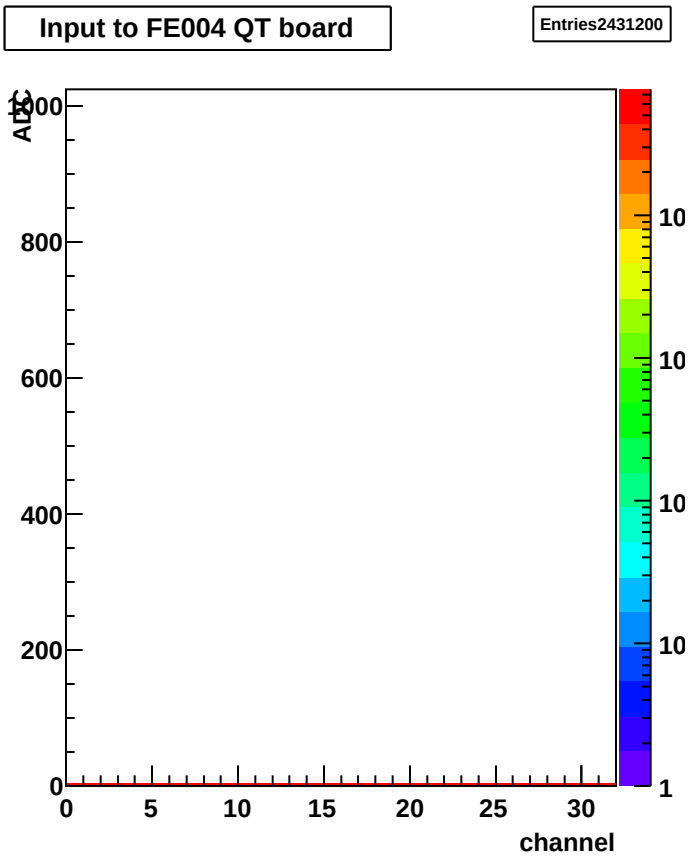

Input to FPD L2 DSM
Entries 535
HT bits - simulated
13
1
50
35
369
1
1
23
1
40
1
SMALL
LARGE-S
LARGE-N

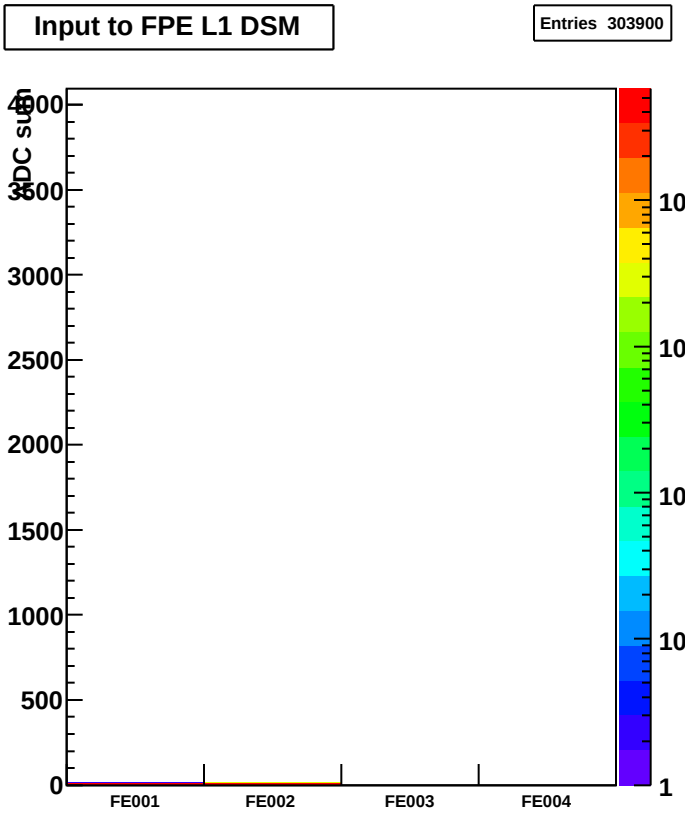

Input to FPD L2 DSM
Entries 303900
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

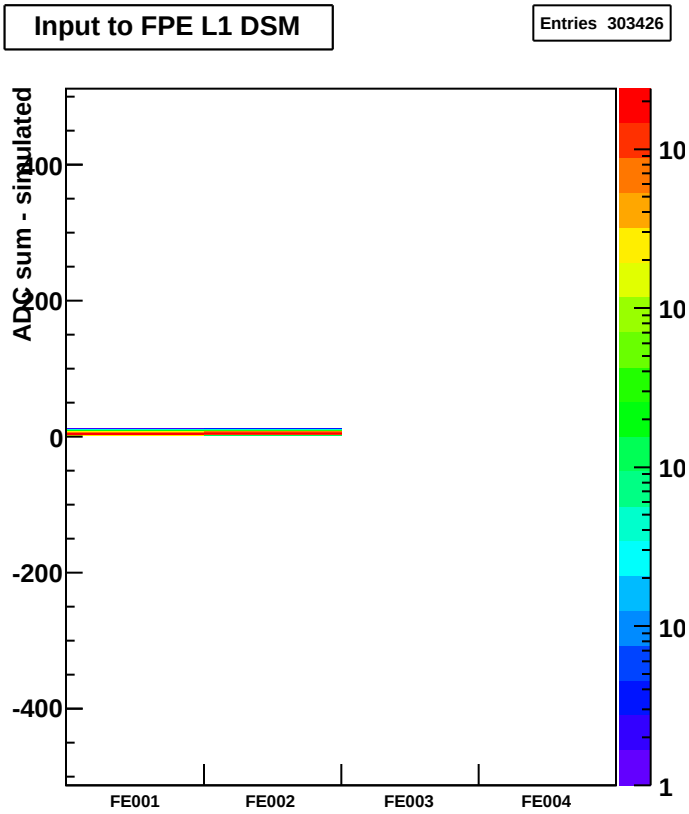

Input to FE001 QT board
Entries2431200
ADC
channel

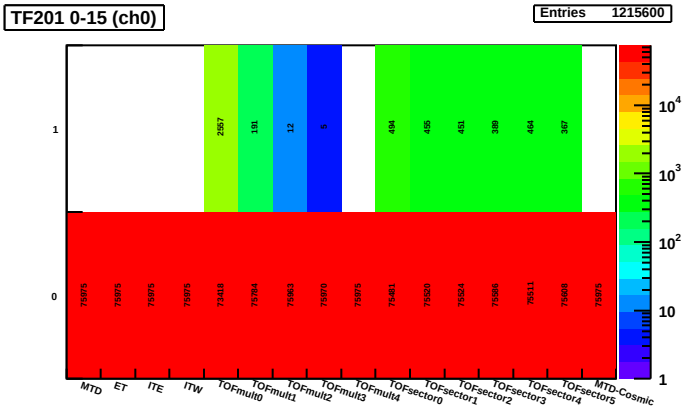

Input to FE002 QT board
Entries2431200
ADC
channel

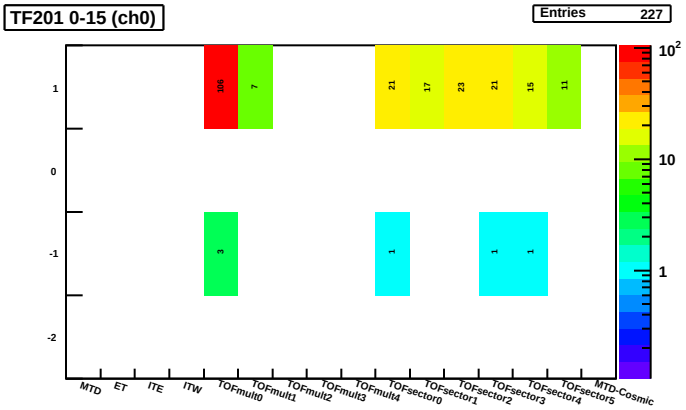

Input to FE003 QT board
Entries2431200
ADC
channel

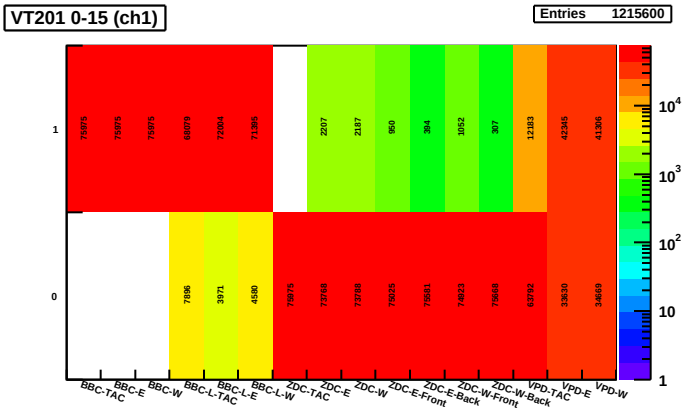

Input to FE004 QT board
Entries2431200
ADC
channel

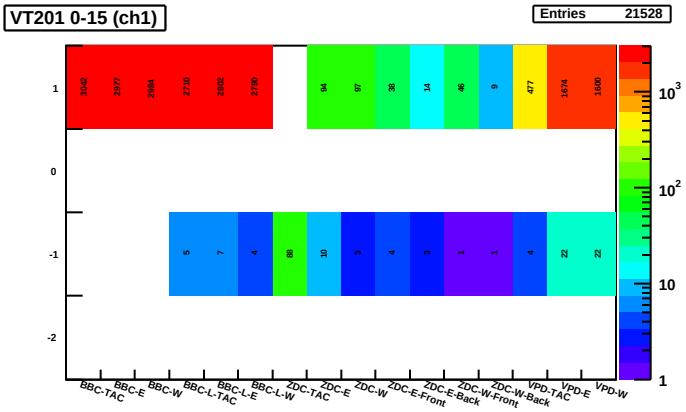

Input to FPE L1 DSM
Entries 303900
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

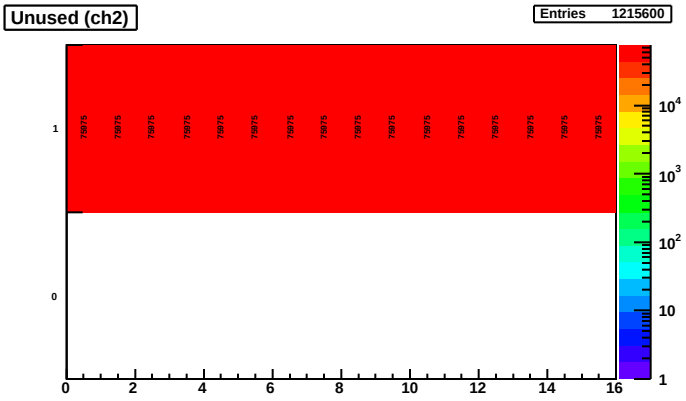

Input to FPE L1 DSM
Entries 303426
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

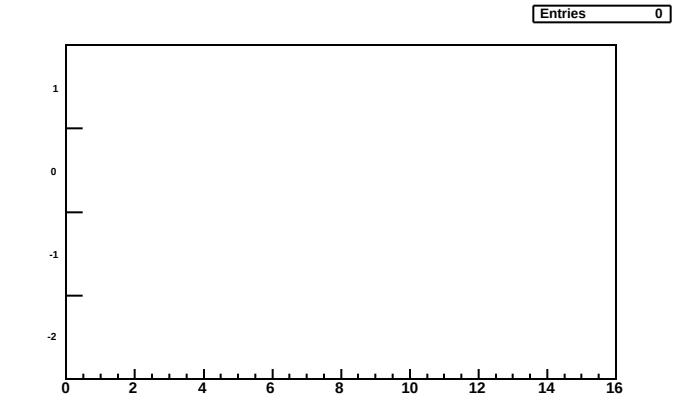

TF201 0-15 (ch0)
Entries 1215600
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

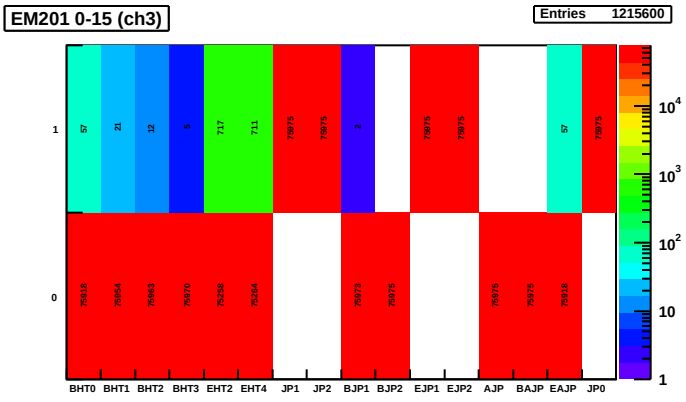

TF201 0-15 (ch0)
Entries 227
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

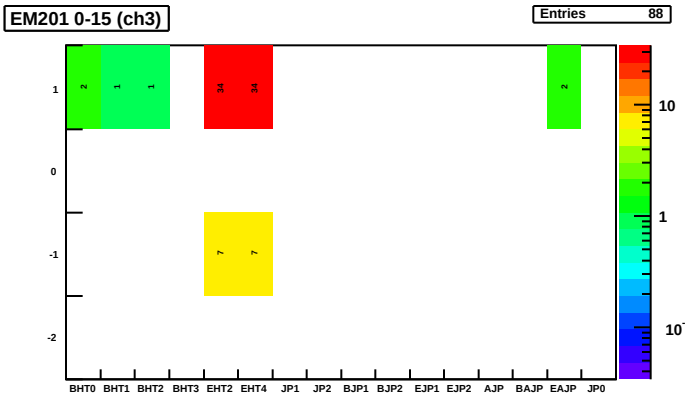

VT201 0-15 (ch1)
Entries 1215600
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 21528
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

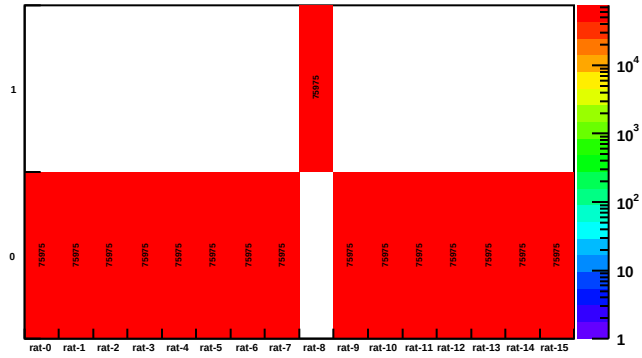

Unused (ch2)
Entries 1215600

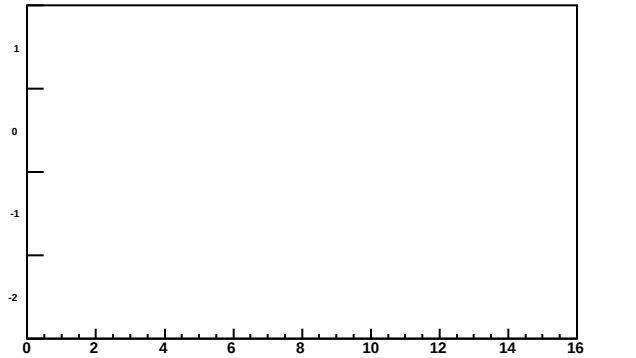

Entries 0


EM201 0-15 (ch3)
Entries 1215600
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

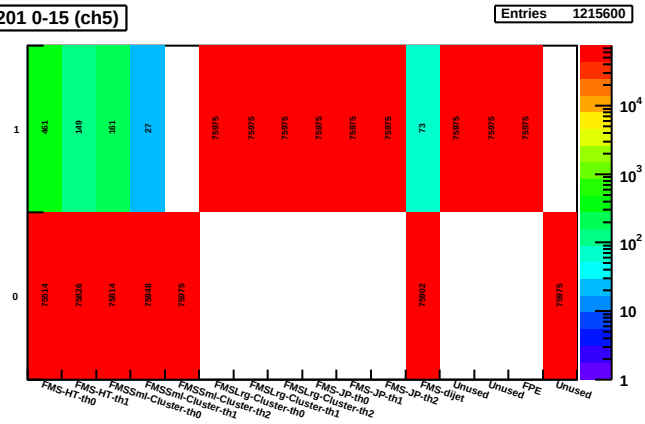

EM201 0-15 (ch3)
Entries 88
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

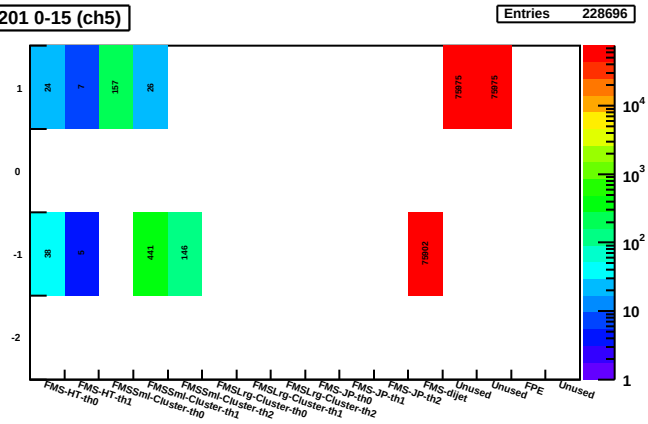




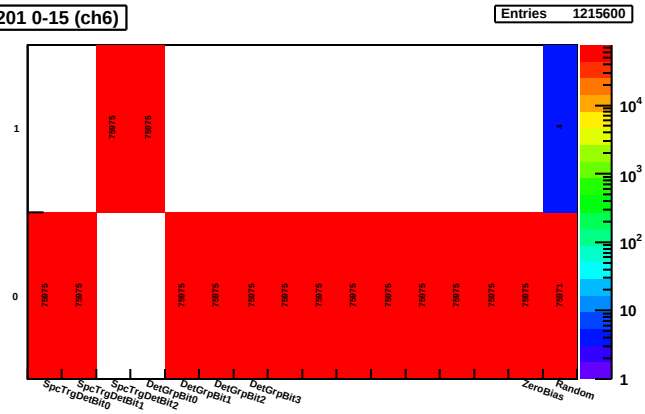
FP201 0-15 (ch5)

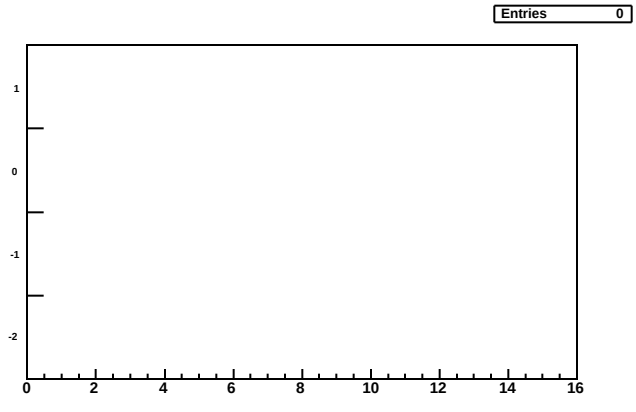
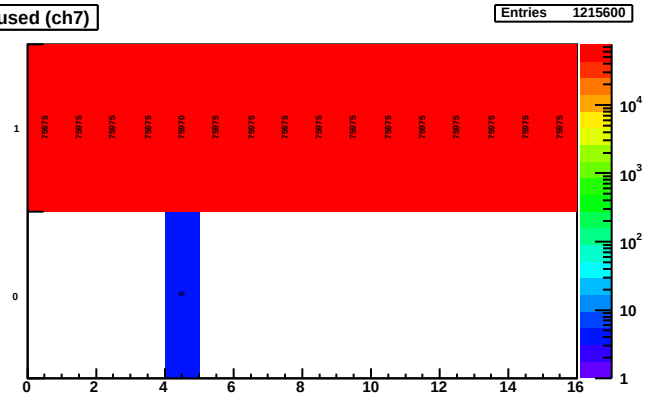
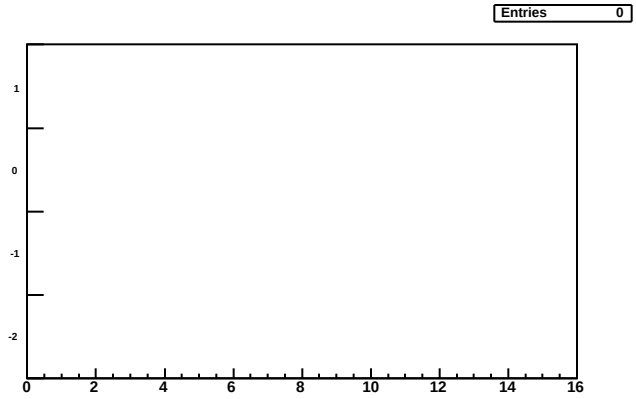


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)