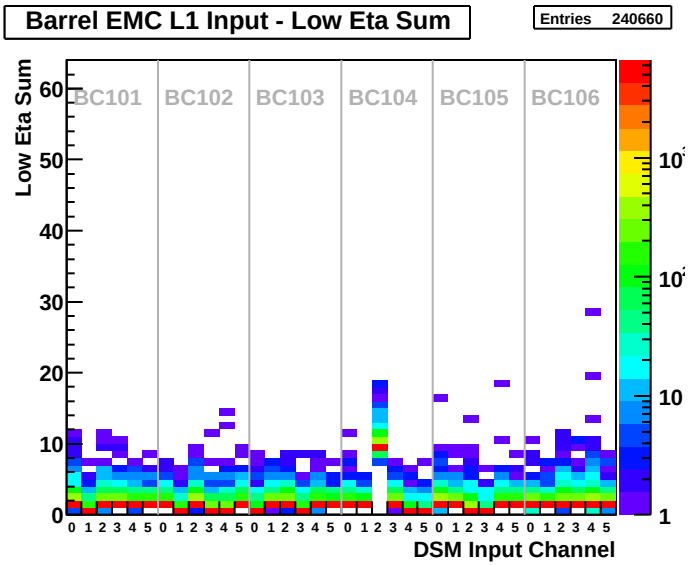
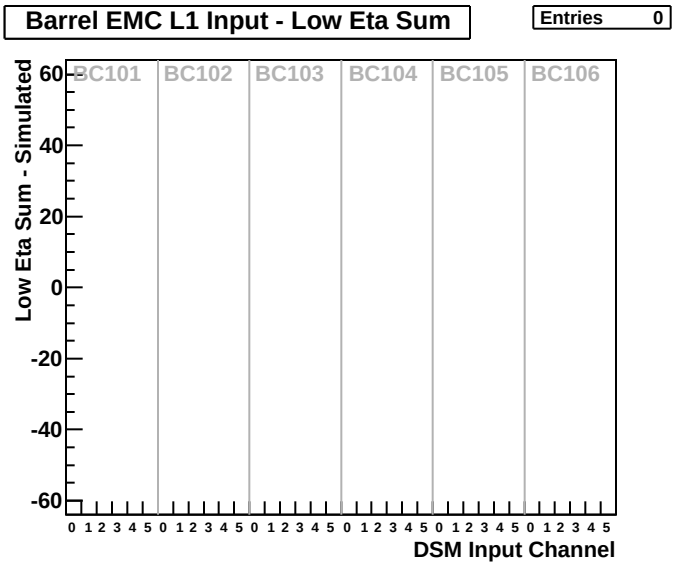
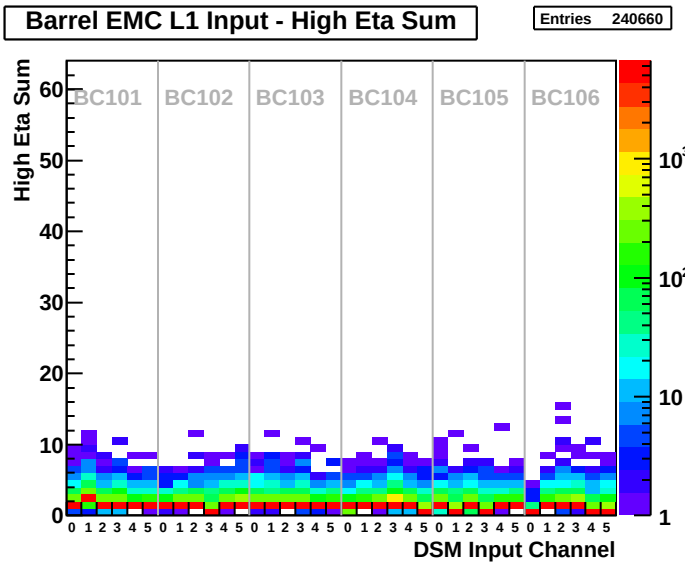
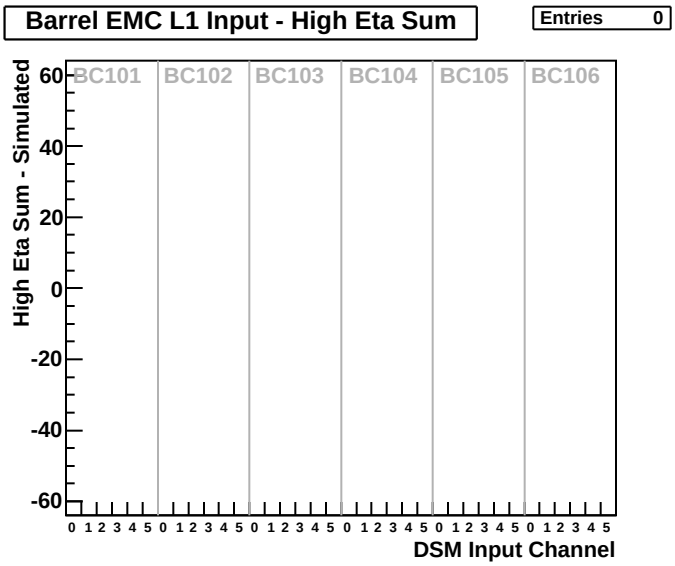
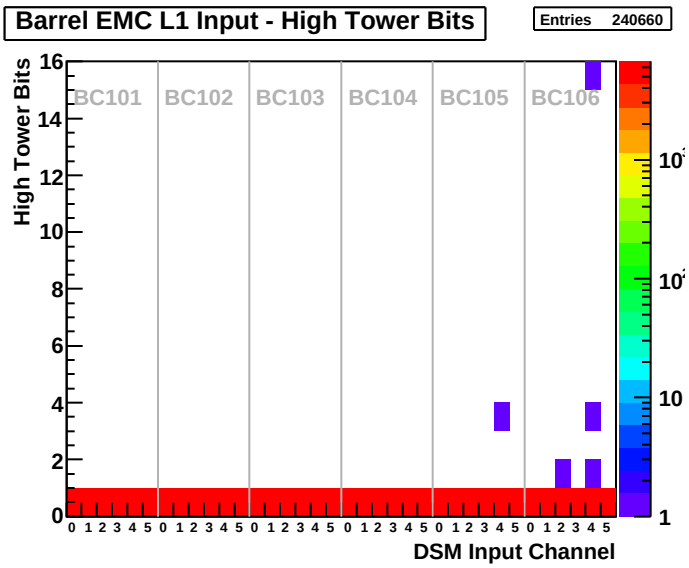
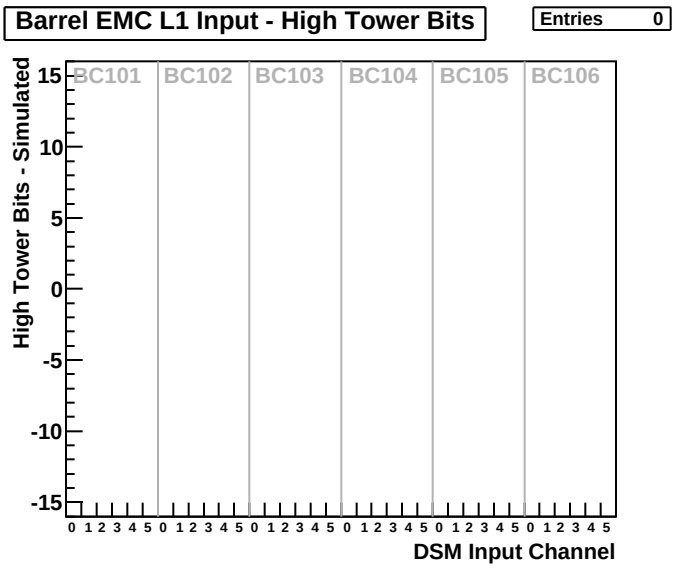

Barrel EMC L1 Input - Low Eta Sum
Entries 240660
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

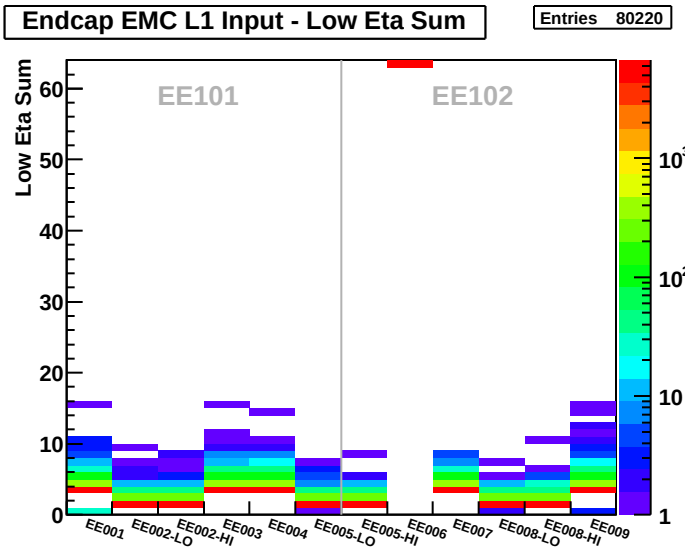

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

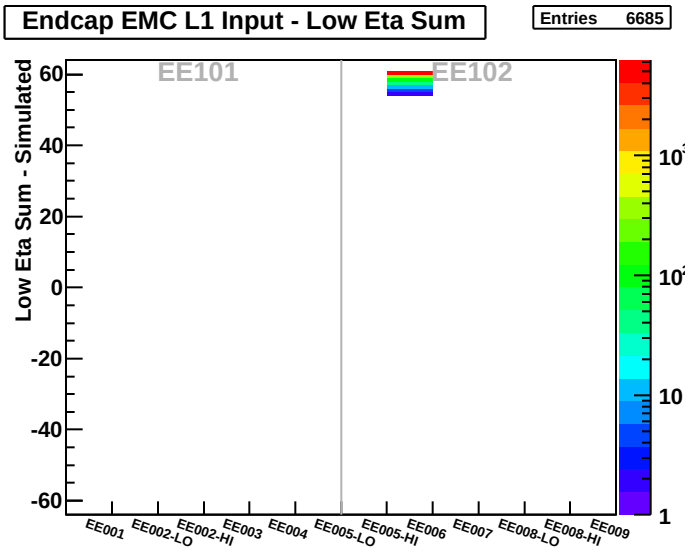

Barrel EMC L1 Input - High Eta Sum
Entries 240660
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

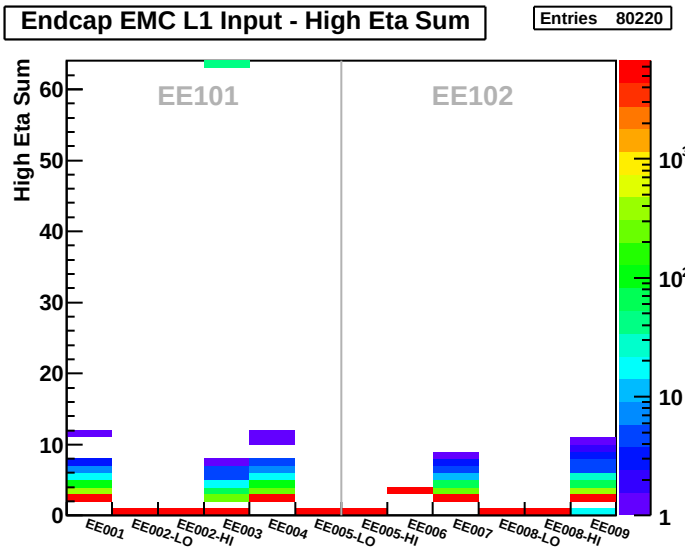

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

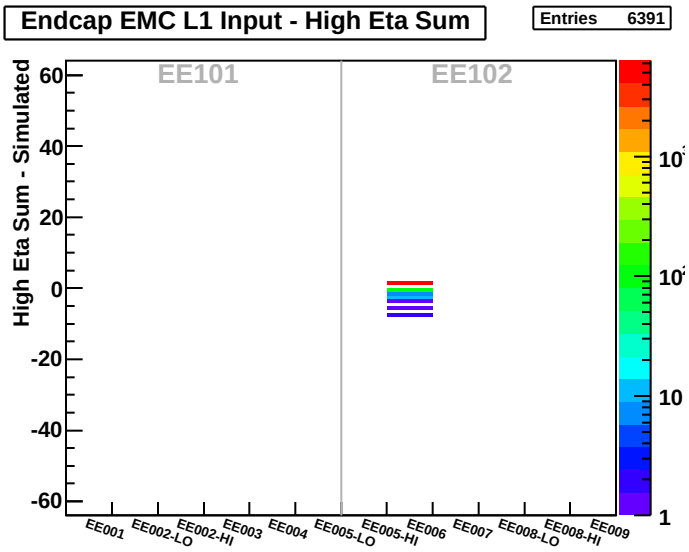

Barrel EMC L1 Input - High Tower Bits
Entries 240660
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

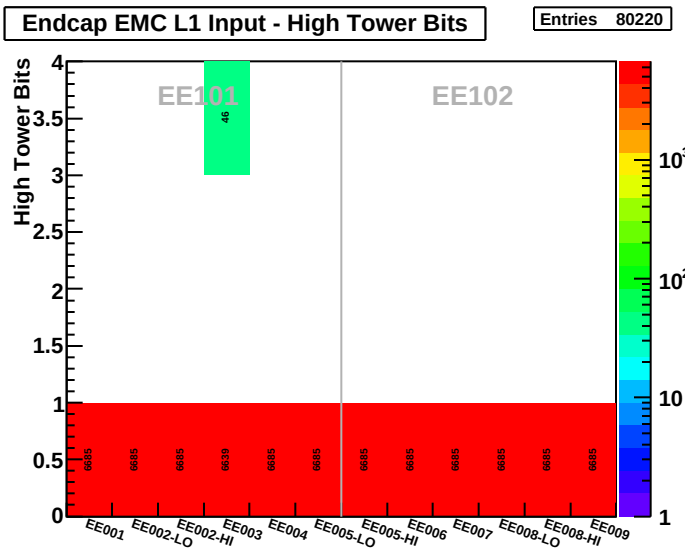

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

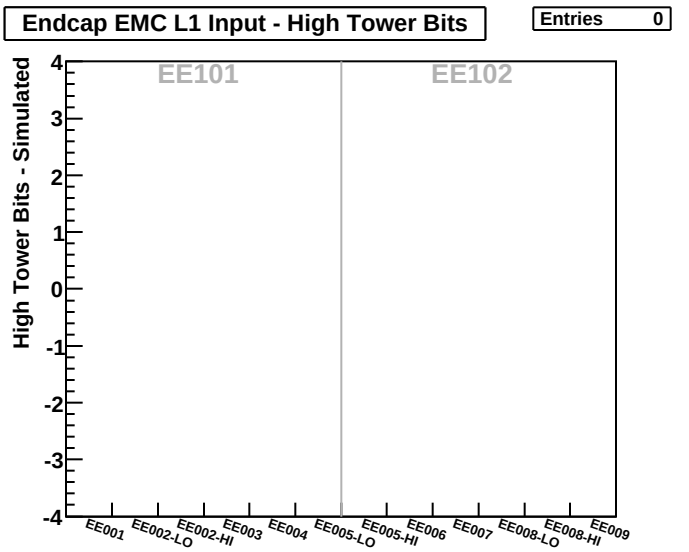

Endcap EMC L1 Input - Low Eta Sum
Entries 80220
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

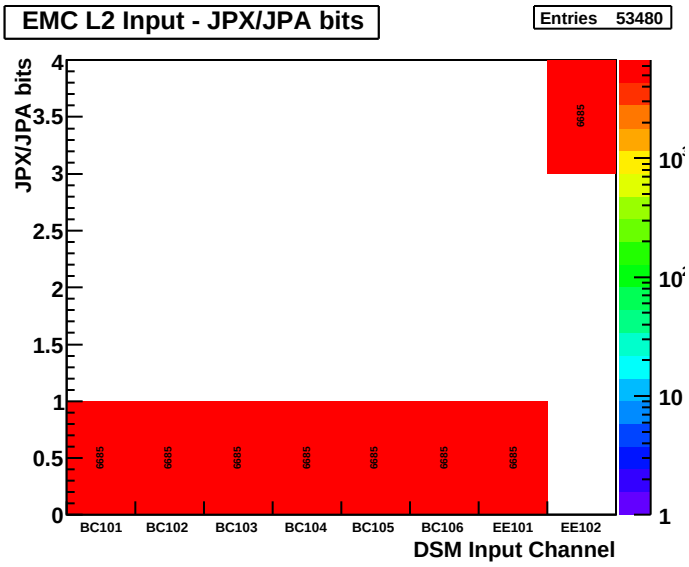

Endcap EMC L1 Input - Low Eta Sum
Entries 6685
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

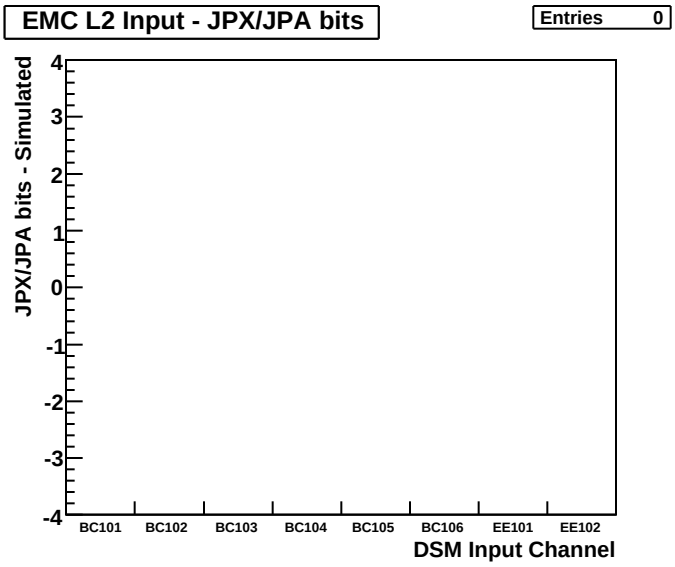

Endcap EMC L1 Input - High Eta Sum
Entries 80220
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

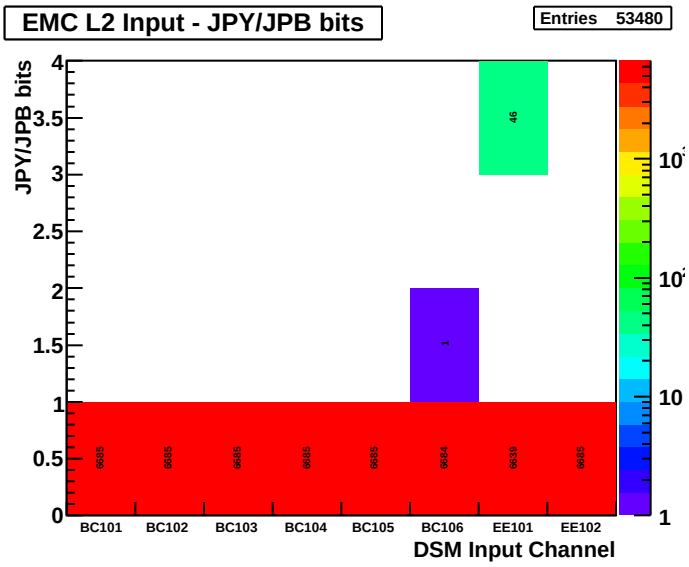

Endcap EMC L1 Input - High Eta Sum
Entries 6391
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

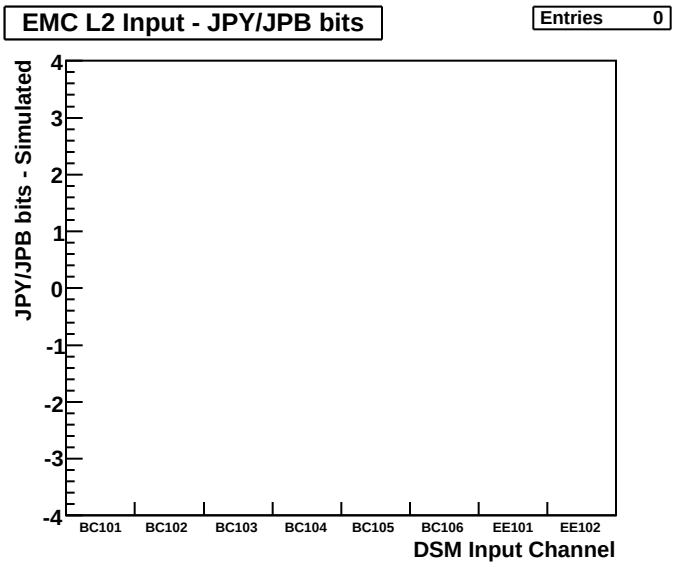

Endcap EMC L1 Input - High Tower Bits
Entries 80220
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

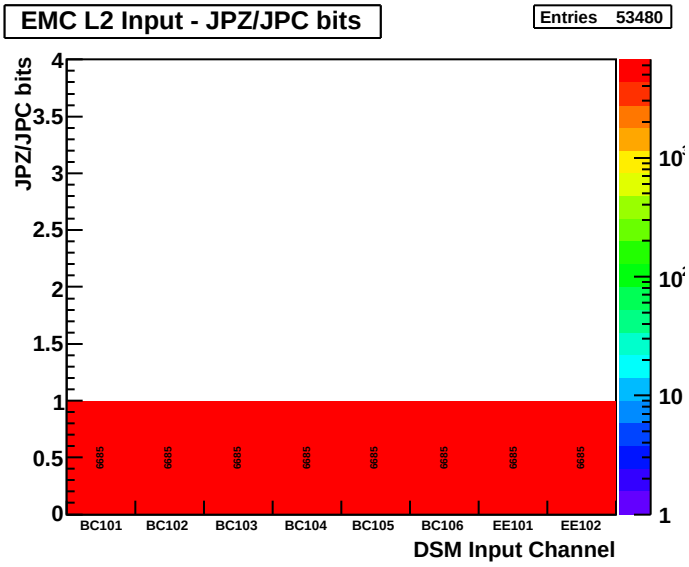

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

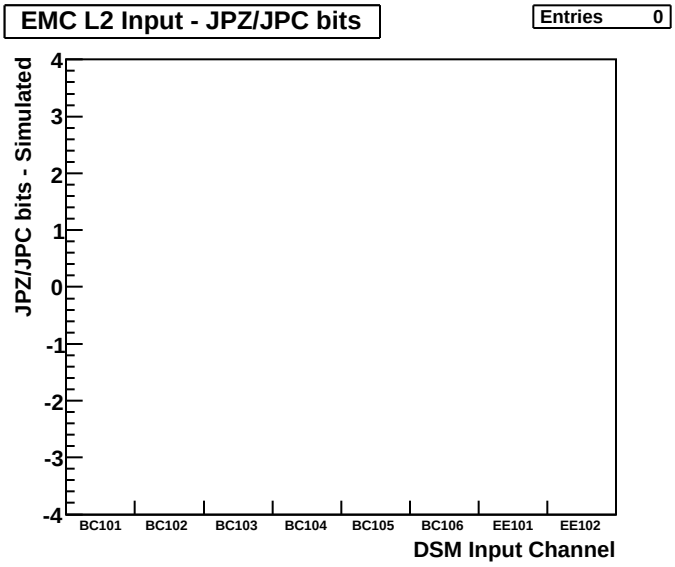

EMC L2 Input - JPX/JPA bits
Entries 53480
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6685 6685 6685 6685 6685 6685 6685 6685

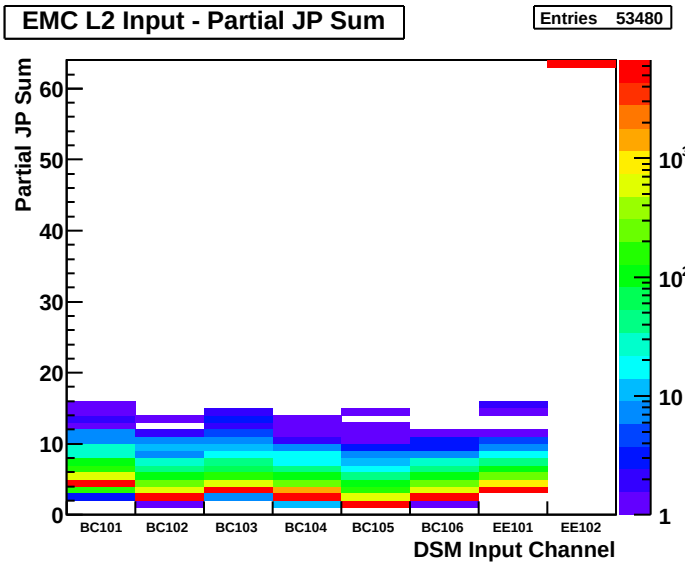

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

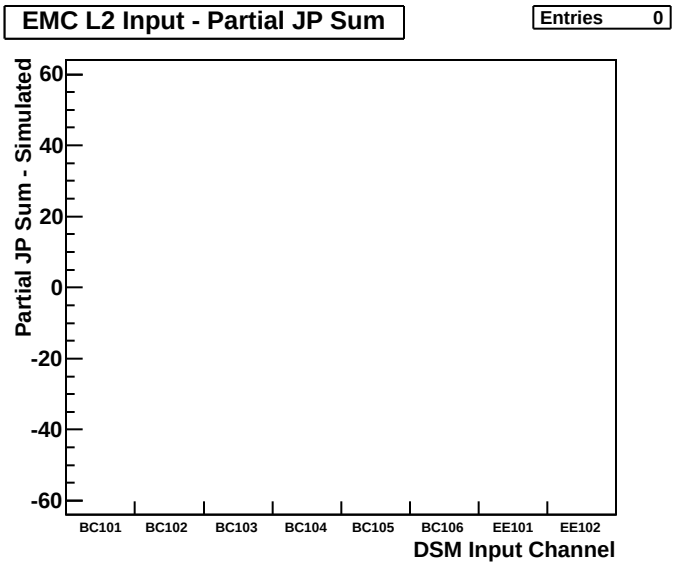

EMC L2 Input - JPY/JPB bits
Entries 53480
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6685 6685 6685 6685 6685 6684 6639 6685
46
1

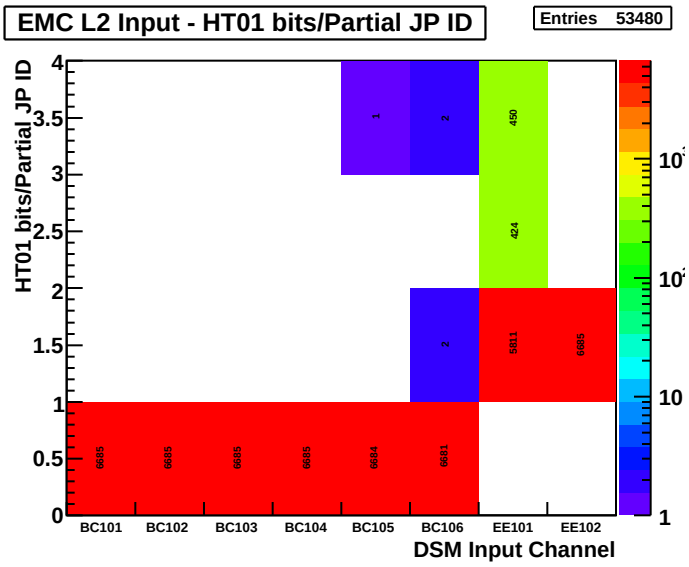

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

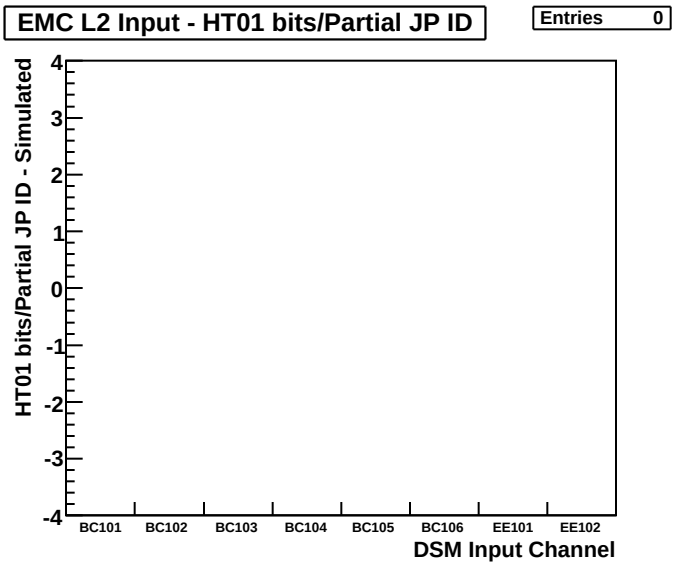

EMC L2 Input - JPZ/JPC bits
Entries 53480
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6685 6685 6685 6685 6685 6685 6685 6685

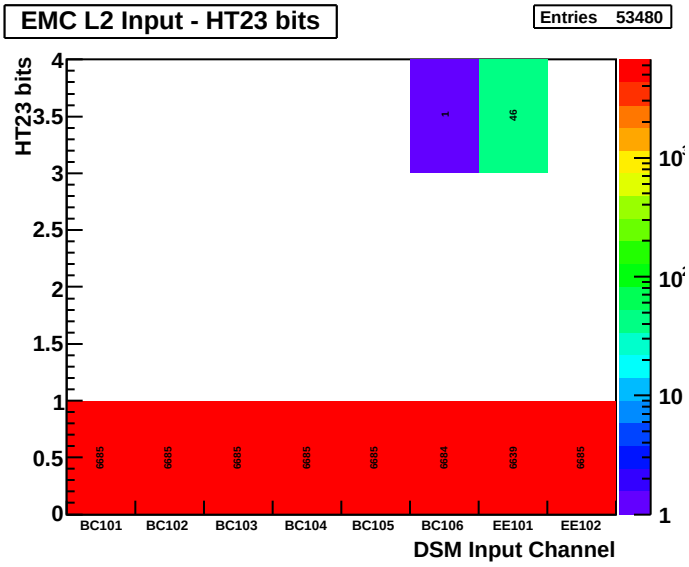

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

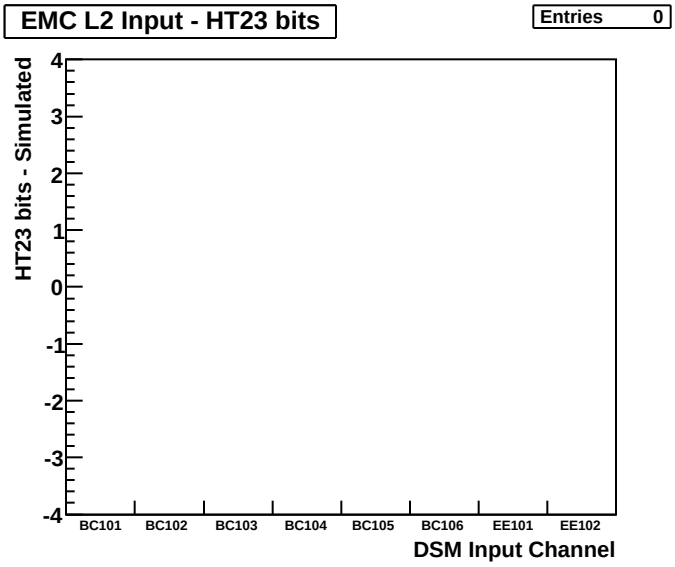

EMC L2 Input - Partial JP Sum
Entries 53480
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

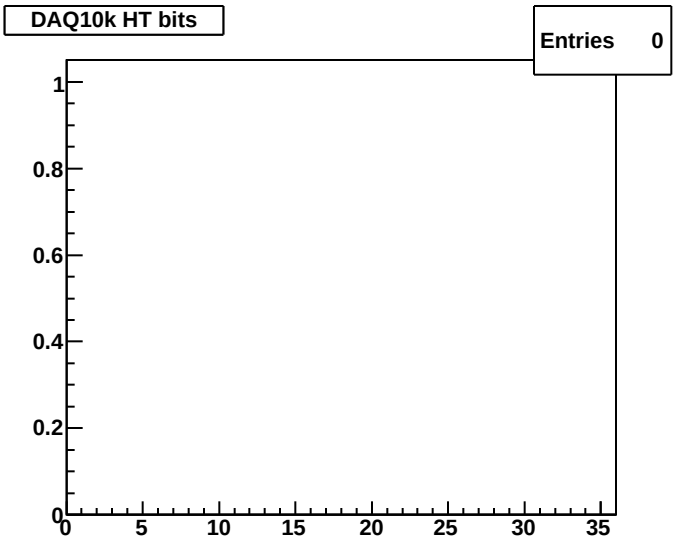

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

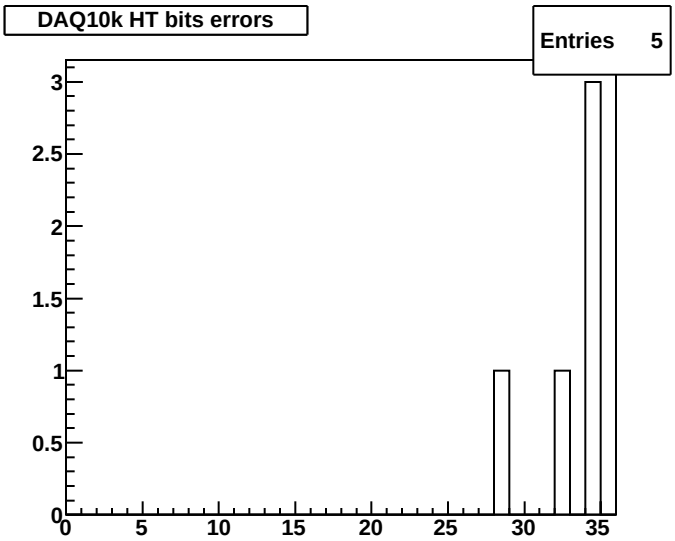

EMC L2 Input - HT01 bits/Partial JP ID
Entries 53480
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

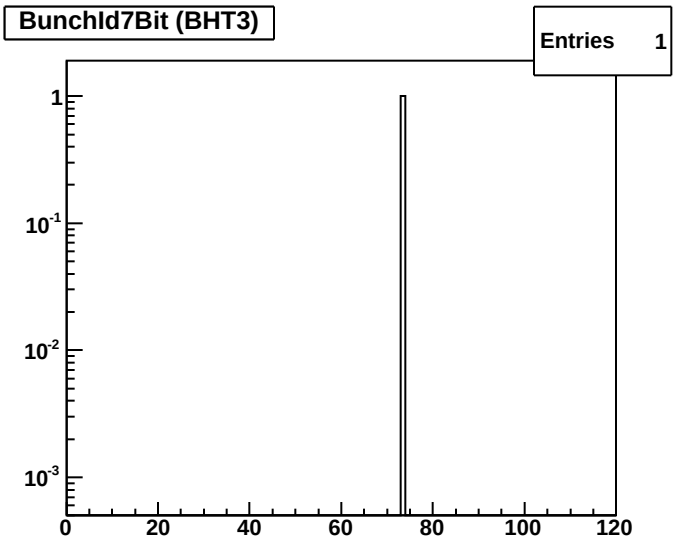

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

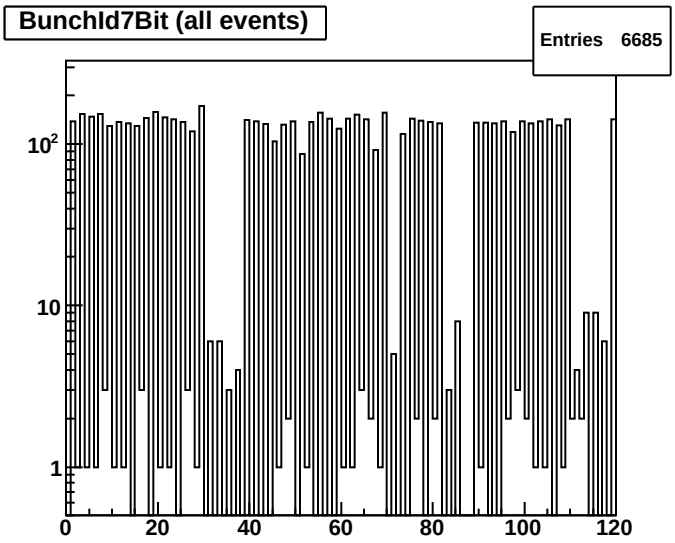

EMC L2 Input - HT23 bits
Entries 53480
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

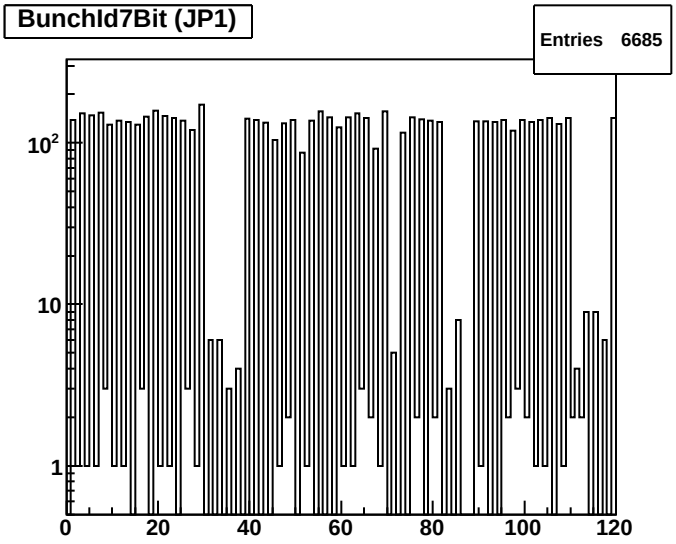

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

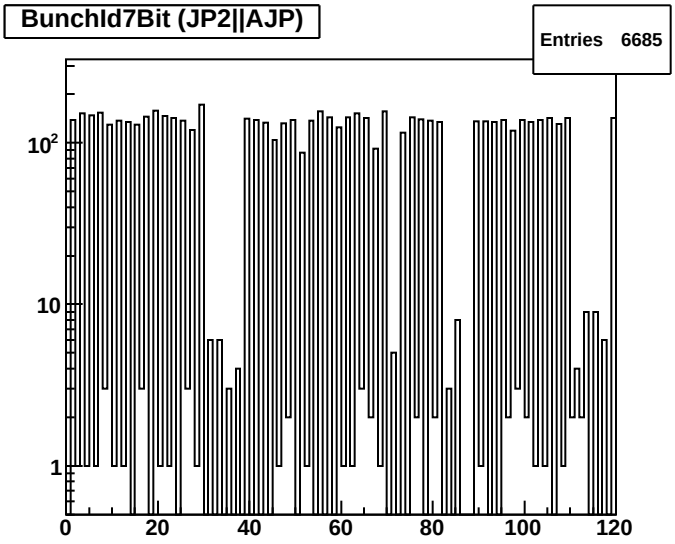

DAQ10k HT bits
Entries 0

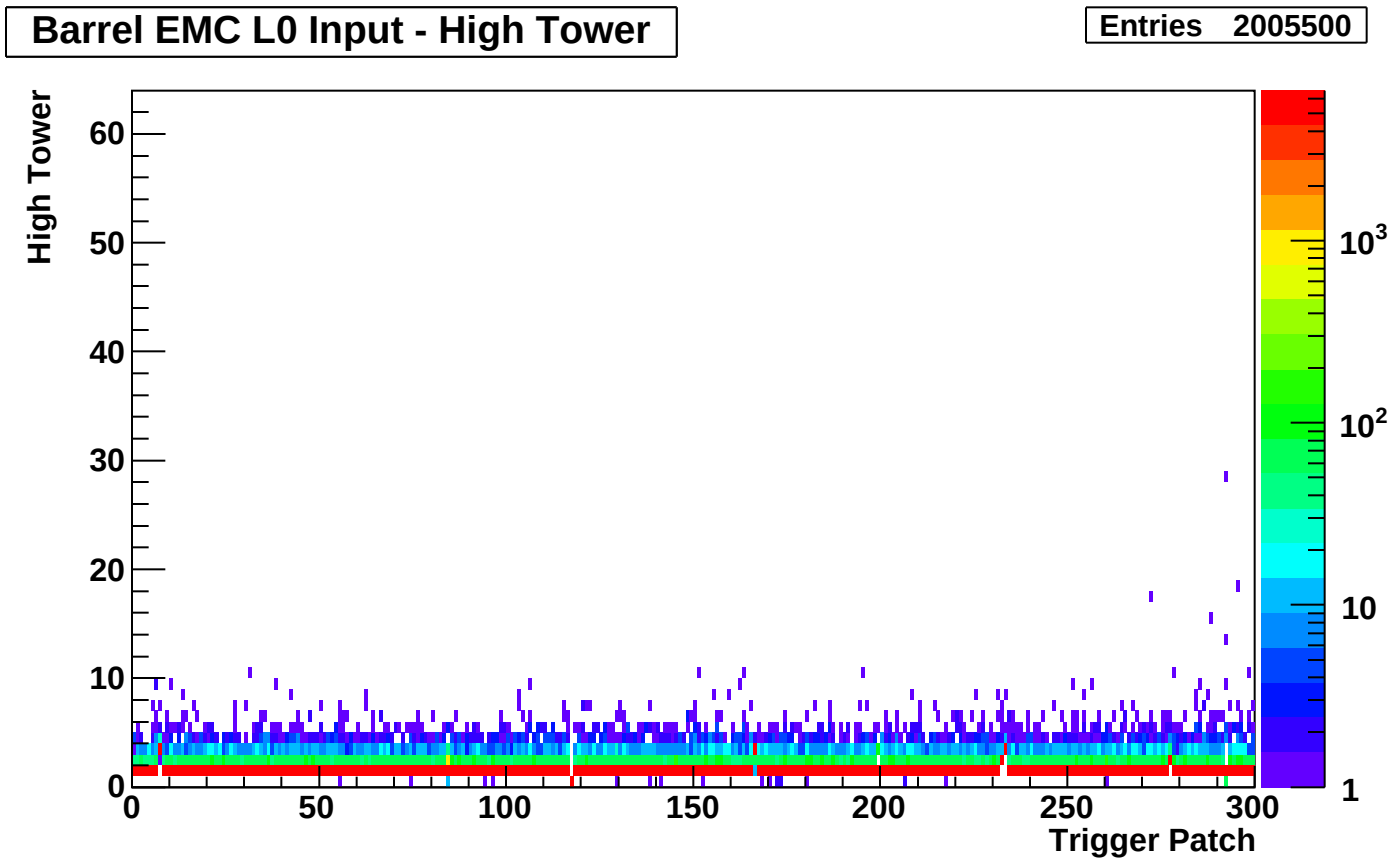

DAQ10k HT bits errors
Entries 5

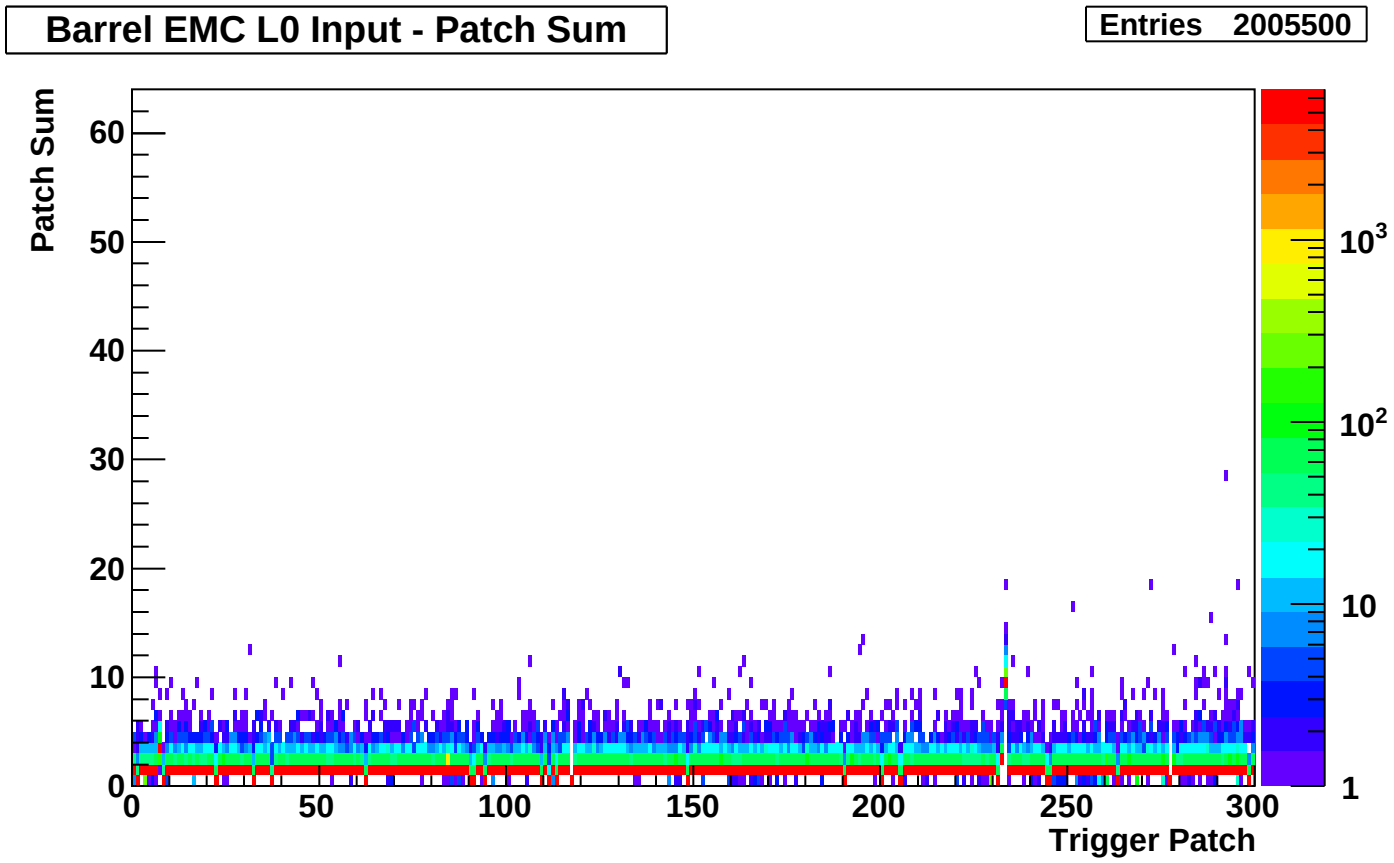

BunchId7Bit (BHT3)
Entries 1

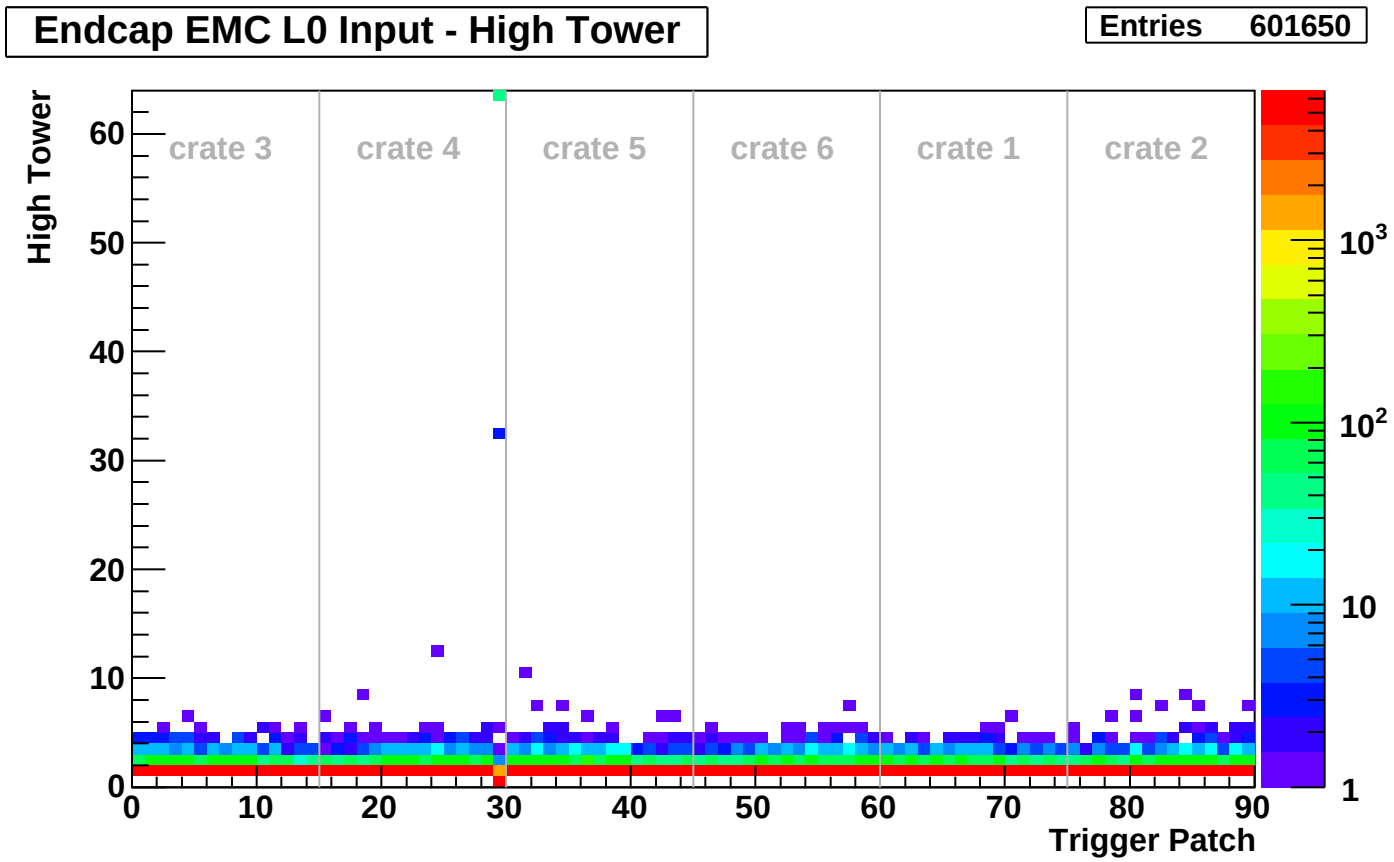

BunchId7Bit (all events)
Entries 6685

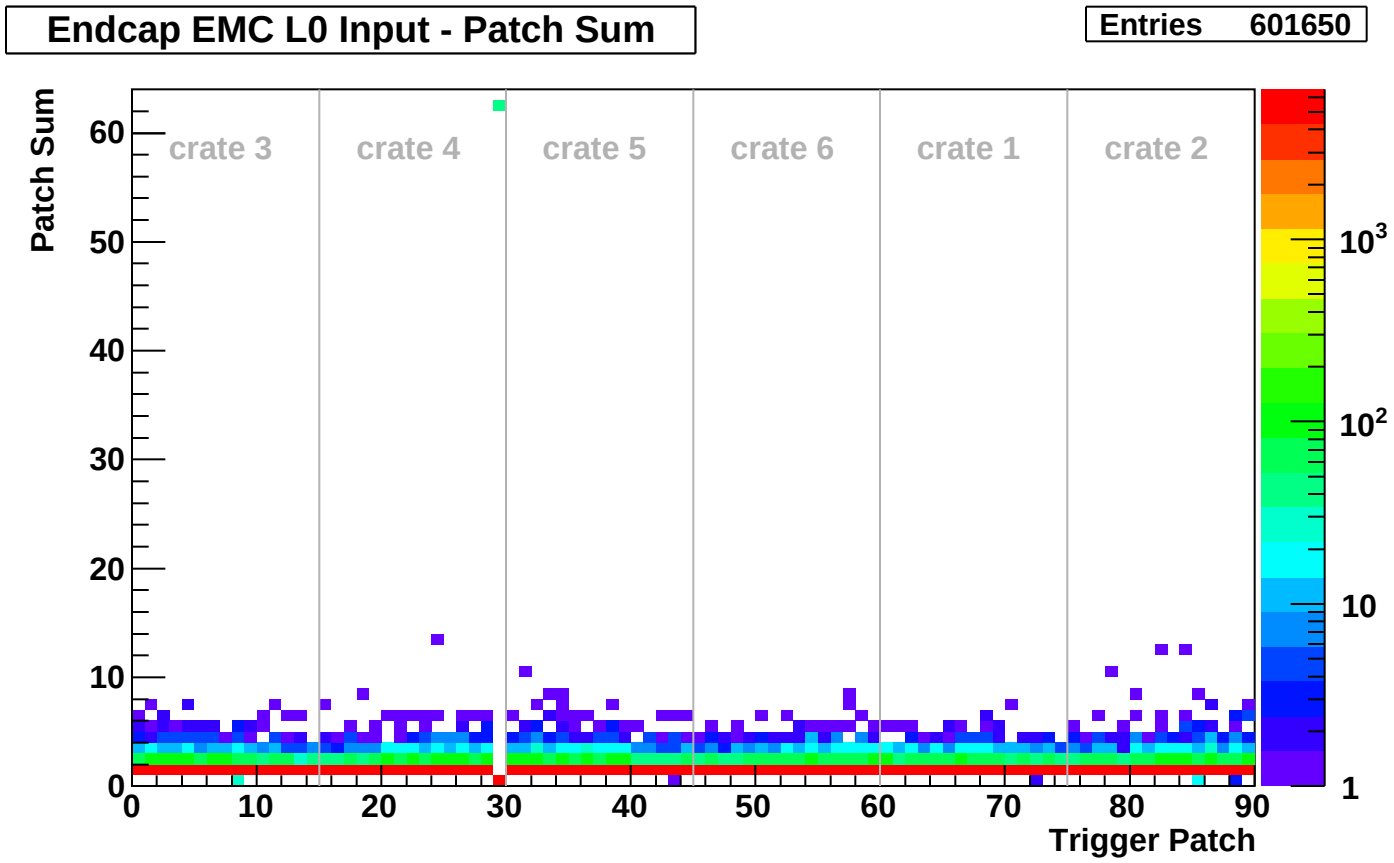

BunchId7Bit (JP1)
Entries 6685

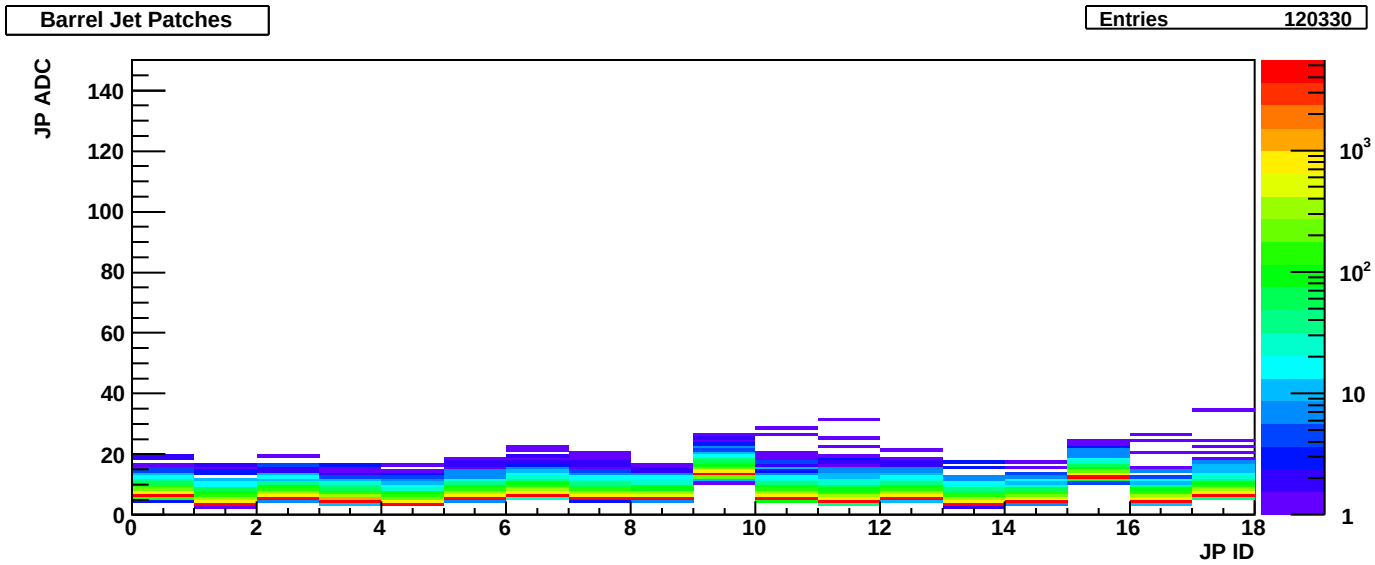

BunchId7Bit (JP2||AJP)
Entries 6685

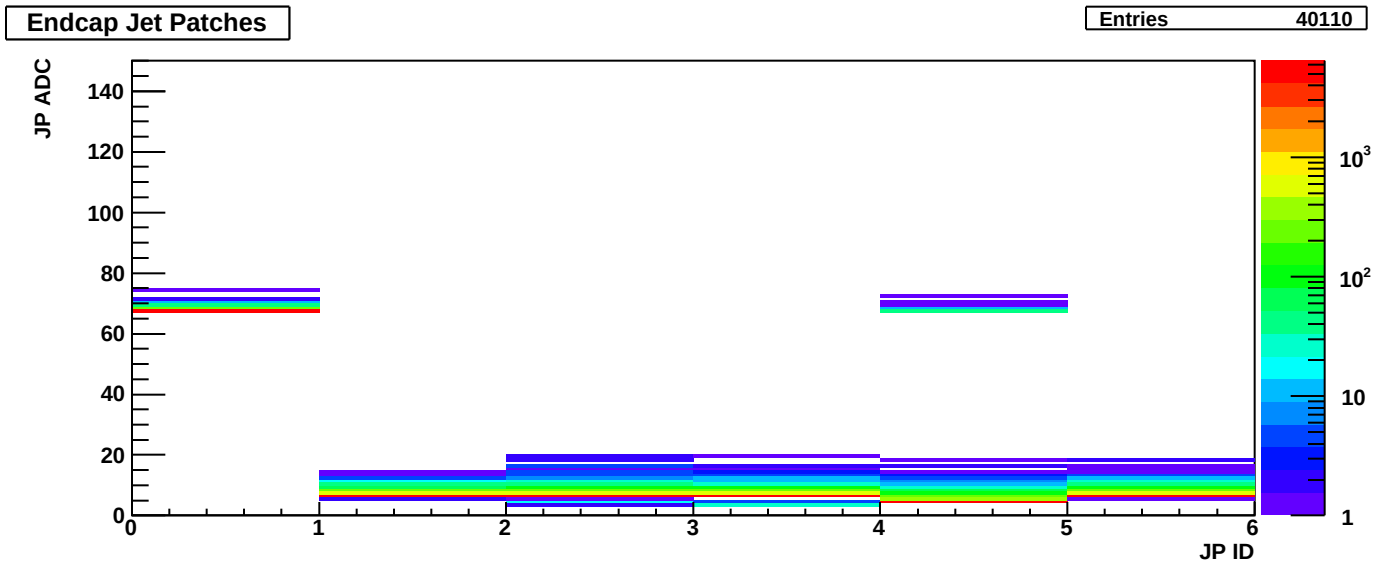

Barrel EMC L0 Input - High Tower
Entries 2005500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

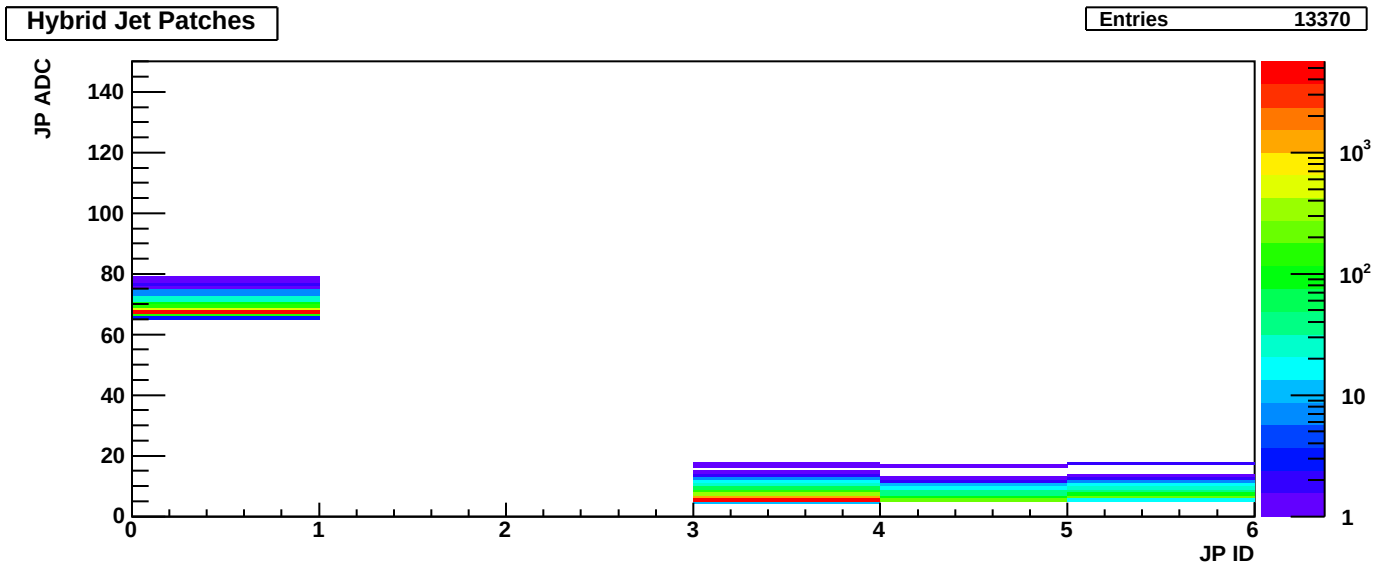

Barrel EMC L0 Input - Patch Sum
Entries 2005500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

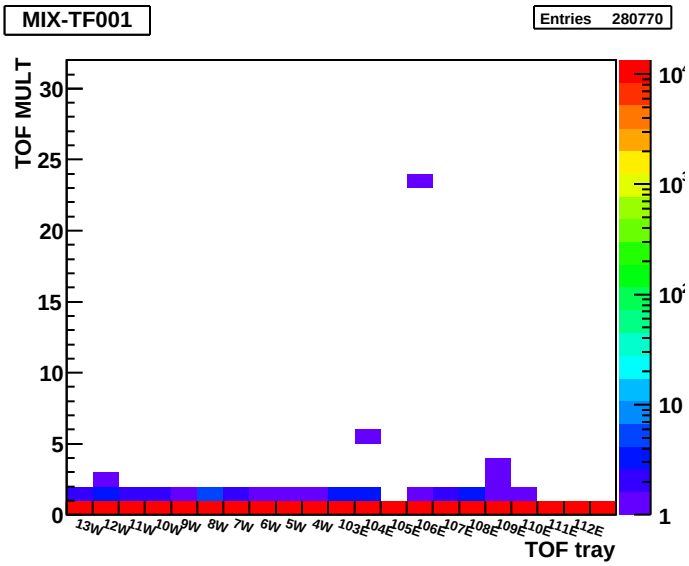

Endcap EMC L0 Input - High Tower
Entries 601650
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

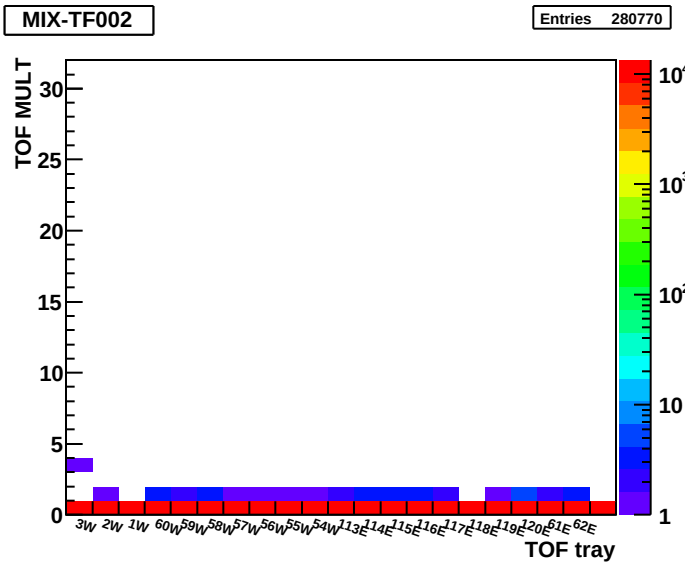

Endcap EMC L0 Input - Patch Sum
Entries 601650
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

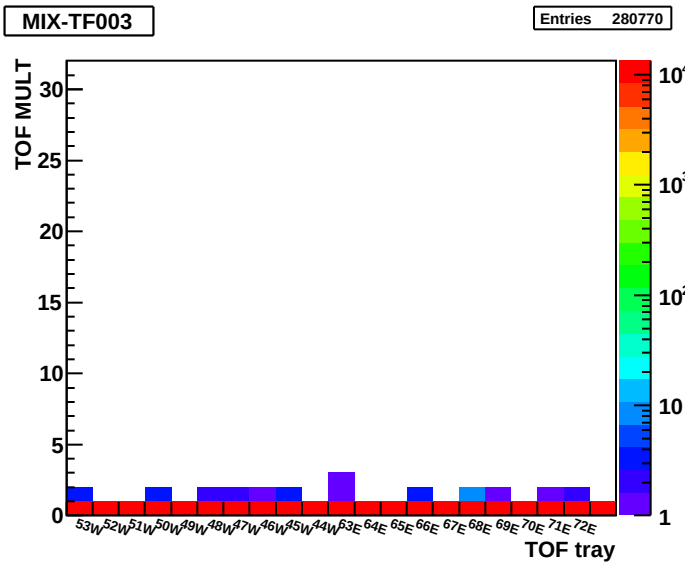

Barrel Jet Patches
Entries 120330
JP ADC
JP ID

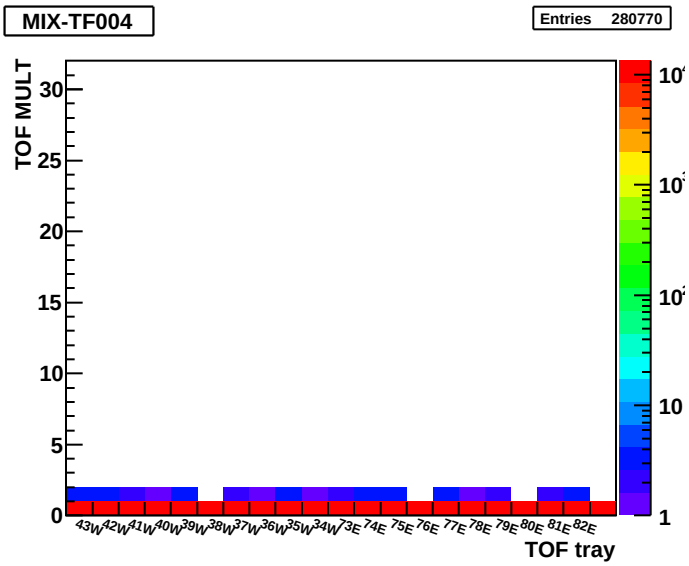

Endcap Jet Patches
Entries 40110
JP ADC
JP ID

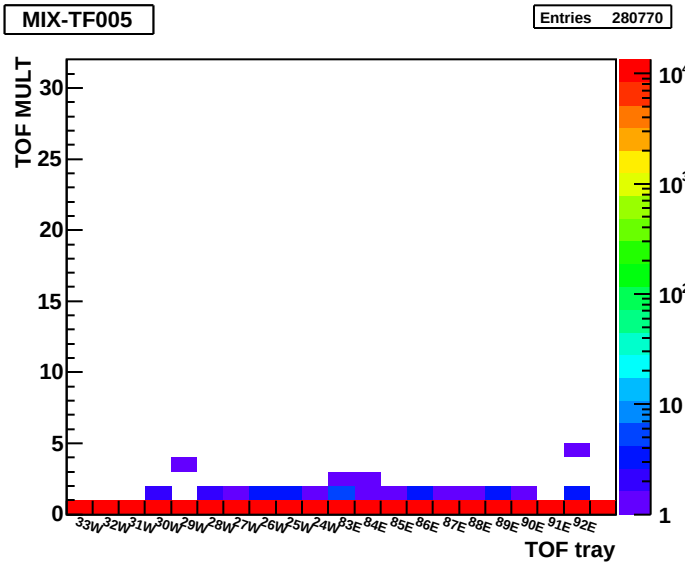

Hybrid Jet Patches
Entries 13370
JP ADC
JP ID

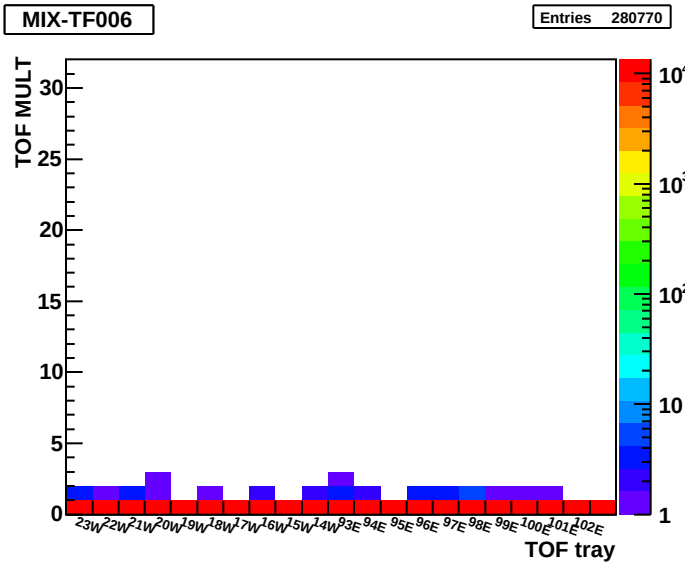

MIX-TF001
Entries 280770
TOF MULT
TOF tray

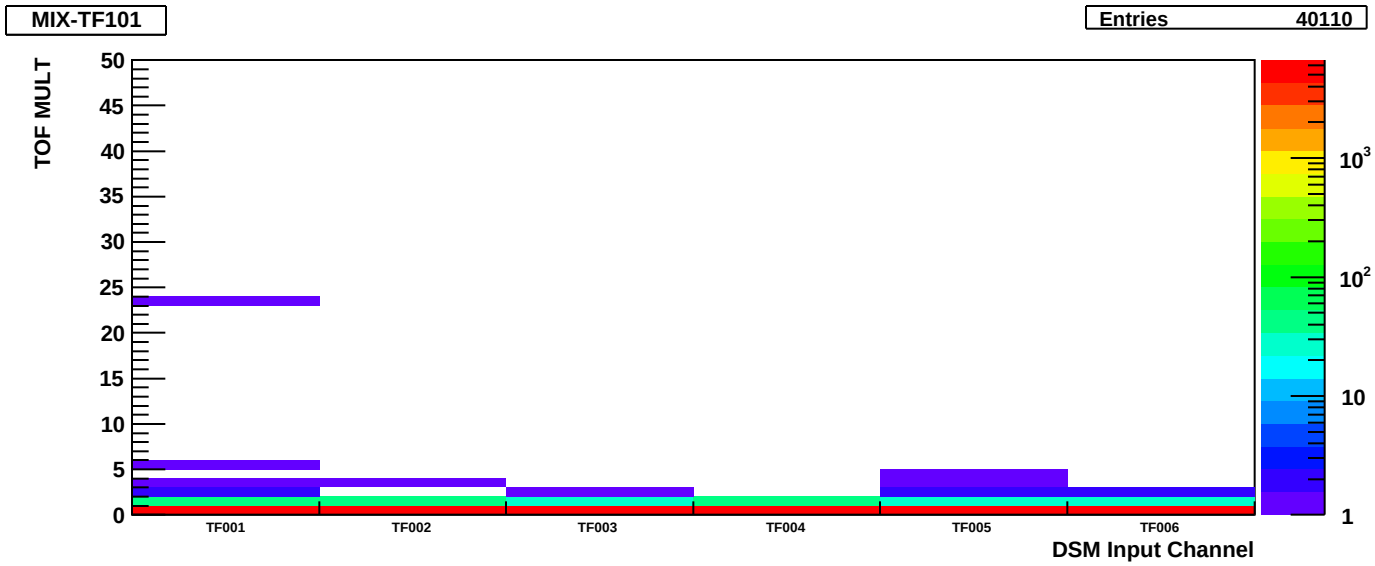

MIX-TF002
Entries 280770
TOF MULT
TOF tray

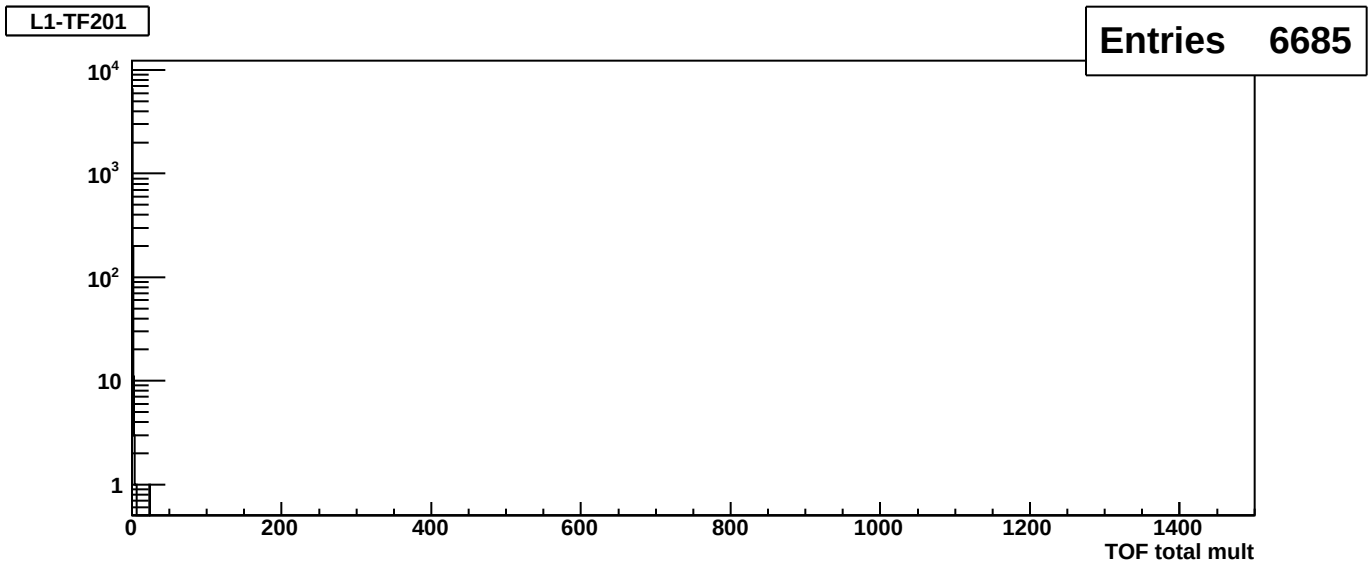

MIX-TF003
Entries 280770
TOF MULT
TOF tray

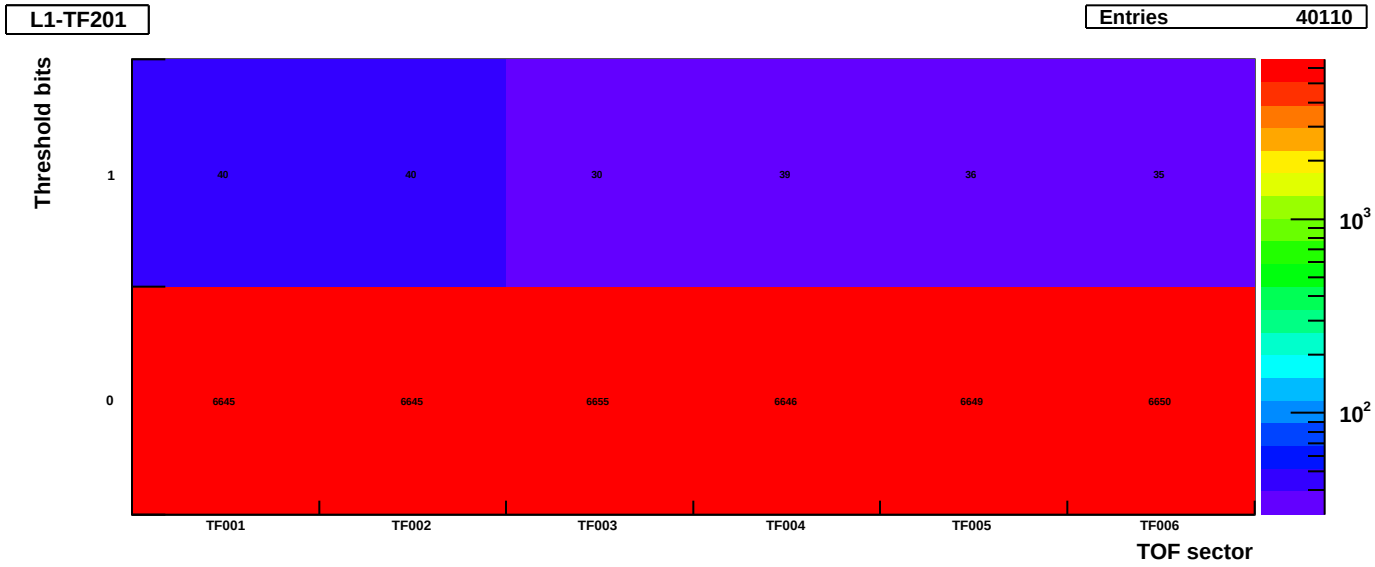

MIX-TF004
Entries 280770
TOF MULT
TOF tray

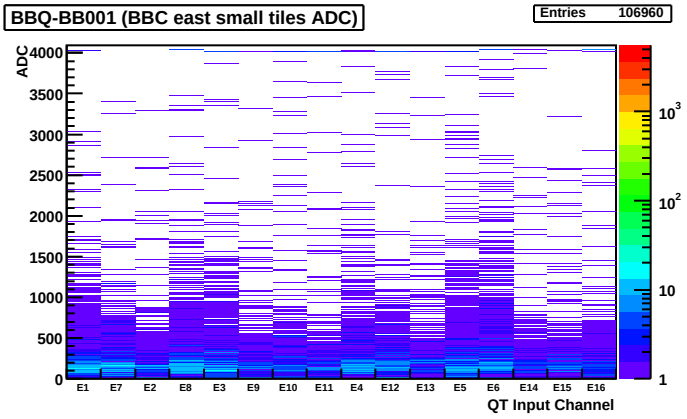

MIX-TF005
Entries 280770
TOF MULT
TOF tray

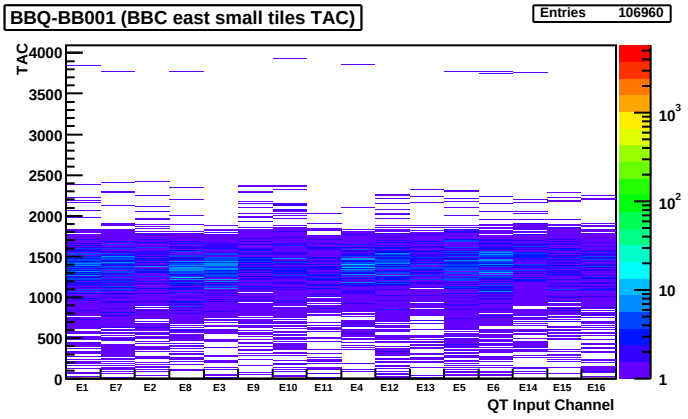

MIX-TF006
Entries 280770
TOF MULT
TOF tray

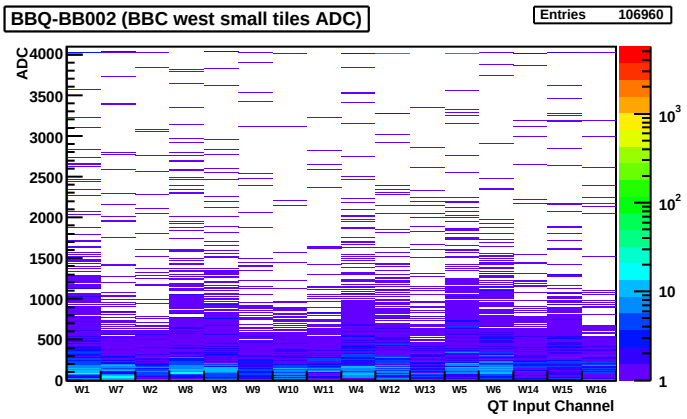

MIX-TF101
Entries 40110
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

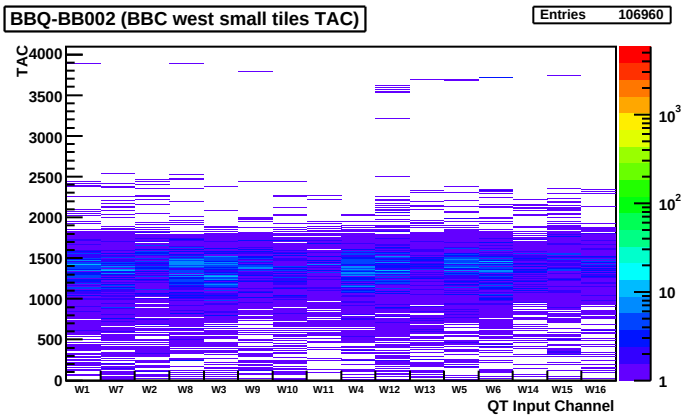

L1-TF201
Entries 6685
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

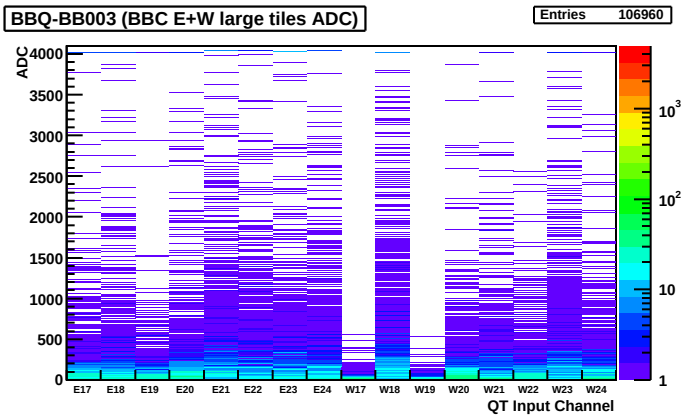

L1-TF201
Entries 40110
Threshold bits
1
0
40
40
30
39
36
35
6645
6645
6655
6646
6649
6650
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3
10^2

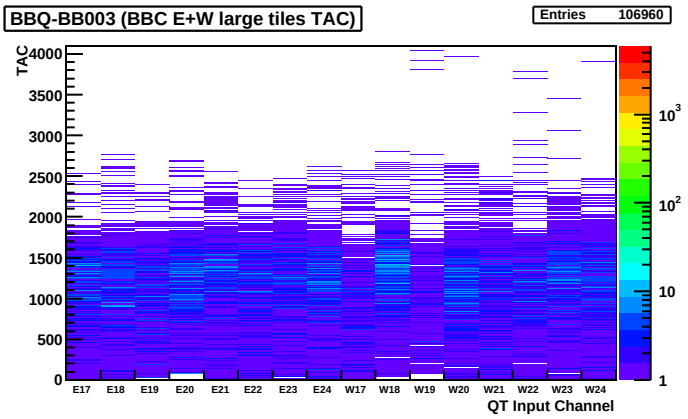

BBQ-BB001 (BBC east small tiles ADC)
Entries 106960
ADC
QT Input Channel

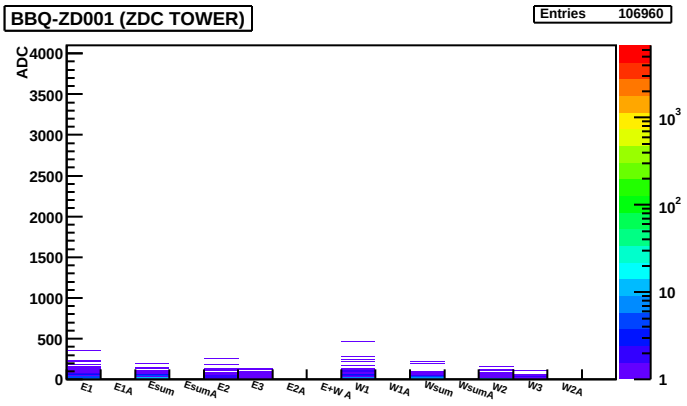

BBQ-BB001 (BBC east small tiles TAC)
Entries 106960
TAC
QT Input Channel

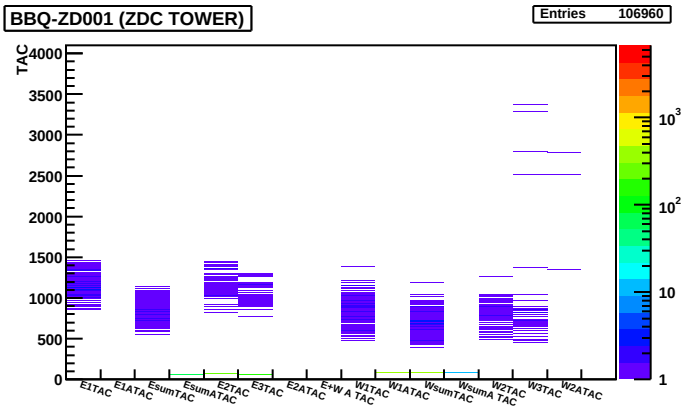

BBQ-BB002 (BBC west small tiles ADC)
Entries 106960
ADC
QT Input Channel

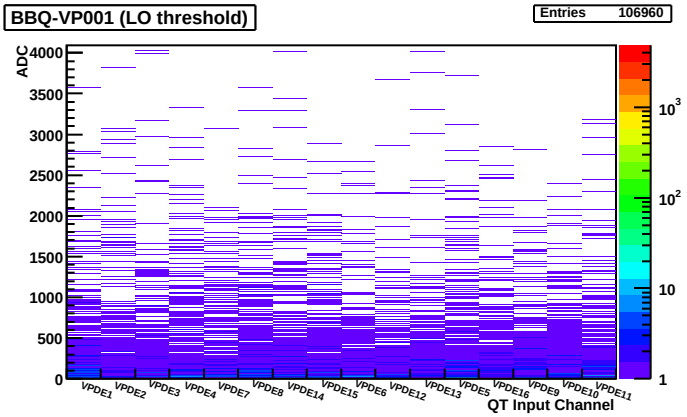

BBQ-BB002 (BBC west small tiles TAC)
Entries 106960
TAC
QT Input Channel

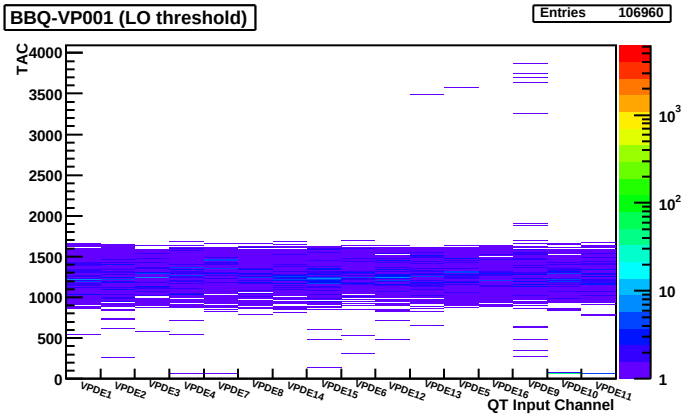

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 106960
ADC
QT Input Channel

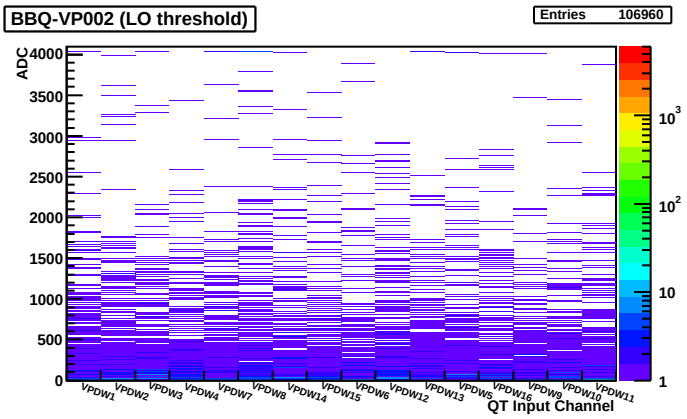

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 106960
TAC
QT Input Channel

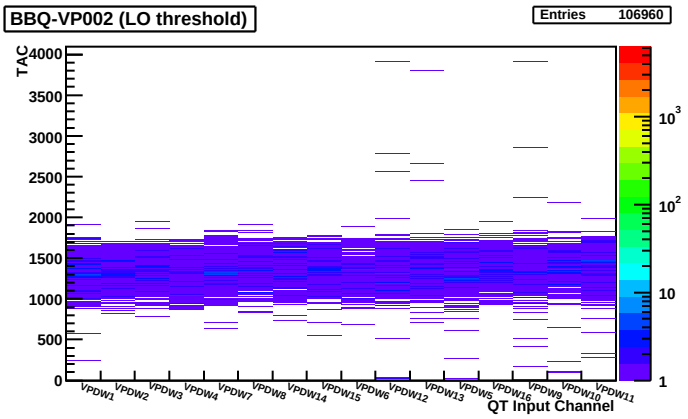

BBQ-ZD001 (ZDC TOWER)
Entries 106960
ADC

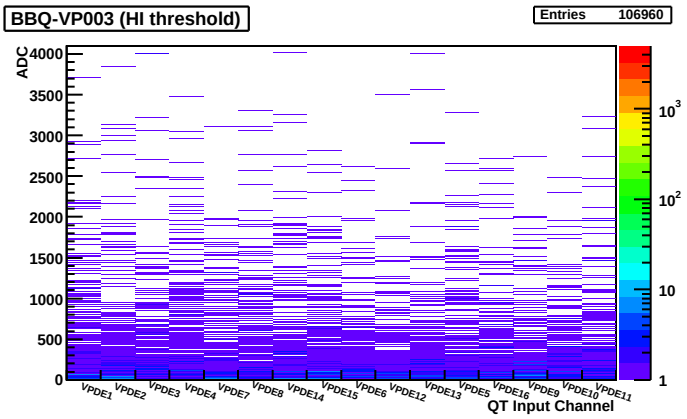

BBQ-ZD001 (ZDC TOWER)
Entries 106960
TAC

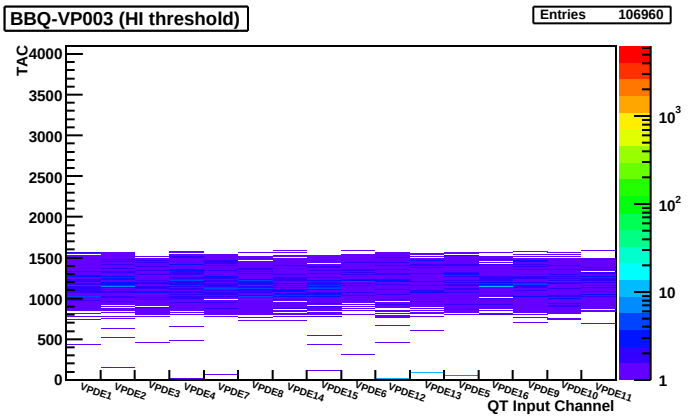

BBQ-VP001 (LO threshold)
Entries 106960
ADC
QT Input Channel

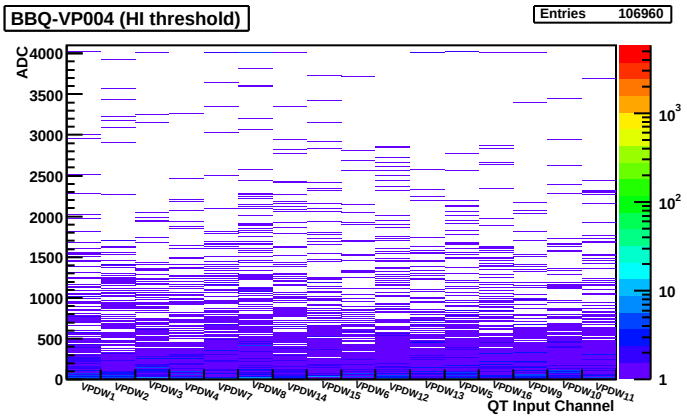

BBQ-VP001 (LO threshold)
Entries 106960
TAC
QT Input Channel

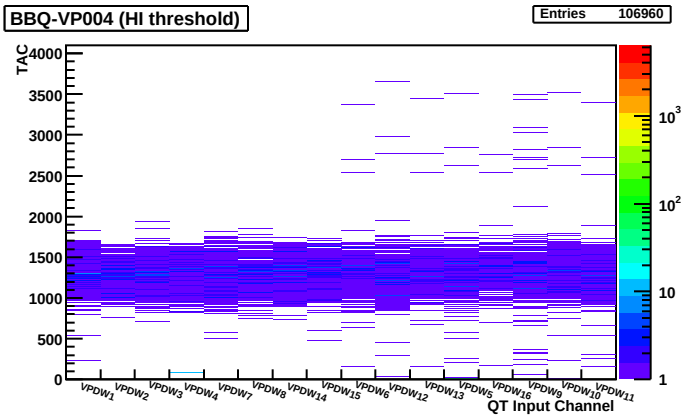

BBQ-VP002 (LO threshold)
Entries 106960
ADC
QT Input Channel

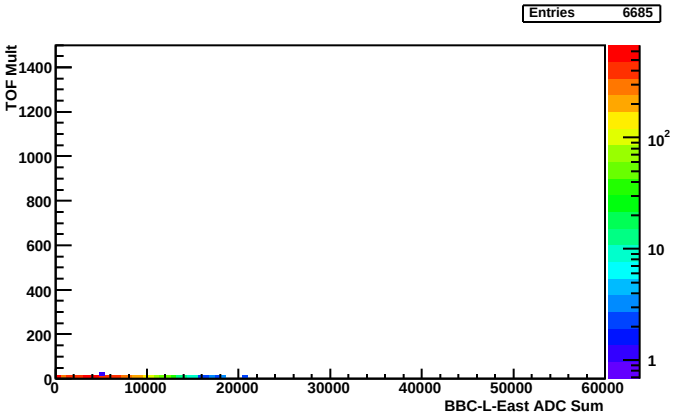

BBQ-VP002 (LO threshold)
Entries 106960
TAC
QT Input Channel

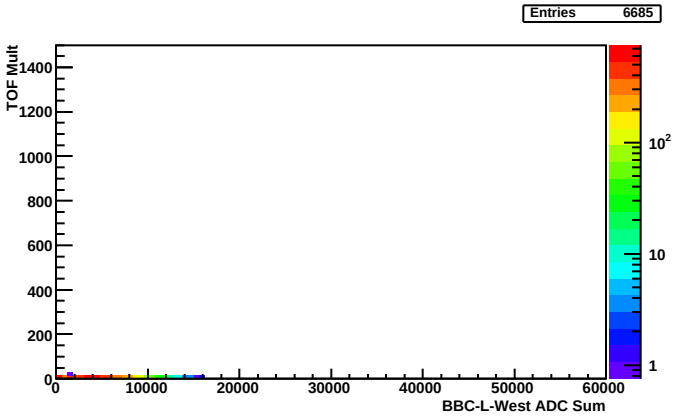

BBQ-VP003 (HI threshold)
Entries 106960
ADC
QT Input Channel

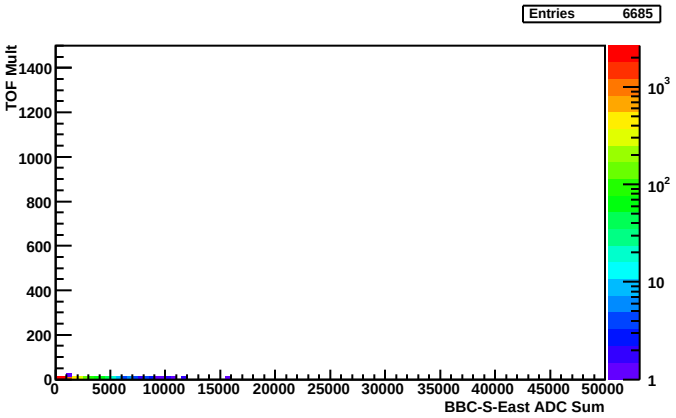

BBQ-VP003 (HI threshold)
Entries 106960
TAC
QT Input Channel

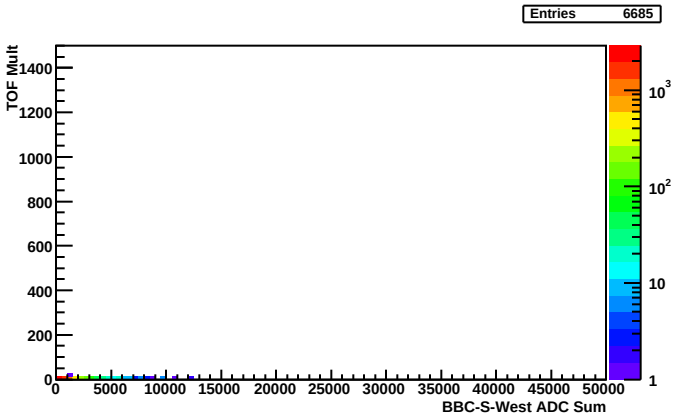

BBQ-VP004 (HI threshold)
Entries 106960
ADC
QT Input Channel

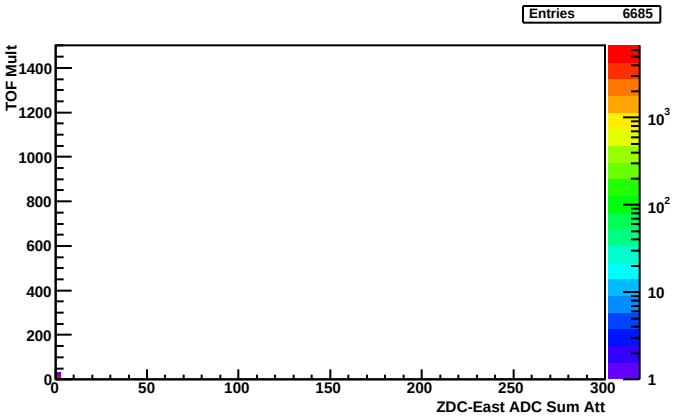

BBQ-VP004 (HI threshold)
Entries 106960
TAC
QT Input Channel

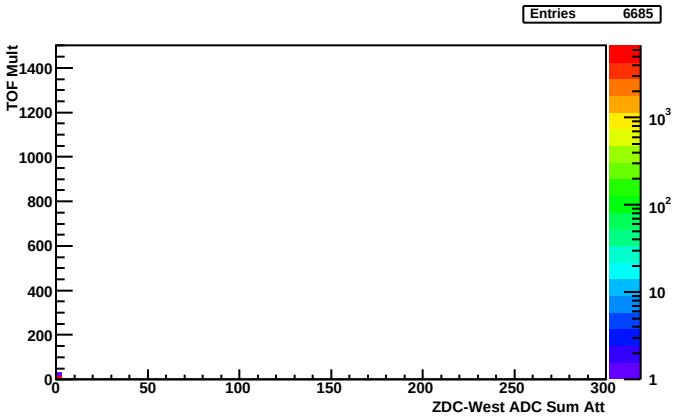

Entries 6685
TOF Mult
BBC-L-East ADC Sum

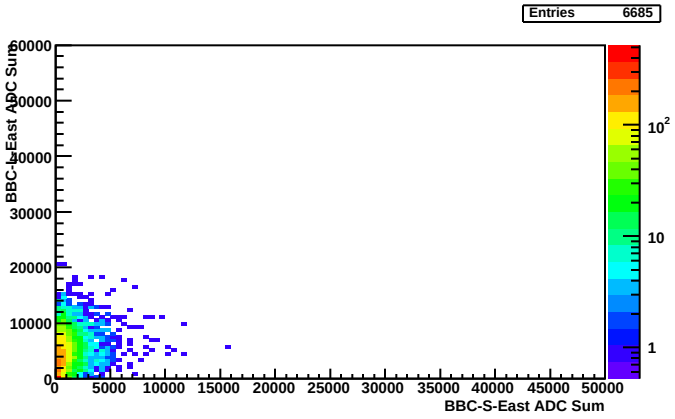

Entries 6685
TOF Mult
BBC-L-West ADC Sum

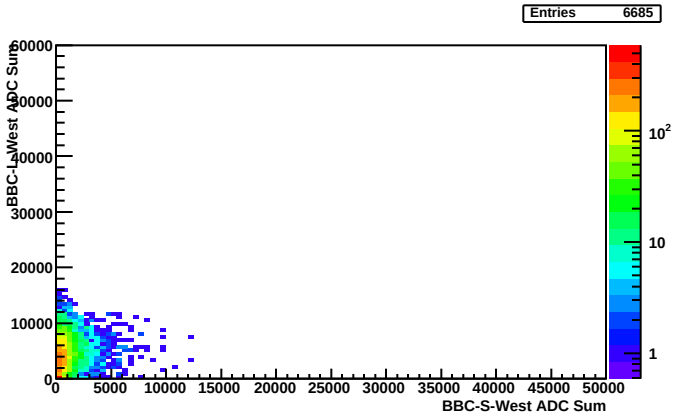

Entries 6685
TOF Mult
BBC-S-East ADC Sum

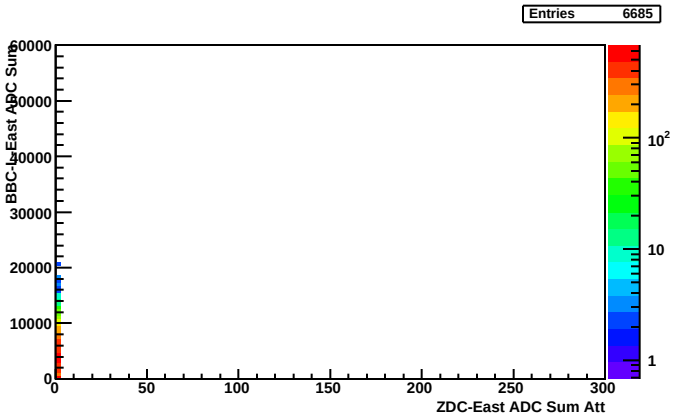

Entries 6685
TOF Mult
BBC-S-West ADC Sum

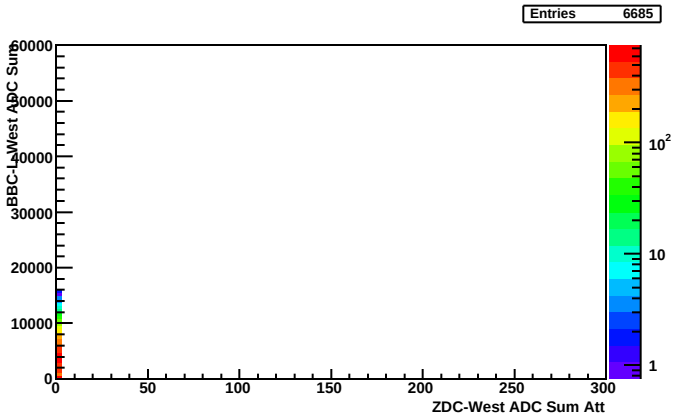

Entries 6685
TOF Mult
ZDC-East ADC Sum Att

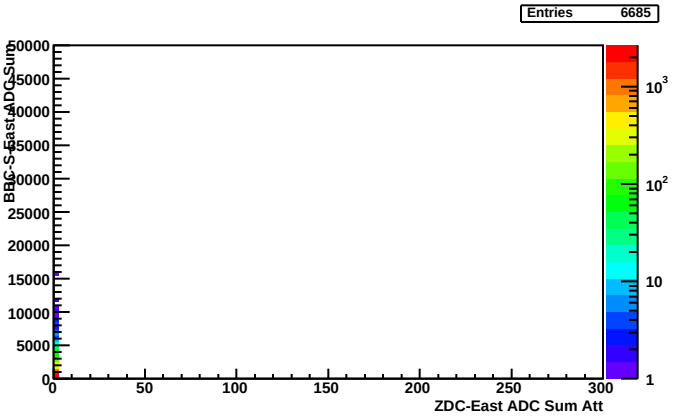

Entries 6685
TOF Mult
ZDC-West ADC Sum Att

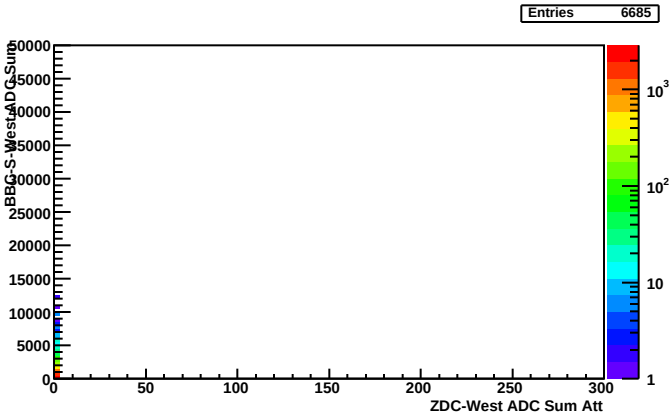

Entries 6685
BBC-L-East ADC Sum
BBC-S-East ADC Sum

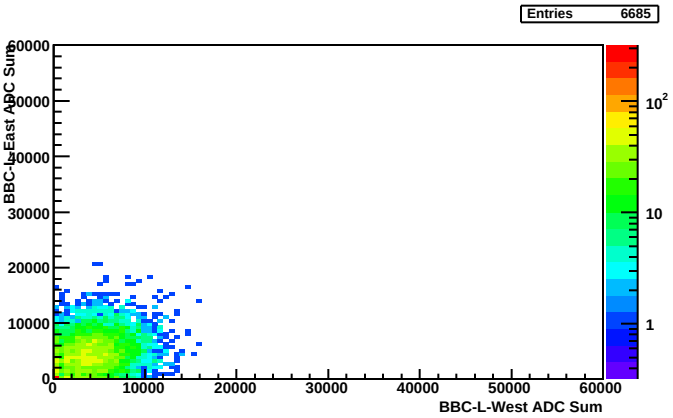

Entries 6685
BBC-L-West ADC Sum
BBC-S-West ADC Sum

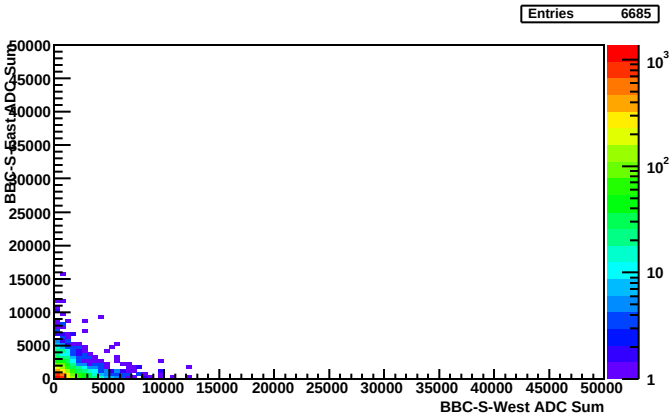

Entries 6685
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

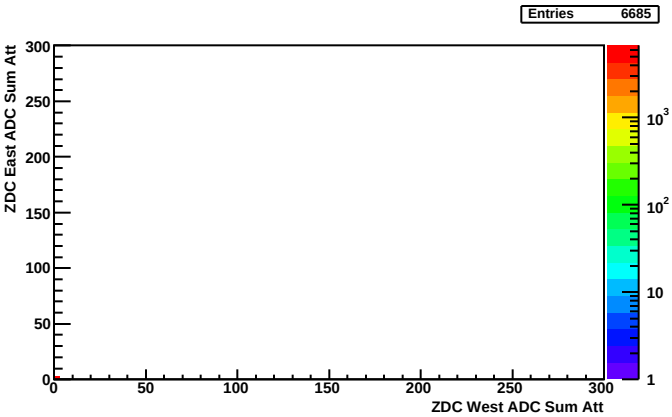

Entries 6685
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

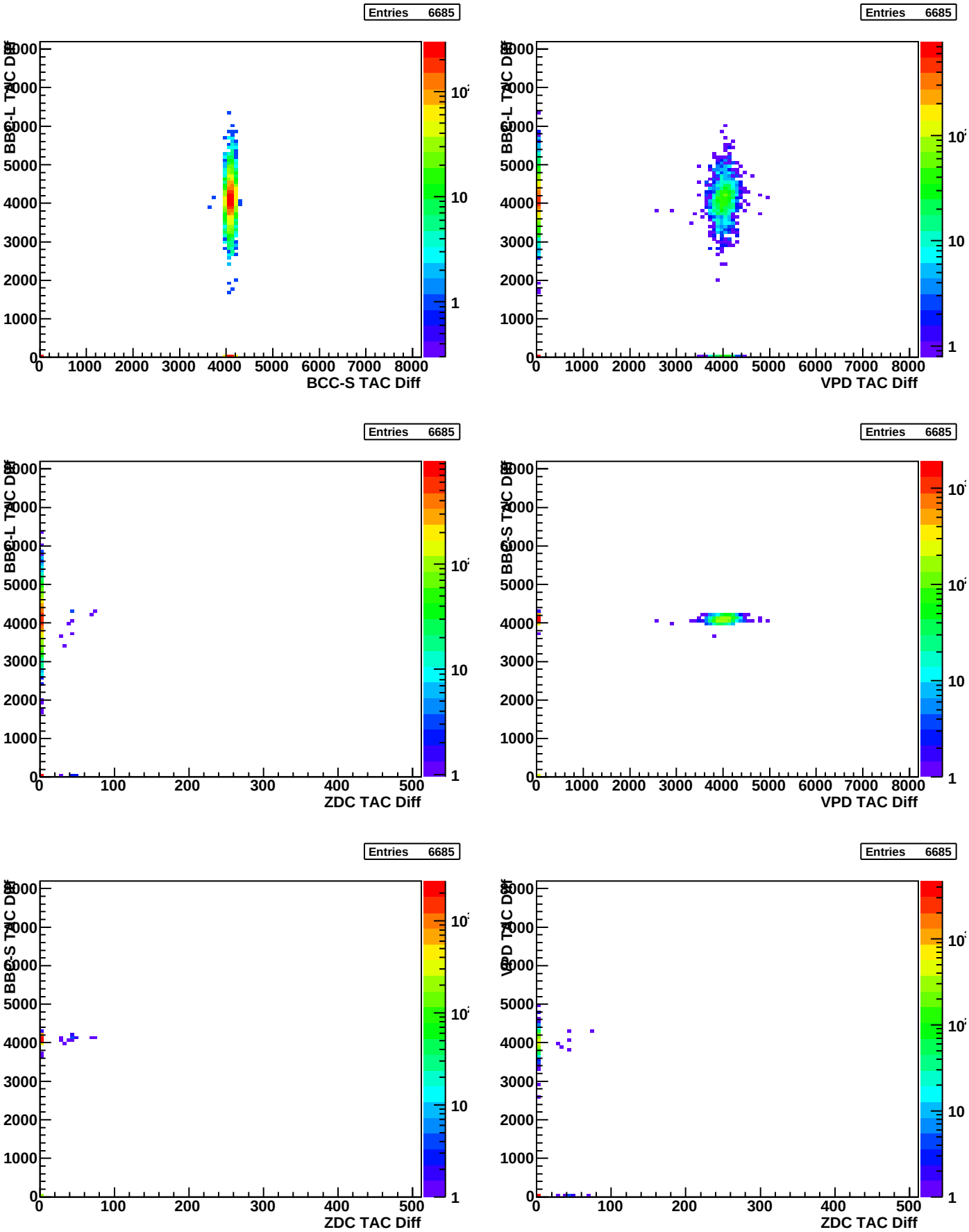

Entries 6685
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

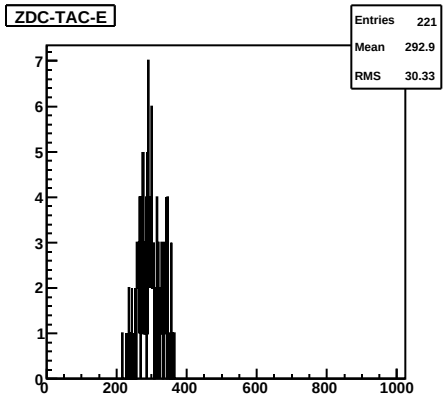

Entries 6685
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

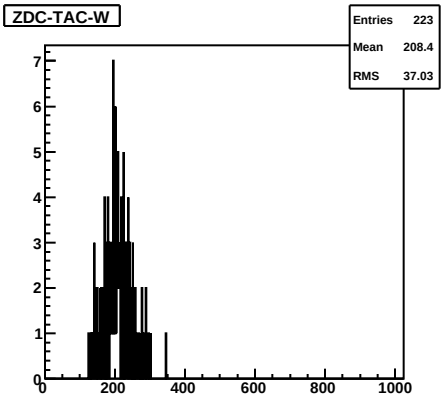

Entries 6685
BBC-L-East ADC Sum
BBC-L-West ADC Sum

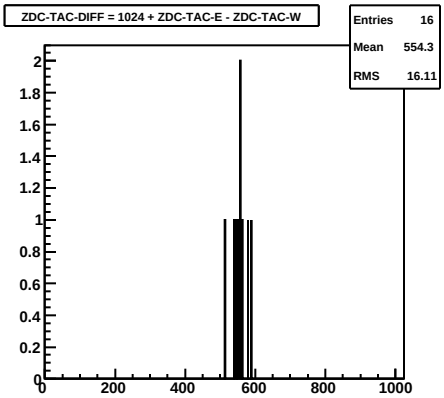

Entries 6685
BBC-S-East ADC Sum
BBC-S-West ADC Sum

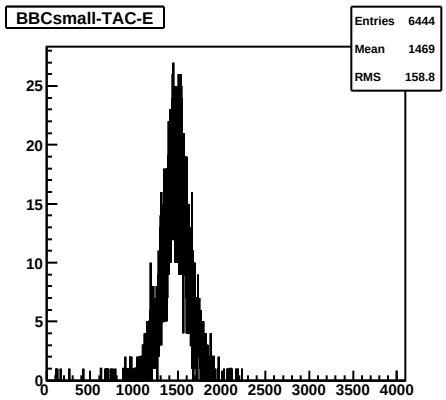

Entries 6685
ZDC East ADC Sum Att
ZDC West ADC Sum Att

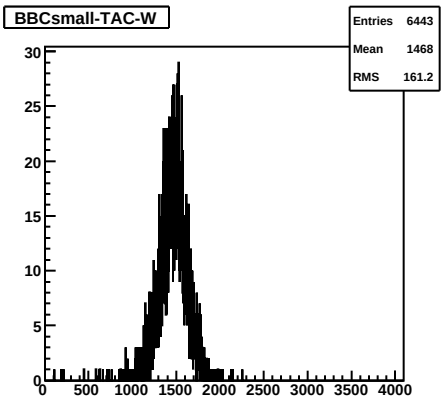

Entries 6685
BBC-L TAC Diff
BCC-S TAC Diff
Entries 6685
BBC-L TAC Diff
VPD TAC Diff
Entries 6685
BBC-L TAC Diff
ZDC TAC Diff
Entries 6685
BBC-S TAC Diff
VPD TAC Diff
Entries 6685
BBC-S TAC Diff
ZDC TAC Diff
Entries 6685
VPD TAC Diff
ZDC TAC Diff

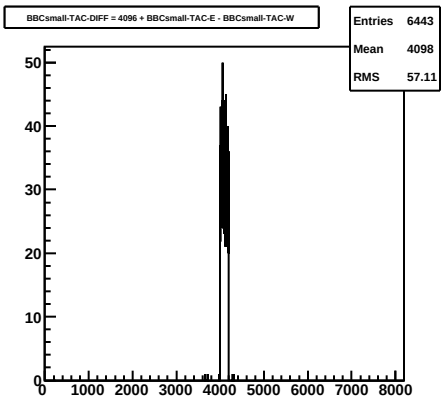

ZDC-TAC-E
Entries 221
Mean 292.9
RMS 30.33

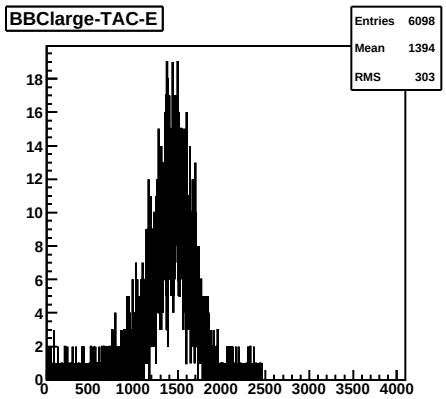

ZDC-TAC-W
Entries 223
Mean 208.4
RMS 37.03

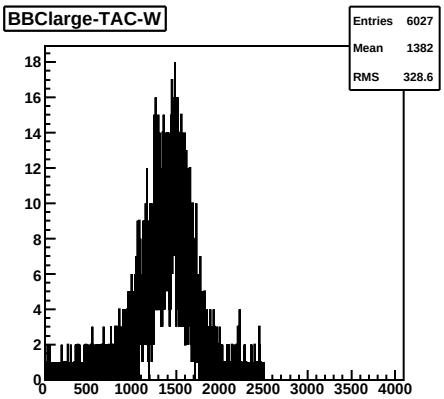

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 16
Mean 554.3
RMS 16.11

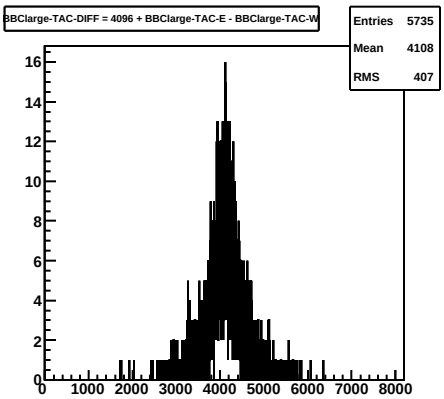

BBCsmall-TAC-E
Entries 6444
Mean 1469
RMS 158.8

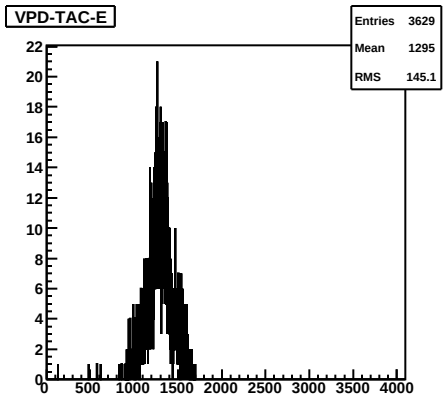

BBCsmall-TAC-W
Entries 6443
Mean 1468
RMS 161.2

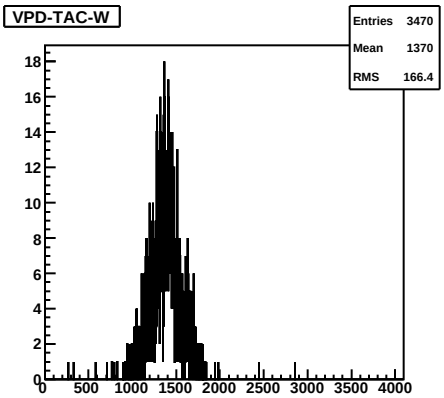

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 6443
Mean 4098
RMS 57.11

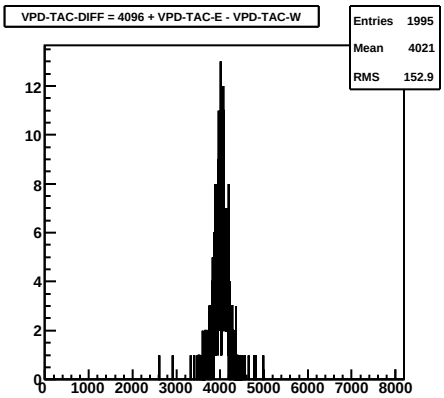

BBClarge-TAC-E
Entries 6098
Mean 1394
RMS 303

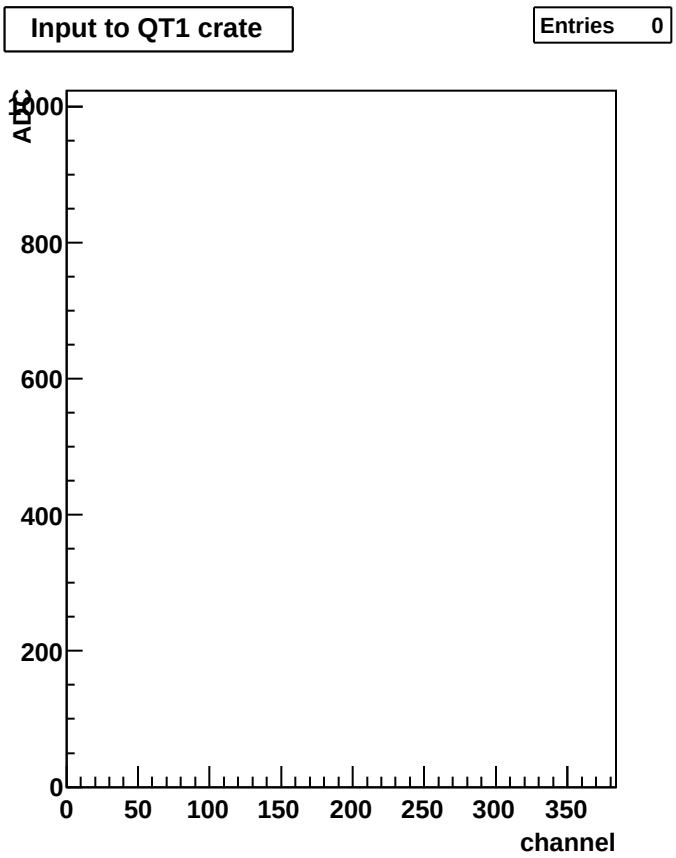

BBClarge-TAC-W
Entries 6027
Mean 1382
RMS 328.6

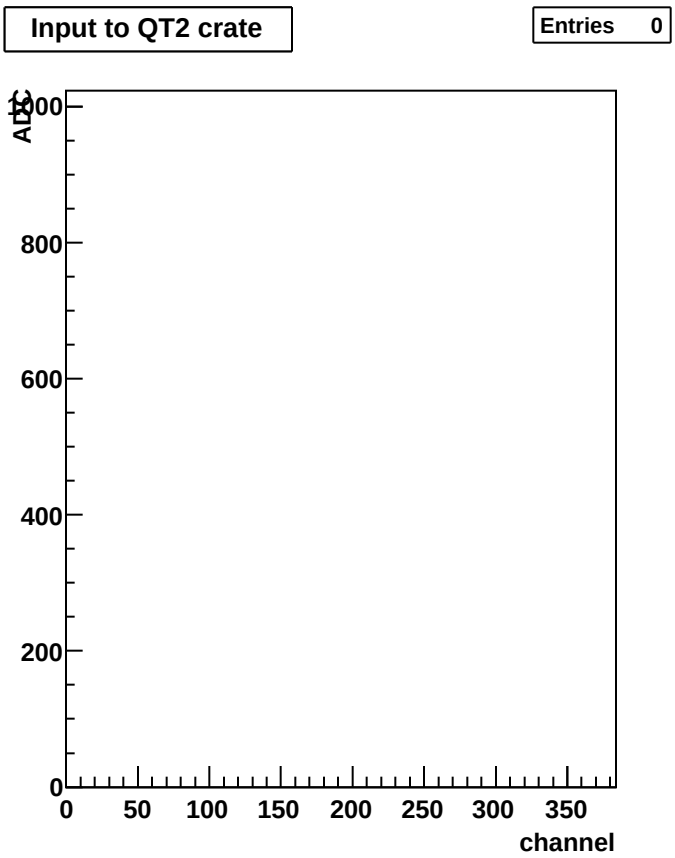

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 5735
Mean 4108
RMS 407

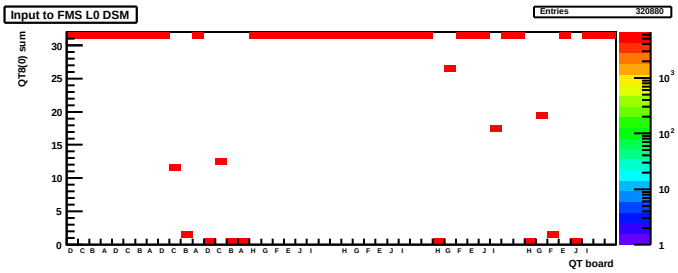

VPD-TAC-E
Entries 3629
Mean 1295
RMS 145.1

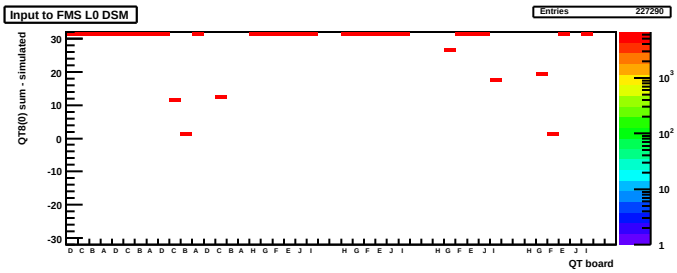

VPD-TAC-W
Entries 3470
Mean 1370
RMS 166.4

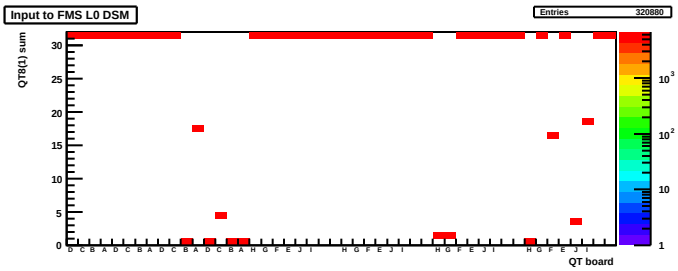

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1995
Mean 4021
RMS 152.9

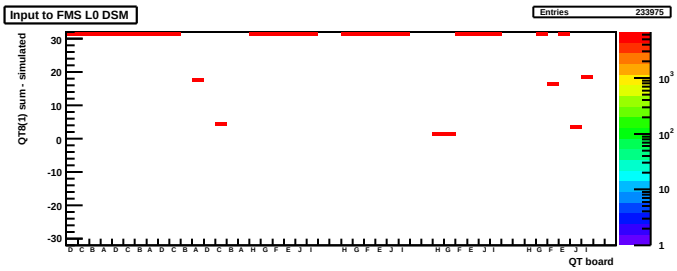

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0
50
100
150
200
250
300
350
channel

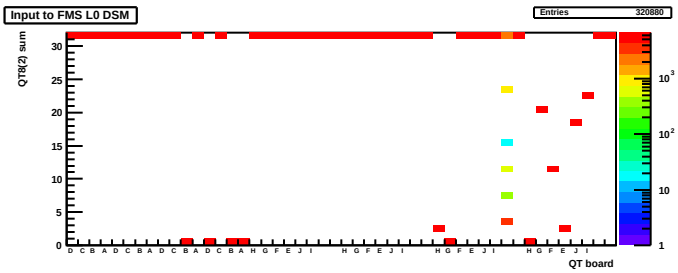

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0
50
100
150
200
250
300
350
channel

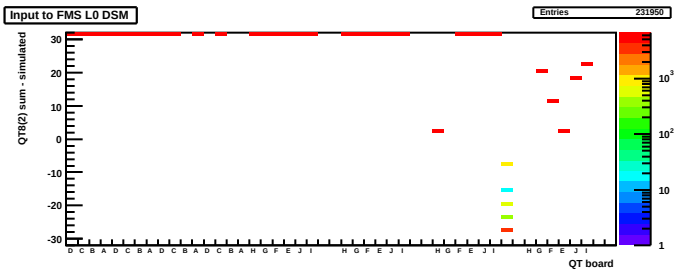

Input to FMS L0 DSM
Entries 320880
QT8(0) sum
QT board

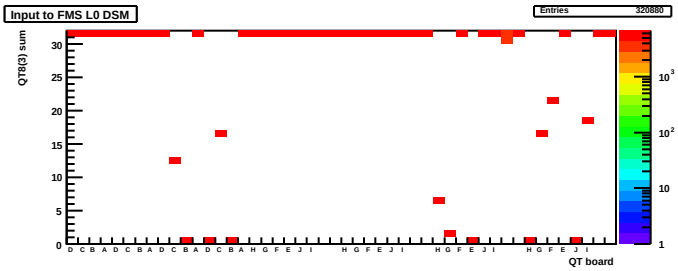

Input to FMS L0 DSM
Entries 227290
QT8(0) sum - simulated
QT board

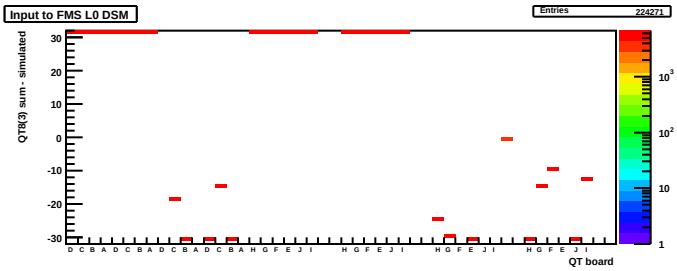

Input to FMS L0 DSM
Entries 320880
QT8(1) sum
QT board

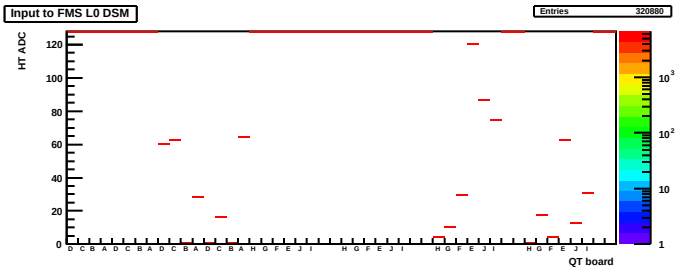

Input to FMS L0 DSM
Entries 233975
QT8(1) sum - simulated
QT board

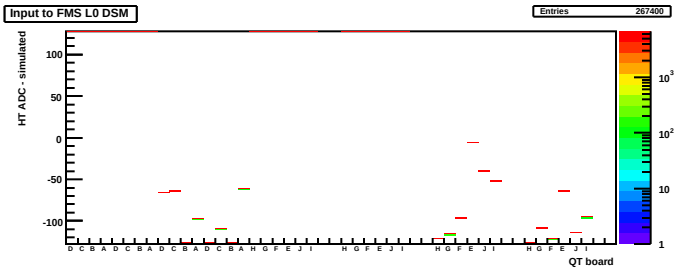

Input to FMS L0 DSM
Entries 320880
QT8(2) sum
QT board

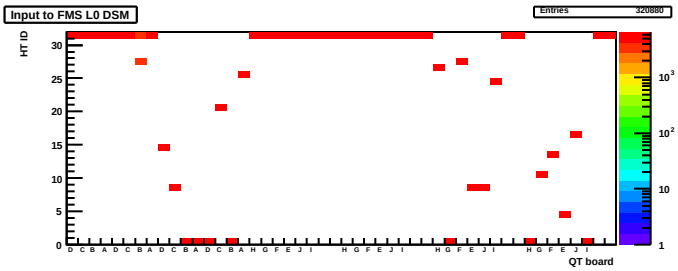

Input to FMS L0 DSM
Entries 231950
QT8(2) sum - simulated
QT board

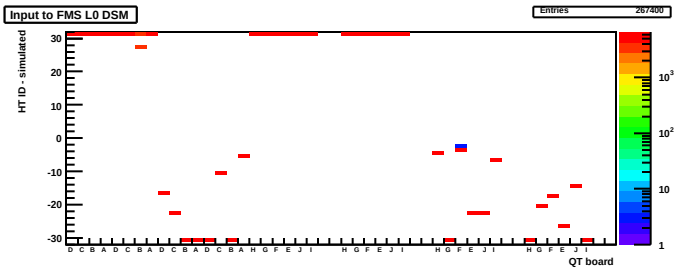

Input to FMS L0 DSM
Entries 320880
QT8(3) sum
QT board

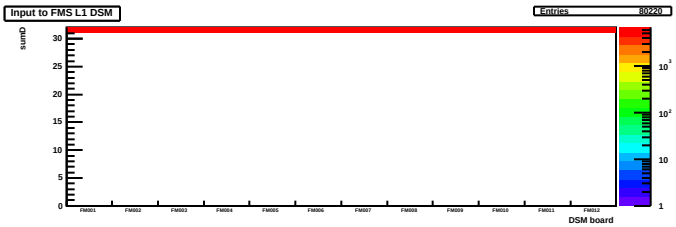

Input to FMS L0 DSM
Entries 224271
QT8(3) sum - simulated
QT board

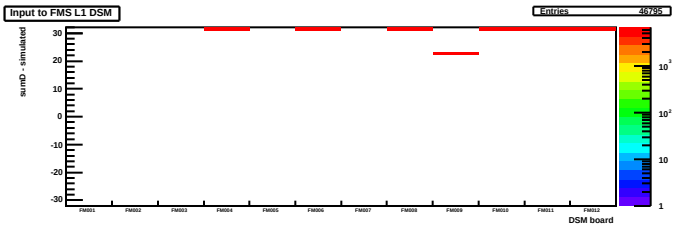

Input to FMS L0 DSM
Entries 320880
HT ADC
QT board

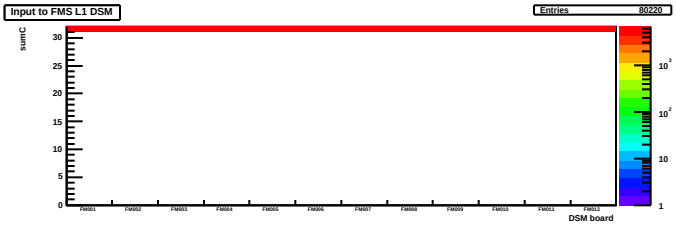

Input to FMS L0 DSM
Entries 267400
HT ADC - simulated
QT board

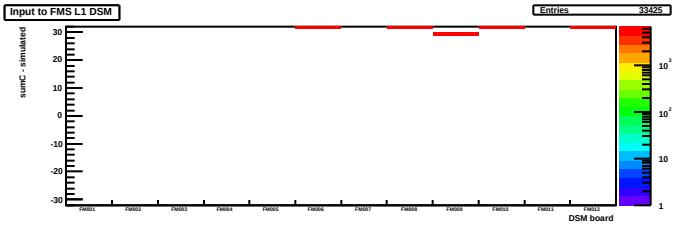

Input to FMS L0 DSM
Entries 320880
HT ID
QT board

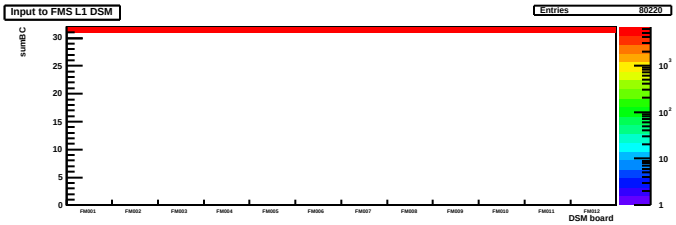

Input to FMS L0 DSM
Entries 267400
HT ID - simulated
QT board

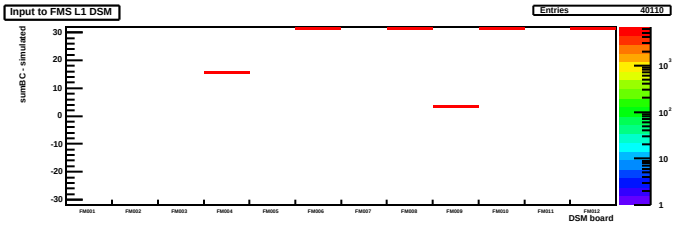

Input to FMS L1 DSM
Entries 80220
sumD
DSM board

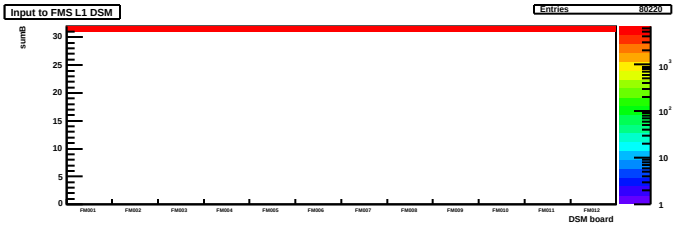

Input to FMS L1 DSM
Entries 46795
sumD - simulated
DSM board

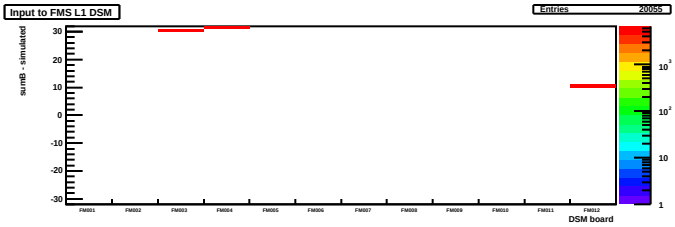

Input to FMS L1 DSM
Entries 80220
sumC
DSM board

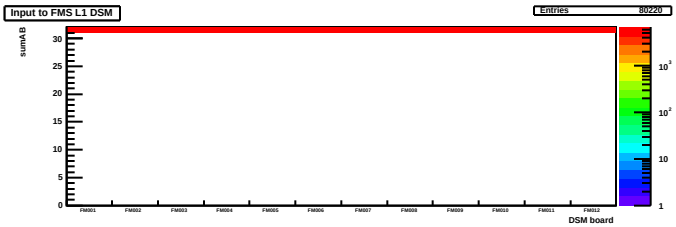

Input to FMS L1 DSM
Entries 33425
sumC - simulated
DSM board

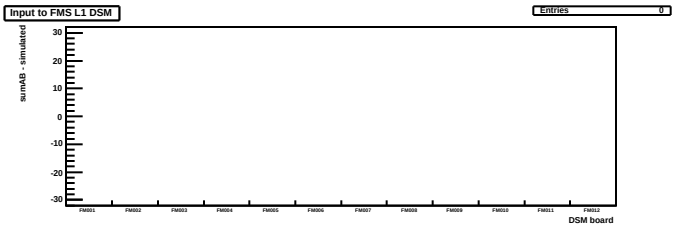

Input to FMS L1 DSM
Entries 80220
sumBC
DSM board

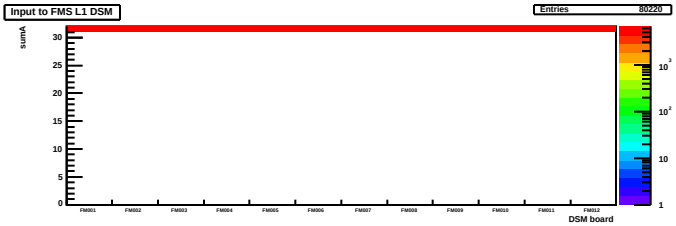

Input to FMS L1 DSM
Entries 40110
sumBC - simulated
DSM board

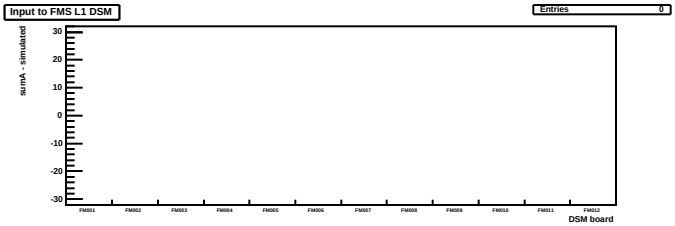

Input to FMS L1 DSM
Entries 80220
sumB
DSM board

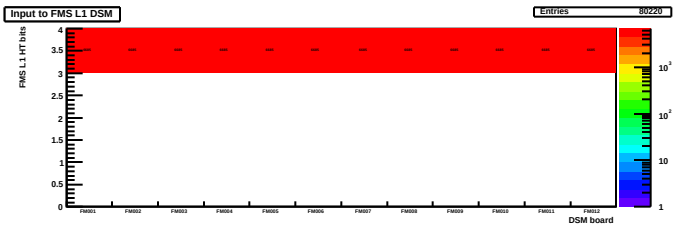

Input to FMS L1 DSM
Entries 20055
sumB - simulated
DSM board

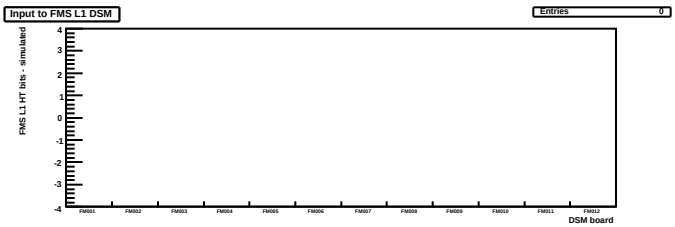

Input to FMS L1 DSM
Entries 80220
sumAB
DSM board

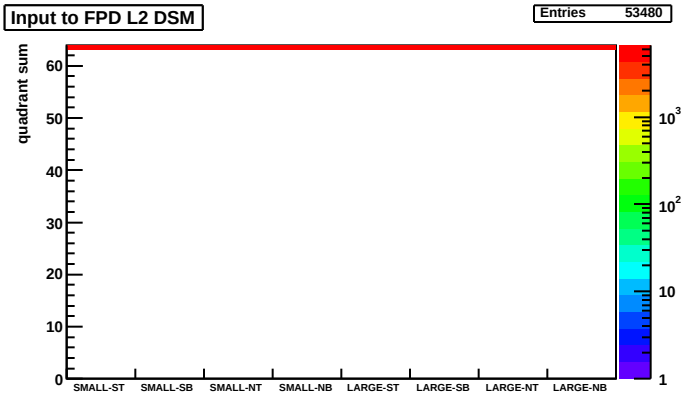

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

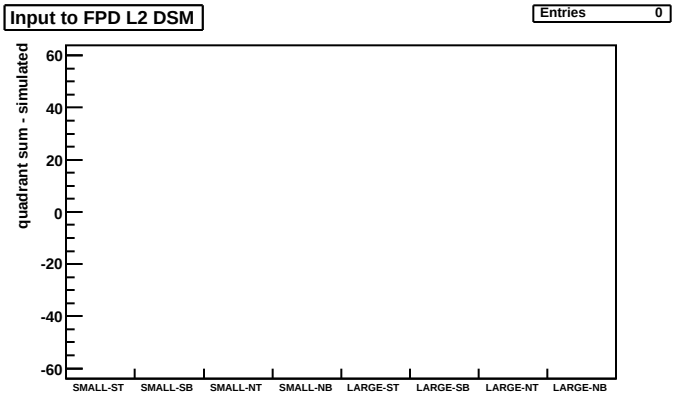

Input to FMS L1 DSM
Entries 80220
sumA
DSM board

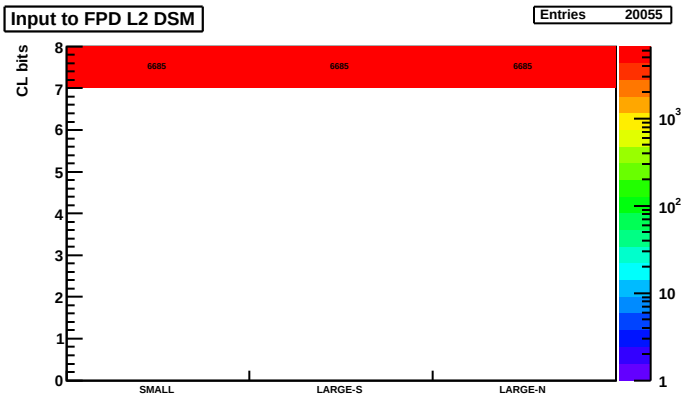

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

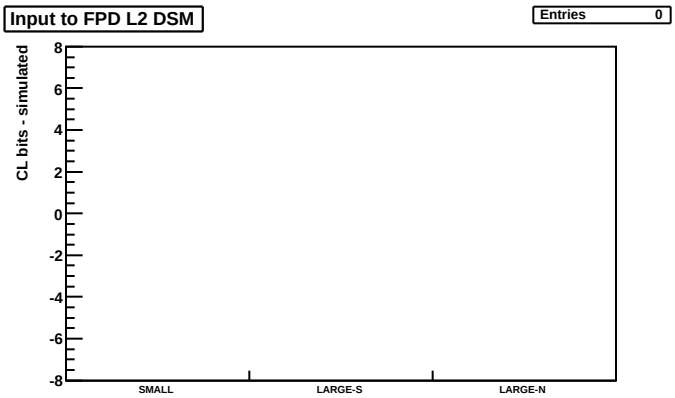

Input to FMS L1 DSM
Entries 80220
FMS L1 HT bits
DSM board

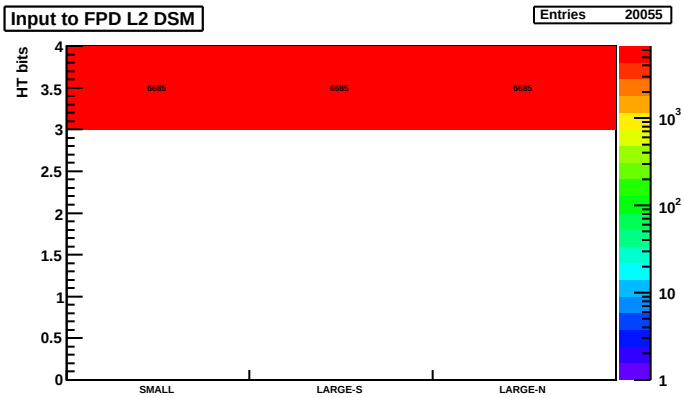

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

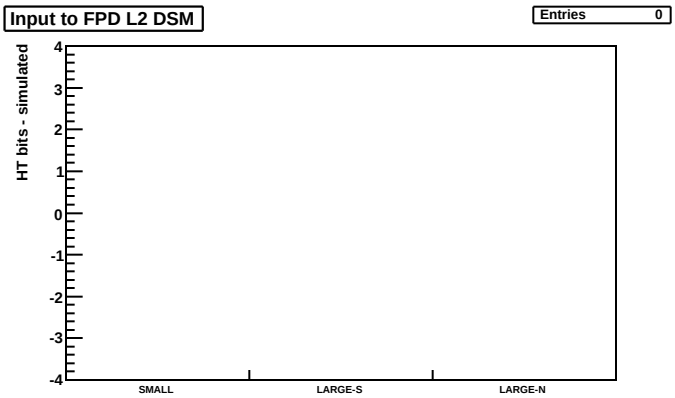

Input to FPD L2 DSM
Entries 53480
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

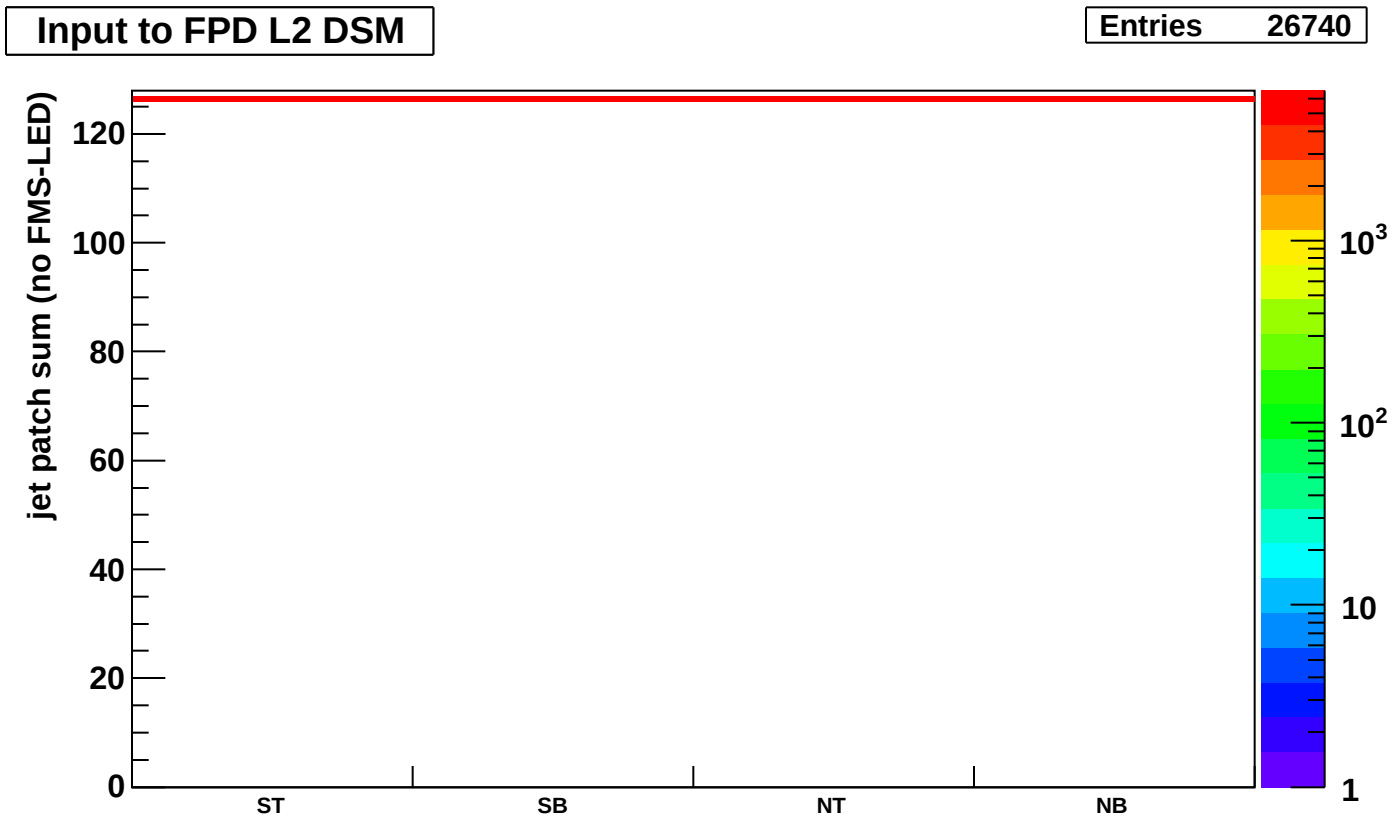

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

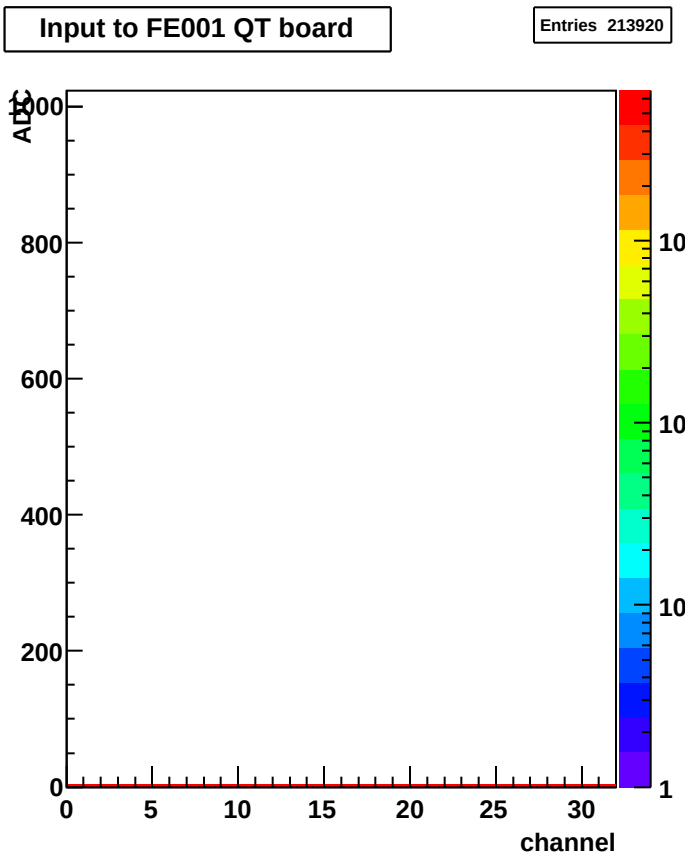

Input to FPD L2 DSM
Entries 20055
CL bits
6685 6685 6685
SMALL LARGE-S LARGE-N

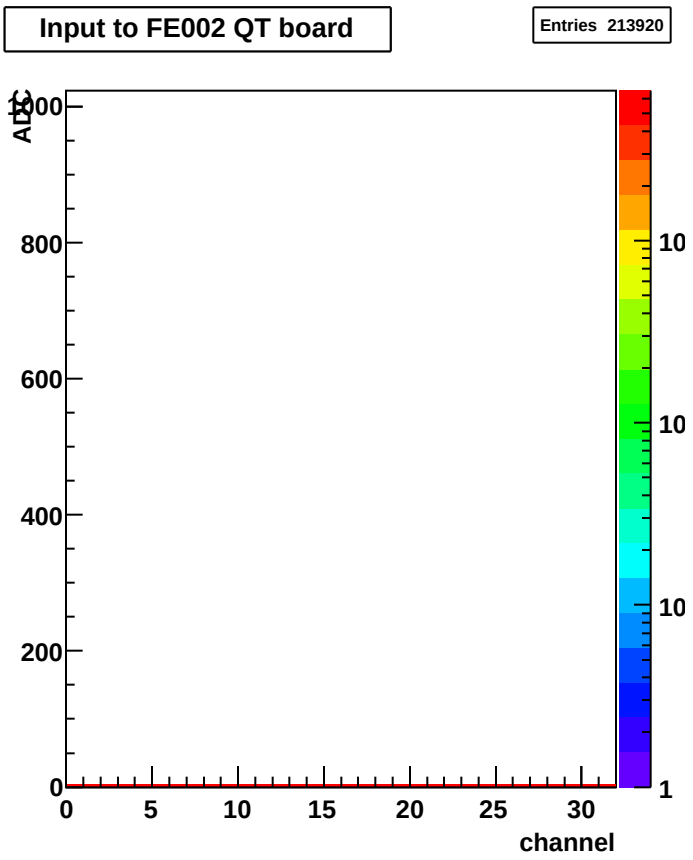

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

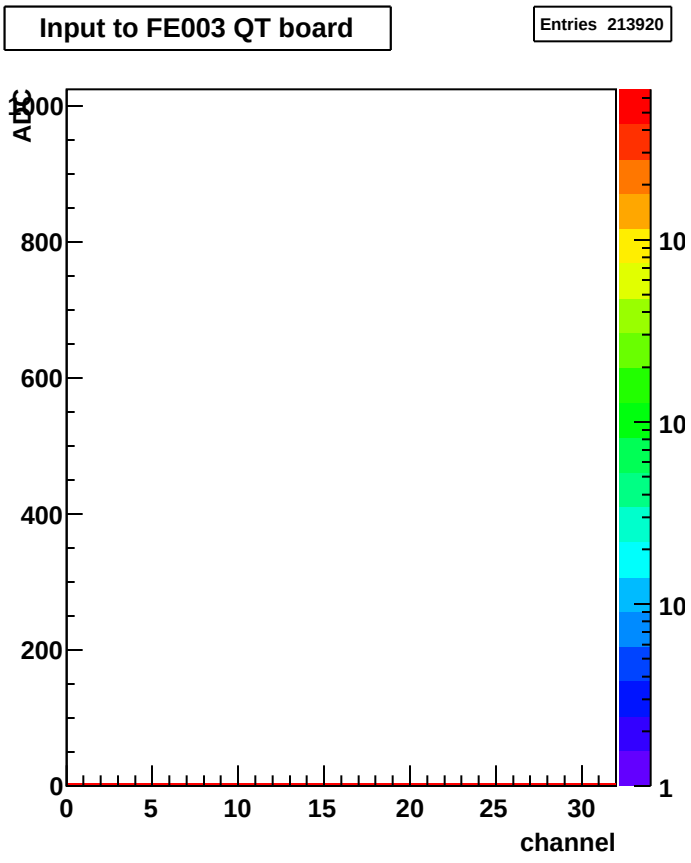

Input to FPD L2 DSM
Entries 20055
HT bits
6685 6685 6685
SMALL LARGE-S LARGE-N

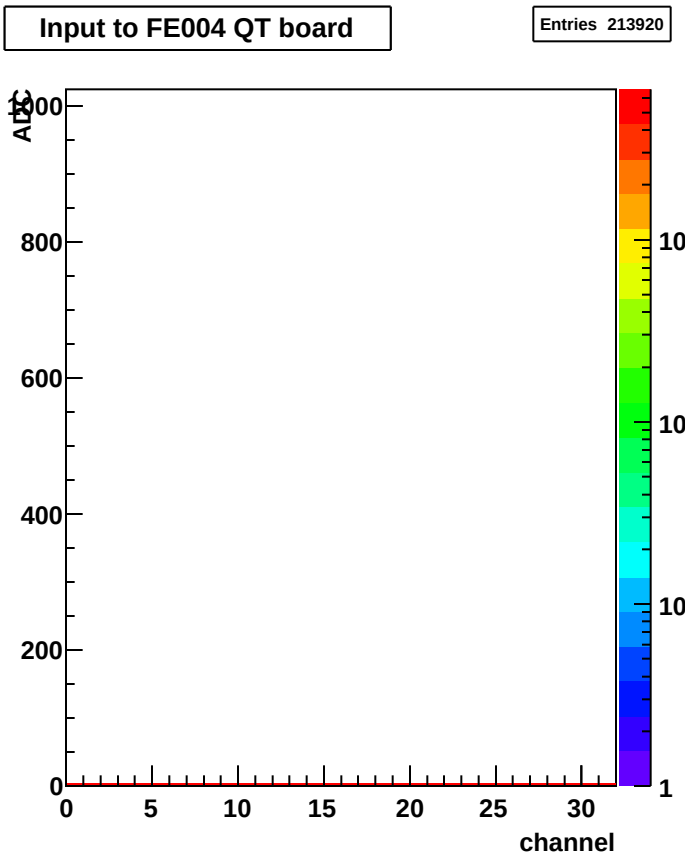

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

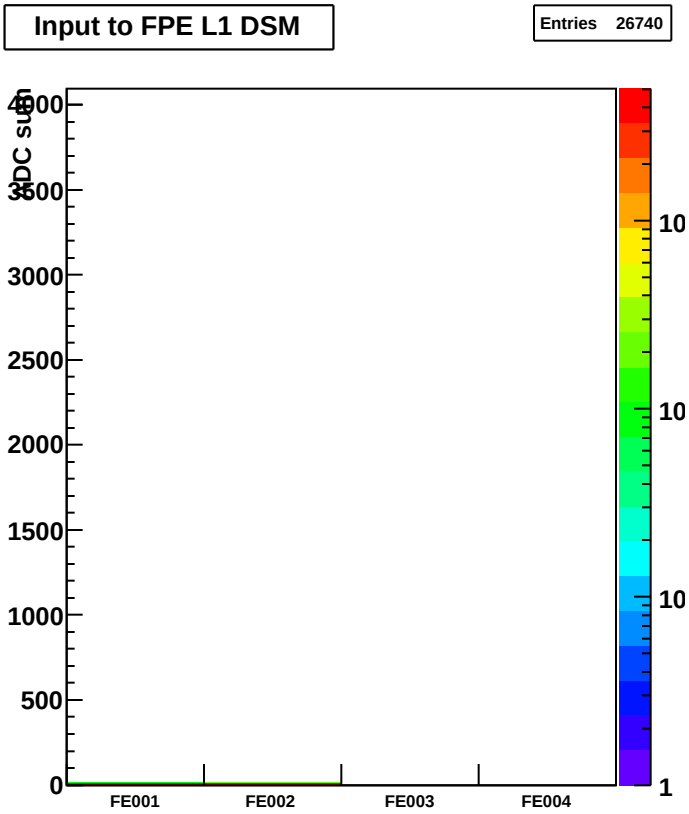

Input to FPD L2 DSM
Entries 26740
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^3
10^2
10
1

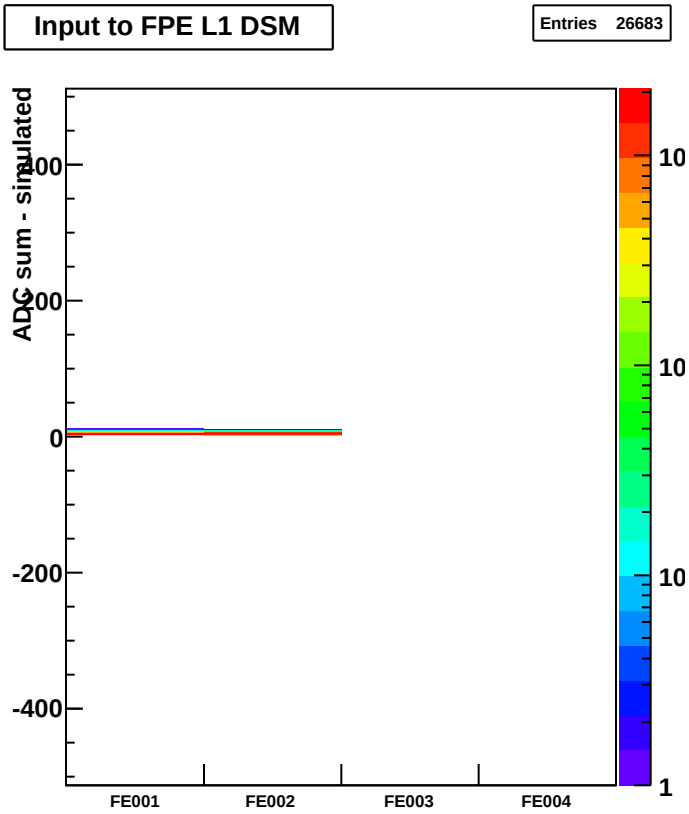

Input to FE001 QT board
Entries 213920
ADC
channel

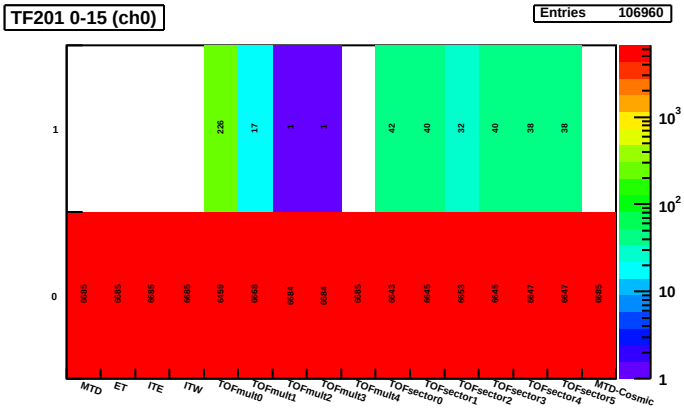

Input to FE002 QT board
Entries 213920
ADC
channel

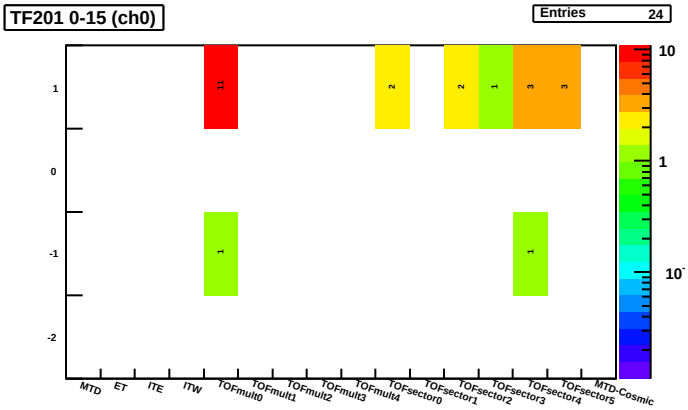

Input to FE003 QT board
Entries 213920
ADC
channel

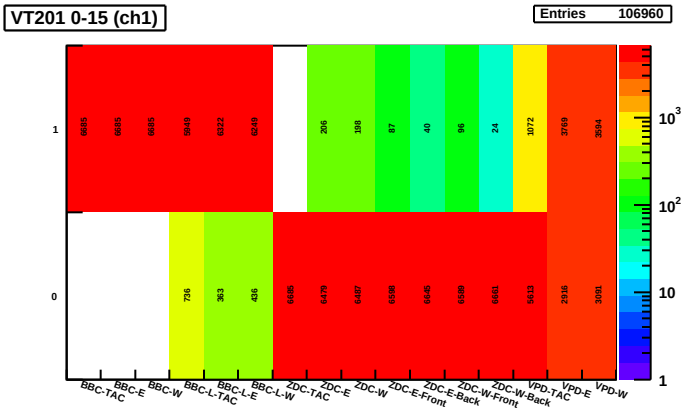

Input to FE004 QT board
Entries 213920
ADC
channel

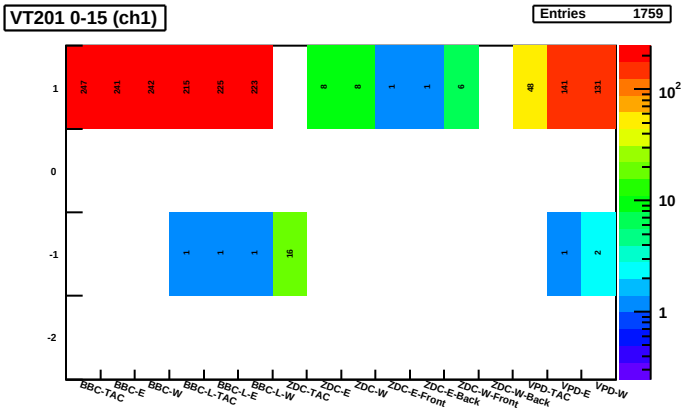

Input to FPE L1 DSM
Entries 26740
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
1

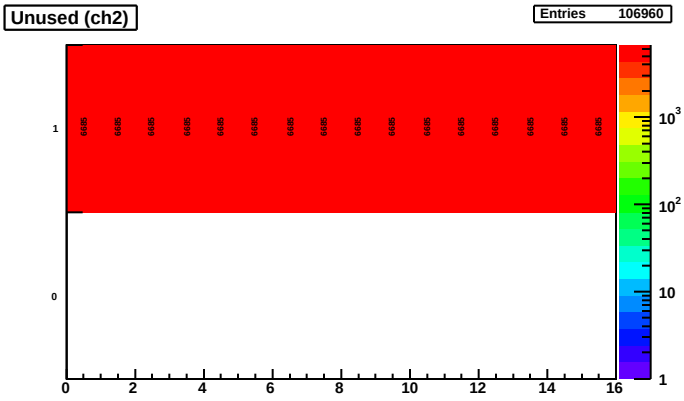

Input to FPE L1 DSM
Entries 26683
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
1

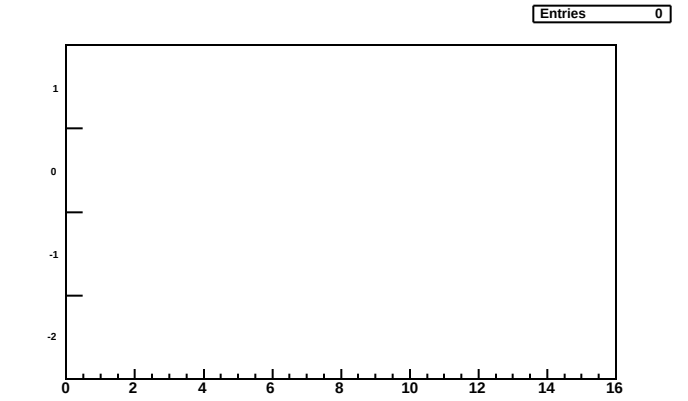

TF201 0-15 (ch0)
Entries 106960
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

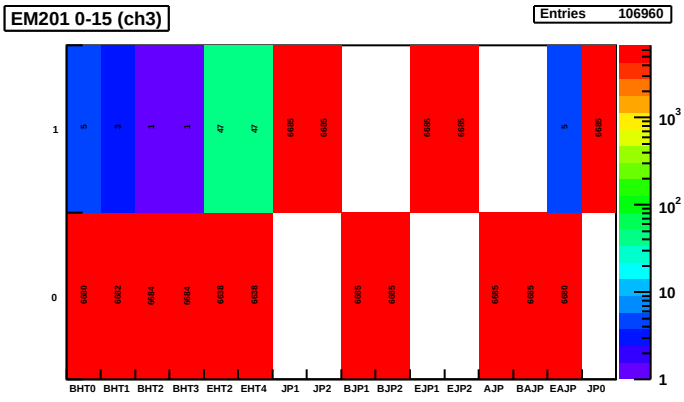

TF201 0-15 (ch0)
Entries 24
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

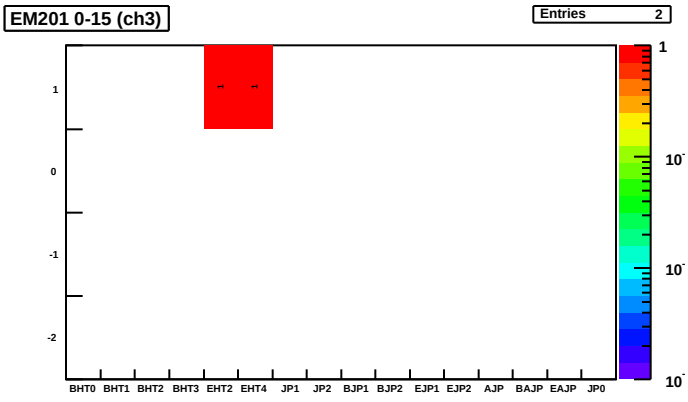

VT201 0-15 (ch1)
Entries 106960
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1759
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

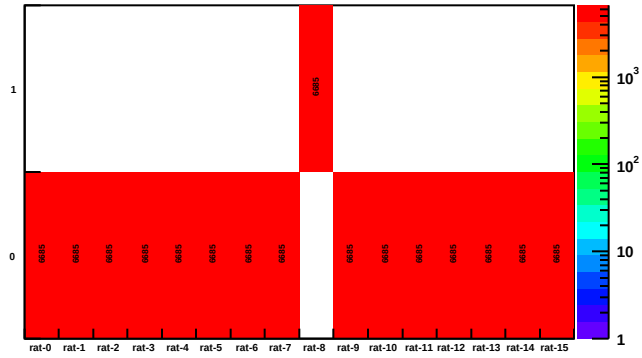

Unused (ch2)
Entries 106960

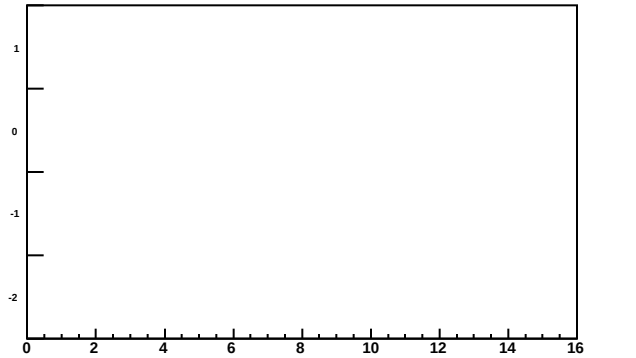

Entries 0


EM201 0-15 (ch3)
Entries 106960
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

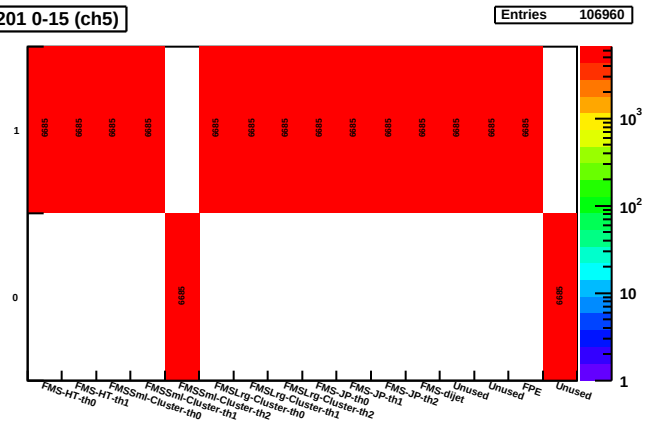

EM201 0-15 (ch3)
Entries 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

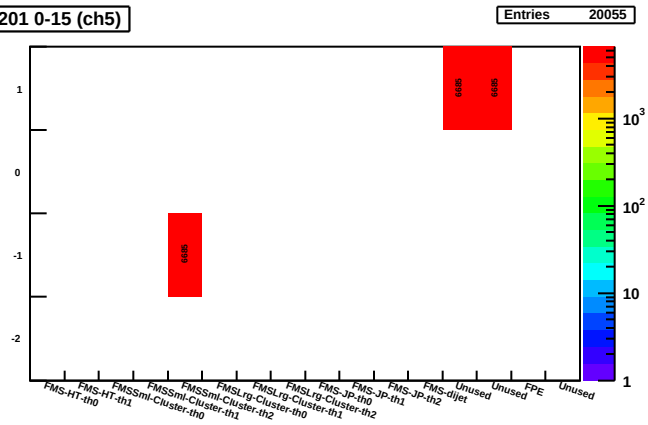




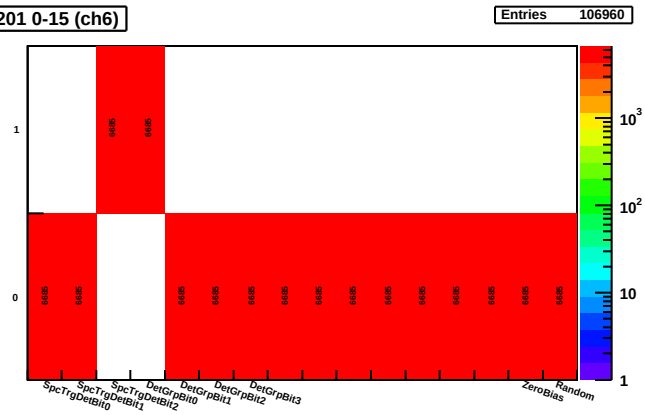
FP201 0-15 (ch5)

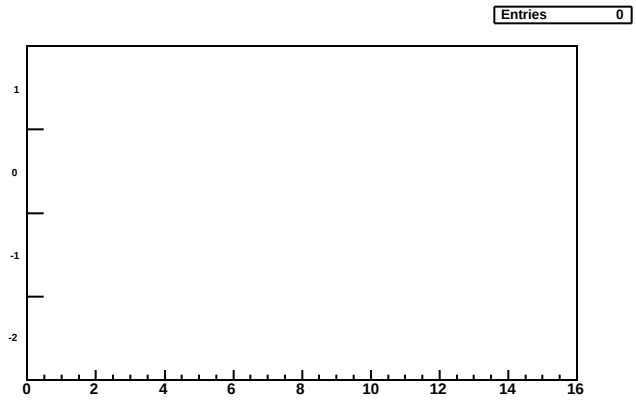
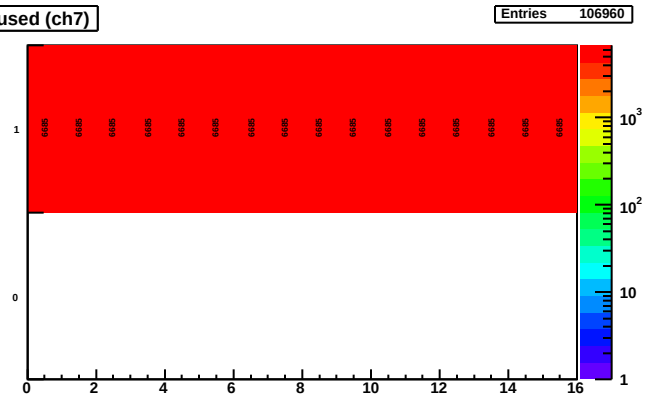
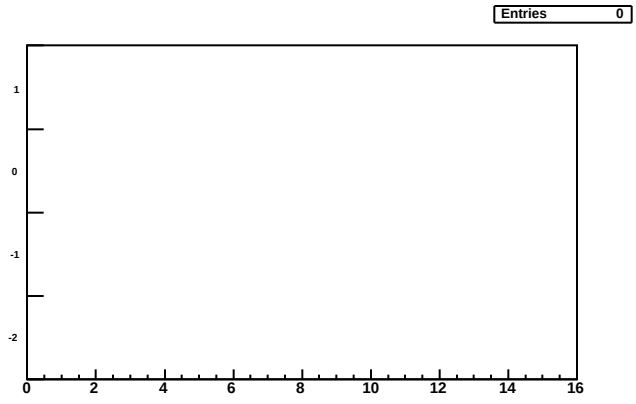


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)